



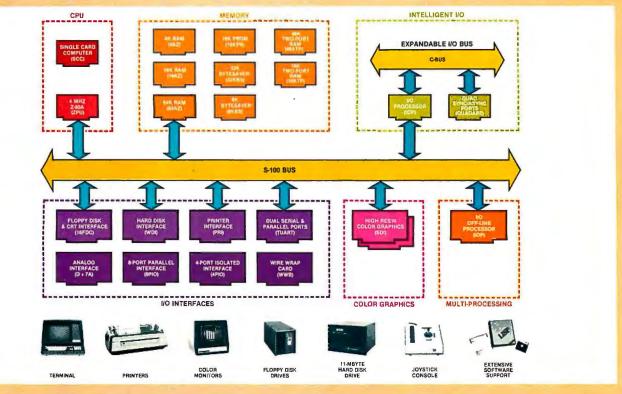
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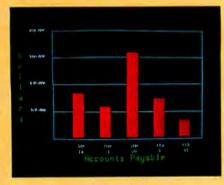
PROCESSORS — 4 MHz Z-80 A CPU, single card computer, I/O processor ● MEMORY — up to 64K including special 48K and 16K two-port RAMS and our very well known BYTESAVERS® with PROM programming capability ● HIGH RESOLUTION COLOR GRAPHICS — our SDI offers up to 754 x 482 pixel resolution. ● GENERAL PURPOSE INTERFACES — QUADART four-channel serial communications, TU-ART two-channel parallel and two-channel serial, 8PIO 8-port parallel, 4PIO 4-port isolated parallel, D+7A 7-channel D/A and A/D converter, printer interface, floppy disk controller with RS-232 interface and system diagnostics, wire-wrap and extender cards for your development work.



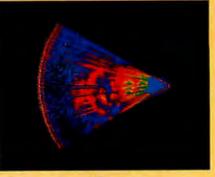
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Here's a color display that has everything: professional-level resolution, enormous color range, easy software, NTSC conformance, and low price.

Basically, this new Cromemco Model SDI* is a two-board interface that plugs into any Cromemco computer.

The SDI then maps computer display memory content onto a convenient color monitor to give high-quality, high-resolution displays (756 H x 482 V pixels).

When we say the SDI results in a highquality professional display, we mean you can't get higher resolution than this system offers in an NTSC-conforming display.

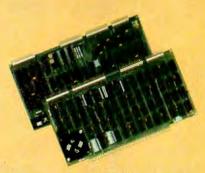
The resolution surpasses that of a color TV picture.

BASIC/FORTRAN programming

Besides its high resolution and low price, the new SDI lets you control with optional Cromemco software packages that use simple BASIC- and FORTRAN-like commands.

Pick any of 16 colors (from a 4096-color palette) with instructions like DEFCLR (c, R, G, B). Or obtain a circle of specified size, location, and color with XCIRC (x, y, r, c).

*U.S. Pat. No. 4121283



Model SDI High-Resolution Color Graphics Interface

HIGH RESOLUTION

The SDI's high resolution gives a professional-quality display that strictly meets NTSC requirements. You get 756 pixels on every visible line of the NTSC standard display of 482 image lines. Vertical line spacing is 1 pixel.

To achieve the high-quality display, a separate output signal is produced for each of the three component colors (red, green, blue). This yields a sharper image than is possible using an NTSC-composite video signal and color TV set. Full image quality is readily realized with our high-quality RGB Monitor or any conventional red/green/blue monitor common in TV



Model SDI plugs into Z-2H 11-megabyte hard disk computer or any Cromemco

DISPLAY MEMORY

Along with the SDI we also offer an optional fast and novel two-port memory that gives independent high-speed access to the computer memory. The two-port memory stores one full display, permitting fast computer operation even during display.

CONTACT YOUR REP NOW

The Model SDI has been used in scientific work, engineering, business, TV, color graphics, and other areas. It's a good example of how Cromemco keeps computers in the field up to date, since it turns any Cromemco computer into an up-to-date color display computer.

The SDI has still more features that you should be informed about. So contact your Cromemco representative now and see all that the SDI will do for you.

Circle 105 on inquiry card.



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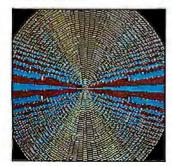
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In This Issue

Did you know that the Vikings were notorious pirates? In Robert Tinney's striking cover painting, executed from an original design by Jonathan Graves, the floppy disk is the "sail" that powers the underhanded business of software piracy. Included are several articles on the legal aspects of protecting software from unscrupulous pirates: Chris Morgan's editorial, "How Can We Stop Software Piracy?" (page 6); Christopher Kern's "Washington Tackles the Software Problem" (page 128), and Stephen A Becker's "Legal Protection for Computer Hardware and Software" (page 140).

Other noteworthy articles in this issue include in-depth examinations of the Extended Color BASIC for the TRS-80 Color Computer, the new Commodore VIC microcomputer, and the Epson MX-70 and MX-80 printers. And this issue begins a new occasional feature on microcomputer video games called "BYTE's Arcade."

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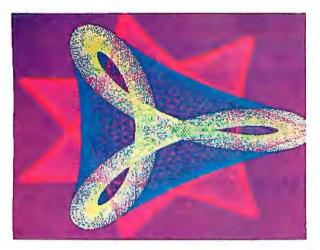
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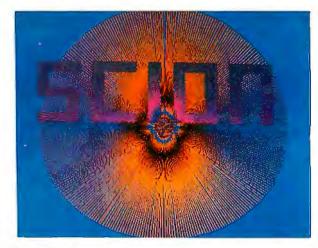
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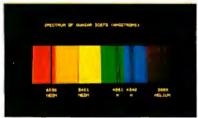
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BYTE, Product Review



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ELECTRONIC DESIGN, 1981 Technology Forecast

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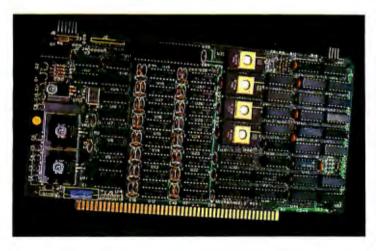
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Editorial

How Can We Stop Software Piracy?

Chris Morgan, Editor in Chief

Software piracy is rapidly becoming a major problem in the personal computer field. The casual copying of programs by computer hobbyists, although not at the epidemic stage, is frighteningly commonplace. Many people fail to see (or prefer not to see) that the practice is not just illegal—it's *unethical*.

But what about making backup copies of important software? What happens if your small business' direct-mail program "dies"? Without a backup, a businessman's only recourse is to return the disk to the manufacturer and hope it won't take longer than a few weeks to get a replacement. Manufacturers understand the problem, and have designed some floppy-disk-based programs that allow the user to make one backup copy. After this, software "jamming" information is automatically added to the original floppy disk to theoretically prevent additional illegal copies. In practice, though, enterprising software experts can crack the protection mechanisms and make copies at will.

The industry is faced with a dilemma: how does the manufacturer serve the customer's legitimate need to make backup copies, while protecting his expensive software investment? There are two possibilities: put the would-be software pirate at a disadvantage if he makes an illegal copy, or, better still, make it virtually impossible for the pirate to make a copy.

The Persuasion Route

Let me make a not-too-perfect analogy between the software industry and the record industry. When tape recorder sales began to increase during the early 1970s, record industry executives predicted that record sales would plummet because of private off-the-air taping. But, in fact, record sales climbed steadily throughout the decade. Why? My opinion is that when people think of a recording, they think of the entire package: the album artwork, the liner notes—in short, there is more to a recording than the sound coming from a pair of loudspeakers. In much the same vein, there is more to a piece of software than the object code: there is the documentation, for instance.

The need to make a copy of the documentation is an additional nuisance for the software pirate. It costs money to make photocopies. Then there's the registration card: legitimate owners of software are often put on mailing lists to receive updates to their programs as well as information about new programs from the manufacturer. A cheap and effective way for manufacturers to fight the pirate is to creatively exploit the latter idea. At the risk of overgeneralization, computer-science people tend to be obsessive-compulsive in their psychological makeup, ie: they hate to miss out on any details about a product they buy—especially a piece of software!

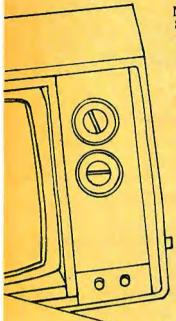
I mentioned earlier that this was a less-than-perfect analogy. The problem is that a \$9.95 recording is one thing—a \$600 program is quite another. The above-mentioned tactics might help the manufacturer of a \$30 or \$50 piece of software, but temptation becomes powerful indeed when the price tag reaches three or four figures.

Editorial continued on page 10

Introducing the COLOR CONNECTION™

Plug A TRS-80* Color Computer into the World of

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Now you can expand Tandy's exciting new TRS-80* Color Computer using proven System-50 products. Expansion possibilities are limitless. And expansion is easy. Plug one end of the COLOR CONNECTION into the Program Pak* connector of the Color Computer. Plug the other end into a System-50 bus motherboard. Now add the functions you want, selecting from an inventory of standard modules manufactured by competent, long-established firms - from the inventory of solid performers, like Percom Data Company.

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Beyond 16K

Another option is incremental memory expansion. Add 8-, 16- or 24-Kbytes of static RAM with our M24SS card; 16-, 32- or 48-Kbytes of dynamic RAM with our M48DSS card. The COLOR CONNECTION prevents contention between internal computer memory and external memory.

System Requirements

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The right motherboard

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The System-50 (SS-50) bus community. With a TRS-80* Color Computer and the COLOR CONNECTION, it's your world. Enjoy!



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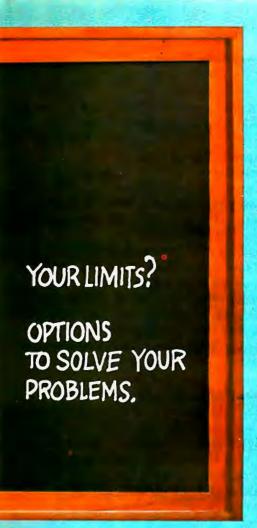
No matter which option you choose, you get the benefit of working with completely integrated products...fully assembled and tested... under one warranty and one price structure... leaving you free to concentrate on value-added application development and sales.

Choose from mainframe options...

Select from three packaging options: Rack-mount, tabletop or front panel models. All three feature our 20 slot S-100 motherboard with 25 amp power supply and are delivered fully assembled and tested with our Series II™ board sets. Any board configuration you choose works with any DPS-1 version, allowing you to vary your package offering, or develop on one version and market another.

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- Rack Mount version—features a heavy gauge frame designed to fit into standard 19" racks. CVT power supply for brown out immunity is standard.

^{*}In Calculus, a fundamental statement in the definition of limit; interpreted here to imply: "For your integration problem, Intersystems has a solution."



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- 6 SI/O six individually softwarecontrollable serial I/O ports with optional interrupts. Each can run RS 232 at up to 19,200 BAUD, as can our VI/O board.
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Micros for bigger ideas.

Circle 206 on inquiry card.

Technological Measures

The ultimate answer is to make it so difficult and costly for the pirate to make copies that the problem goes away. A good first step is to put teeth into software protection laws. The revised copyright act of 1976 had a major impact on phonograph record pirates because of the much more stringent penalties for convicted offenders. You may have noticed the Psign on commercial records and tapes: it's an indication that they're protected by the new law. (For further legal background, including information on the latest Supreme Court decisions, see "Washington Tackles the Software Problem," page 128, and "Legal Protection for Computer Hardware and Software," page 140.)

We come next to the most intriguing weapon in our arsenal: hardware "locks" on the software. The concept of the I.D. ROM is a recent development now being used, among other places, in conjunction with a program called RCS/Micro Modeller, developed in England by Intelligence (UK) Limited. The program allows a person to use an Apple II computer to create financial planning models and high-resolution color displays featuring pie charts, histograms, and so on. A novel feature of the program is its "electronic slide show" capability: a hand-held control, similar to a slide projector control, plugs into one of the paddle ports of the Apple and allows the user to cycle through an electronic "slide show" on the video screen. Built into the control is a special ROM containing an identification number that is duplicated on the program floppy disk. The program periodically checks for the presence of the I.D. ROM. If it's not found, the program crashes.

This technique puts one more stumbling block in the way of the pirate, and it does not add appreciably to the total cost of the software (the I.D. ROM costs about \$20). Alas, there are some experts in Europe who have cracked the code of another I.D. ROM used in conjunction with a program called Wordcraft, which is being distributed by Commodore in England. So the technique, while making it much more difficult to copy software, is not the ultimate answer. Still, I welcome this type of innovative approach to a mind-boggling problem. Readers interested in further information about the RCS/Micro Modeller program (not yet available in the United States) should contact David Low, ACT (Microsoft) Ltd, 5/6 Vicarage Rd, Edgbaston, Birmingham B15 3ES England.

Two of the most promising solutions to the software protection problem come from West Coast inventor Marc Kaufman. He has filed a patent for an "execute-only ROM," a new type of read-only memory which produces a sequence of executable code in the normal manner, but prohibits the user from randomly accessing memory addresses. As Kaufman explains, the user begins execution of the program at a known address. A "secret" executive routine, built into the ROM, contains a table of the legal next steps for every given step in the program. Only those steps listed in the table can be accessed by the

user. For example, if the program contains a branch to one of two places, *only* those two places can be examined by the programmer at that time. If a program contains enough branches, it would take an inordinate amount of time for the user to run through every permutation of the program to get a complete listing of the code, even if a computer did the searching. Kaufman is presently working with both hardware vendors and users to develop the idea. An unreadable EPROM is also in the works, enabling the do-it-yourselfer to create secure programs.

Kaufman's second idea is to add a "black box" to a personal computer. Every piece of software would come with a magnetic key (or other type of hard-to-duplicate key) that plugs into the black box and contains a coded I.D. number that matches the I.D. number on the floppy disk. The program resides on the disk in encrypted form. In order to decode the program, the key must be plugged into the box. With this scheme, the user can make as many backup copies as desired, but only one of them can be used at a time. The drawback to such a system is the need for the black box. But if the idea catches on, the price would probably come down. Interested readers can contact Marc Kaufman at Kaufman Research, 14100 Donelson Pl. Los Altos Hills CA 94022.

Stopping the pirate is vital. Piracy has reached near epidemic levels in Europe, where it is not uncommon for an entire computer club numbering in the hundreds to line up their computers and make hundreds of copies of programs from United States manufacturers for the use of the entire club! Then there is the phenomenon of the "software library." Some of them are legitimate, but all too many cavalierly offer copies of programs to their members at a fraction of the retail cost.

Illegitimate copies of programs threaten the fabric of personal computing. The software innovators in our field must be compensated fairly for their work, or we will no longer see the high-quality programs that currently grace the marketplace.

I welcome comments from readers about this all-important issue, and would like to begin a dialog featuring your comments. Please send your thoughts to: Software Protection, c/o BYTE Publications Inc, POB 372, Hancock NH 03449.■

Articles Policy

BYTE is continually seeking quality manuscripts written by individuals who are applying personal computer systems, designing such systems, or who have knowledge which will prove useful to our readers. For a more formal description of procedures and requirements, potential authors should send a large (9 by 12 inch, 30.5 by 22.8 cm), self-addressed envelope, with 28 cents US postage affixed, to BYTE Author's Guide, 70 Main St, Peterborough NH 03458.

Articles which are accepted are purchased with a rate of up to \$50 per magazine page, based on technical quality and suitability for BYTE's readership. Each month, the authors of the two leading articles in the reader poll (BYTE's Ongoing Monitor Box or "BOMB") are presented with bonus checks of \$100 and \$50. Unsolicited materials should be accompanied by full name and address, as well as return postage.



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Letters

Educational Dialog

As a junior-high-school teacher with several years of experience, I want to call into question some of the underlying assumptions in Seymour Papert's "New Cultures from New Technologies." (See the September 1980 BYTE, page 230.)

Mr Papert seems to believe that children and child-initiated explorations are inherently good and, conversely, that parents, teachers, schools, and their limits and expectations are inherently bad. Also, he seems to believe that all learning can and should be as swift, natural, accurate, and frustration-free as the learning of spoken language, and that learning by rote or rite is without meaning and is harmful to the child.

To the first supposition, I can only reply that there is a time and place to be child-centered, and a time and place to be goal-directed. To the second supposition, language acquisition has little to do with other types of learning-it is a highly specific capability that is "hard-wired" into the brain from birth. Finally, rote and rite learning are common elements in spontaneous children's play, to say nothing of adult culture.

Piagetian learning is at best an unfortunate choice of words on Mr Papert's part, because Piaget did not focus on learning at all. He studied the cognitive processes in children that depended on maturation, not learning, and were indeed highly resistant to any learning experiences he was able to devise. His great contribution to education was to point out that there are thresholds and there are ceilings to what an immature mind can learn. The insight-oriented "new math" failed in public education for this reason: its proponents were asking grade-school children to perform abstract reasoning, which Piaget terms formal operations, before they were ready to do so.

Anyone wishing to teach young children to program computers, regardless of formal language instruction, had better remember a few things: Piagetian formal operations begin in adolescence. It is not safe to assume that a preadolescent is doing what you think he is doing, in the way you think he is doing it, or for the reason you think he is doing it. You ignore Piaget at your own peril.

In summation, no single development is going to revolutionize education, because it is a "soft" field-too many factors are operating already. The computer probably will be the biggest thing ever to hit the field, but not for the reasons Papert thinks.

Charles Heckel 1624 Hillcrest Glendale CA 91202

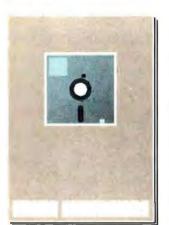
Seymour Papert Replies:

I agree with Mr Heckel that one ignores Piaget at one's peril. I have tried not to ignore him. I spent about 5 years working in his center for Genetic Epistemology in Geneva, Switzerland. In my book Mindstorms: Children, Computers and Powerful Ideas, I argue that our work on Logo is in the spirit of Piaget's theory even if it seems to contradict some of his empirical findings.

I grant that children in many countries have been found to follow a fixed pattern of intellectual development. I grant that psychologists have failed when they tried to change this pattern of development by exposing children to a few hours of special treatment under laboratory conditions. But, I argue in Mindstorms that the penetration of computers into the lives of children (indeed into the whole culture) will exert a much more massive influence on intellectual development than any experiments in the past. I suggest that it is possible that these more massive influences will have correspondingly massive effects. I don't see how any of Piaget's experiments could conceivably be held to exclude this possibility.

In addition to these general issues, there is one specific point of Piagetian interpretation on which I must express disagreement with Mr Heckel. Piaget certainly did not believe, as Mr Heckel asserts, that the acquisition of language "has little to do with" other types of learning or that it is "hard-wired." This sounds more like Noam Chomsky's position against which Piaget argued with increasing vigor in the last years of his life.

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Ada Manual Available

The reference manual for the Ada programming language, July 1980 version, is now available from the Government Printing Office. The supply in the Defense Department's DARPA office (referred to in "BYTELINES," January 1981 BYTE, page 200) is now exhausted. Requests should be sent to:

Superintendent of Documents US Government Printing Office Washington DC 20402

Order number: 008-000-00354-8

Cost: \$5.50 per copy.

I learned this when I requested information from DARPA about the manual,

Mike Robinson Rt 4, Box 70 Ringgold GA 30736

Hard Disk to Buy

I was quite amused to read that manufacturers are unable to understand why small hard disks aren't selling as expected. (See "Winchester 8-inch Drives Off to Slow Start," December 1980 "BYTE-LINES," page 214.) Perhaps the reason could be the typical \$5000 to \$8000 price tag—more than a little difficult to justify to your wife, mother, girlfriend....

Besides the normal budgetary problems, I have no way to interface a hard disk to my Heath H-8 computer, either in hardware or software. Another problem is that most hard disks are not removable. Imagine the added utility of a drive using an 8- or 14-inch cartridge, holding about 20 megabytes, costing \$2000, and removable (so you can take it to your friend's house). Come to think of it, that's a good description of a DEC (Digital Equipment Corporation) RK05 cartridge disk-pack drive.

John F Priebe 4804 Mt Airy Rd Sylvania OH 43560

Plot: North by Northwest

I found John Beetem's article "Vector Graphics for Raster Displays" enjoyable. (See the October 1980 BYTE, page 286.) But, when I read R H Rae's letter, I had to respond. (See "Intercepting Raster," January 1981 BYTE, page 14.) Beetem's vector-generator routine works beautiful-

ly for its intended purpose. But Rae's alternative suggests that there are those who could profit from a little "compuservation" (running faster on fewer bytes).

The routine I use to drive my Houston Instrument Hiplot plotter is a modification of the one that appears in Hiplot brochures (it is actually Algorithm 162 by Fred G Stockton; Collected Algorithms from ACM, 1963). I offer it in a minimal BASIC as Houston Instrument did. It assumes that the PRINT statement goes to the Hiplot, which ignores all characters except "p" thru "w," and "y" and "z." "p" means move the pen one increment (0.005 inch) north, "q" northeast, "r" east, and so on to "w" meaning northwest.

- 10 A\$="rqvwpsvupqpwtstu"
- 20 INPUT X,Y
- 30 PRINT"z":REM PEN DOWN COMMAND
- 40 GOSUB 100
- 50 PRINT"y":REM PEN UP COMMAND
- 60 GOTO 20
- 70 REM *** VECTOR GENERATOR SUBROUTINE ***
- 80 REM THIS SUBROUTINE DRAWS THE BEST STRAIGHT
- 90 REM LINE FOR A COORDINATE CHANGE OF (X) AND (Y)
- 100 I=1: IF X<0 THEN X=-X: I=3
- 110 IF Y < 0 THEN Y = -Y: I = I + 4

 120 IF Y < Y THEN T = Y: Y = Y: Y = T
- 120 IF X < Y THEN T = X: X = Y: Y = T: I = I + 8
- 130 E = -X/2: C = 0
- 140 IF C>X-.5 THEN RETURN
- 150 E=E+Y: IF E>0 THEN E=E-X: PRINT MID\$(A\$,I+1,1): GOTO 170
- 160 PRINT MID\$(A\$,I,1)
- 170 C=C+1: GOTO 140

This routine is marvelous; no multiplications and only an avoidable right shift in line 130 (the entire routine, including the array and double-precision variable storage, requires less than 130 bytes of 8080 code).

The byte miser in me demanded that I understand this routine. When I found its logic as simple as the routine, I couldn't resist configuring it for screen graphics and animation, turning a printer into a plotter, and tackling the awesome task of massaging my plotter into a super printer.

If it is not too late, Mr Rae, you might consider using Stockton's algorithm for your commercial graphics product.

William A McWorter Jr Mathematics Department Ohio State University Columbus OH 43210

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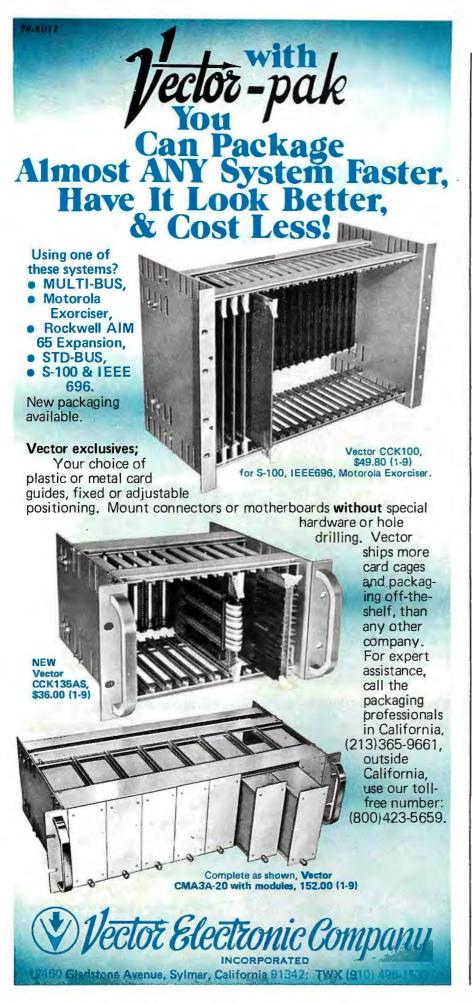
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BYTE's BOMBworks

My December 1980 BYTE did not include the usual Reader's Service and BOMB cards, so here are my December BOMB votes.

My vote for the best article of the year is Grady Booch's Micrograph series. (See "Micrograph, Part 1: Developing an Instruction Set for a Raster-Scan Display," November 1980 BYTE, page 64; "Part 2: Video-Display Processor," December 1980 BYTE, page 120; and "Part 3: Software and Operation," January 1981 BYTE, page 238.) I eagerly awaited my January BYTE for the concluding part.

Mr Booch's design was good, but the hardware could have been upgraded for better performance. According to my calculations for the color chip, the Z80 microprocessor is active only 12% of the time with the hardware configuration shown. The Motorola spec sheets give a better hardware implementation: isolate the display memory from the processor memory when the display circuitry is accessing display memory. Such an approach would allow fuller utilization of the Z80, as well as remove response-time problems from the interface to the host computer (ie: lost time when the Z80 is locked out by the display). All in all, Mr Booch's articles were excellent!

I had a different opinion of the competing serials on graphics. Alan Grogono's "Graphic Color Slides" articles gave no insight into the more general problem of graphics. (See the November and December 1980 BYTEs, pages 126 and 96, respectively.) Allen Watson's "A Simplified Theory of Video Graphics" presented little if any new information on either hardware or software. (See the November and December 1980 BYTEs, pages 180 and 142, respectively.) He might as well have referred to some of the many articles and books on the television signals (eg: the TV Typewriter Cookbook or some such). I rate both of these articles poor.

On a more positive note, I enjoyed all of the game reviews and would like to see more for other software packages. These, however, would rate only a good, with the exceptions of "On the Road to Adventure"; "Odyssey: The Compleat Apventure"; and "Zork and the Future of Computerized Fantasy Simulations." I rate all of these excellent. (See the December 1980 BYTE, pages 158, 90, and 172, respectively.) I'd also place Steve Ciarcia's "Computerized Testing" in that category. (See December 1980 BYTE, page 44.)

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Circle 350 on inquiry card

I want to compliment BYTE's Production Director, Nancy Estle, on the layout of BYTE. BYTE articles generally manage to stay in one piece, rather than starting in the front and continuing piecemeal throughout the remainder of the magazine. I would like to see even more segregation between articles and advertising, however. I do not object to the ads, in fact I conscientiously read through them, hoping that I won't miss any new developments. But having to wade through the ads to find article continuations is annoying.

Arthur Throckmorton 5657 S Oak St Littleton CO 80127

The CBT is Dead: Long Live the CBT

In regard to Mr James R Boatright's letter in the December 1980 BYTE, the reported demise of the CBT is somewhat exaggerated. (See "The End of the CBT," page 300.) The CBT-1001D DAA (dataaccess arrangement), though no longer available from Bell, is currently manufactured by Precision Components, Elgin, and Terminal Systems, etc. It is available from many distributors who are typically listed in the yellow pages under 'Telephone Equipment & Systems." The CBT is used extensively by manufacturers in the medical-data field.

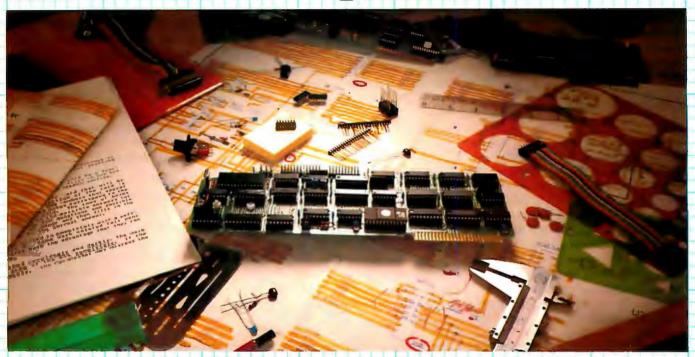
Please be advised, Mr Boatright, you need not discard your equipment requiring use of CBT, CBS, or other types of DAA.

Carl E Osborne Jr President O & J Electronics Inc 4027 Knight Arnold Rd, Suite 105 Memphis TN 38118

More on HP-41C

Congratulations to BYTE and to Bruce D Carbrey for the excellent article on the HP-41C "calcuputer." (See "A Pocket Computer? Sizing up the HP-41C," December 1980 BYTE, page 244.) With a few enhancements, I used the "CODE-BREAKER" demonstration-game program over the holidays with my grandchildren. It is a fine example of the capability of the HP-41C.

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- P Features auto-line feed, transparent terminal mode, Apple tabbing, line length, delay after carriage return, local echo of output characters, simultaneous serial/parallel output, lower to upper case conversion, discarding of extraneous LFs from serial input
- Uses the powerful 2651 serial PCI chip
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But just as any program or product can be improved, so can any article. It is most unfortunate that Mr Carbrey failed to mention two important aspects of the HP-41C:

- 1. The HP-41C continues the use of RPN (reverse Polish notation) logic. Since my first experience with RPN in the 1960s on a Friden CRT desk-top calculator (it used RPN well before Hewlett-Packard), there has been no question that RPN is the only way to go. Not just because it may use less keystrokes, but because its logic is unambiguous, straightforward, and simple to remember. This is a most important attribute of the HP-41CI
- 2. Even more important, Mr Carbrey failed to mention that all Hewlett-Packard programmable calculators, including the HP-41C, are supported by an active, independent user's organization known as the PPC-Personal Programmers Club. (Formerly known as the HP-65 User's Group,) The PPC has no connection with Hewlett-Packard or its Users Library. A periodic publication, the PPC Calculator Journal, is available to members only. Club members have discovered that many things can be done with the HP-41C and

its predecessors. Although some of these capabilities are not "supported" by Hewlett-Packard, their use can greatly improve almost any program. The club is currently designing a custom ROM (readonly memory) to make these features available to its members.

Anyone seriously using the HP-41C should join the PPC. To get further information, send a 9- by 12-inch stamped, self-addressed envelope with 2 ounces postage to Richard J Nelson, Editor/Publisher PPC Calculator Journal, 2541 W Camden Pl, Santa Ana CA 92704. You will receive a sample issue of the Journal and further membership information.

B F Wheeler 22 Wilkins Ave Haddonfield NJ 08033

Chessmate

In the December 1980 BYTE, John Martellaro presented a review of the Sargon II chess-playing program. (See "Sargon II, An Improved Chess-Playing Program for the Apple II," page 114.) He

states that it is the first chess program he has seen that sets a trap. He also says that it is the strongest chess program money can buy-dedicated chess-playing devices included. Does this include the Chess Challenger 7 by Fidelity Electronics?

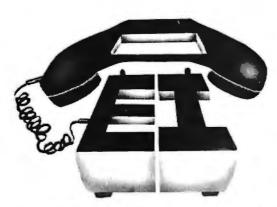
My Chess Challenger 7 on level 7 (tournament level) played exactly the same game as Sargon II, including the trap, through step 12. At step 12, Sargon played Nc3-d5 (N/B3-Q5); Chess Challenger 7 played Qd2-d1 (Q-Q1). My response was Qf6-g6 (Q-KN3), at which point Chess Challenger 7 conceded the game,

I would like to see an entire issue of BYTE devoted to this kind of competition between computers. Does BYTE have such an issue planned?

Tom Disque Rt 7, Waldrap Dr Mayfield KY 42066

No such issue is planned, but we will continue to publish reviews of chess programs and playing machines as they come in to us (hint). (See "The Newest Sargon: 2.5" in the January 1981 BYTE, page 208.)

Letters continued on page 268



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Hardware Review

The Epson MX-80 and MX-70 Printers

Kevin Cohan, Technical Editor

Small system users soon realize that effective programming is difficult without hard copy upon which to make notes, corrections, and general scribblings. However, realization often turns to dismay when the "professional" quality printer carries a price tag larger than that of an otherwise complete popular disk-based microcomputer system. In the past, inexpensive printers (when available) have been slow, unreliable, inconvenient (eg: many require expensive thermal or electrostatic paper), and generally lacking in desirable features. Those users with less than \$1000 to spend have been faced with a choice of such a printer or a refurbished IBM Selectric or Teletype ASR33.

Epson Inchas aimed its two new low-priced dot-matrix printers, the MX-80 and the MX-70, squarely at this under-\$1000 market (see photo 1). Both have features normally found only in professional printers that are priced accordingly. (Active in the computer printer business in Japan for over fifteen years, Epson has also supplied print heads and mechanisms for such well-known printer manufacturers as Anadex.)



Photo 1: The Epson MX-70 and MX-80 printers. The MX-70 (left) is a prototype of the final version which has a tan rather than a cream body.

The MX-80

The more expensive MX-80 printer has so many features that a complete learner's manual accompanies the instruction manual. This manual (written by David A Lien and published for Epson by Compusoft) guides the user through basic setup procedures and also describes the less obvious capabilities of the MX-80: it can do much more than provide hard-copy listings!

Measuring 37.4 cm wide by 30.5 cm deep by 10.7 cm high (14% by 12 by 4% inches), the MX-80 is not much larger in size than a stack of five or six issues of BYTE. It has a 9-wire print head that prints 96 ASCII (American Standard Code for Information Interchange) characters with lowercase descenders and 64 graphics characters on a 9 by 9 dot matrix, as shown in listing 1. The print head has an estimated life of over 50,000,000 characters, and it can be easily replaced. Print speed is 80 cps (characters per second) bidirectionally, and a long-life print ribbon is contained in an easily removable cartridge.

External features (shown in photo 2) include a metal paper-guide rack, manual paper-advance knob, power switch, Centronics-type 36-pin cable connector, three control pushbuttons, and four green indicator LEDs (light-emitting diodes). In addition, the MX-80 has a tractor-feed paper mechanism and can use three-ply paper (original and two carbon copies). The On-Line pushbutton toggles the printer between on- and off-line modes. The FF (form feed) and LF (line feed) pushbuttons, functional only when the printer is off-line, advance the paper by one form (ie: page length) and one line, respectively. The distance that the paper advances may be changed under software control.

The four LEDs indicate Power, Printer Ready, No Paper, and On-Line. A software-controllable buzzer is located inside the printer case and is activated by a reed switch on the paper guide when the printer runs out of paper. A self-test mode may be activated by turning the printer on while depressing the LF pushbutton; in this mode, all characters provided by internal software are

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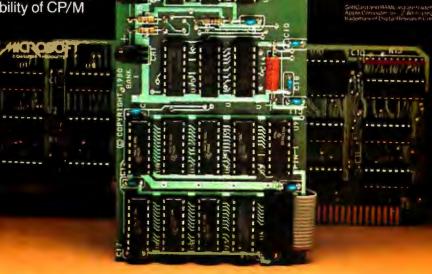
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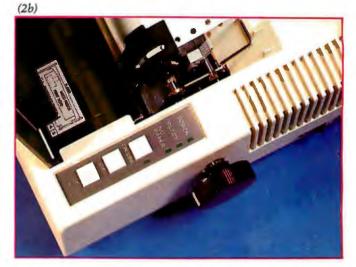


Photo 2: Control panels for the Epson MX-70 and MX-80 printers. Photo 2a shows the FEED (paper feed) button and the green Power LED (light-emitting diode) on the MX-70. Photo 2b shows the control panel of the MX-80, which has Power, Ready, No Paper, and On-Line LEDs, and On-Line, FF (form feed), and LF (line feed) buttons.

At a Glance.

Name

Epson MX-80

Use

Dot-matrix impact printer

Manufacturer

Epson America Inc 23844 Hawthorne Blvd Torrence CA 90505 (213) 378-2200

Dimensions

37.4 cm wide by 30.5 cm deep by 10.7 cm high (14% by 12 by 4% inches)

Price \$645

Features

Prints 96 ASCII and 64 graphics characters in a 9 by 9 dot matrix (lowercase letters have descenders); 80 cps bidirectional print speed with end-of-line seeking function (increases average print speed); tractor-feed paper mechanism; prints TRS-80

graphics, Japanese Katakana set, special characters for the US, England, France, and Germany; prints an original and up to two carbon copies; programmable tabs; replaceable print head; and a long-life ribbon cartridge

Additional Hardware Interface card needed for Apple II

Documentation

MX-80 User's Manual by David A Lien, 22 by 28 cm (8½ by 11 inches), about 100 pages

Options

TRS-80 cable (about \$25); Apple II interface card with cable (about \$110); IEEE-488 or serial interface (about \$65 each); serial interface with 2 K-byte buffer (about \$150); 960 dot-per-line graphics option (about \$100)

repeatedly printed out to test the operation of the print head, ribbon guide, and motor mechanisms.

Internally, the MX-80 is a truly intelligent printer that incorporates its own microprocessor: an Intel 8049 single-chip 8-bit processor with 2 K bytes of masked ROM (read-only memory), 128 bytes of programmable memory, and twenty-seven I/O (input/output) lines. This microprocessor coordinates the internal logic and controls the two precision stepper motors. One motor moves the print head, while the other advances the paper. The microprocessor is aware of the position of the print head at any given moment and actively seeks the shortest means of travel to the next print position. This feature, in combination with the bidirectional printing capability, constitutes the logical-seeking function, which increases the effective printing speed and minimizes head-travel time to reduce head wear.

Several options may be selected via two internal DIP (dual in-line pin) switches; these include auto line-feed, a full TRS-80 graphics set or a Japanese Katakana character set, and special characters for the US, England, Germany, and France (see listing 2). This last feature allows the printing of umlauts, accented letters, and other characters that are generally unavailable on personal computer printers.

Under software control, the user may select one of three print densities: 2, 4, or 6.5 characters per centimeter (5, 10, or 16.5 characters per inch), which results in 40, 80, or 132 characters on a line. Line spacing (ie: the distance the paper advances when a line-feed code is transmitted) has a default value of 0.423 cm (½ inch), but it may be set from 0.035 cm (½ inch) to 3.00 cm (1½ inch) in increments of 0.035 cm (½ inch)—the distance between two wires on the print head. This presents some interesting possibilities.

The number of lines per form defaults to sixty-six but may be set at any whole number less than that. The user may specify up to sixty-four vertical tabs per form and up to 112 horizontal tabs per line. An emphasized character

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Listing 1: ASCII character set as printed on the Epson MX-80 (figure 1a) and the MX-70 (figure 1b) low-cost printers. Note the lack of descenders on lowercase letters in the MX-70 example.

1a

!"#\$%&"() **, -./O123456789:; <=>?aABCDEF6 HIJKLMNOPORSTUVWXYZ[\]^_ *abcdefghijklmno pqrstuvwxyz{|}~

16

! "#\$%&^()*+,-./0123456789:;<=>?@ABCDEFG HIJKLMNOPQRSTUVWXYZ[\]^_*abcdef9hijklmno parstuvwxyz{;}~

Listing 2: The MX-80 has several user-selectable font options, including graphics characters that are TRS-80 compatible (2a), Japanese Katakana (2b), and special characters for the US, England, France, and Germany (2c).

2a

26

2c

U.S.A: # @ [\] { | } ~

ENGLAND:

* () () 6 1

FRANCE:

a " 9 5 6 û 6 "

GERMANY:

5 A 5 D A 5 U 6

mode (where each character is overprinted a second time) and a boldface mode (where the paper is advanced 0.0118 cm [1/216 inch] before overprinting) are also available (see listing 3). The printer slows to 40 cps in these special modes.

For a cost of about \$650, this is more printer for the money than any other available.

The MX-70

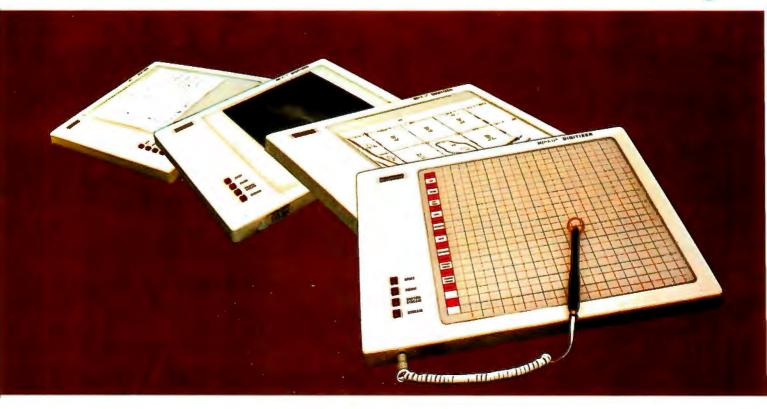
Similar in appearance to the MX-80, but with fewer features, the MX-70 is available for about \$200 less (suggested retail price, \$449). A 7-wire print head produces characters on a 7 by 5 dot matrix at a rate of 80 cps, but the unit does not offer the bidirectional logical-seeking capabilities of the MX-80. The MX-70 has only one green LED for power indication and only one general paperadvance (line feed that repeats if held down) pushbutton. The MX-70 uses the same self-test mode as the MX-80.

Internal jumpers select one of two character sets and auto-line-feed on or off. The MX-70 may be ordered with

either the Japan/USA or the England/Germany special character set in ROM. The user may software-select 40 or 80 characters per line, or a high-resolution graphics mode where binary bit images are directly printed on a 480 by 7 dot per line matrix (ie: the user can print any combination of dots within this graphics density). Line spacing may be from 0.035 cm to 3.00 cm (1/12 inch to 111/1/22 inch). The ability to advance the paper by the distance between two wires on the print head, combined with the high-resolution graphics mode, gives the user an effective resolution of 480 by 792 dots per standard form. The actual form length may be set from 0.424 cm to 51.2 cm (1/2 inch).

If it seems strange that the MX-70 offers bit-map graphics and the MX-80 doesn't, it will be no surprise for you to learn that by the time this article is printed, Epson will be offering a retrofit option on the MX-80. For about \$100, this option will give the MX-80 bit-mapped graphics at either 480 or 960 dots per line: the latter density is twice that of the MX-70.

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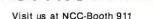
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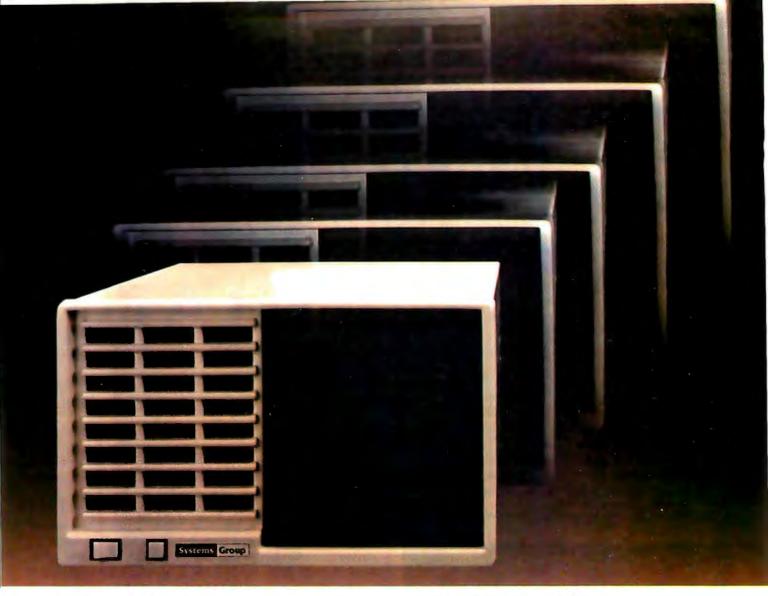
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At a Glance_

Name

Epson MX-70

Use

Dot-matrix impact printer

Manufacturer

See "At a Glance" box for Epson MX-80

Dimensions

Same as MX-80

Price \$449

Features

Prints 96 ASCII characters in a 5 by 7 dot matrix; 80 cps print speed; tractorfeed paper mechanism; prints an original and up to two carbon copies; includes a

high-resolution graphics mode. replaceable print head. and long-life ribbon cartridge

Additional Hardware

Interface card needed for Apple II

Documentation

MX-70 User's Manual by David A Lien, 22 by 28 cm (81/2 by 11 inches), about 80 pages

Options

Choice of either USA/Japan or England/Germany special character sets in ROM; TRS-80 cable (about \$25); Apple II interface with cable (about \$110)

Listing 3: The MX-80 features five various character modes (figure 3a), several of which may be combined to produce different effects. The MX-70 has only two character modes (figure 3b), but has a high-resolution graphics mode (not shown) as a standard feature.

.3a

STANDARD CHARACTERS

BOLDFACE CHARACTERS

DOUBLE STRIKE CHARACTERS

COMPRESSED CHARACTERS

DOUBLE WIDTH CHARACTERS

36

REGULAR CHARACTERS

EXPANDED CHARACTERS

Editor's note: I was very pleased with the quality and reliability of both printers, but would like to mention two very small complaints. First, the MX-80 has a piercing alarm tone that sounds for three seconds whenever it receives a "bell" character. This causes some annoyance when the printer is used with an Apple II, which beeps during printing errors and causes the Epson printer to beep. Second, both printers are so quiet when not working (hardly a criticism) and the power-on LED is so small, that it is easy to overlook these indications and leave the printers on overnight....GW]

Interfacing

Both the MX-80 and MX-70 printers communicate through an 8-bit parallel port that is available on a 36-pin Centronics-type cable connector. Some computers require a special interface in order to use the Epson printers, but all necessary interface components are available from Epson Inc. TRS-80 owners may use the standard Radio Shack printer cable, but due to a slight difference in connections, only the official Epson cable allows the separation of the carriage return and line feed characters. This permits the user to underline and overstrike characters, a capability that is not possible with the Radio Shack cable. Apple users will be glad to know that Epson is marketing a special interface card with cable that will plug directly into a peripheral slot in their computer. However, due to a peculiarity of the Apple's video memory, the Apple interface card will not transmit ASCII codes greater than decimal 127, thus preventing use of the MX-80 graphics set. [Computer Corner of New Jersey, 439 Route 23, Pompton Plains NJ 07444, telephone (201) 835-7080, modifies either the Ep-



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Time & Money. Commodore, Atari & Apple users get more with VisiCalc software.

A financial VP in Massachusetts is cutting the time it takes to prepare month-end reports from three days to three hours.

A California company is replacing most of its time-share computer service with a personal computer and VisiCalc,

saving at least \$30,000 the first year.

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more alternatives and forecasting more outcomes. It really increases your decision-making batting average!

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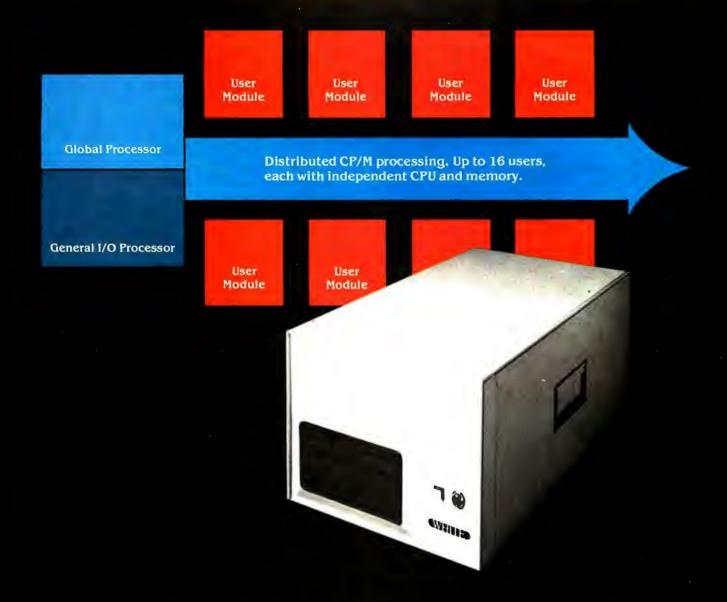
son or the Apple parallel interface cards to allow access to the graphics characters on the MX-80 printer. The modification is simple—the data-bit-7 line to the printer (the line that controls the highest bit of the 8-bit interface) is isolated from the interface board and connected via a wire to one of the annunciator output bits coming from the Apple II game socket. A POKE statement can then toggle this line, causing the MX-80 to print either normal ASCII characters or Epson graphics....GW]

In addition to the standard TRS-80 cable and Apple II board/cable interfaces, which are available for both printers, the MX-80 will also have the following interfaces: IEEE-488, serial, and buffered serial (which includes a 2 K-byte character buffer). Approximate prices are given in the MX-80 "At a Glance" text box.

Conclusions

- •The Epson MX-80, at \$645, and the MX-70, at \$449, both represent an unprecedented level of performance for the price. Although the low price of the MX-70 is particularly attractive, the added features of the MX-80 make it worth the extra \$200. The most important features are the intelligent bidirectional printing (which significantly increases the printing speed) and the 9 by 9 dot matrix for letters (which allows true descenders on lowercase letters like "y" and "g" and results in a more readable text).
- Both printers require tractor-feed paper, which limits the user's choices (eg: standard letterhead stationery can't be used), but also assures precise placement of text on a page. And what other low-cost printer prints on ordinary





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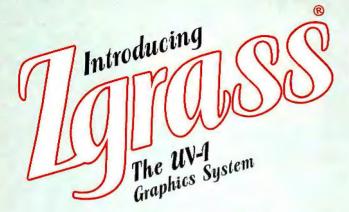
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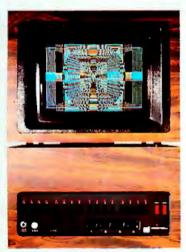
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paper (as opposed to thermal or electrostatic) and produces an original as well as up to two carbon copies by using multiple-ply paper? This ability, due to the fact that both are impact printers, is of particular interest to small business users.

In addition, the print head can be changed (recommended after 50,000,000 characters) by the owner, at a cost of about \$30. A quieter print head (5 dB quieter than the standard head during printing) is available for about \$40. Like the standard replaceable print head, it can be installed by the user.

- Although the MX-70 and the MX-80 share many features, each has its own graphics option. The MX-70 has bit-mapped graphics that permit control over any dot in a 480 by 7 dot array, one 7-dot column at a time. The MX-80, on the other hand, has the same graphics set as the TRS-80, and an option for bit-map graphics.
- •Epson America is beginning to enter the US market and has already begun to train many of its distributors and dealers to act as authorized service centers. The three Epson factory centers, located in Dallas, San Francisco, and Great Neck, New York, also provide service—a major consideration when investing in a unit that is mechanical as well as electronic in nature. (The unusual potential of these machines to do more than simple printing has also led to the founding of an independent Epson Users' Group. For more information, contact Frank Barden, Epson Users' Group, c/o 1017 Trollingwood Ln, Raleigh NC 27604.)
- •Both the Epson MX-80 and MX-70 offer a variety of features at a price well below that of any comparable printer on the market. These features, the reputation of Epson, and the thorough engineering that is apparent in the two units, allow me to recommend these printers to any personal computer owner. ■



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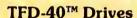
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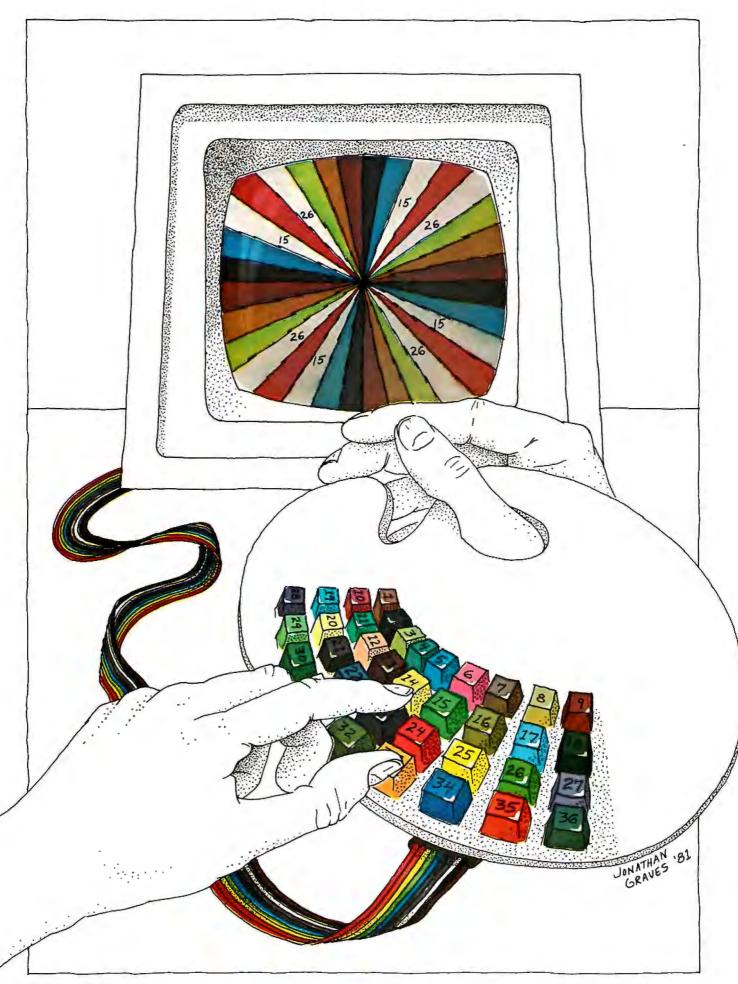
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Extended Color BASIC for the TRS-80 Color Computer

Stan Miastkowski, Technical Editor

Inexpensive and easy-to-use color graphics have been the goal of personal computer makers for a number of years. Although graphics have been available, they've been neither inexpensive nor easy to use. Many of the systems currently on the market require the skills of an experienced machine-language programmer in order to generate high-resolution graphics. Some manufacturers have simplified the process; but, for the most part, generating a full-color graphics display is still a tedious exercise.

Radio Shack has released the first truly easy-to-use and inexpensive system that generates full-color graphics. Extended Color BASIC is available for the TRS-80 Color Computer and was developed by Microsoft. In fact, the message:

EXTENDED COLOR BASIC 1.0 COPYRIGHT (C) 1980 BY TANDY UNDER LICENSE FROM MICRO-SOFT

appears when you turn the Color Computer on. Extended Color BASIC is fast, memory-efficient, and so well designed that anyone (even children) can create graphics shapes in a few minutes. Best of all, it's fun to use and has features that advanced programmers will appreciate.

Getting Into Graphics

If you have a TRS-80 Color Computer, you can add Extended Color BASIC for \$99. The computer must be returned to Radio Shack for the modification. Extended Color BASIC also requires 16 K bytes of programmable memory, which, if you don't already have it, adds \$119 to the price of modification. The complete Extended Color Computer sells for \$599. You'll still need a color monitor—although the family television is still the most popular alternative.

Radio Shack has released the first easy-to-use and inexpensive system that generates full-color high-resolution graphics.

Graphics Modes

Extended Color BASIC has five distinct graphics modes available—two low-resolution, two medium-resolution, and one high-resolution (see table 2). The low- and medium-resolution modes each offer a choice of two-color or four-color modes. When memory space is at a premium, the two-color modes are

handy for space conservation. The high-resolution mode has only a two-color mode available. Entering any of the five graphics modes is simple—a PMODE command is the first line of any graphics program. The command is followed by the number (0 thru 4) of the graphics mode you wish to use.

Even though the size of the graphics blocks (or pixels) differ widely in the three main graphics modes, all points are plotted on a 256-by-192 grid (49,152 points). This greatly simplifies matters if you decide to modify any program that uses the graphics modes—if you change the resolution, you don't have to change the parameters of the graphics commands.

Color Combinations

The TRS-80 Color Computer has available a set of nine colors (see table 3). It's interesting to note that the powerful Motorola 6847 Video Display Generator, a key component in the Color Computer, has the capability of displaying a very large number of distinct shades. It's possible to take a look at them by turning on the computer, waiting for the Extended Color BASIC message to appear, and then *rapidly* turning the computer off and on.

Attempting to figure out the color combinations available in each of the

CIRCLE (x,y), r, c, hw, start, end

Draws a circle, partial circle, or ellipse.

- x is the x-coordinate of the circle's centerpoint.
- y is the y-coordinate of the circle's centerpoint.
- r is the radius of the circle. Each unit is equal to one graphics point on the screen.
- c is a number (0 to 8) which specifies the color of the circle. The number must be one of those specified for the mode/color set combination. If this value is omitted, the foreground color defaults to the previously specified color.
- hw is the height/width ratio of the circle (from 1 to 255). If it's omitted, 1 (a perfect circle) is used.
- start is the starting point of the circle (from 0 to 1). This is optional and if omitted, 0 is used.
- end is the endpoint of the circle (from 0 to 1). If it's omitted, 1 is used.

COLOR foreground, background

Sets the foreground and background screen colors within limits specified by the mode/color set combination.

foreground is a color code (0 to 8).

background is the background color (0 to 8).

DRAW line

Draws a line (or series of lines) by specifying the direction, angle, and color.

line is a string expression and may include:

Motion Commands

M = Move the draw position

U = Up

D = Down

L = Left

R = Right

E = 45-degree angle

F = 135-degree angle

G = 225-degree angle H = 315-degree angle

X = Execute a substring

K = Execute a substring and return

Modes

C = Color

A = Angle

S = Scale

Options

N = No update of draw position

B = Blank (no draw, just move)

EDIT

Allows editing of program lines.

nC Changes n characters.

- nD Deletes n characters
- I Allows insertion of new characters.
- H Deletes remainder of line and allows insertion of new characters.
- L Lists current line and continues edit.

nSc Searches for nth occurrence of character c.

X Extends line.

SHIFT Escape from subcommand.

n SPACE Moves cursor n spaces to the right.

n Moves cursor n spaces to the left.

GET startpoint—end point, destination, G Places the graphics contents of a specified rectangle within a specified array.

startpoint is the coordinate of the upper-left corner of a rectangle on the screen.

endpoint is the coordinate of the lower-right corner of the same rectangle.

destination is the name of a predefined array that will
store the contents of
the rectangle. G tells
the computer to store
the rectangle's contents with full graphic
detail.

LINE (x1,y1)-(x2,y2), a,b

Draws (or erases) a line between two specified points. Also draws a box using the coordinates as the opposing corners.

x1,y1 is the starting position of the line.

x2,y2 is the endpoint of the line.
a is either PSET or PRESET.
b is either B (for box) or BF (for

PAINT (x,y) ,c,b

Fills a specified area with a specified color. (The color is limited by the mode/color set combination.)

x is an x-coordinate.

y is a y-coordinate.

filled box).

c is the color code (from 0 to 8). The color selected must match one of the colors available in the particular mode/ color set combination in use.

b is the border color (0 to 8) at which painting will stop.

PCLEAR n

Clears a specified number of memory pages (1536 bytes each) for graphics use. n is the number of graphics pages (1 to 8).

PCLS color

Clears the video display. co/or is the number (0 to 8) of one of the colors available for the mode/ color set combination in use. If co/or is omitted, the existing background color is used.

PCOPY source TO destination

Copies the contents of one memory page to another memory page. source and destination are memory page numbers (1 to 8).

PLAY

Plays music of a specified note (Athru G or 1 thru 12), octave

(1 thru 5), volume, note duration, tempo, and pause. It also allows the execution of substrings and will handle the specification of sharps and flats.

PMODE mode, start-page

Selects the graphics mode and the memory page on which a program starts.

Mode is the graphics mode (0 to 4). The default value is 2.

Start-page is the number of the graphics page (1 to 8) on which the program will start.

PSET (x,y,c)

Turns on selected graphics points. x is the position on the x-axis. y is the position on the y-axis. c is the color of the dot (0 to 8).

PRESET (x,y)

Turns off graphics points which were turned on by the PSET command.

x is the coordinate on the x-axis.

x is the coordinate on the x-axis. y is the coordinate on the y-axis.

PUT startpoint—endpoint, source, action Places the graphics contents of a rectangle stored in an array by the GET command at a specified posi-

tion.

startpoint is the coordinate of the upper-left corner of the rectangle.

endpoint is the coordinate of the lower-right corner of the rectangle.

source is the name of a predefined array that contains the data to be written into the rectangle.

action determines how the data is to be written into the rectangle and can be the following:

PSET—Sets the points that were set in the original rectangle.

PRESET—Resets the

points that were set in the original rectangle. AND—Compares the points stored in the original rectangle with the destination rectangle. If both are set, then the screen point will be set; if not, the screen point is

OR—Compares the points as above. If either is set, the screen point will remain set.

NOT—Reverses the state of each point in the desti-

reset.

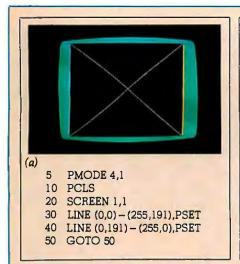
of each point in the destination rectangle.

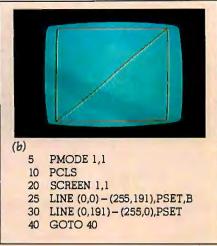
SCREEN type, color set

Tells the computer whether you want to use a text screen or a graphics screen and selects the color set.

type is either 0 (text screen) or 1 (graphics screen).

co/or set is either 0 or 1 (see table 4).





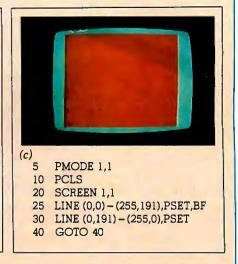


Photo 1: Three examples of the LINE statement in Extended Color BASIC. Photo 1a shows the high-resolution mode (PMODE 4,1). Photo 1b is the low-resolution mode (PMODE 1,1) and shows that when the suffix "B" is added to the LINE command in line 25, a box is created which uses the endpoint coordinates as opposing corners. Photo 1c shows what happens when the suffix "BF" is added to line 25. A box is created and filled with the foreground color. (Note that the line created by line 30 was drawn, but it's invisible because it's the same color as the filled box.)

graphics modes is, at first glance, probably the most complicated aspect of using Extended Color BASIC. Choosing what's called the color set is done by the SCREEN command. This command has two parameters: The first tells the computer whether you want the graphics mode or text mode. The second parameter selects the color set. This is where things get a bit tricky. The three two-color modes (low-, medium-, and high-resolution) each offer a choice of either black and green or black and buff. The two four-color modes (low- and mediumresolution) offer color sets of either green/yellow/blue/red or buff/cyan/ magenta/orange. None of the graphics modes allow you to use all nine colors at one time.

A further "complication" is the COLOR command, which instructs the computer to use specified foreground/background colors. The

specified color codes must be in the allowable color set for the graphics mode you're using (see table 4)—otherwise you'll be greeted with an error message when you attempt to run the program.

Extended Color BASIC divides the available graphics memory into eight pages of 1536 bytes each.

Although all this seems extremely complicated, I found that within a few hours of using Extended Color BASIC, the graphics modes and available color sets became second nature. Besides, the system sets default values for you if you don't want to bother remembering all the combinations at first.

Graphics Pages

Extended Color BASIC divides the available graphics memory into eight pages of 1536 bytes each. An optional PCLEAR command can be used in the program to specify the number of pages you want to use. (The default is 4.) A PCOPY command is also available which can copy the contents of one page into another page (as long as the new page was allocated by PCLEAR). In addition, the PMODE command has a second parameter that specifies which page to start the program on.

It doesn't take long to realize that the memory pages offer a number of interesting and creative possibilities. Switching between pages offers the opportunity for limited animation—especially since it's possible to update

PMODE Number	Grid Size 256 by 192	Color Mode Two-color	Memory Pages Used
3	128 by 192	Four-color	4
2	128 by 192	Two-color	2
1	128 by 96	Four-color	2
0	128 by 96	Two-color	1

Table 2: The five graphics modes of Extended Color BASIC (two low-resolution, two medium-resolution, and one high-resolution). All modes are selected by the PMODE command and are mapped onto a 256 by 192 grid.

0-4	0.1	
Code	Color	
0	Black	
1	Green	
2	Yellow	
3	Blue	
4	Red	
5	Buff	
6	Cyan	
7	Magenta	
8	Orange	

Table 3: Colors available on the TRS-80 Color Computer.

one page while another is on the screen.

Creating Graphics

Once you get used to the graphics and color modes, using Extended Color BASIC to actually create graphics displays is easy. Although it is possible to use the PSET and PRESET commands (the equivalent of the familiar SET and RESET commands found in other TRS-80s), the 50,000 or so graphics points available in the high-resolution mode make the setting of individual points a very time-consuming exercise (although this might be necessary in a few cases).

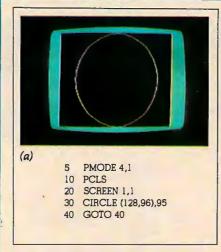
The people who designed Extended Color BASIC have made it simple—such commands as LINE, CIRCLE, DRAW, and PAINT (see photos) make the creation of very sophisticated shapes an easy job. The most-used commands include:

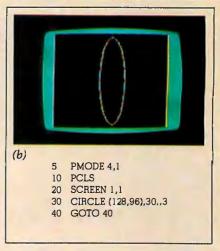
- •LINE—Draws a line between two specified sets of coordinates. It will also draw a box and, if desired, fill the box with the foreground color.
- •CIRCLE—Draws a circle with a specified radius at a specified coordinate. You also have the option of changing the height/width ratio and drawing only parts of the circle.
- •DRAW—Draws a line or series of lines. You specify the direction, angle, and color,
- •PAINT—Fills a specified area with a color you pick.
- •GET—Places the graphics content of a specified rectangular area of the display within an array.
- •PUT—Takes the array used to store the GET information and redraws the graphics within an area that you specify.

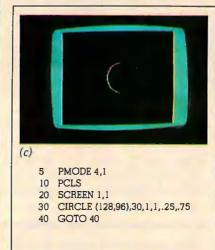
(For a complete list of Extended Color BASIC graphics commands, see table 1).

Music

Although fast and easy color graphics is the bread and butter feature of Extended Color BASIC, the system has a number of other strong points, including the ability to perform some pretty fancy music. The non-modified version of the TRS-80







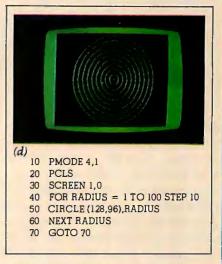


Photo 2: Four variations of Extended Color BASIC's CIRCLE statement, all in the high-resolution graphics mode. Photo 2a is a simple circle with coordinates (128,96) as the centerpoint and 95 graphics blocks as the radius. In photo 2b, the height/width ratio has been specified as 3, creating an oval. The ratio can be specified from 0 to 255. If > 1, the circle is "higher" than it is wide; if < 1, it is wider than it is high. If the ratio is 0, the circle is infinitely higher than it is wide and becomes a straight line. Photo 1c uses the start and finish parameters to specify which part of the circle to draw. Photo 1d uses a single CIRCLE statement and a FOR-NEXT loop to create a bullseye.

Color Computer (without Extended Color BASIC) allows you to create music by the SOUND command, which gives a range of notes from F₃ to E₇ with a duration of 6/100 to 6/10 seconds. Obviously, there are limitations to this; there is a limited range, each note requires a separate program line, and you have no control over the tempo or volume. Playing all but the most simple tune is a tedious job.

All of those problems have been eliminated in Extended Color BASIC through the use of one powerful command—PLAY. The PLAY com-

mand allows you to control the note, octave, duration of notes and pauses, and volume through the use of a single string. You can also execute substrings, making the playing of certain kinds of music a much easier proposition (see listing 1). Notes (over a five-octave range) can be specified by using either the numerals 1 thru 12 or the notes themselves from C to B (including sharps and flats). Duration of notes can be varied from a whole note to a 1/255th note! Thirty-one volume levels can be specified, and tempo and pause-length have a range of

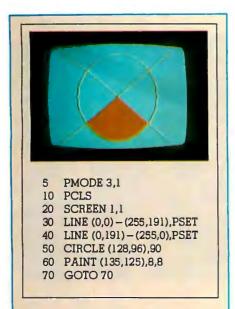


Photo 3: An example of the PAINT statement. The lines and circles shown are in the medium-resolution twocolor mode (PMODE 3,1). The PAINT statement in line 60 specifies the beginning point of the painting (135,125), the color choice, and the color number at which the painting will stop.

from 1 to 255. If you're musically inclined, you'll find the PLAY command an interesting one, despite the inability to play chords. Even for one not schooled in musical theory, these capabilities are useful for adding sound to program displays, graphics, and animation.

The Added Extras

Extended Color BASIC adds to the TRS-80 Color Computer commands and functions. This makes it substantially the same as the well-known Radio Shack Level II BASIC. After using the non-extended BASIC for a while, it was good to have back such familiar commands as TRON and TROFF (trace on and off), and ON ERROR GOTO, Functions added include PEEK (strangely enough, nonextended color BASIC does have POKE but not PEEK), SOR, EXP, COS, LOG, TAN, and USR.

There are a number of differences. Since both extended and non-extended color BASIC use device numbers for I/O (input/output) operations (0 for the keyboard and video

PMODE	Color Set	Two-Color	Four-Color
Number	Color Set	Combination	Combination
4	0	Black/Green	
	1	Black/Buff	
3	0		Green/Yellow/Blue/Red
	1		Buff/Cyan/Magenta/Orange
2	0	Black/Green	
	1	Black/Buff	
1	0		Green/Yellow/Blue/Red
,	1		Buff/Cyan/Magenta/Orange
0	0	Black/Green	
-	1	Black/Buff	

Table 4: Color combinations (sets) that can be used within Extended Color BASIC. (Color set is the second parameter of the PMODE command.) The two low- and medium-resolution modes each have a two-color and a four-color set available. The single high-resolution mode is two-color and only allows combinations of black/ green or black/buff.

Listing 1: A demonstration of Extended Color BASIC's music capabilities. Lines 55 thru 80 create six string variables (A\$ thru F\$) and assign to them note, duration, octave, tempo, and volume-level information. Line 85 assigns string variable X\$, a string of commands to execute (X) substrings A\$ thru F\$. The music is played by the PLAY command in line 90, which calls the nested substrings.

```
1 '*** BACK TO BACH ***
2 '
5 CLS
10 PRINT @ 96, STRING$(32,"*")
20 PRINT @ 320, STRING$(32,"*")
25 PRINT @ 201, "BACK TO BACH"
40 FOR X = 1 TO 1000: NEXT X
55 A$ = "T6;02;L2;G;L4;C;D;E;F;L2;G;C;P16;C;"
60 B$="L2;A;L4;F;G;A;B;O3;L2;C;O2;C;P16;C;F;L4;G;
   F;E;D"
G5 C$="L2;E;L4;F;E;D;C;L2;O1;B;O2;L4;C;D;E;C"
70 Ds="L2;E;L1;D;L2;G;L4;C;D;E;F;L2;G;C;P16;C"
75 Es="L2;A;L4;F;G;A;B;03;L2;C;02;C;P16;C;F;L4;G;
   FiEiD"
80 Fs="L2;E;L4;F;E;D;C;D;E;L2;F;01;B;L1;02;C"
85 X$="XA$;XB$;XC$;XD$;XE$;XF$;"
90 PLAY X$
```

screen, -1 for the cassette, and -2for the printer), OPEN, CLOSE, IN-PUT, and EOF (end-of-file) statements are available. Therefore, dumping a program to a line printer is done by the PRINT# -2 command instead of LPRINT.

Also, because Extended Color BASIC includes a USR function, it is possible to call machine-language subroutines from BASIC programs (unlike the non-extended version). The technical information appendix of the Extended Color BASIC manual says, "The ROM (read-only memory) contains many subroutines that can be called from machine-language programs." From this statement, you might think that a long list of ROM subroutines would be included. Unfortunately, such is not the case. A total of seven follows, all dealing with cassette, joystick, and keyboard I/O. To be fair, the lack of ROM subroutine information is not Radio Shack's fault-its license with Microsoft prevents publication of such information.

Despite the lack of specific subroutine information, there are three new statements within Extended Color BASIC which are designed to help out the machine-language programmer:

5 PMODE 4,1

10 PCLS

20 SCREEN 1,1

25 DRAW "BM40,80;U40;R40;D40;L40"

30 DRAW "BM + 20,20; U40; R40; D40; L40"

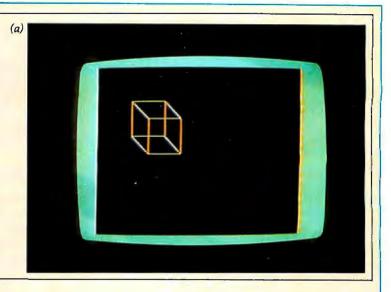
40 LINE (60,100) - (40,80), PSET

50 LINE (60,60) - (40,40),PSET

60 LINE (100,60) - (80,40),PSET

70 LINE (100,100) - (80,80),PSET

80 GOTO 80



5 PMODE 4,1

10 PCLS

20 SCREEN 1,0

30 DRAW "BM98,96;NU80;NE56; NR80;NF56;ND80;NG56;NL80;NH56"

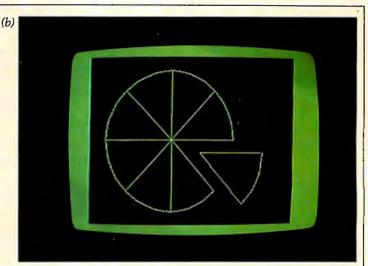
40 CIRCLE (98,96),80,1,1,.125,1

50 CIRCLE (135,110),80,1,1,1,.125

60 LINE (135,110) - (190,167),PSET

70 LINE (135,110) - (235,110),PSET

80 GOTO 80



5 PMODE 4,1

10 PCLS

15 SCREEN 1,0

20 DRAW "BM50,50R60D10NL20D20L20NU20L20NU20 L20U20NR20U10" TOP VIEW

25 DRAW"BM50, 100R20ND20R20ND20R20D20 NL20D10L60U10NR20U20" 'FRONT VIEW

30 DRAW "BM150,100R30D30L30U10NE20U20"
"SIDE VIEW

35 'OBLIQUE VIEW—LINES 40-60

40 DRAW "BM150,50U5E15R10BF20BD30NR5L20H25U10

45 DRAW "BMI50,50U5F8U15R15H8F8L15F8NR15D15F8 ND10E15NR10H8

50 LINE (175,30) - (200,55),PSET

55 LINE - (200,80), PSET

60 LINE (167,60) - (183,46),PSET

65 GOTO 65

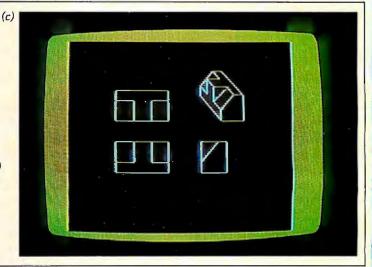
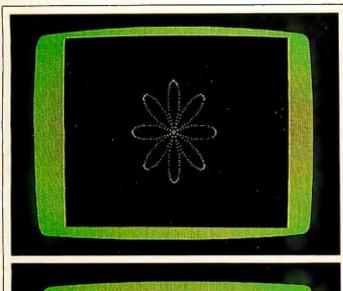
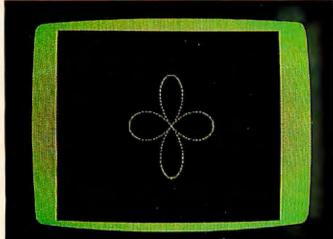
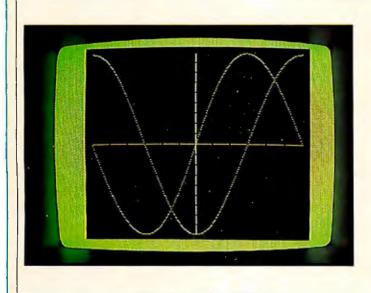


Photo 4: Three examples of the DRAW statement, which allows you to specify the starting point, direction, angle, and color of a figure. The cube in photo 4a was created by DRAWing two squares (lines 25 and 30) and connecting them with four LINE statements (lines 40 thru 70). Photo 4b is an example of the DRAW statement's "no update" option. Each of the lines radiating from the center of the "pie" is drawn individually, with the computer returning each time to the centerpoint of the circle (98,96). The detached "slice" was created using the CIRCLE statement's start/end parameters and two LINE commands. Photo 4c uses all of the parameters of the DRAW statement to create the four projection studies of a figure.





PCLEAR 8 (a) 10 PMODE 4,1 **PCLS** 11 12 SCREEN 1,0 13 PI = 3.14159 15 A1 = 0: A2 = 2*PI20 N = 360:A = 50 25 X = (A2 - A1)/N30 FOR I = Al to A2 STEP X 35 R = A * COS (4*I) 40 X = R *SIN(I)45 Y = R * COS (I) 50 PSET(128 + X,96 + Y,5)55 NEXT I 60 GOTO 13



10 PMODE 4,1 (b) 20 **PCLS** 30 SCREEN 1,0 40 LINE (127,5) - (127,185), PSET LINE (7,95) - (247,95),PSET 50 FOR XSCALE = 7 TO 247 STEP 20 70 PRESET (XSCALE,95) 80 NEXT XSCALE FOR YSCALE = 5 TO 185 STEP 10 90 100 PRESET (127,YSCALE) 110 **NEXT YSCALE** 130 FOR X = -180 TO 180 STEP 1.5AX = X/57.29578140 $145 \text{ XP} = \frac{x}{1.5} + \frac{127}{1}$ 150 F1 = -(SIN(AX)*90) + 95 $160 ext{ F2} = -(COS(AX)*90) + 95$ 170 PSET(XP,F1,1): PSET(XP,F2,1) 180 NEXT X

190

GOTO 190

- CLOADM—Loads a machine-language program from cassette. You can also specify a memory offset.
- CSAVEM—Writes a machine-language program to cassette.
- •DLOADM—Loads a machine-language program at the speed you specify (300 or 1500 bps [bits per second]).

Advanced programmers should be able to use its speed and efficient use of memory space to avoid the tedium of machine-language programming.

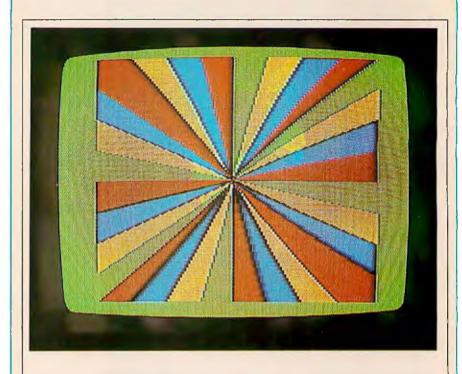
Although a lack of machine-language information might be considered a handicap by some, it is not. One of the most striking features of Extended Color BASIC is that it is fast—despite the fact that the microprocessor runs at the relatively slow speed (for computers) of .894 MHz (million cycles per second). It's evident that the 6809E is an extremely powerful microprocessor. Creating graphics by the PSET (point-bypoint) method is slow, but the LINE, CIRCLE, DRAW, and PAINT statements are surprisingly fastobviously calling machine-language subroutines in the Extended Color BASIC ROM.

The Editor

The color graphics and musical ability of Extended Color BASIC are the most interesting features; however, the addition of a full-feature editor (once again similar to the Level II BASIC editor) will surely be appreciated. It only takes a couple of times of retyping long program lines to correct a single error to convince any programmer that editing capability is not a luxury.

Documentation

As usual, the Radio Shack people have done an outstanding job of providing a manual aimed squarely at the "average" user of Extended Color BASIC (ie: the non-programmer).



- 5 PCLEAR 8
- 50 GOTO 600
- 60 LINE ((255 X),(191 Y)) (X,Y),PSET
- 61 J = J + 1:IF J > A THEN J = 0:A = RND(50)
- 63 RETURN
- 600 REM ROTATING FAN
- 601 FOR I = 1 TO 5 STEP 4
- 602 PMODE 3.1
- 603 PCLS
- 604 SCREEN 1,0
- 605 A = 25:X = 0: Y = 0: J = 0
- 610 FOR X = 0 TO 254
- 612 COLOR X/32 + 1,5
- 615 GOSUB 60: NEXT X
- 620 FOR Y = 0 TO 190
- 623 COLOR Y/24 + 1,5
- 625 GOSUB 60: NEXT Y
- 630 FOR X = 255 TO 1 STEP -1
- 633 COLOR X/32 + 1,5
- 635 GOSUB GO: NEXT X
- 640 FOR Y = 191 TO 1 STEP -1
- 643 COLOR Y/24 + 1,5
- 645 GOSUB 60: NEXT Y
- 650 NEXT I
- 660 FOR I = 1 TO 5 STEP 4
- 670 PMODE 3.1
- 680 SCREEN 1,0
- 690 FOR T = 1 TO 30: NEXT T
- 700 NEXT I
- 710 GOTO 660

Photo 6: Advanced programming in Extended Color BASIC. The program uses the available parameters of LINE, SCREEN, and COLOR to create a multicolor rotating display.

Hexadecimal Address	Decimal Address	Contents
0-3FF	0-1023	System Use
OFF	255	Direct Page Memory
3FF	1023	Extended Page Memory
400-5FF	1024-1535	Text Screen Memory
		Graphic Screen Memory
600-BFF	1536-3071	Page 1
C00-11FF	3072-4607	Page 2
1200-17FF	4608-6143	Page 3
1800-1DFF	6144-7679	Page 4
1E00-23FF	7680-9215	Page 5
2400-9FF	9216-2559	Page 6
2A00-2FFF	2560-12287	Page 7
3000-35FF	12288-13823	Page 8
		Program and Variable
3600-3FFF	13824-16383	Storage
8000-9FFF	37768-40959	Extended Color BASIC
A000-BFFF	40960-49151	Color BASIC
C000-FEFF	49152-65279	Cartridge Memory
FF00-FFFF	65280-65535	Input/Output

Table 5: TRS-80 Color Computer memory map. (Map as shown is with Extended Color BASIC and 16 K bytes of programmable memory installed.)

Technical Writer Ionathan Erickson has written a manual ("documentation" is a dirty word in the halls of Radio Shack, since they feel it connotes non-readability) in Radio Shack's informal, chatty, and very readable style. He's also managed to do this without talking down to the reader. Best of all, the material is well organized so that finding specific information is quick and easy.

Summary

Radio Shack's Extended Color BASIC is a breakthrough in color graphics for personal computers. It's fast, easy-to-use, and capable of producing striking graphics. In addition, advanced programmers should be able to use its speed and efficient use of memory space to avoid the tedium of machine-language programming. It lends itself well to the development of games and is also a great way for children to get involved with programming. For experienced programmers, "getting into" the system in

order to broaden its features will present a challenge and eventually result in even more exciting graphics.

Extended Color BASIC (in its present form) and the TRS-80 Color Computer system do not readily lend themselves to a professional or business environment. The inability to mix graphics and text on the screen makes it difficult to set up charts and graphs. But better things are coming-Radio Shack will introduce a floppy-disk drive for the Color Computer within a few months and also plans to market a low-cost plotter/ printer for the system,

Finally, Extended Color BASIC is the first incarnation of Microsoft's continual development of software dedicated to computer graphics, one of the fastest growing fields of the future. If Extended Color BASIC is an indication of the beginning for personal computers, we can expect amazing products in the years to come.

At a Glance___

Name

Extended Color BASIC

Type of package

Color graphics, music, and BASIC extension

Manufacturer

Radio Shack 1300 One Tandy Ctr Fort Worth TX 76102

Price

\$99 to add to existing TRS-80 Color Computer: \$599 for complete system (less video display)

Format

ROM (read-only memory)

Language used **BASIC**

Computer needed

Radio Shack TRS-80 Color Computer with 16 K bytes of programmable memory.

Documentation

"Going Ahead With Extended Color BASIC" 215 pages, 22 by 28 cm (81/2 by 11 inches)

Of interest to

Everyone

Additional comments

If Extended Color BASIC is to be added to an existing TRS-80 Color Computer, the unit must be returned to Radio Shack for modification.

The Commodore VIC 20 Microcomputer:

A Low-Cost, High-Performance Consumer Computer

Gregg Williams Senior Editor

"Why haven't you bought a personal computer yet?" This question will elicit varying responses from people interested in buying one. However, most of them fit into two categories: "They're still too expensive," or "The ones I can afford are not a good long-range investment." There are some good general-purpose microcomputers around, but they're in the \$1000 price range. And some computers cost as little as \$200; that's certainly the right price, but you know you're sacrificing something (quality of materials, expandability, etc) to get such a low price.

The Commodore VIC 20 micro-computer may change all this. It is well constructed, has color, sound, and graphics, and is easy to use. It comes with everything needed to use it (except an ordinary color television set), includes a well-written instruction manual, and is supported by a line of optional extensions, peripherals, and documentation (see figure 1). Looking at a picture of the

Acknowledgment

I would like to thank Ramon Zamora, David Cole, and the rest of the Avalanche Inc staff for their assistance during the writing of this article. version selling in Japan (photo 1) might cause you to think \$600 would be a fair price. It is, compared to the cost of other units. But it does not cost \$600—the VIC 20 retails for \$299.95.

The Commodore VIC 20 is well constructed, has color, sound, and graphics, and is easy to use.

Physical Characteristics

The VIC (which stands for Video Interface Computer) is a small unit, about the size of the main (keyboard) component of the Radio Shack TRS-80 Model I. It measures 40.3 by 20.4 by 7.2 cm (15.9 by 8 by 2.8 inches) and is small enough to easily fit on a work desk or a shelf. In fact, it is small enough to fit into a suitcase (along with its external power supply and RF (radio-frequency) modulator), making it usable as a portable personal computer.

The first thing I noticed about the VIC was its keyboard. It is the equal of any personal-computer keyboard

in both appearance and performance. This is a remarkable accomplishment, almost unbelievable considering the price of the entire unit. Three of its closest competitors, the Atari 400, the Radio Shack TRS-80 Color Computer, and the Sinclair ZX80, have keyboards that are less than perfect as a result of cost cutting. In this respect, the Commodore VIC 20 stands clearly ahead of its competition.

Photo 2a shows the rear panel of the VIC 20. The long slot on the left is used to plug in memory cartridges, program cartridges, or a VIC Master Control Panel, which allows up to four cartridges to be plugged in. Immediately to the right of the cartridge slot is the TV output socket. The signal from this plug goes directly to a video monitor or through the RF modulator and a TV switch box to a standard television set. (The necessary cable, RF modulator, and switch box are supplied with the VIC.)

The middle (round) connector on the rear panel is a serial interface that drives a single 5-inch floppy disk and a printer. Up to five peripheral devices can be daisy-chained through each other to this connector. The next slot to the right (the short rectangular

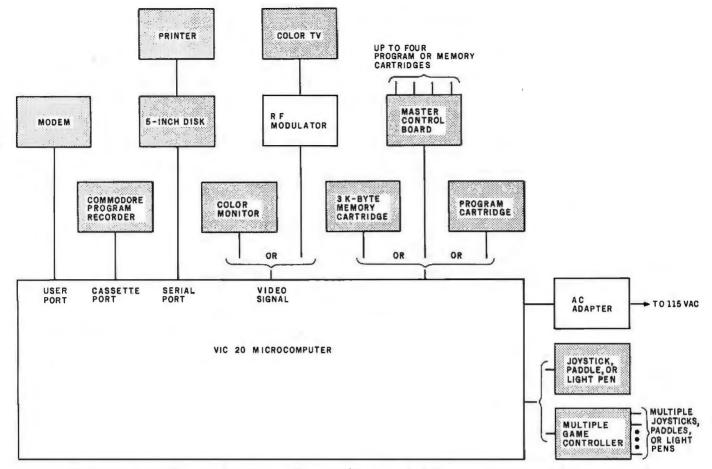


Figure 1: A block diagram of the Commodore VIC 20 system (shaded components are available at extra cost).

slot) goes to the VIC cassette recorder (which is available separately). The rightmost slot contains a "user port" that can be connected to a printer, a modem, or one of several other peripheral devices. With an optional RS-232C adapter card, this port can

also be used with RS-232C devices.

The left-side panel (see photo 2b) contains (from left to right) a game port, a rocker-type on/off switch, and a socket to receive power from the VIC power supply. The game port, according to Commodore, can

accept a joystick, a light pen, a game paddle, or a VIC Multiple Game Controller (which allows several game devices to be connected to the VIC).

When the VIC 20 is turned on, the video display (a color television tuned to channel 3 or 4) stays dark for about three seconds, then shows the display given in photo 3. The VIC display has 23 lines of 22 characters or graphics symbols per line, with cyan (greenish blue) letters on a white background. The active display area in the VIC is delineated by a border of a different color (in photo 3, a cyan border). The border crisply marks the working area of the VIC. For me, it has the psychological effect of making the screen area seem bigger; this is important, since the VIC displays fewer characters per line than any of its competitors.



Photo 1: The Commodore VIC 20 microcomputer. This unit, a final prototype based on the Japanese version of the VIC microcomputer, differs from the American model only in the model number.

VIC Graphics

The VIC 20 graphics character set is virtually identical to that of its predecessors, the Commodore PET and CBM (Commodore Business

Machine). The standard VIC can display over sixty graphics symbols, shown on the front faces of most of the keys (see photo 1). Since these symbols are directly available from the keyboard and can be stored in string variables and displayed by PRINT statements, it is easy for even the inexperienced BASIC user to combine these symbols into larger pictures. This character-size buildingblock approach is used by Atari, Commodore, Ohio Scientific, and Sinclair. It is a good way to generate graphics that are easy to understand and use without having to design a separate graphics mode. Such graphics are better than simply being able to turn on and off coarse graphics blocks (as in the TRS-80 Models I and III and the Color Computer) because character-oriented graphics allow more detailed images (although, unlike the graphics-blocks system, character graphics do not allow full control of the image).

All the graphics characters in the VIC are accessible directly from the keyboard. For characters shown on the fronts of key caps, pressing either

shift key or the Commodore key (the key in the lower left corner of the keyboard) causes one of these characters to be displayed. Pressing the Commodore key with a given key causes the character on the left half of the front face to be displayed; pressing either shift key with a given key causes the character on the right half to be displayed.

All the graphics characters in the VIC are accessible directly from the keyboard.

Both uppercase and lowercase characters can be displayed, but you lose access to all the characters on the right half of the key front faces. Toggling between this uppercase/lowercase/graphics mode and the default uppercase/graphics mode is done by pressing the shift key, holding it down, pressing the Commodore key, and releasing both keys. The graphics characters on the left half of the key front faces are still available with

lowercase letters. Commodore grouped what it believes are the most useful graphics characters (ones that might be used with lowercase letters in business applications) on the left half of the key front faces.

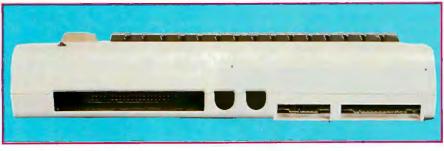
Finally, the number of graphics characters that can be displayed is doubled because any character can be displayed as is or in reverse (see photo 3). This can be done immediately or during program execution. Pressing the RVS ON key (the CTRL key plus the 9 key simultaneously) causes all displayed characters to appear in reverse on the screen. (If you are programming and hit the RVS ON key while defining a character string, a reverse R will appear and subsequent keystrokes will not be reversed. However, when you print that string, the reverse R will not appear but will cause all subsequent characters to be displayed in reverse.) Pressing the RVS OFF key (CTRL plus the 0 key) causes all displayed characters to appear unreversed on the screen. (When included in a character string, the RVS OFF key causes all subsequent characters to be displayed normally; its symbol appears in the character string as a reverse underline.)

VIC Color

To quote an adage from photography, "If you can't make it good, make it red." There is an element of truth in that—color does make things more exciting, and it's always one of the most striking features of a microcomputer video display. The VIC has an impressive color display due largely to the complete control you have over the placement and combination of colors.

The VIC allows you to display normal and reversed characters (including all graphics symbols) in eight colors: black, white, red, cyan, purple, green, dark blue, and yellow. The color of the flashing cursor and all subsequent characters displayed on the video screen is set by simultaneously pressing the CTRL key and the appropriate color key (one of the keys numbered 1 through 8). As described for the RVS ON and

(2a)



(2b)

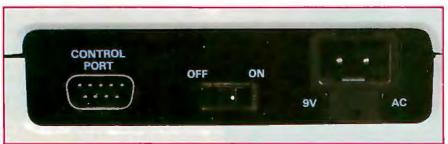


Photo 2: Connections to the VIC 20 microcomputer. Photo 2a shows the rear panel of the VIC; from left to right are a slot for program cartridges and connections to a television or video monitor, a floppy disk, a Commodore cassette recorder, and a printer or other peripherals. Photo 2b shows a game device port, an ON/OFF rocker switch, and a connector for an external power supply.

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Want to change your image? The magical Microline 80 really does tricks. It prints upper

and lower case, condensed and double width characters and block graphics for charts, graphs and diagrams.

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OKIDATA

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Photo 3: The VIC 20 video display immediately after being turned on.

RVS OFF kevs, pressing a color kev within a character string causes a reverse character to be placed in the string. This tells the VIC not to immediately change the display color, but to change it when that string is printed. Photo 5 shows the eight colors available, each of which is displayed by printing the corresponding color control character followed by a line of reverse spaces (which appear as solid squares of the current color). The computer displays all ouput in the current color. In photo 5, since the last color used was yellow, the VIC responds with its end-of-program message in yellow.

The VIC also allows you to change the background color of the working area in the center and the border that surrounds it. Choose from sixteen background colors and eight border colors (ie: 128 background/border combinations). The two are changed by executing (either directly or from a program) the statement:



Photo 4: The character set of the VIC 20. Any character can be displayed in reverse.

POKE 36879.X

where X is a value as given in table 1. The background colors can be any of the eight character colors or orange, light orange, pink, light cyan, light purple, light green, light blue, or light yellow. The border colors can be any of the eight character colors.

An unusual thing about the VIC is that the background color can change independently of the character color (other color microcomputers can't do this). Combined with the color and reverse keys, this allows a tremendous amount of control over the video display. Photos 6a and 6b show a run of a program differing only in the value poked to memory location 36,879. Photo 6a shows a light green background and a cyan border; this was accomplished by poking the value 219 to that location. Photo 6b shows a light cvan background and a red border: this was accomplished by poking the value 186 to that location.



Photo 5: The eight character colors available on the VIC 20. All characters can be displayed in any of these colors.

In addition, notice the two sets of angle brackets on each line. The first set contains an X symbol, a space, and a small square. The second set contains the *reverse* of each of these characters. Notice the role of the background and character colors in these reversed and nonreversed characters. If the background color were changed with those characters on the screen, the characters would assume the new background color but retain the old character color.

Photo 7 contains a listing of the program that produced photo 6b. Several control characters appear in this listing as seemingly arbitrary reverse characters. These are screenmanipulation characters stored for later use because they appear within a character string; if a quote mark had not been previously typed on the same line, the character would have been executed immediately and would not have appeared on the screen. The reverse heart in line 100 is the VIC symbol to clear the screen and put the cursor in the upper left corner. The reverse R and reverse underline in line 110 correspond to the RVS ON and RVS OFF keys, respectively. They cause the three characters between them to be displayed in reverse. The reverse characters in lines 120 through 180 are the result of pressing the corresponding color keys (CTRL plus the keys 1 through 8, respectively). They cause all printed characters to be displayed in the given color, as shown in photo 6b.

The VIC video display is memorymapped (ie: the contents of the screen are determined by the contents of a given range of memory locations inside the VIC). Because of this, the

Background				Bord		-		
	Black	White	Red	Cyan	Purple	Green	Blue	Yellow
Black	8	9	10	11	12	13	14	15
White	24	25	26	27	28	29	30	31
Red	40	41	42	43	44	45	46	47
Cyan	56	57	58	59	60	61	62	63
Purple	72	73	74	75	76	77	78	79
Green	88	89	90	91	92	93	94	95
Blue	104	105	106	107	108	109	110	111
Yellow	120	121	122	123	124	125	126	127
Orange	136	137	138	139	140	141	142	143
Light orange	152	153	154	155	156	157	158	159
Pink	168	169	170	171	172	173	174	175
Light cyan	184	185	186	187	188	189	190	191
Light purple	200	201	202	203	204	205	206	207
Light green	216	217	218	219	220	221	222	223
Light blue	232	233	234	235	236	237	238	239
Light yellow	248	249	250	251	252	253	254	255

Table 1: Background and border color combinations in the VIC 20 microcomputer. Poking decimal location 36,879 with the values given in this table gives a video display with the colors shown.

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screen can be directly manipulated by poking values into certain memory locations. Memory locations 7680 through 8185 (decimal) contain the code for a given character; memory locations 38,400 through 38,905 contain the code for the *color* of the respective character. Locations 7600 and 38,400 determine the character in

(6a)



(6b)



Photo 6: Variations in character, background, and border colors on the VIC 20. Photos 6a and 6b differ only in the value stored in location 32,879, which determines the background color (from sixteen choices) and the border color (from eight choices).



Photo 7: A VIC BASIC program utilizing color, graphics, and reverse video. This program produces the video display shown in photo 6b. The reverse character before each color word in the PRINT statements is a control character determining the color of everything displayed after it. See the text for details.

the upper left corner. Locations 7601 and 38,401 determine the character to its right, and so on down to the character in the lower right corner.

VIC Sound and BASIC

The VIC 20 can produce three independent "voices" of music and one voice of noise through the speaker of the attached television set. Each voice, covering a three-octave range, covers a different part of the audio spectrum. The voices are labeled "tenor," "alto," and "soprano"; they are activated by poking a number between 128 and 254 into locations 36,874 through 36,876. The noise generator is similarly activated at location 36,877, and an overall volume control (which takes values between 0 and 15) is located at

36,878. Table 2 lists important memory locations in the VIC 20. Table 3 lists the values to be poked into the music-voice locations to give a certain musical pitch within the three-octave range of that voice.

VIC BASIC is a version of Microsoft BASIC modified by Commodore. It is a full-blown BASIC with the features found on most microcomputers, allowing the VIC to accept other BASIC programs with little or no modification. A list of BASIC keywords accepted by the VIC is given in table 4. The keywords listed have the standard definitions given by Microsoft BASIC.

The VIC Product Line

Although prices and availability of VIC peripheral devices were not

Memory Location (in Decimal)	Use	
7680 to 8185	contains character contents of VIC video display; characters are mapped by row, with location 7680 corresponding to the upper left cor-	
	ner of the display	
36,874	corresponds to tenor music "voice"; should contain either 0 (for silence) or 128 through 254 (for note; see table 3)	
36.875	corresponds to alto music "voice"	
36.876	corresponds to soprano music "voice"	Į
36,877	corresponds to a noise-producing "voice"; accepts values of 0 and 128 through 254; higher values give higher-pitched white-noise sounds	
36,878	volume control for all music and noise "voices"; effective values are 0 through 15	
36.879	control byte for background and border colors; see table 1	ı
38,400 to 38,905	contains character color contents of VIC video display; mapped to video display in the same way as the character contents (see above)	

Table 2: Some important memory locations in the VIC 20 microcomputer.

Note	Value	Note	Value
С	135	G	215
C#	143	G#	217
	147		219
D		Α	
D#	151	A#	221
E F	159	B C	223
	159 163		225
F#	167	C#	227
Ğ	175	Ď.	228
Ğ#	179	D#	229
Ä"	183	<u> </u>	231
7,4		É F	
A#	187		232
В	191	F#	233
A# B C	195	G	235
C#	199	G#	236
D	201	Α	237
D#	203	A#	238
D# E F	207	В	239
F	209	C	240
F#	212	Č#	241

Table 3: Values used in the generation of music on the VIC 20 microcomputer. On the VIC, these values are stored in memory locations 36,874 through 36,876 to generate the appropriate note within the three-octave range of a given music voice.

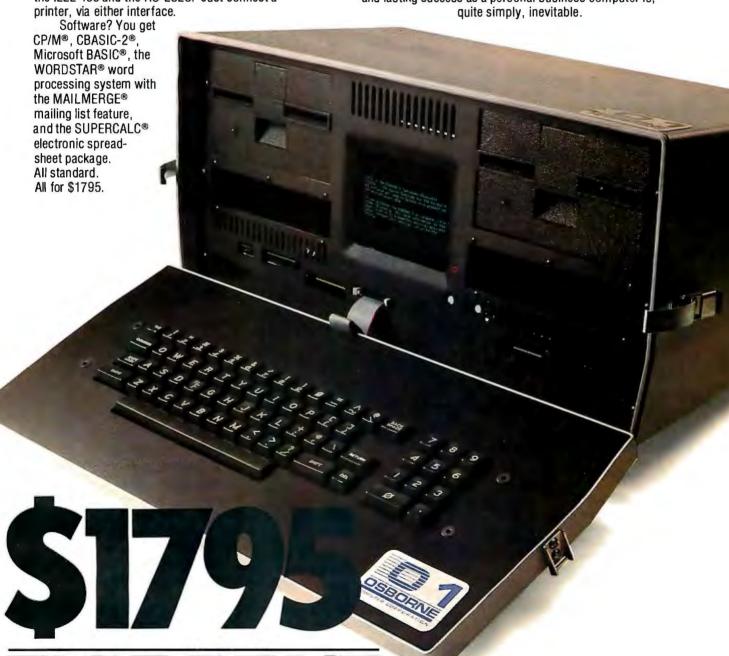
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definite at press time, Commodore has announced an extensive line of products to be "introduced during and throughout 1981." (By the time you read this, Commodore expects to have the VIC computer itself available through Commodore dealers.) This list of peripheral devices and accessories includes:

•Memory-expansion products—

Commodore will sell a line of cartridges that add programmable memory to the VIC, increasing the size and complexity of programs that can be run. A 3 K-byte cartridge can be plugged directly into the VIC, and 8 and 16 K-byte cartridges can be plugged in through a Master Control Panel that plugs into the VIC cartridge slot and accepts up to four cartridges. The maximum amount of programmable memory is 32 K bytes.

• Storage peripherals—Commodore will sell both a low-cost cassette recorder (although existing Commodore recorders work with the VIC) and a low-cost single 5-inch floppydisk drive. The disk drive will hold up to 170 K bytes of data.

• Other peripherals—These include a dot-matrix printer, joysticks, light pens, game paddles, and a Multiple Game Controller (discussed earlier).

• Interfaces—Commodore plans two interfaces for the VIC, a modem and an IEEE-488 bus interface. The modem allows communication with other computers over telephone lines. The IEEE-488 interface allows the VIC (like the PET and CBM machines) to interface with PET peripherals and a wide variety of test instruments and devices that use this standard bus.

• Firmware—A wide range of software will be distributed in cartridge form; three firmware cartridges have already been announced. The first, the RS-232C Interface Cartridge, allows you to use the VIC and a modem to communicate with other computers and access information utilities like MicroNet and The Source. The second, the VIC Programming Cartridge, will include a machine-language monitor and a number of utility functions useful during programming; it will also use the four function keys (on the righthand side of the keyboard) to execute predetermined functions. The third, the VIC Super Expander Cartridge, will add 3 K bytes of programmable memory, a new level of highresolution graphics, and additional music-related capabilities. The highresolution graphics (which I have not seen) are said to be excellent (176 rows by 176 columns of graphics dots, also called pixels).

• Documentation—In addition to the VIC User's Manual, supplied with the VIC, Commodore plans a series of book-plus-cartridge packages explaining several aspects of using and programming the VIC. (Documentation is discussed in greater detail later in this article.)

Arithmetic Operators: ABS, ATN, LET, SGN, INT, SQR, RND, LOG (to base e), EXP (to base e), COS, SIN, TAN, +, -, *, I, 1(exponentiation), <, >, =

Character Operators: CHR\$, ASC, SPC, TAB, LEN, STR\$, VAL, LEFT\$, RIGHT\$, MID\$, + (to concatenate strings)

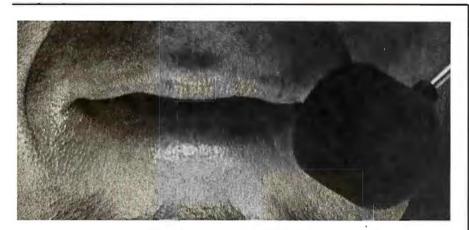
Control Words: FOR, TO, STEP, NEXT, GOTO, IF, THEN, GOSUB, RETURN, ON (used with GOTO and GOSUB), WAIT, END, USR

File and I/O Words:
OPEN, CLOSE, INPUT, INPUT#n, PRINT, PRINT#n, GET, READ, DATA, DIM, RESTORE

RUN, STOP, LOAD, SAVE, VERIFY, CONT, LIST, NEW, CLR

Miscellaneous Words: AND, OR, REM, DEF FNx, FNx, POKE, NOT, FRE, PEEK

Table 4: A list of VIC BASIC keywords.



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Human Engineering on the VIC

When the microcomputer industry was smaller, hobbyists put up with about anything in a computer as long as it worked. But now that major corporations are marketing microcomputers for the general public, human engineering—the design of systems to make them easy and efficient to use—has become the most important factor in the usability of computer

systems. The VIC deserves high marks in human engineering because it is easy to understand and use.

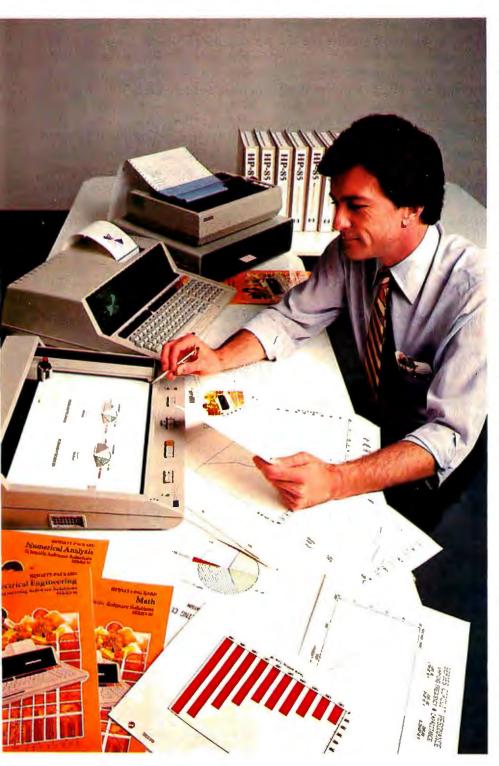
The VIC keyboard is one of the best I've seen. It is well constructed and has a good feel during typing. The key names on the top and front faces of the keys are highly visible and easy to read. In most cases, key functions have been wisely chosen and named. For example, the key

used to stop a program from executing is labeled as the RUN/STOP key. Pressing it (instead of the arbitrary control-C combination used by many computers) causes the VIC to stop executing the program and print out the line number where the program was stopped. Use of the CLR/HOME (clear-screen-and-home-cursor-to-upper-left-corner/home-cursor) and INST/DEL (insert/delete

Name of Computer	Atari 400	Commodore VIC 20	Ohio Scientific Challenger 1P	Radio Shack TRS-80 Color	Sinclair ZX80
Microprocessor used	6502	6502A	6502	6809E	Z80A
System clock frequency	1.8 MHz	slightly more than 1 MHz	1 MHz	slightly less than 1 MHz	3.25 MHz
List price	\$499/\$630 (two models, 8 K or 16 K)	\$399.95	\$479	\$399	\$199.95
Type of keyboard	touch-sensitive flat panel; slightly smaller than normal keyboard	full-size normal keyboard; very good feel	full-size normal keyboard	full-size normal keyboard; keys have feel of calculator buttons (not good)	touch-sensitive flat panel; much smaller than normal keyboard
Amount of programmable memory supplied	8 K or 16 K bytes (see above)	5 K bytes	8 K bytes	4 K bytes	1 K bytes
Maximum programmable memory possible	16 K bytes	32 K bytes	32 K bytes	16 K bytes	16 K bytes
Type of BASIC	full BASIC	full BASIC	full BASIC	limited BASIC (extended BASIC for more sophisticated music and graphics at extra cost)	limited BASIC (extended BASIC available at extra cost)
Video screen size (in characters)	16 rows by 32 columns	23 rows by 22 columns	24 rows by 24 columns or 12 rows by 48 columns	16 rows by 32 columns	24 rows by 32 columns
Lowercase letters available?	yes	yes	yes	accepts lowercase letters but displays uppercase as inverse capitals	no
Color available?	yes	yes	yes, at extra cost (\$229 extra)	yes	no
Graphics characters available?	yes; characters available from keyboard	yes; characters available from keyboard	yes: graphics available only through POKE and CHR\$ statements	no, but unit color block is ¼ normal character size	yes; characters available from keyboard
High-resolution graphics available?	yes, included (320 by 192 pixels)	yes, at extra cost (176 by 176 pixels)	no	yes, at extra cost (256 by 192 pixels)	no
Music available?	yes, three voices of music; can mix noise with each voice	yes, three voices of music, one of noise	yes, one voice of music (needs external speaker and amplifier)	yes, one voice of music	no
Extensions to BASIC for color, low-resolution graphics, and music?	yes, uses BASIC commands to manipulate all three	no, uses control characters and pokes to manipu- late all three	no, uses pokes to manipulate all three	yes, uses BASIC commands to manipulate all three	low-resolution graphics available from keyboard
Uses program cartridges?	yes	yes	no	yes	no
Machine-language monitor included?	no	no	yes	yes	no
Assembly-language assembler available (at extra cost)?	yes	yes	yes	no	no

Table 5: A comparison of five low-cost microcomputers, including the Commodore VIC20.

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HP Series 80 Personal Computers for Professionals: HP-85 (\$3250*) and HP-83 (\$2250*) specifications: 16K RAM expands to 32K, 32K ROM expands to 80K; CRT with integrated graphics; (HP-85 only; built-in thermal printer, cassette tape unit); Software includes VisiCalc™ PLUS, Information Management, Graphics Presentations, Surveying, Data Communications (Spring '81), Statistics, Regression Analysis, Math, Linear Programming, Waveform & Circuit Analysis, BASIC Training. HP peripherals include floppy discs, printers and plotters.

VisiCalc is a trademark of Personal Software, Inc. *Suggested retail price excluding applicable state and local taxes—Continental U.S.A., Alaska & Hawaii.



text) keys is obvious when they have been used a few times.

The RESTORE key performs a valuable function in a computer where so many changes in character, background, and border color are possible. It resets the VIC to its state when it was turned on, except that it leaves the current program in memory (unlike some reset keys). Finally, the four large keys marked "f1/f2" through "f7/f8" have no predefined use but can be used by a programmer (through use of the GET statement) to produce a specific function within the program. By using the shift key, these four keys can trigger up to eight user-defined functions. These keys are also used in some application cartridges to execute predefined functions.

As I mentioned earlier, the VIC video display is well designed. The large letters are easy to read, even on an inexpensive color television, and

the border around the active area of the display is restful to the eye. The narrow screen width (22 characters) will be a problem for some users, especially people using programs that need to display large amounts of data. Still, the screen width was a design decision reflecting the intended market, and I think that Commodore made a good decision under the circumstances.

Probably the most unexpected feature of the VIC is that it will be able to exchange both tape and disk files with the Commodore PET and CBM machines. Whether or not the program runs correctly on the other machines depends on whether it contains system-dependent code. For example, a CBM program using the full 80 columns of the CBM video display will not run correctly on the VIC, nor will a program larger than 32 K bytes. The ability to exchange data and programs among machines from the same manufacturer is almost unheard of. One good example of its usefulness is a situation where someone buys several VIC 20s to be used for data entry and feeds the results into a Commodore CBM computer.

I also found the screen-manipulation characters and POKE statements for music easy to use. By manipulating color, graphics, and sound without using any new BASIC keywords, Commodore has achieved two advantages. First, VIC programs are syntactically equivalent to PET programs. Programs can be transferred between machines without syntax errors due to unrecognized keywords; also, Commodore probably developed VIC BASIC faster and at less cost because of its similarity to PET BASIC. Second, VIC BASIC is easier to learn for people who know PET BASIC or another version of Microsoft BASIC.

An interesting thing about the VIC not apparent at first is the lightness of the unit. It literally has fewer components inside than you would expect. This is possible because it is built around a custom "video interface chip" built by MOS Technology for its parent company, Commodore. This integrated circuit handles all the interaction between the 6502 microprocessor (also manufactured by MOS Technology) and the color television (this function is done by a handful of integrated circuits in many other microcomputers). The low component count plus Commodore's ability to manufacture and assemble almost all of the VIC within its own factory account for the lighter weight and extremely low cost of the unit.

One final human-engineering feature of the VIC that will be appreciated by machine-language users and software developers shows Commodore's willingness to learn from hard-earned experience. The developers of VIC BASIC separated a kernel of I/O (input/output) subroutines from the rest of BASIC. They have written these routines as true subroutines and have devised a method for passing parameters to them so they can be used by anyone who wants to develop software for

At a Glance_

Name

VIC 20

Manufacturer

Commodore Business Machines 950 Rittenhouse Rd Norristown PA 19401 (215) 666-7950

Price \$299.95

Dimensions

40.3 by 20.4 by 7.2 cm (15.9 by 8 by 2.8 inches)

Processor name and type 6502, 8-bit

System clock frequency slightly over 1 MHz

Memory 5 K bytes

Mass storage

cassette recorder or floppy disk optional

Other hardware features

character-size graphics symbols, keyboard, uppercase and lowercase letters, eight-color foreground and sixteen-color background video display, threepart music generator, external RF (radio-frequency) modulator and power supply, built-in serial port

Software included

16 K-byte VIC BASIC in ROM (read-only memory)

Hardware options

cassette recorder, floppy disk, dot-matrix printer, modem, IEEE-488 interface, joystick, light pen, game paddle, extra memory cartridges (up to a total of 32 K bytes), RS-232C adapter

Software options

VIC Programming Cartridge (includes programming utilities and machine-language monitor), VIC Super Expander Cartridge (adds 3 K bytes more memory, highresolution graphics capability)

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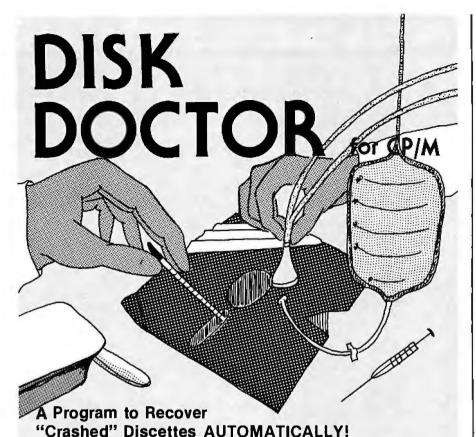
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Maybe it was a lightning storm, static from the rug, or just too late at night to be working. Whatever the cause, when a discette "crashes" and valuable data or programs are destroyed, the loss is enormous, both in time and money.

DISK DOCTOR is a program which automatically recovers bad discettes. Best of all DISK DOCTOR does not require any knowledge of CP/M file structure! If you can operate CP/M, then you can use DISK DOCTOR. The entire system is menu driven with key information displayed.

DISK DOCTOR is comprised of five "wards", each capable of performing a specific discette recovery operation.

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- Ward B: Copies whatever can be read from a "crashed" file and places it into a good file under user control.
- Ward C: Copies discettes without stopping for bad sectors. Bad sectors are replaced by spaces.
- Ward D: "Un-erases" files. That is, Ward D will recover accidentally erased disk files.
- Ward E: Displays directory of recoverable erased files.
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the VIC. In addition, all I/O routines called by BASIC are called indirectly through programmable-memory pointers holding the addresses of the true I/O routines; in this way, users can substitute their own I/O routines to be executed in place of those provided within the VIC.

These design decisions (which will be documented to interested parties by Commodore) do two things. First, they encourage the potential software developer to write software for the VIC by eliminating the need to write custom I/O routines. Second, they help isolate the structure of VIC BASIC from some machine-language code that may need to be changed; in this way, Commodore can prevent having several versions of VIC BASIC at some time in the future (a problem that plagued the PET and CBM machines).

Problems and Limitations

The VIC 20 is a very good machine, but it is not without some problems; fortunately, none of them are major.

The juxtaposition of several key pairs on the keyboard is unfortunate. First, the CLR/HOME key is next to the INST/DEL key; while inserting or deleting characters in a BASIC line, you may inadvertently clear the screen or return the cursor to the upper left corner of the screen. More annoying are the reversals of the colon and semicolon keys and the RETURN and RESTORE keys (see photo 1). Touch typists and keyboard users are used to finding these key pairs in different positions (eg: the RETURN key in the same row as the top row of letters). Since the VIC keyboard does not have the layout of previous Commodore machines, it is unfortunate that the keyboard was not laid out in a slightly different way.

Another problem has to do with the music voices. Once a music voice is turned on by the appropriate POKE statement, only poking that location to zero, turning off the sound on the television set, or turning off the computer will shut off the sound. Neither stopping the program that turned on the sound nor typing the keyword

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END will stop it. (The Atari 400 has a similar problem, but typing END causes it to silence all sound generators.)

Another problem is shielding against RFI (radio-frequency interference). Although the Federal Communications Commission has passed a set of rules to eventually keep personal computers and similar devices from interfering with television and radio reception, most manufacturers have received extra time to modify their products. In the case of Commodore, only units manufactured after March 1981 must meet the new requirements. I have been told by Commodore that unshielded units will be marked as such, If you live in close proximity to other people, I recommend that you wait for a shielded unit. If you use an unshielded VIC, people nearby may not be able to use radios and televisions while the computer is on.

The most serious problem I found can be avoided with some forethought. The VIC tape recorder, once put into play or record mode, can be started and stopped by the computer. A potential problem occurs when you have just done a LOAD and are about to do a SAVE (to save, for example, a revised version of the program just loaded). When you did the LOAD, the VIC instructed you to press the play button to begin the loading process. When it finished loading the

One of the most important components of a consumer-oriented microcomputer is its documentation.

program, it stopped the tapetransport motor but left the play button depressed. If you then give the SAVE command, the VIC initiates the process, even though the record button has not been pressed. (If no recorder buttons are pressed when the SAVE command is given, the VIC instructs you to press both the play and record button, and the recording process occurs without error.) The RUN/STOP key will not abort the loading process, although pressing the RUN/STOP and RESTORE keys will. Still, there are two chances to lose the program: one, not realizing that the program is not being recorded; two, realizing it but turning the VIC off from not knowing that the SAVE command can be aborted and restarted.

Documentation

One of the most important components of a consumer-oriented microcomputer is its documentation. Microcomputer documentation was neglected in the past because it was seen as being too expensive and timeconsuming to justify the perceived benefits. Now, however, good documentation can make the difference between the average consumer using or ignoring the same machine. Microcomputer documentation has a heavy burden to carry because of the multiple functions it needs to perform, First, it must tell the user how to unpack the computer. get it running, and use it with prepackaged software. Second, it must guide the user carefully through the first sessions with the computer (because many people still have some uneasiness or fear of computers). Third, it must educate the user about microcomputers in general so its potential for use can be seen. Fourth. it must document the features of the microcomputer in a way that is both complete and easy to understand.

Commodore recognized the need for good documentation. Avalanche Inc (of Palo Alto, California) has been commissioned to produce several books about the VIC. The first, the VIC User's Manual, is supplied with the VIC and is a good introduction to the VIC and its features. Its style is informal, friendly, and respectful of the reader's intelligence, but it assumes no previous knowledge of computers. There are illustrated chapters on setting the VIC up and on using its graphics, color, and music. Each feature of the VIC is illustrated with several short programs (5 to 25 lines each), making it

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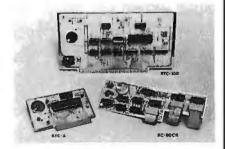
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easy to begin learning about the computer. Most of the chapters do not rely on material from previous chapters, meaning that the reader can learn about the features in any order.

Avalanche has produced two more books, Introduction to Computing ...On the VIC and Introduction to BASIC Programming...On the VIC. Both books, part of the Commodore Learning Series, are available at extra cost. They are written in the same friendly style and cover the use of the VIC in greater depth. What makes these books so innovative is that each book is sold with a program cartridge containing longer example programs that are used in the book. This allows the reader to learn from longer programs without the drudgery of having to type them in.

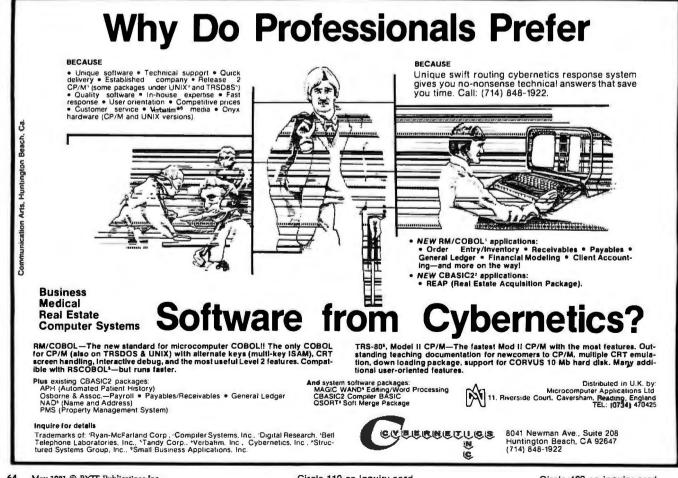
Comparison to Other Computers

Table 5 gives a comparison of five low-cost, consumer-oriented microcomputers: the Atari 400, the Commodore VIC 20, the Ohio Scientific Challenger 1P, the Radio Shack TRS-80 Color, and the Sinclair ZX80. Although the VIC is a very good machine, some of the others have features that may make them the best choice for you. The Atari 400 has the most sophisticated design; it allows detailed video graphics (although they are more difficult to program) and is the logical choice of anyone wanting access to sophisticated arcade-like games. The TRS-80 Color Computer might be the best choice if you want the convenience of getting service and repairs from a Radio Shack store. In any case, the best computer for you depends on your needs and your budget.

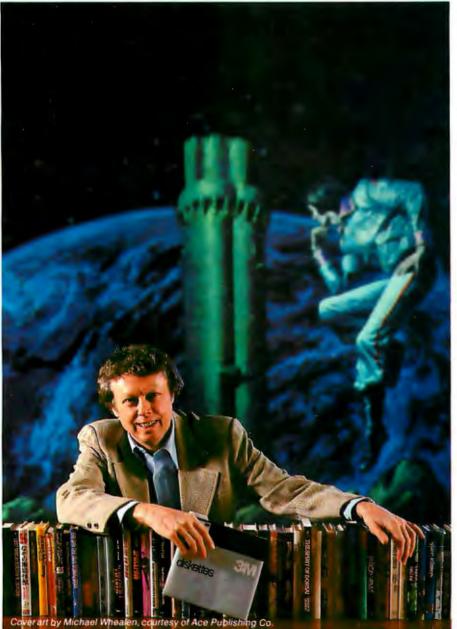
Conclusions

• The final verdict on the Commodore VIC 20 is not in yet because of the large amount of hardware and software not yet commercially released. But if the rest of the product line is as good as the VIC 20 microcomputer is, the VIC computer system will be one of the strongest on the market.

- The VIC 20 computer unit is unexcelled as a low-cost, consumeroriented computer. Even with some of its limitations (eg: screen size of 23 rows by 22 columns, maximum programmable memory of 32 K bytes), it makes an impressive showing against more expensive microcomputers like the Apple II, the Radio Shack TRS-80, and the Atari 800.
- The low cost of the VIC (\$299.95) is made possible by a custom computerto-video interface circuit that replaces several other integrated circuits and by Commodore's manufacturing most of the VIC at in-house factories in Japan.
- The VIC is well designed and easy for the novice to use. A large part of its suitability for first-time users is due to its excellent documentation and attention to human-engineering factors. The unit has some small design flaws, but they are minor.



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Many of you grew up as I did, taking all your toys apart. In most cases, the wrapping was scarcely off a gift before a screwdriver was skillfully applied to pry it apart.

I haven't changed much over the

years. I still take most of my gadgets apart. Five months ago, I bought the Milton-Bradley Big Trak toy tank for use in a project. Instantly, I had the screwdriver and pliers ready to do their job. I unpacked the Big Trak, installed the batteries, placed it on the floor, and pressed the Test button. The tank beeped a few times and executed a preprogrammed test se-Everything quence. worked, so I began to disassemble it. The time from my unpacking the box to unscrewing the case wasn't more than a minute and a half.

I took Big Trak apart because I was interested in the motorized mechanism inside the vehicle. I found it an impressive engineering accomplishment that such sophisticated control could be provided with inexpensive motors. My previous experience led me to believe that only industrial-quality DC (direct-current) motors could be controlled so well. It seems that many

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Photo 1: A PM (permanent-magnet) DC motor can also be used as a generator-type tachometer, or tachometer-generator. When the shaft is turned, a DC current proportional to the speed is produced. In the case shown, a small PM DC motor is secured in a vise, and the shaft is slowly turned (by the belt attached to the shaft and extending to the lower right). The digital voltmeter above the motor indicates the actual generator output voltage. In this case, the shaft is turning at about 150 rpm.

things have changed since I was a kid: permanent-magnet DC motors aren't what they used to be.

DC motor controls are not the same, either. They are simpler, more accurate, and cheaper. Using DC mo-

tors has become relatively easy. It's no longer a black art.

I hope this article discussing the principles of DC motors will dispel your reluctance to experiment with them. First the basics, then some examples of motor use.

What Is a DC Motor?

The DC motor was invented by Michael Faraday early in the nineteenth century. He determined that when a currentcarrying conductor is placed in a magnetic field, a force is applied to the conductor, causing it to move. Shown graphically in figure 1, the direction and magnitude of this force are functions of the conductor current and the direction of the magnetic field. Conversely, moving



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In VEDIT, the screen continuously displays the region of the file being edited, a status line and cursor. Changes are made by first moving the cursor to the text you wish to change. You can then overtype, insert any amount of new text or hit a function key. These changes are immediately reflected on the screen and become the changes to the file.

VEDIT has the features you need, including searching, file handling, text move and macros, plus it has many special features. Like an 'UNDO' key which undoes the changes you mistakenly made to a screen line. The Indent and Undent Keys allow automatic indenting for use with structured programming languages such as Pascal and PL/I. The disk write error recovery lets you delete files or even insert another disk should you run out of disk space during an edit session. And you have the ability to insert a specified line range of another file anywhere in the text. Disk access is very fast and VEDIT uses less than 12K of memory. The extensive 70 page, clearly written manual has sections for both the beginning and experienced user.

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Included is a setup program which allows you to easily customize many parameters in VEDIT, including the keyboard

layout for all cursor and function keys, screen size, default tab positions, scrolling methods and much more. This setup program requires no programming knowledge or 'patches', but simply prompts you to press a key or enter a parameter.

The CRT version supports all terminals by allowing you to select during setup which terminal VEDIT will run on. Features such as line insert and delete, reverse scroll, status line and reverse video are used on 'smart' terminals. The memory mapped version supports bank select and a hardware cursor such as on the SSM VB3. Special function keys on terminals such as the H19, Televideo 920C and IBM3101, and keyboards producing 8 bit codes or escape sequences are also supported.

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a conductor through a magnetic field was found to induce a current in the conductor proportional both to the intensity of the field and the velocity of the conductor as it passes through the field.

Faraday found the best way to obtain useful work from this magnetic force. He assembled a rotating disk-shaped conductor within the magnetic field. The resultant force vectors caused the disk to spin. To attach current-carrying leads to the spinning conductor, he used sliding contacts.

These two discoveries became the basis of the DC motor and the DC generator. Eventually, the disk was replaced with many turns of wire placed in deep slots of a laminated iron rotor. This part is the armature. The externally applied magnetic field, the stator field, was produced by an electromagnet (or a permanent magnet) and the sliding contacts

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became carbon brushes and commutators.

The optimum DC-motor configuration has the most conductors in the magnetic field. Maximum force is developed at a right angle to the stator field. Between these positions, the resultant force is a function of the sine of the angles between the two fields. As the rotor turns, the magnetic field rotates with it unless some provision has been made to switch the direction of current flow in individual armature conductors so they maintain the maximum force vector.

This switching is done with a commutator, as shown in figure 2 on page 70. Current flows in through brush A and out through brush B. During clockwise rotation, the current in coils 3 and 6 will have reversed after one sixth of a revolution past the position shown. In fact, after every one sixth of a revolution, the current in two opposite armature conductors changes directions. As a result, the current-flow and field vectors in the

armature occupy a fixed position in space independent of rotation of the coils. This provides steady, unidirectional torque.

Motor Classification

DC motors are often classified by the type of stator field used. Fractional-horsepower DC motors using electromagnets to generate the stator field are called "wound-field motors." There are three basic types: series field, shunt field, and compound field. A graphic comparison of speed, torque, and current of these three motors is given in figure 3 on page 72.

Series-field motors provide the greatest torque at start-up because the high initial armature current flows through the stator field as well. As the speed increases, the current decreases. This further increases the speed. If not for internal friction and coil-winding energy losses, this type of motor could theoretically run away under no-load conditions. This type of motor is best used where large starting torques are required, such as automotive propulsion. A schematic representation and speed/torque graph are shown in figure 3a.

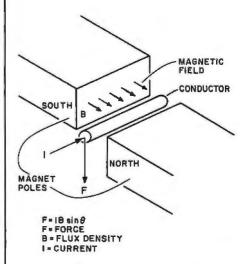
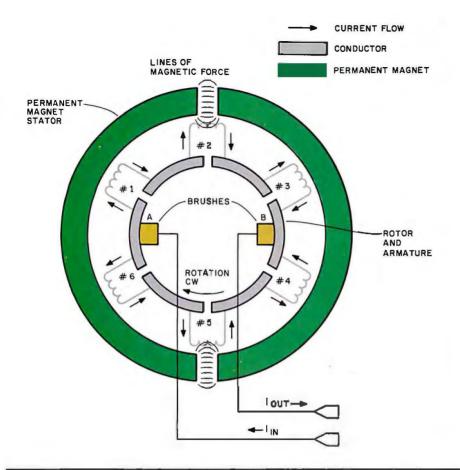


Figure 1: Simplified diagram of the basic electromagnetic principles behind the DC motor. When a current-carrying conductor is placed in a magnetic field, the conductor feels a mechanical force, F, in the indicated direction, perpendicular to the current and the magnetic field. The force is greatest when the current is flowing perpendicular to the lines of flux ($\theta = 90^{\circ}$), as shown here. The force is zero if current flows parallel to the lines of flux ($\theta = 0^{\circ}$).







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Figure 2: Internal structure of a typical PM (permanent-magnet) DC motor. Brushes transfer current to the armature coils. As the armature rotates, the brushes contact the assembly at different points, reversing the direction of current flow in the appropriate coils to maintain the electromagnetic force and provide continuing torque.

Shunt-field motors, shown in figure 3b, have the armature and field coils connected in parallel. The lower-current field winding, used only for creating a magnetic field and not required to carry the heavy armature current, makes this motor popular for fixed-speed applications. Except at start-up, the shunt-field motor has greater torque than the series-field motor for a given speed.

Compound-field motors have both series- and shunt (parallel)- field windings. These motors exhibit high starting torque and relatively flat function curves for speed/torque characteristics. While useful in providing rotation in one direction, this motor is difficult to reverse since connections to both windings must be reversed in polarity. Complex switching circuits are required for reversal control.

Permanent-Magnet Motors

In a PM (permanent-magnet) motor, the stator field is produced by a permanent magnet, not an electromagnet. The PM motor has a speed/torque curve that is linear over an extended range, as shown in figure 3d.

The obvious advantage of using a permanent magnet is that it requires no electrical power to generate the stator field. Because the actual electrical-to-mechanical energy conversion takes place in the armature, the major part of the power supplied to the electromagnetic field coil in a wound-field motor is lost as heat. The PM motor requires less power and less cooling.

The PM motor is not new. It has been around for many years and was used in your childhood toys. However, high-power PM motors were very expensive and rarely found in the home. Only recently has the in-



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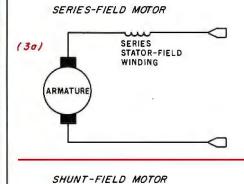
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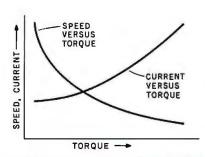
‡TM Digital Equipment §TM of Tandy Corp. *TM U. of California corporation of new ceramic magnet materials made the PM motor practical for low-cost/high-power applications. Previously, most PM motors used *alnico*-alloy magnets, which are susceptible to demagnetization.

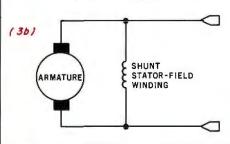
The magnet material in all PM motors is magnetized during manufacture by placing it into a strong electromagnetic field. If, later on, the motor is not carefully regulated while

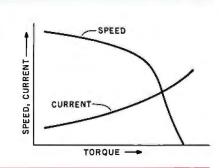
in use, high armature currents can produce fields exceeding the original magnetization flux. Consequently, this can demagnetize the stator magnet.

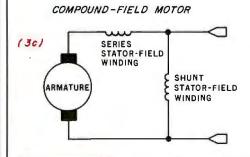
The current at which this phenomenon occurs is approximately seven or eight times the stated normal operating current of the motor. A PM motor with a 3 A current rating would have problems at currents exceeding 24 A. While such values seem

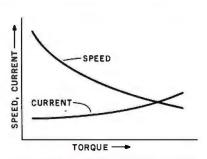


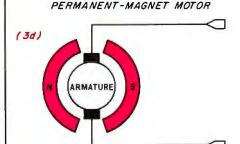












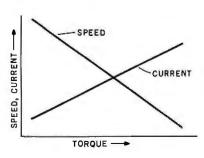


Figure 3: Different types of DC motors are distinguished by the type of stator field. Three types use an electromagnet to produce the stator field; the fourth uses a permanent magnet. Different methods of connecting windings in the stator electromagnet produce different speed/torque and current/torque function curves.



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unlikely in normal use, very high currents are often incurred in low-speed, high-torque, pulsed operation. The greatest risk occurs during a high-torque, high-speed, rapid-reverse situation. The sum of the applied voltage and counter EMF (electromotive force) of the motor at the instant of reversal can create excessive current due to relatively low armature resistances. This article primarily covers low-speed PM-motor applications, so this shouldn't be a problem.

Speed Control in PM Motors

Controlling the speed of a PM motor is much easier than controlling a wound-field motor because the speed/torque characteristics are linear. If you apply a fixed voltage to a PM motor, it rotates at a fixed speed. Double the voltage or reduce the torque (load) requirement by half, and the speed increases by a linearly proportional amount.

Therefore, the least complicated speed control is one which adjusts the voltage applied to the armature. This

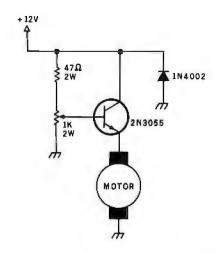


Figure 4: A simple open-loop linear motor-speed control. Operating the controlling transistor in the linear region of its characteristic curve leads to loss of energy as heat.

can be physically accomplished using a rheostat, an autotransformer and rectifier, or a linear transistoramplifier circuit (such as the one shown in figure 4). The objective is to apply a relatively constant current to the armature. In the case of the linear amplifier, however, considerable power is wasted as heat loss when the control component (here, a transistor) is not fully turned on (saturated). The worst case occurs when high torque is required at low speed. This condition can be overcome by *pulsing* the power to the armature through an on/off switch or a switching amplifier. The resulting average current creates the same effect as the linear amplifier without the power-dissipation problems.

There are three basic types of switching amplifiers used in PM-motor controls: PWM (pulse-width modulation), PFM (pulse-frequency modulation), and SCR (siliconcontrolled-rectifier) pulse-width modulation. Essential characteristics of these three forms are shown in figure 5 on page 76.

The pulse-width-modulated controller works by switching the full voltage of the DC power supply to the motor on and off at a fixed frequency with a varying duty cycle. At low speeds, the duty cycle is short,

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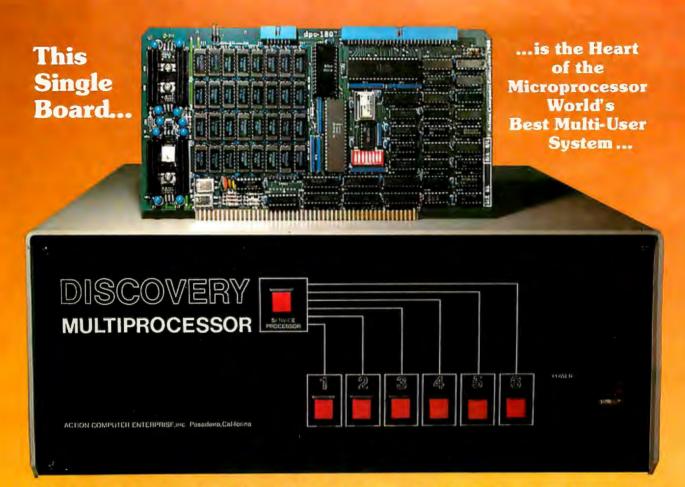
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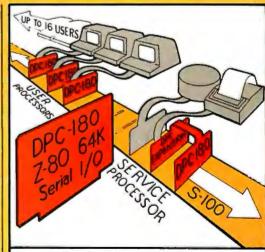
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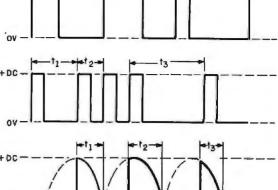
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Figure 5: Comparison of three basic switching-amplifier control-circuit output waveforms. The controlling semiconductors are saturated; the average amount of electrical current transferred to the motor is limited by rapidly cutting the current off and

and the average voltage applied to the armature is low. At high speeds, the duty cycle is much longer, and the average voltage is increased.

The pulse-frequency-modulated controller, on the other hand, switches the DC supply on for a fixed period of time at a varying repetition rate. At slow speeds, the switching frequency is low, and the resulting average applied voltage is low. At higher speeds, the pulse width of the applied power is the same, but the switching frequency is increased to raise the average voltage level.

Figures 6 and 7 on page 78 illustrate simple circuits allowing you to experiment with PWM and PFM speed controls. The components and frequencies in the schematics are selected for high-current DC motors such as those found in electric drills. (For use on high-speed/low-torque hobby motors, the frequencies and pulse widths may require adjustment.) In figure 6, 10 to 100% PWM speed control is accomplished by adjusting the duty cycle of a one-shot (monostable multivibrator) triggered from a fixed 100 Hz frequency source. In figure 7, PFM speed control is obtained by varying the frequency of 1 ms pulses applied to the motor.

The third method, using an SCR as the switching element, is a variation on PWM. SCR speed control is nearly

always used at the power-line frequency (50 or 60 Hz). It functions by changing the firing angle (ie: the point in the waveform where conduction is triggered) between 0 and 180 degrees and applying a specific fraction of each voltage waveform to the motor. At low speeds, the firing time is short, resulting in a low average voltage applied to the motor. At high speeds, the firing time becomes longer, resulting in a higher average voltage.

The SCR controller does not have the precise control resolution of the linear amplifier, but its major advantages are high power-conversion efficiency in the switching mode and low forward-voltage drop. The predominant use of SCRs in fractionalhorsepower DC-motor controls is primarily due to the simplicity of the circuitry. A typical wide-range SCR speed-control circuit is shown in schematic form in figure 8 on page 80. Figure 9 illustrates a speed-control circuit which maintains constant speed under varying load conditions.

Closed-Loop Speed Control

The speed-control designs presented so far have been open-loop controllers. They are adequate for setting speeds where torque requirements are constant. For applications where there is a variation in load demand or where constant velocity is required, a closed-loop control system must be

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	six	fifty	BOhertz tone	flow	lesa	over	star	Ř	У
	seven	sixty	20ms silence	fuel	lesser	parenthesis	start	i	2
	eight	seventy	40ms silence	gallon	limit	percent	slop	i	_
	nine	eighty	60ma silence	80	low	please	than	k	
	ten	ninety	160ms silence	gram	lawer	plus	the	1	
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Figure 6: A simple PWM (pulse-width-modulated) motor-speed control. The duty cycle of the monostable multivibrator (74121) is adjusted by the variable resistor to change the average integrated (in the mathematical sense) electrical current supplied to the motor through the driving transistors. Pin 14 of the 74121 should be connected to +5 V, while pin 7 should be connected to ground. The 2N3055 transistor must be mounted on a heat sink.

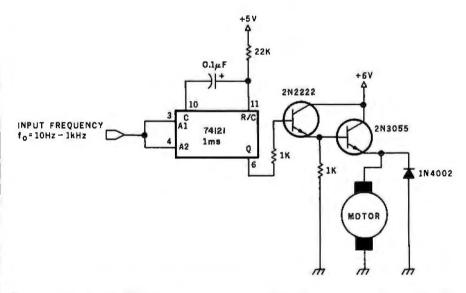


Figure 7: A simple PFM (pulse-frequency-modulated) motor-speed control. The number of constant-duration pulses supplied to the driving transistors over a given interval controls the speed.

employed.

Figure 10a on page 84 shows an open-loop controller; figure 10b shows a closed-loop system. Both controllers use an amplification device to drive the motor. The amplifier block can be broadly interpreted to represent any of the driving methods discussed (PWM, linear amplifier, etc). In the open-loop controller, any variation in load demand causes the motor to speed up or slow down.

The basic difference between the open- and closed-loop control methods is that the latter uses a sensor attached to the motor shaft to monitor the actual motor speed. The sensor provides a feedback signal proportional to the shaft's speed. This can be compared with the desired value of the signal (the set point) to find out if the motor is running fast or slow. If the speed is too low, the comparator applies more voltage to the amplifier to bring the speed up. When

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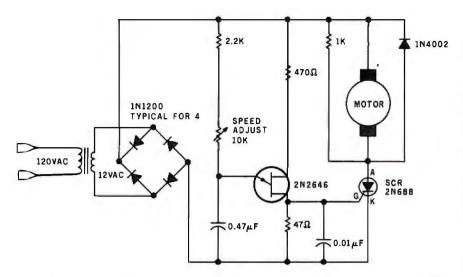


Figure 8: An SCR- (silicon-controlled rectifier) controlled motor-speed circuit. This method, a variation of PWM (pulse-width modulation), has a wide speed range, high power-conversion efficiency, and low forward-voltage drop across the controlling semiconductor, but not the precise control resolution of a linear-amplifier circuit.

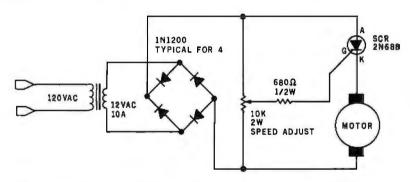


Figure 9: A second type of SCR-controlled motor-speed control. This design has a limited speed range but maintains constant speed under varying load conditions.

the speed sensor indicates the speed is too high, the comparator reduces the current to the motor, and the speed drops.

The speed sensor is generally a DC generator. This is nothing more than another PM motor operated in reverse. When the armature is turned, its coils cut through the PM statorfield lines, inducing a current in the armature windings. A motor with a rating of 500 rpm per volt, when used as a generator, produces an output of approximately 4 V if the armature is rotated at 2000 rpm. Such generatortype tachometers (or tachometergenerators) are useful for mediumand high-speed applications when they have a reasonably detectable and steady output. Photo 1 shows a PM motor being used as a generator.

At low speeds, an incremental encoder is often used instead of the generator-type tachometer. An in-

cremental encoder generates a pulse when the shaft has rotated through a given angular increment. They are most suitable in low-speed and position-mode controllers. Photo 2 on page 81 shows a simple incremental encoder. More on this later.

Servo Controls

So far, we have discussed openand closed-loop speed controls. We can turn a potentiometer and set a speed of 2000 rpm on a PWM-controlled motor. We can even attach a tachometer to regulate the speed at this set point. All these controls, however, are scalar and unidirectional. When the speed control is adjusted, we are setting a fixed number of revolutions per minute, rather than attempting to rotate the motor shaft to a particular position or to have it make ten revolutions and stop.

When control systems capable of

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Operating in Quadrants

The torque/current and torque/ speed function curves of figures 3a, 3b, 3c, and 3d on page 72 all lie in the first quadrant of a Cartesian coordinate system. In these graphs, torque and speed are considered positive when the motor's shaft is rotating in the forward direction, and current is positive or negative according to its direction of flow.

During most modes of operation, the curves remain in the first quadrant; only when sudden stopping and reversing take place do the curves enter other quadrants.

For instance, in dynamic braking, the inputs to the armature coils are shorted together. As the motor continues to rotate, the existing magnetic field induces in the coils a counter electromotive force that attempts to produce a field opposing the existing field. The opposition of the two fields produces negative torque and surprisingly fast braking action. The current of this counter electromotive force is negative, and the torque/current function curve momentarily moves into the third quadrant.

providing positive- and negativeoutput voltages for four-quadrant operation in conjunction with feedback control are discussed, we are no longer talking about mere speed controls, but about servo systems. Servo systems are usually configured to provide velocity, position, or torque control, or combined velocity/position control. The definition encompasses all DC-motor applications beyond first-quadrant fixed-speed operation (see the text box above).

The simplest type of servo opera-

tion is a forward/reverse motor control. Reversing the rotation on a PM DC motor is accomplished by reversing the polarity of the applied voltage. While this can be done manually by using a switch, in automatic-control systems it is most frequently done with transistors. Two typical circuits are illustrated as schematic diagrams in figures 11a and 11b on page 86. In figure 11a, a forward-control signal turns on transistors O1 and O4, routing the current through the motor as shown. A



Photo 2: The most frequently used nongenerator speed-feedback device is the incremental encoder. This is a homemade encoder, consisting of a plastic disk attached to the motor shaft. Around the perimeter of the disk are slots or holes. A light source is placed on one side; a light sensor on the other side. As the shaft turns, the disk interrupts the light seen by the photo sensor and creates a pulsed output with a pulse rate proportional to the speed of the rotation.

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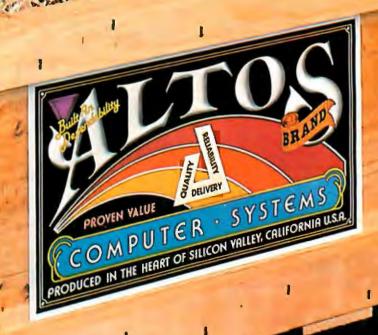








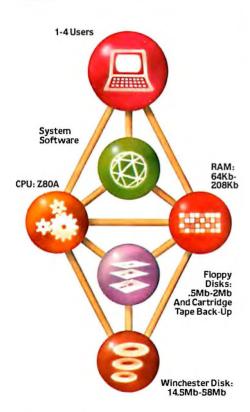




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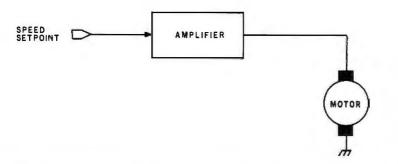


Figure 10a: Block diagram of an open-loop controller. Variations in mechanical load cause the motor to speed up or slow down.

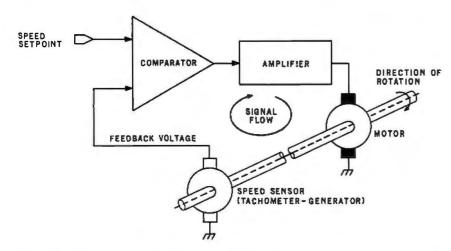


Figure 10b: Block diagram of a closed-loop controller. The speed sensor detects too-fast or too-slow motion and keeps the motor running without variation in speed over wide variations in load.

reverse-control signal enables transistors Q2 and Q3 to route the current through the motor in the opposite direction. This circuit, frequently called a *bridge output*, uses only a single DC supply voltage and is generally reserved for use in PWM or PFM controllers. Figure 11b shows a complementary output driver. It is more suitable for linear-control operation, and it requires two opposite-polarity power-supply voltages.

Incremental-Motion Systems

Usually, we don't think of performing positional control with DC motors. Most of our experience has been with 7000 rpm, 3 V PM motors salvaged from toys. However, using special DC motors, it is possible to perform repeatable intermittent or incremental motion. These are the motors generally used in computer-peripheral magnetic-tape transports and line-feed mechanisms. In these, it is frequently necessary to run the

motor at fast speeds to achieve high media-slew rates, as well as slow incremental motion. (Stepper motors generally cannot attain the high speeds required.)

The incremental drive is basically a high-performance velocity-controlled

Special DC motors are used in computer peripheral devices where widely varying speeds are needed.

DC-servo system. The incremental motion is obtained by applying variable-amplitude voltage pulses to the input and accelerating the armature for predetermined periods of time. Figure 12 on page 88 shows the control waveforms.

With the system initially at rest, a high positive step voltage, t_1 , is applied to the input. This causes the

motor to accelerate almost instantaneously. Shortly thereafter, the voltage is reduced to a level, t_2 , maintaining constant rotational speed. Some time later, the shaft rotation is stopped by applying a reverse-polarity input, t_3 . Attempting to accelerate in the opposite direction causes the motor to brake. The exact timing of these pulses depends upon the specific motor and torque requirements.

The entire process takes only a few milliseconds and may move the armature a fraction of a revolution. This incremental motion is repeatable, enabling practical application. If, for example, it is applied at 100 steps per second while using an incremental encoder for speed control, the motion will appear to be produced by a high-torque stepper motor.

Build a Motorized Platform

Experimenting with incrementalmotion controls on permanentmagnet DC motors is not as difficult as you might imagine. Once you discover the capabilities, you may find yourself experimenting with different mechanisms, as I have.

The cheapest high-power low-voltage PM DC motor I found was the one in a hand-held battery-operated drill. The motor I used was from a Black & Decker Model 9001 ¹/₄-inch cordless drill. This same motor is probably used in a variety of other tools and appliances, possibly hedge trimmers and the like.

The basic unit consists of a power pack (containing a 4.8 V rechargeable nickel-cadmium battery and a charger) and the motor/drill-chuck assembly. The motor/chuck assembly contains the PM motor, reduction gears, and drill chuck.

A major stumbling block in building a transport mechanism that might be used in a robot has been the expense of the motors and gears. In lightweight assemblies, designers often incorporate stepper motors because they are easily controlled and their motion is repeatable. In larger and heavier vehicles, use of stepper motors becomes prohibitively expensive, and alternative drive mechanisms are required.

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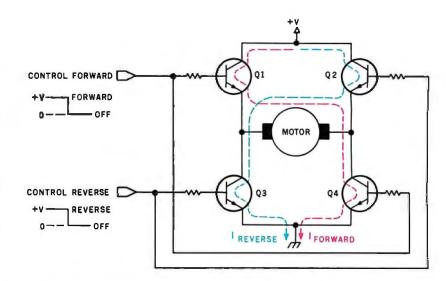


Figure 11a: One of two basic reversing motor-control circuits. This bridge-type switch uses a single DC supply voltage and is used mostly in PWM or PFM controllers.

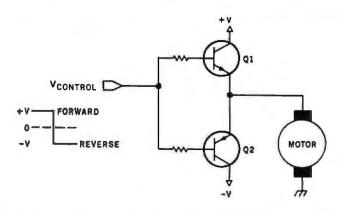


Figure 11b: A complementary-type reversing circuit. It is more suitable for linear-amplifier control operation, while requiring two opposite-polarity power-supply voltages.

While I did not intend to build a 300-pound "Son of Robbie," I wanted to experiment with some form of remote-controlled transport. Since the drills contained gear-reduced, low-voltage/high-torque motors and a chuck to attach an axle, it was natural to consider their use. The only problem I envisioned was reducing the nominal 750 rpm motor speed to a fairly constant value around 60 rpm. An incremental-motion controller was the answer.

The result of my experimentation is the motorized platform shown in photos 3, 4, and 5 on pages 90 and 92. A sketch of the major parts is shown in figure 13 on page 88. The platform consists of a T-shaped metal frame with a drive motor on each "arm" and a swivel wheel on the "leg." I designed it in a T shape so the drive motors could provide steering con-

trol, as well as forward/backward motion. In a conventional four-wheeled vehicle, this can be accomplished only by turning the axis of two wheels in the direction of the turn. This could not be accommodated in the present mechanism.

With the T shape, steering is like simple rotation. For forward motion, both motors rotate clockwise; for reverse motion, both motors turn counterclockwise. Turns are accomplished by driving the motors in opposite directions. For a right turn, motor A goes clockwise and motor B goes counterclockwise. A left turn, or left rotation, occurs with the opposite settings. The effect is that it rotates in place. Usually, reversing the polarity to the motors is handled through transistor switches, but I found that the voltage drop through the switch-

Text continued on page 90

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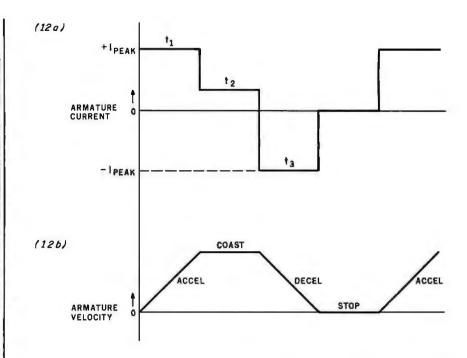
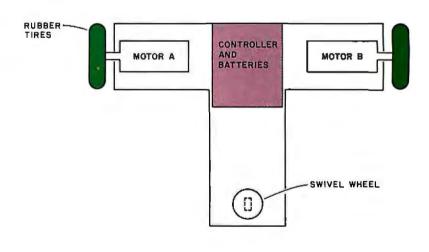


Figure 12: Precise control can be achieved using incremental-motion controllers. During predetermined periods of time, variable-amplitude voltage pulses are applied to the motor's coils. With the system initially at rest, a high positive step voltage, t_1 , is applied to the motor. After motion has begun, the voltage is reduced to a lower continuing value, t_2 . When the motor is to be stopped, a negative braking voltage, t_3 , is applied.



	DIRECTION C	F ROTATION
FUNCTION	MOTOR A	MOTOR B
FORWARD	CW	CW
RIGHT TURN	CW	CCW
LEFT TURN	CCW	CW
BACKWARD	CCW	CCW

Figure 13: Arrangement of components of the motorized platform. Steering is done in the simplest case by rotation. Both motors turn in the same direction for straight motion, whereas for a turn, one motor turns CW (clockwise) and the other turns CCW (counterclockwise).



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Text continued from page 86:

ing network was too much in this low-voltage system. Instead, I used relays to switch polarities and enable motion.

The greatest design obstacle was the actual velocity-control system. Even though the drills contained gears, the no-load speed was 750 rpm. With a wheel and axle inserted into the chuck, the platform's uncontrolled speed with no load was 10 feet per second. About 9 inches per second, corresponding to 60 rpm,

seemed considerably more manage-

To attain this lower speed, an incremental-motion/PWM controller was designed. One controller is reguired for each motor. The schematic diagram is shown in figure 14 on page 96. Component values were experimentally determined for use with the Black & Decker PM motor specified. Other PM motors may not operate in exactly the same manner.

Basically, the circuit is a closedloop controller, consisting of a comparator, driver amplifier, and speedfeedback sensor. The desired speed is selected through a ten-turn potentiometer. The set-point voltage so derived is compared to an integrated feedback voltage from an optical incremental encoder. If the speed is too slow, the pulses out of the comparator are made longer. If the speed is too fast, the pulses are cut shorter. A negative voltage applied to the driver input between pulses assures complete turnoff.

The low pulse-frequency rate required to keep the speed at or below 60 rpm results in an incrementalmotion condition. The start pulse is at the full DC supply voltage, creating a high-velocity start-up. A reverse-step pulse is not necessary to stop the motor, however, due to the high mechanical load presented to the motor through the gears. They serve to immediately dampen any coasting. The result is smooth, low-speed rotation, in rapid discrete increments, at a predictable constant velocity.

Maintaining constant motor speed is imperative when the motors must run synchronously for forward and backward motion. Turns are not as critical, but you realize what happens when one motor runs faster than another.

The 60 rpm speed is too slow to use a tachometer-generator without considerable complication. Instead, an incremental encoder (shown in photo 6) generates pulses as the wheels turn. Ordinarily, I would have used a slotted or perforated disk interrupting a light beam, but it wouldn't fit in the space available, Instead, I wrapped reflective aluminized tape with black stripes parallel to the axis of rotation around the chuck. An LED (light-

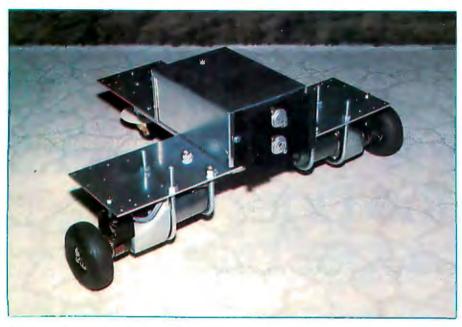


Photo 3: A simple application of the DC motor controls presented in this article is to build a small mobile motorized platform. This one uses two battery-operated drill motors and a swiveling furniture caster. The T-shaped structure has complete mobility and can turn and pivot, as well as follow a straight line. The large box in the center of the platform contains the two motor controllers, relays, and batteries.

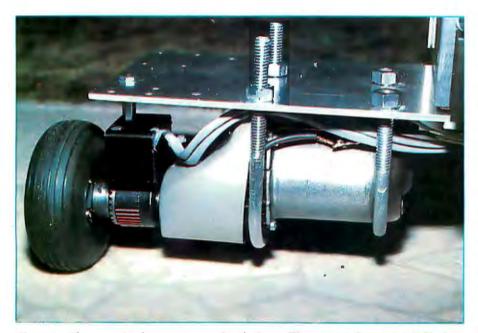


Photo 4: Close-up of a drive motor on the platform. The motor is from a 4.8 V Black & Decker battery-operated 4-inch drill. The drill's case and battery pack have been removed. It is secured to the aluminum T-frame with two U bolts. A 3/12-inch brass rod that serves as an axle is inserted into each drill chuck. The tires are air-filled 34-inch diameter rubber tires used on model airplanes.

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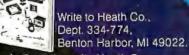
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emitting diode) and phototransistor sense the light and dark areas of the tape as the shaft rotates. The greater the number of divisions or stripes per inch, the greater the resolution of the feedback system. While I was able to set the same speed on both motors, more encoder divisions would have been better.

Ideas for Computer Control

This article wouldn't be complete unless I described how my motorized platform can be remotely controlled from the computer. Essentially, it reguires three signals controlling one power-on/off relay and two forward/reverse relays (10 A contacts).

Text continued on page 98



Photo 5: The rear of the T-frame is supported on a furniture caster. This is a simple scheme allowing motion in any direction,

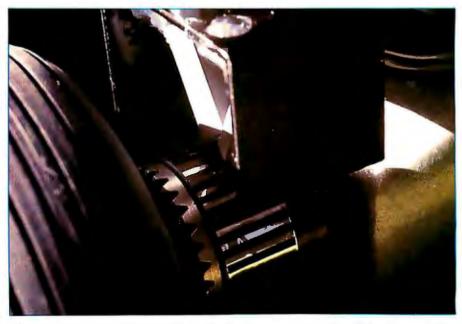


Photo 6: It was nearly impossible to fit the incremental-encoder disk of photo 2 between the motor and the wheel. Instead, a piece of reflective aluminized tape with black stripes was wrapped around the drill chuck. An infrared LED (light-emitting diode) and phototransistor are aimed at the tape so the light is reflected to the sensor. As the shaft turns, the light is interrupted much the same as the disk version.

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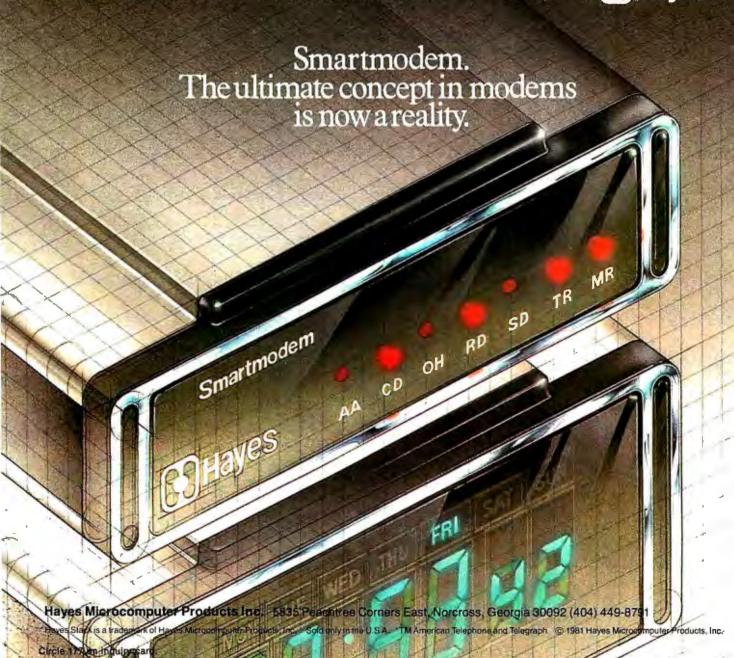
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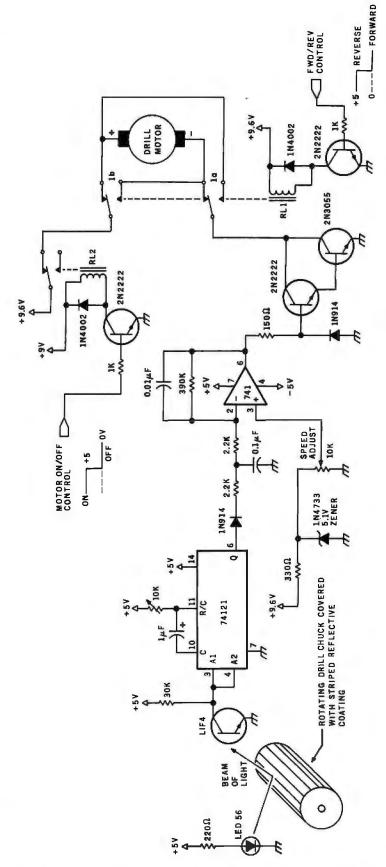


Figure 14: The motor-control system of the platform, featuring incremental-motion control and reversing capability. Two such circuits were used, one for each motor. Values of the components were experimentally chosen for use with the motor from a Black & Decker Model 9001 portable drill. The 2N 3055 transistor must be mounted on a heat sink. The L1F4 phototransistor is made by General Electric.

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Some models require hardware/software modification.

Text continued from page 92:

The forward/reverse relays set the intended motor directions, and the power-on/off relay starts both motors. As long as the power is on, the platform goes in the direction set by the two forward/reverse relays.

Computer direction of the relays is accomplished with 3 control-signal bits from a parallel output port. For wireless remote-control operation, the communication control link presented in my article "A Computer-Controlled Tank" (BYTE, February 1981, page 44) can easily be adapted to this task.

In Conclusion

You may never see my contraption again. I don't consider this the start of a serious robot-building project. The total expense for the platform was under \$50. It was just an experiment. I had always wanted to try using inexpensive electric-drill motors as servos. While I had mixed success, it did serve as a vehicle for a general article on DC-motor control.

Building the platform was the only way to truly test the theory. I was surprised that the final unit, weighing 10 pounds, had no problems with insufficient driving torque (unfor-

tunately, the small batteries lasted only about 5 minutes in constant use). Even with an additional 5 pounds of payload (a bottle of Hennessy cognac and two heavy BYTEs), it worked well.

I don't expect many of you will try to build a motorized platform. I do, however, anticipate that more of you will consider using permanentmagnet DC motors for future designs where you thought only stepper motors could be used. If you already own a battery-operated drill, connect it to the control circuit of figure 6 or figure 9. You will be surprised at the capabilities it demonstrates.

Next Month:

Add a speech-synthesizer circuitboard assembly to your computer.

Editor's Note: Steve often refers to previous Circuit Cellar articles as reference material for the articles he presents each month. These articles are available in reprint books from BYTE Books, 70 Main St, Peterborough NH 03458. Ciarcia's Circuit Cellar covers articles appearing in BYTE from September 1977 thru November 1978. Ciarcia's Circuit Cellar, Volume II presents articles from December 1978 thru lune 1980.

Many Circuit Cellar projects are available as kits. To receive a complete list, circle 100 on the Reader Service card.

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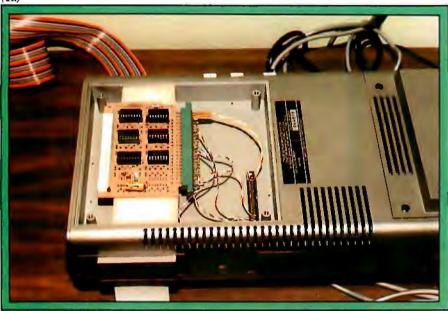
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System Notes

Improve TRS-80 Disk Operation

Add an External Data Separator



(1b)

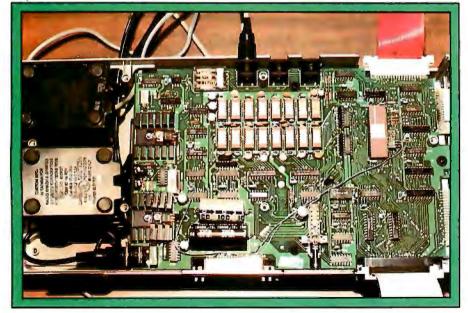


Photo 1: External data separator circuitry as installed in the Radio Shack TRS-80 Expansion Interface. Most of the integrated circuits can occupy the space intended for the RS-232 interface (photo 1a). Irreversible changes can be avoided by bending a few pins on the FD1771 to obtain the necessary signals (see the wires leading from the FD1771, under the red cable, in photo 1b).

Ken Kline 3821 Penitencia Creek Rd San Jose CA 95132

When I first added a floppy-disk drive to my Radio Shack TRS-80 Model I computer, I was very disappointed in its operation. My records indicated that, on the average, I was getting an error for every four disk accesses. These errors were independent of the type of access (ie: they occurred while accessing programs, data files, utilities, and even the bootstrap loading routine). In desperation, I called the Tandy Corporation in Fort Worth, Texas, and was told to use a better grade of disk. I tried this and noticed an improvement (to one error in eight accesses), but the lack of reliability was intolerable.

Discussing my problem with owners of other home computer systems, I came to the conclusion that the FD1771-01 floppy-disk controller part was the culprit. Don't misunderstand. I am not downgrading the FD1771. If you have studied the specifications and application notes of the FD1771 as much as I have you will realize that it is guite a marvelous piece of silicon. However, quoting from Western Digital Corporation's FD1771-01 Application Notes (document Number A0104, page 2) "In order to maintain an error rate better than 1 in 108, an external data separator is recommended."

The data separator that I finally ended up with is shown schematically in figure 1. It is a modification of one of the external data separators recommended by Western Digital

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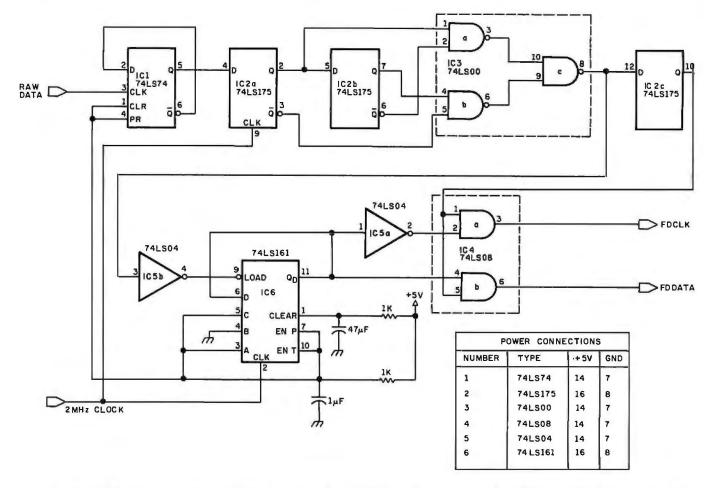


Figure 1: External data separator circuitry. This design was developed from one recommended by Western Digital in an applications note for its FD1771. This circuit adds a power-on reset feature.

shown on page 5 of the same document). After adding the external data separator to my TRS-80, access errors virtually disappeared.

The data separator was constructed on an old printed-circuit board. It already had the voltage and ground connections run to all integrated-circuit-socket positions, and it had edge-card connections. The circuit board now resides in the compartment of the TRS-80 Expansion Interface reserved for the RS-232C interface or other extra circuitry (see photo 1).

This circuit varies from the one in the Western Digital application notes in the use of +5 V on some integrated circuit pins (through a 1 k-ohm pull-up resistor) and a resistor/capacitor network that provides a lag of about 45 ms on the 74LS161 counter's CLEAR input (IC6,

pin 1) to insure that it is cleared on power-up.

In order not to make any irreversible changes in the printed-circuit board of the TRS-80 Expansion Interface, the three connections to the FD1771 floppy-disk controller can be made through a 3-pin length of a dip strip, a type of socket. Remove the 1771 from its socket and carefully bend pins 25, 26, and 27 out from their normal position. Then reinsert the 1771 into its socket and push the 3-pin dip strip onto the three pins sticking out.

Pin 25 must be connected to ground when using an external data separator (pin 25 is normally pulled up to +5 V for internal data separation). Pins 26 and 27 are the separated clock and data inputs to the 1771. The raw data from the disk drive to the external data separator is avail-

able at pin 8 of integrated circuit Z32 in the Expansion Interface, and the 2 MHz clock signal is picked up at pin 3 of Z25.

All signals are sent to Expansion Interface connector J1 and are available on the internal expansion connector inside the additional circuitry compartment. Ground is available on pins 41 and 42 of that connector, and +5 V is available on pins 39 and 40 (see the right edge of the second page of the Expansion Interface schematic, page 41, in the Radio Shack Expansion Interface manual).

I measured the current required to operate the external data separator (using LS-type integrated circuits) and believe that the 40 mA it draws is certainly less burden on the Expansion Interface power supply than the RS-232C interface that might use this position. ■

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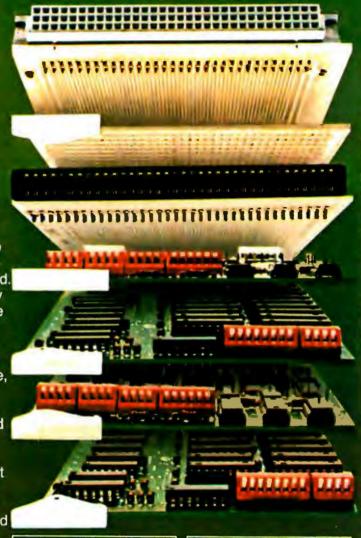
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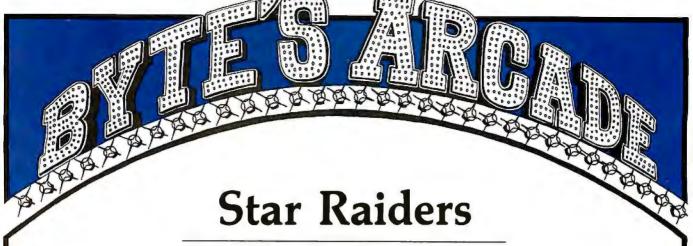
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Gregg Williams, Senior Editor

hat can you say about a game that takes your breath away? There are not enough superlatives to describe Star Raiders. Just as the VisiCalc software package from Personal Software has enticed many people into buying Apple II computers, I'm sure that the Star Raiders software cartridge from Atari Inc has sold its share of Atari 400 and 800 computers.

What is Star Raiders? It's a video arcade game

that isn't hungry for quarters. I first saw Star Raiders at the West Coast Computer Faire in May 1979, and in the two years that have passed since the first public viewing of the game, no one—I repeat, no one—has created either a home-computer game or a coin-operated video game that is better than Star Raiders. (This fact is even more surprising when you consider the speed with which new standards are set in this industry.)

For the people who haven't seen Star Raiders in action, I'll attempt a brief description. Star Raiders is

HYPERMARP ENGAGED

U: GR K: GR E: BR T 13

o: + GG P: + GG R: + 13 5

Photo 1: The view from the bridge of the Star Raiders ship during a hyperspace jump. A static photo cannot do justice to the excitement you feel as stars streak by prior to the jump.

loosely modeled on the "Star Trek"-type game that has been running on micro- and larger computers for the past eight years. You, as commander of a starship, must search out and destroy all enemy spaceships in the galaxy (which is subdivided into a rectangular array of units called "sectors"). Of course you have only a certain amount of energy, and when you fight an enemy ship that is in the sector you occupy,

it can fight back and damage your ship.

Star Raiders is a descendant of this kind of game in the same way that the new pocket computers are descendants of a four-function mechanical adding machine. The many innovations in Star Raiders make you feel that you are actually piloting the spaceship instead of just typing in commands (and endlessly pressing the ubiquitous RETURN key).

Star Raiders has color, sound, and joystick input to make the game more realistic, but the feature that gives it life is its real-time animation. When you patrol a sector, you see a field of stars passing you in all directions, as if you were actually moving through a three-dimensional field of stars. When you steer the ship using your joystick, the stars outside your ship veer realistically in the opposite direction. Enemy ships (called Zylons) appear from above or below, receding in size as they speed past. A battle claxon sounds when you enter a sector containing enemy ships. Attacking Zylons shoot balls of energy at your ship; if they hit, your shields flicker and you hear a destructive crash. And the hyperspace effect (used to

Why spend all those quarters on arcade games? With a microcomputer and a few weeks' worth of arcade money, you can enjoy at home microcomputer games that are just as good as (and sometimes identical to) the popular coin-operated video arcade games. BYTE's Arcade is an occasional feature that reviews the best of these fast-action games. If you would like to review or give an opinion of a favorite microcomputer game of this type, please write to: BYTE's Arcade Editor, POB 372, Hancock NH 03449.

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Photo 2: The Star Raiders Galactic Chart. Each square represents a sector of space. The star symbols represent sectors containing starbases; all other squares marked with symbols represent sectors containing Zylon enemy ships. Your ship is located in the square near the center, marked by a small dot.



Photo 3: The view from the bridge during combat. "Star Trek" games were never like this! When you occupy the same sector as enemy ships (here, top and bottom center) their size will increase and decrease as you move toward or away from them.

At a Glance.

Name Star Raiders

Type Arcade-style game

Manufacturer Atari Inc Consumer Division 1195 Borregas Ave Sunnyvale CA 94086 (408) 745-2000

Price \$59.95

Author Doug Neubauer

Format Game cartridge

Language 6502 machine language

Computer Atari 400 or 800

Documentation 10 pages, 22 by 28 cm (8½ by 11 inches)

move you from one sector to another) must be seen to be believed!

I could continue to describe the intricacies of Star Raiders, but words cannot evoke the sensation of actually playing the game. To Doug Neubauer of Atari, who wrote Star Raiders, my unbounded thanks. To all software vendors, this is the game you have to surpass to get our attention. And to Atari, I can only say that if you offer us games like this, we can't refuse.

Super Nova

Bob Liddil, POB 66, Peterborough NH 03458

Arcade video games are extremely popular throughout the world. It would seem natural, therefore, that these games would take hold in the TRS-80 marketplace, where good graphics programs are in short supply. There is, to be sure, a good deal of mediocrity on the market, such as early versions of Space Invaders. Super Nova, however, is an example of how well a program can be created if its designer takes enough time and care with it.

The instant the program (a standard machine-language system tape) is loaded, Super Nova spins into a stunning

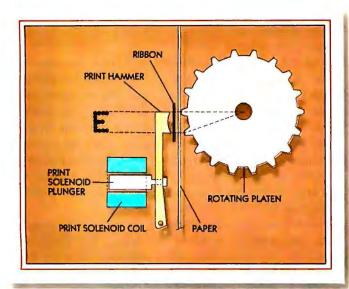
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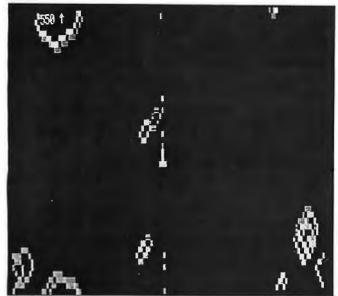


Photo 1: The Super Nova game in play.

three-dimensional starburst display that looks so real it makes you dizzy. The depth of field is absolutely startling. This is the most striking high-speed animation I have ever seen (with the possible exception of the hyperdrive display of Atari's Star Raiders. The graphics work in Super Nova is fast, stunning, and very uncharacteristic of TRS-80 games.

As with its coin-operated counterpart, Atari's Asteroids game, the object of Super Nova is to destroy objects that appear on the screen while avoiding your own destruction. Meteors, of all shapes and sizes, make up the bulk of these targets. When you hit the larger asteroids, they shatter into smaller and smaller chunks, and, if you're lucky or skillful, they finally disintegrate. It should be noted that the supply of meteors is unlimited.

Not content to menace the player with mere rocks hurtling through the void, Super Nova thoughtfully provides missile-firing alien spaceships. Three less-dangerous craft appear when there are six or less meteors on the screen. Two larger ships, worth more as targets, appear when you reach a score of 10,000 points.

Some of the aliens have special shields that allow them to pass harmlessly through meteors. Not so for your fighter—touch something, anything, and you're destroyed. The game ends when you have lost three ships.

Super Nova has a well-thought-out keyboard setup that enhances the playability of the game. Five keys control your ship's action in a fashion similar to the buttons supplied in coin-operated video games. The R and T keys turn the ship counterclockwise and clockwise, respectively. The O key applies engine thrust in whatever direction the ship is pointing, and the P key fires your missiles. Finally, the space bar launches the ship into hyperspace. The keys are located so that you play the game with the first two fingers of each hand touching the keys and

At a Glance.

Name Super Nova

Type Arcade-style game

Manufacturer Big Five Software POB 9078-185 Van Nuys CA 91409

Price \$14.95

Format
Cassette
Language
Z80 machine code

Computer TRS-80 Model I with 16 K bytes of memory and Level I or Level II BASIC

Documentation 1-page insert sheet

either thumb working the space bar.

Super Nova would be an enjoyable game if it had only the features I've described so far, but it offers even more. This game has refinements that distinguish a truly great computer game from a good one. The propulsion formula used to control the behavior of your ship, for example, is Newtonian in nature, closely simulating the actual response you would expect from a real spaceship. Going too fast or too far? Turn your ship in the opposite direction and increase thrust just enough—remember, opposite thrusts cancel each other out—and your ship stops.

The rotation controls (the R and T keys) turn the ship in 45° increments, which is the best you can do with the limited TRS-80 graphics. As a last resort, hitting the space bar throws your ship into hyperspace. So if three large meteors and an enemy ship are converging on you from different directions, this action might save you. I say *might* because a hyperspace jump ends with your ship popping up anywhere on the screen. Since there are obstacles everywhere, you may find yourself in a worse position than when you started.

Game programs that cross my desk receive many a trial, but none is so grave or deadly as 12-year-old Richard's, my young neighbor and resident computergame buff. With his attention span of less than 5 minutes, he rips through normal TRS-80 games with uncanny speed. His response to Super Nova, however, was an enthusiastic "Excellent!" He stayed with it for 3 hours, until his mother appeared to drag him away for homework. There is no higher recommendation available.

In summation, Super Nova is fast, entertaining, and professional. It is well worth its \$14.95 price tag. I fully agree with Richard—Super Nova is excellent!■



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Tranquility Base

Robin Moore, Warner Hill Rd, RFD 5, Derry NH 03038

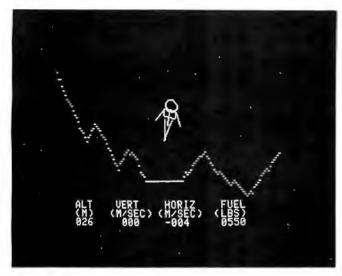


Photo 1: The Tranquility Base game in progress.



Bill Budge has written a lunar-lander-style arcade game for the Apple II. Called Tranquility Base, the game uses Apple high-resolution graphics to portray the lunar-lander module and the moonscape below. The player attempts to bring the lunar module out of orbit and land it safely on one of several flat areas on the lunar surface. A fixed amount of fuel is provided, and the score is based on the number and quality of successful landings.

Playing the Game

The game is simple, although not necessarily easy to play. A key is pressed to start the action, and the lunar-lander module appears, orbiting from left to right over a detailed moonscape. The rockets are controlled with the Apple II's game paddle 0, while the "1" and "2" keys on the keyboard adjust the rotational attitude of the lander. Each keypress rotates the ship slightly in one direction or the other. There are no steering rockets, so the lander's horizontal motion must be controlled by rotating the ship and using the main rockets.

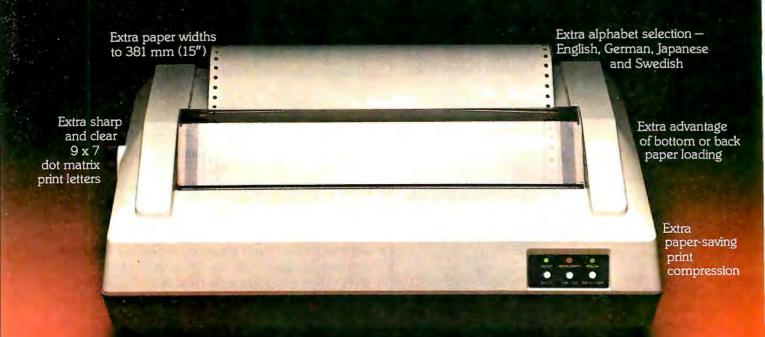
It is difficult to make a successful landing. The landing areas are never much larger than the width of the ship, and the rocket control is quite sensitive, so you might cause the ship to take off just as you are gently touching down. If the lunar module touches anything except a flat landing area, it crashes and explodes. Landing too quickly can also cause a crash and an explosion. The score for each successful landing is derived from the horizontal and vertical velocities of the ship when it touches down.

Graphics and Sound

Consistently excellent graphics are a hallmark of Bill Budge's games, and the Tranquility Base graphics are no exception. From the title display that shows the lunar module, moonscape, and starfield (with little apples as planets) to the final module explosion, the graphics are great. The lunar module is nicely detailed, and when it explodes, pieces fly off and tumble in various directions. Even the rocket flame is detailed: it flickers realistically and provides visual feedback by smoothly changing size as the rocket thrust is varied.

When the lunar module orbits off the right edge of the screen, a new section of scenery snaps into view below, and the lander orbits in from the left. Tranquility Base also provides a close-up view of the lander and the moon-scape when the lander is a certain distance from the ground: this will help you make a smooth landing. Fuel

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level, horizontal and vertical velocities, and altitude are displayed in text form below the graphics display. This aspect might have been improved by using analog displays simulated with graphics.

Most arcade-type games make extensive use of sound effects to enhance the realism of the simulation. Unfortunately. Tranquility Base takes little advantage of the Apple II's sound capabilities. Sound is used when the lander crashes and explodes, but it is not very realistic. I would have preferred some rocket-motor sounds varying with the thrust level, and perhaps a warning tone to indicate unsafe landing parameters.

At a Glance_

Name

Tranquility Base

Type

Arcade-style game

Manufacturer

Stoneware 50 Belvedere San Rafael CA 94901

Price \$24,95

Author Bill Budge

Format

51/4-inch floppy disk

Language

6502 machine language

Computer

Apple II or Apple II Plus with one disk and 32 K bytes of memory

Documentation

Instructions in game

Conclusions

- Tranquility Base is a medium-speed lunar-landerstyle arcade game with excellent graphics. Like most of Bill Budge's games, it is well done and functions flawless-
- The game is fairly difficult to play, enough so that it tends to discourage some new users. After a little practice, however, it becomes more enjoyable and exciting.
- •Whether or not Tranquility Base is worth \$25 depends on how much you enjoy the game and how often you play. I suggest that you try it out at a local computer store before you make a decision.

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Asteroids in Space and Planetoids

Oliver Holt Old Nashua Rd Amherst NH 03031

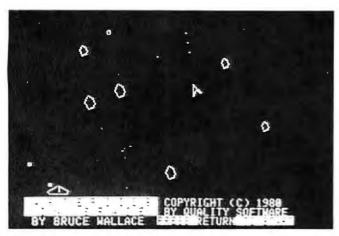


Photo 1: Asteroids in Space is the title of the Asteroids game for the Apple from Quality Software. It is similar to the actual arcade game; the spaceship is controlled via the game paddles.

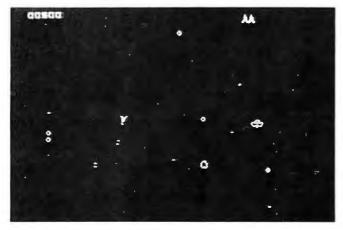


Photo 2: Planetoids is Adventure International's offering. The use of machine-language programming combined with highresolution graphics results in smooth action without a jittery picture.

Asteroids by Atari Inc is certainly one of the most popular arcade games in this country, inspiring people of all ages to deposit their quarters with devotion. Due to this popularity, it was only a matter of time before a home-computer version was developed. Asteroids in Space (by Quality Software, referred to as QS) and Planetoids (by Adventure International, or AI) both closely simulate the Atari game, in which a player must destroy asteroids and alien ships by accurately firing a laser. An off-target laser shot or slow response is fatal. The Apple's high-resolution graphics capabilities allowed the authors to reproduce almost exactly the display features of the original game. Both games skillfully employ realistic sound effects. The two versions use game paddles to control the motion of a spaceship and to fire lasers, but because of differences in the method of control used each game has a unique feel.

Planetoids

On start-up, Planetoids (from AI) displays a menu that includes several levels of play. This menu is part of a HELLO disk program written in both Integer and Applesoft BASIC, allowing use in either an Apple II or an Apple II Plus. The options in this menu give a choice of easy, regular, or hard modes of play, as well as a demo mode to display how the game works.

In the easy mode everything on the screen is very explosive. Every planetoid particle has the potential to destroy your spaceship unless your laser beam gets to the particle first. (Points are based on the number of planetoids you destroy.) The regular mode is supposed to be an emulation of the actual arcade game, but it does not appear to be significantly different from the easy mode. In the hard mode, the planetoids behave differently; they migrate toward your ship as if pulled by gravity. This characteristic becomes particularly annoying when one of your ships is destroyed and you still have other ships left to play. At this point, the planetoids gather around

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At a Glance_

Name

Planetoids

Type of package Arcade-style game

Manufacturer

Adventure International POB 3435 Longwood FL 32750

Price \$19.95

Format 51/4-inch floppy disk

Language

6502 machine language (has inenu programs in both Integer and Applesoft BASIC)

Computer needed

Apple II or Apple II Plus with 48 K bytes of memory and one floppy-disk

Documentation

One page with description of the game; additional instructions in the actual program.

the spot where your next ship will appear, making it difficult to escape without being destroyed. Sometimes your spaceship will reappear directly under a planetoid and explode before you even realize that your ship has (momentarily) returned. When this happens you have no choice but to sit there and watch your spaceships dwindle away with no hope of retaliation.

Planetoids uses one paddle and the keyboard to control the ship. You rotate the paddle to turn the spaceship and press the paddle button to apply thrust. The spaceship will continue to move in the direction it is pointed as long as the button is depressed, but it stops as soon as the button is released. Pressing any key on the keyboard fires a laser in the direction the ship is pointing. However, there is no provision for putting the ship into hyperspace, as in the original coin-operated version.

Asteroids in Space

Quality Software's Asteroids in Space has two choices on start-up, offering either a normal or demo game. When in demo mode, the spaceship randomly moves around in space shooting the laser beam in all directions until the ship itself is destroyed. Watching this can be useful if you have never played this kind of game before, but most users will want to go directly to the normal mode. This mode of play offers separate choices for either normal or fast lasers and asteroids. According to the documentation, higher scores may be obtained with either fast lasers or fast asteroids, or both. The game's difficulty increases, however,

Both game paddles are used to control the action in this version. One paddle controls the movement of the spaceship, rotating it by turning the paddle, and thrusting it by pushing the button. However, this game incorporates momentum into the action of the spaceship, requiring you to use the thrust to slow the ship or to change its direction of movement. [I have trouble playing this version because I spend all my time trying to stop my ship from moving....GW Unlike the AI game, your ship can move in one direction while it fires in another. Lasers are fired using the game button on the other paddle. This method of control is harder to mentally and physically coordinate, making the game more challenging and frustrating. This game, like Planetoids, does not have the hyperspace feature of the original Atari version.

Scoring for both games is determined by the number of alien spaceships and asteroids (or planetoids) you can destroy. The QS version awards from twenty to thirty points for larger asteroids, more for smaller ones. Alien spaceships are worth up to 300 points. The AI game allows only ten points for the planetoids and up to fifty for the alien ships.

The graphics in both games are very good, very similar to the original arcade game. All the objects move

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At a Glance -

Name

Asteroids in Space

Type of package Arcade-style game

Manufacturer

Quality Software 6660 Reseda Blvd Suite 105 Reseda CA 91335

Price \$19.95

Format 51/4-inch floppy disk

Language

6502 machine language

Computer needed Apple II or Apple II Plus with 48 K bytes of memory and one floppy-disk drive

Documentation

One page with description of the game; additional instructions in the actual program.

smoothly without the annoying "jumping" or jitter effect predominant in lower-resolution video games and some of the poorer high-resolution graphics games. Sound effects were also similar to the arcade game, but I felt the QS version to be more realistic and of higher quality. The AI sounds were barely audible over the pounding of the keyboard while I was firing at objects on the screen.

Conclusions

Having played both games, I feel it's difficult to choose between them. The QS version offers different speed variations, while the AI version offers three levels of play. I like the AI version better because it can be slightly easier to play and there are three distinct variations to the game. The more astute game player might prefer the greater physical dexterity and mind/eye coordination required by the QS version. However, the games are different enough to entice most people to own both.

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Programming Quickies

Using Page Two with Apple Pascal Turtle Graphics

Bruce Wallace, 333 Escuela Ave #316, Mountain View CA 94040

So, you have Pascal up on your Apple and you're ready to use the built-in turtle graphics. One of the first things you probably notice is that the Pascal manuals never mention which high-resolution graphics page you are working with. In fact, the manuals don't even mention that a second page exists. Well, it does. And, it turns out to be fairly simple to use the unit TURTLEGRAPH-ICS on either page. There are three things to be considered:

- 1. reserving the page two memory space
- 2. getting TURTLEGRAPHICS to plot on page two
- 3. getting the Apple to display page two

Before we get into graphics, we'll need a technique for PEEKing and POKEing. This can be done with the help of the following declarations:

```
TYPE byte = 0..255;
      pab = PACKED ARRAY[0.. 1] OF byte;
      multitype = RECORD
               CASE integer OF
                  1 : (int:integer);
                  2 : (ptr:fpab);
                  3 : (dptr:linteger)
               END:
```

A variable declared to be of type "multitype" can be referred to as either an integer or a pointer variable. This leads to the following definitions:

```
PROCEDURE poke(addr:integer; value:byte);
VAR local:multitype;
BEGIN
   local.int := addr;
   local.ptrf[0]:= value
END;
FUNCTION peek(addr:integer):byte;
VAR local:multitype;
REGIN
   local.int : = addr;
   peek := local.ptrf[0]
```

Now that we can access memory directly, we need to reserve the memory space for high-resolution page two;

otherwise. Pascal might try and use it for stack or heap space. The UCSD extension routine RELEASE will do the trick for us. Assume that "save" is declared to be of type "multitype." The code segment:

```
save.int := 24576:
release(save.dptr);
```

will reserve all of low memory up to address hexadecimal 6000 (24 K). This is done once at the beginning of your program.

Next, inform TURTLEGRAPHICS which page it is to use. Do this by placing a 2 or a non-2 value into a particular memory location for page-two or page-one plotting, respectively. A pointer to this location resides as the eighth entry in a pointer table. The table itself is pointed to by the contents of absolute locations 254 and 255 decimal. This leads to the following routine, which sets the page to be plotted on:

```
PROCEDURE setdraw(page1:boolean);
VAR local;multitype;
   local.int := 254;
   local.int := local.dptrl + 14;
   IF page1 THEN local.dptr1 := 1 ELSE
       local.dptrt := 2
END:
```

Finally, we must be able to switch the page that Apple is displaying. After we are in the high-resolution mode via a call to GRAFMODE, we simply PEEK or POKE as we would in BASIC. Using the above PEEK or POKE routines, access -16299 or -16300 for page two or page one, respectively.

In general, INITTURTLE only works with page one, and, in fact, it even resets the display mode to page one. Use FILLSCREEN to clear page two. Also, the turtle position is not moved when changing the high-resolution page via "setdraw" above. For example, if you left off plotting at x,y position 50,50 with an angle of 45°, that's where you will start plotting on the other page.

Armed with these handy code segments, you can now get smooth animation by flipping from page to page. This should open up new possibilities for Apple Pascal graphics users.

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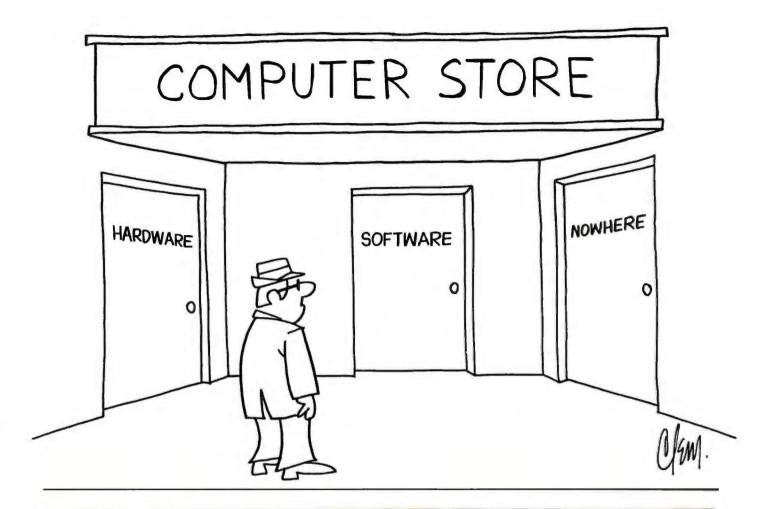
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Washington Tackles the Software Problem

Christopher Kern 201 I St SW, Apt 839 Washington DC 20024

There was a time when a personal computer was nothing more than a microprocessor, some support circuits, a couple of thousand bytes of memory, and a few light-emitting diodes. In those bygone days, "software" consisted of a painstakingly crafted 1280-byte nano-BASIC interpreter, which was stored as perforations in a long, thin strand of paper and loaded into the machine by a device known, quaintly, as a papertape reader.

Today, all you have to do to get your new 16-bit, 8 MHz, 12 M byte, 512-by-512 pixel, hand-held color widget going is to break the cellophane. And as long as you haven't managed to clobber the widget's sophisticated mega-tasking, ultra-user operating system, or the various editors, high-level language compilers and interpreters, and powerful application programs that come as standard equipment, you are up and running.

All that fancy software is as much a part of the widget as the hardware that it runs on, and the attempt by the Widgetizer Corporation and others like it to protect their investment in

software development is the reason why the courts and Congress now find themselves confronted with the "software problem."

The Software Problem

The software problem actually existed before the advent of the microcomputer, but spectacular improvements in microcomputer hardware have increased the demand for sophisticated software. At the same time, reduced production costs for hardware have radically enlarged the computer market, making it increasingly difficult to control software piracy.

Most microcomputer products are based on one of a relatively small set of microprocessors, so it is technically as well as economically practical to copy software, moving it from one hardware environment to another. Within the hobby market, this typically takes the form of one hobbyist copying commercial programs for a few friends. At the least, this is probably a violation of the purchaser's contractual obligation to the software vendor: it is certainly the moral equivalent of larceny. But although this practice is obviously a serious matter for those who sell software to the home market, its relative economic significance is fairly small. The real problem is the commerical duplication-often entirely legal-of

software and software-based products for commercial purposes.

The Copyright Problem

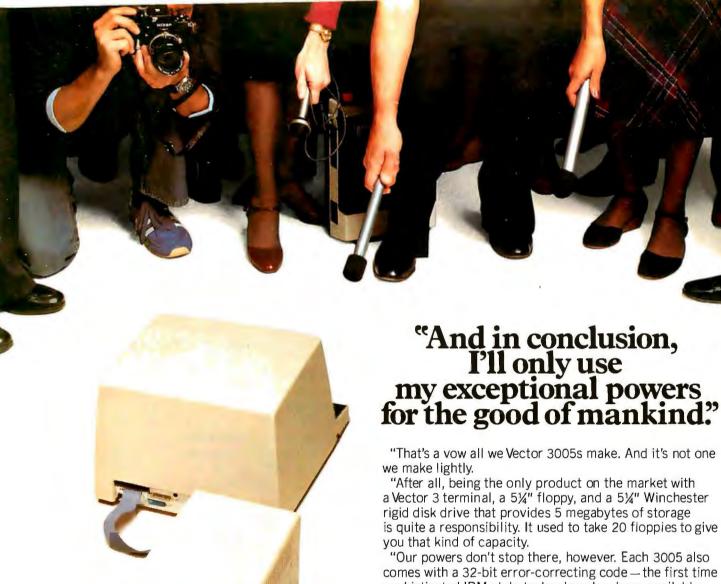
When Congress overhauled the nation's copyright laws in 1976, it sidestepped the software problem by failing to specify the extent to which computer programs were eligible for copyright protection. A source listing clearly could be protected by copyright; a listing of a program is, after all, just a text. But what about the program as it appears in other forms? It was not clear whether object code, stored as a series of binary electronic impulses in memory or as magnetic fields on a mass storage device, was also subject to the creator's copyright.

One notorious illustration of the problem involved a microcomputer chess game sold by a Florida company called Data Cash Systems. The Data Cash game appeared on the market in 1977 and sold for \$169. A year later, JS & A Group Inc of Chicago introduced a competitive chess game for \$99. The program it used was identical to the one used in the Data Cash machine.

Although the two programs were unquestionably the same, Data Cash lost its copyright infringement suit on the grounds that the law, as it then existed, did not protect software in object-code form. The trial court rul-

About the Author

Christopher Kern is a lawyer by training, a journalist by trade, and a computer programmer just for the fun of it.



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Supreme Court Takes a Softer Look at Software

"A claim drawn to subject matter otherwise statutory does not become nonstatutory simply because it uses a mathematical formula, computer program, or digital computer." Justice William Rehnquist, Majority Decision, Diamond v. Diehr, March 1981

With this somewhat cryptic remark, the Supreme Court has, in the words of software and patent expert Morton C Jacobs, "removed the shackles from the software innovator," The Diamond v. Diehr decision (described in the accompanying article) was the culmination of years of court cases involving the patentability of software.

The key word in the above quote is "statutory." According to patent law, an invention is statutory if it is a "process," "machine," "article of manufacture," or "composition of matter." All other inventions are said to be nonstatutory. For example, computer programs or mathematical algorithms are currently considered to be nonstatutory by the court. In the Diehr case, the Supreme Court decided for the first time that an invention does not become ineligible for a patent simply because of the presence of a

computer program in the invention. However, an invention must still fall in a statutory category and must pass the traditional tests for merit: it must be "novel." "useful." and "unobvious."

The court has yet to take the final step and say that software is patentable, but this important decision points in that direction.

Jacobs feels that now small businesses can afford to once again become innovators in the software field, Small-business entrepreneurs need patent protection to raise venture capital to bring their ideas to fruition.

Ruth M Davis, former director of the Center for Computer Services and Technology of the National Bureau of Standards, agrees that "there is a small-business potential to innovate in the software field...the patent system is important in stimulating [this] technological innovation."

The closeness of the 5 to 4 decision in the Diehr case has led some observers to conclude that the court is evenly divided on the software issue, but Jacobs is quick to point out that the court is becoming progressively more and more "pro-software" in its recent decisions. Further, the Supreme Court has had the benefit of advice and testimony from computer experts over the years, and the growing sophistication of its decisions reflects this.

Of course, the answers aren't all in yet. For example, what if an enterprising inventor puts a new program in a computer so that he can claim the novelty of the entire machine? This effectively preempts the algorithmic content of the program. The courts have balked at this approach in the past. Even so, the day may soon come when a program residing on a floppy disk will be granted a patent...CM

ing was affirmed by the US Court of Appeals for the Seventh Circuit, and precipitated considerable concern within the data-processing industry. It appeared that in the future, the only realistic defense against software piracy would be strict enforcement of licensing agreements. But a licensing agreement binds only those who are party to it. It has no legal effect on a pirate who obtains the software without signing an agreement.

The copyright problem was resolved by the Computer Software Copyright Act of 1980, which was passed in the waning days of the 96th Congress and signed by President Carter just before he left office. The Act amends the 1976 copyright stat-

ute by defining a computer program as "a set of statements or instructions to be used directly or indirectly in a computer to bring about a certain result." The word "directly" refers, of course, to the object code. But while the new copyright law protects both the source statements and the sequence of machine instructions in the program, it does not protect the underlying logic of the program—the operations that the software is designed to perform.

The Patent Problem

The most effective way to prevent unauthorized use of computer programs would be to patent them. A patent would protect the process that

a program carries out, regardless of its specific form, True, the duration of a patent is short (17 years), but in a rapidly changing industry that disadvantage is only theoretical; for practical purposes, the protection afforded by a patent borders on the absolute.

Several attempts have been made to get the Supreme Court to recognize the patentability of computer software, In Gottschalk v. Benson (1972). the Court unanimously rejected a patent claim for an algorithm that converted numerical data in binarycoded-decimal form to pure binary. In his opinion for the Court, Justice William O Douglas started with the long-established proposition that "an

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idea of itself is not patentable," and concluded that granting a patent for the BCD-to-binary algorithm would amount to giving the applicant exclusive ownership of a mathematical abstraction.

At the same time, Douglas disclaimed any intention of foreclosing patent protection for computer programs altogether. He hinted that it would be best if Congress would resolve the issue of patentability of computer software. But his opinion suggested that until Congress acted, the Court would avoid any sweeping

The protection afforded by a patent borders on the absolute.

ruling on the patent law and allow its interpretation to evolve on a case-bycase basis.

The Flook Decision

A few years later, in Parker v. Flook (1978), the Supreme Court ad-

dressed an attempt to circumvent its ruling that an algorithm could not be patented. The case involved an application for a method of determining when a catalytic conversion process had exceeded certain predefined parameters. A computer program calculated alarm limits, which indicated when an inefficient or dangerous condition existed. While the applicant admitted that an algorithm was crucial to the patent application, he argued that he had tied its use to a specific industrial process—the catalytic chemical conversion of hydrocarbons.

The Supreme Court rejected Flook's contention by a vote of 6 to 3, holding that the only novel part of the process was the algorithm embedded in the computer program. The algorithm itself, under Benson, was of course not patentable. In his opinion for the Court, Justice John Paul Stevens said that both the chemical and mechanical processes involved were well known, and concluded that the patent application "simply provides a new and presumably better method for calculating alarm limit values." For patent purposes, mathematical algorithms, like laws of nature, were to be treated as though they had previously been known, even though in fact they were newly discovered by the applicant. "Respondent's process is unpatentable," Justice Stevens wrote, "not because it contains a mathematical algorithm, but because once that algorithm is assumed to be within the prior art, the application, considered as a whole, contains no patentable invention."

A Recent Interpretation

Was the Flook decision a fluke? Recent cases suggest it may have been. In the case of Diamond v. Chakrabarty (1980), the Court considered a patent claim for a laboratory-created bacterium. Superficially, computer programs and man-made bacteria have little in common (program bugs belong to a different species). Yet computer software and genetic engineering are alike in two respects: (1) Congress was unaware of either one when it wrote the basic patent

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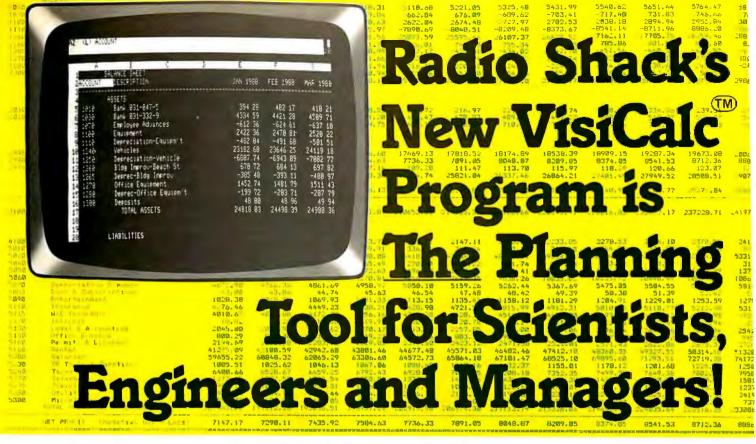
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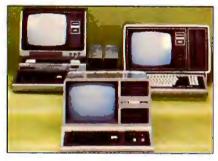


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law, which is only slightly changed from the language drafted by Thomas Jefferson in 1793, and (2) both programming and genetic engineering involve the manipulation of coded information which is stored (in one instance, in the electronic or magnetic memory of a computer and, in the other, in the molecular memory of a cell). But a 5 to 4 majority of the Supreme Court ruled in Chakrabarty that man-made microorganisms are indeed eligible for patents.

In March of this year, the Court cited its reasoning in Chakrabarty as justifying patent eligibility for a process involving a computer program. The case, Diamond v. Diehr, was also decided by a 5 to 4 vote. The Court ruled that a patent could be granted for a new method of curing synthetic rubber that was designed around a computer program. The program calculated the time required for the curing process by monitoring the temperature inside the curing furnace and continuously updating the time remaining. This allowed the program to stop the process the instant the rubber had been properly cured.

The Justice Department, which opposed the patent application, said that the facts of the Diehr case were indistinguishable from those of the Flook case. Both patent applications were for industrial processes that were new because of the way they used computer programs. But Justice William Rehnquist, speaking for the Court, said there was a vital difference between Diehr and Flook. In Flook, the algorithm used to calculate alarm limits for the catalytic conversion process was new, but the idea of calculating alarm limits was not. In Diehr, the entire process was new; the essence of the patent application was that no one had ever successfully monitored the temperature inside the furnace and then used a computer program to continuously calculate when to stop the curing process.

Prospects

At this point it is difficult to tell whether or not the Supreme Court is in the process of reversing direction on the issue of software patentability. The most that can be said with any assurance is that the narrow majorities that have decided the recent cases indicate a deep division in the Court. A stinging dissent in the *Diehr* case by Justice Stevens, who was the author of the Flook opinion and who opposes any extension of patent protection for software, makes it clear that the debate is a long way from being resolved.

The Court was expected to take the case law one step further in its current term. It had agreed to rule in the case of Diamond v. Bradley, which involved a patent application for readonly memory routines used in the central processor of a computer for machine control. The Court of Customs and Patent Appeals, which has tended to be well ahead of the Supreme Court in authorizing patent protection for computer programs, held that the application should be granted. The Patent Court ruling was affirmed, but only because Chief Justice Warren Burger removed himself from the case (as is customary, he gave no explanation for his decision not to participate), leaving the other members of the Court evenly divided.



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While such a split leaves the lower court ruling intact, it has no value as legal precedent.

What Does This Mean to Us?

For those of us with a recreational interest in the computer industry, there is little to lose and potentially something to gain from the change Congress has made in the copyright law and the possibility that the Supreme Court will increase the patent protection afforded computer software. True, now that object code is clearly subject to copyright, you will be breaking the law if you copy your commerical BASIC interpreter

Object code is now clearly subject to copyright laws.

for a friend. But the added protection provided by the new copyright amendments may encourage more software development, giving experimenters a wider selection of software products. It is even possible that vendors will begin to sell source code for microcomputer system programs (some even withhold information about useful program entry points)

because the code will be protected by copyright.

It is not clear to what extent the personal-computer market, a relatively small part of the overall microcomputer market, would be affected by a Supreme Court ruling that would enlarge the patent protection already granted to software-based industrial processes. But I suspect that any change in the patent laws that encourages innovation will increase the industry's interest in sources of innovation-that includes the tinkerers who develop potentially marketable software purely for their own amusement.

New Technology Clashes With Old Laws

Over the decades, different laws have been developed to protect different kinds of creative works. But computer software is not quite like anything that has preceded it. On the one hand, a software package may be thought of as a work of authorship. On the other hand, it is functionally mechanistic. Things are further complicated by the fact that it has become remarkably easy to copy large amounts of information quickly. Of course, the easier it is to reproduce a protected work, the harder it is to protect it.

The United States Constitution, in listing the powers of Congress, specifies that Congress shall have the power "to promote the progress of science and useful arts, by securing for limited times to authors and inventors the exclusive rights to their respective writings and discoveries" [Article I, Section 8]. Congress has exercised this power by enacting patent and copyright laws.

Patent law is set forth in Title 35 of the United States Code. It affords strong protection, for a period of 17 years, to demonstrably useful, novel, and nonobvious inventions. Whereas copyright is designed to protect the "expression" of an idea or process, a patent is designed to protect inventions, which are products or processes in themselves.

Although patents have been awarded to software, the rigid standards of novelty and nonobviousness have made application difficult.

Similar confusion has existed with regard to the applicability of copyright laws. The disagreement among those caught up in the necessity of applying old laws to new phenomena was brought into focus during the 1970s as Congress attempted to overhaul the 1909 copyright laws.

Concurrent with the activity in Congress, a commission was formed in 1975 to address the copyright problems of data processing. CONTU (the National Commission on New Technological Uses of Copyrighted Works) examined various existing laws that could, presumably, be modified to protect data bases and software. In 1978, CONTU issued its Final Report, a study that recommended appropr ate changes to the copyright law, based on the results of its research. (Final Report, stock number 030-020-00143-8, is available from the US Government Printing Office.)

Although a new Copyright Act was passed in the fall of 1976 (effective January 1, 1978), Congress decided that the implications of data processing and reproduction technology had to be further clarified before they could be properly reflected in the new law. Accordingly, a stop-gap paragraph was inserted which indicated that the old laws, though ambiguous, still pertained, Subsequent revision (most particularly the Computer Software Copyright Act of 1980) continues to provide inadequate protection.

An interesting historical parallel to the debate over software protection occurred in 1908, when the Supreme Court held that a piano roll was not a "copy" of music because it was not, for most purposes, h manly readable (White-Smith Music Publishing Co v. Apollo Co, 209 US 1). For similar reasons, it has been argued that a program in object code lacks communicative potential and might therefore be constitutionally uncopyrightable. But, as CONTU points out, copyright protection has been extended by the courts to such diverse works of authorship as freight tables, interest tables, and lists of similarly meaningless five-letter code "words." These works of authorship, like computer programs, are valued for their utility, rather than their artistic merit.

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Question: How can a hobbyist or small businessman, with limited resources, guarantee that the law will provide protection against such unfair competition?

Answer: There are no guarantees. Patents, copyrights, and trade secrets are the three basic forms of legal protection that are primarily applicable to computer-related innovations. Unfortunately, there is no single form of protection for all the different varieties of hardware and software that is entirely satisfactory to the small businessman. In fact, this also applies to large businesses with virtually unlimited resources.

About the Author

Stephen A Becker has a master of science degree in electrical engineering. He has been granted two patents for his work in electronic control systems while working as a research engineer. After obtaining a law degree in 1975, he entered the field of patent law. Attorney Becker specializes in the protection of intellectual property innovations, with particular emphasis on computers, and is a partner in the patent law firm of Lowe, King, Price & Becker.

The following discussion provides some general legal background on a very complex and growing subject. However, I encourage you to confer with a patent attorney (registered with the United States Patent and Trademark Office) who specializes in all forms of intellectual property protection, prior to entering the marketplace. Also remember that this discussion concerns US law only. If you have an international market, professional advice is even more essential.

Patents

Patents provide a formidable protection for innovations that meet the rather stringent legal requirements of patentability. The right to a patent is fragile and can be lost by certain avoidable acts, such as public disclosure or an offer for sale more than one year before the patent is applied for. A patent, once granted, gives the patent owner the exclusive right to make, use, or sell the patented innovation in this country for 17 years. The patent owner has the right to stop others from infringement and collect damages even if the infringer later developed the same invention independently. After the 17-year period has expired, the innovation is considered to be in the public domain and available to all without limitation,

In order to qualify for a patent, the invention must be new, useful, and unobvious in view of existing technology. In fact, before a patent is granted by the United States Patent and Trademark Office, a patent examiner conducts technological research to determine whether the invention is adequately different from the existing technology to merit an award of "Letters Patent." About one dozen patent examiners, who specialize in computer technology, work for The Patent and Trademark Office.

Unfortunately, the procedure of applying for a patent is very expensive. In most cases, a patent attorney or agent must be retained to prepare a patent application and to submit arguments in favor of patentability before the Patent and Trademark Office during the approximately 18-month period of examination. During this time no patent protection exists. Patent rights are created only when a patent is actually issued. Furthermore, there is no guarantee that you will receive a patent. The Patent and Trademark Office may rule that the invention does not qualify for patent protection. They may do this for one of two reasons: because the invention is not the type that patents are designed to protect (eg: mathematical algorithms) or because the invention is simply too close to existing technology to be considered "unobvious."

It is definitely possible to obtain a patent on hardware innovations, such as peripherals, interface circuitry, or construction techniques. There is considerable uncertainty, however, concerning what types of computer software, if any, can be protected by a patent, In 1972 and 1978, Supreme Court litigation between patent applicants and the Patent and Trademark Office resulted in denials of patent protection on programs that



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are essentially mathematical algorithms, such as numerical conversion.

But in more recent cases (in 1980) and 1981) the Supreme Court begged the question of whether or not other types of software may be patentable. The Court of Customs and Patent Appeals (CCPA), which reviews Patent and Trademark Office decisions and is highly regarded for its competence in patent matters, has held that certain other types of software may be patentable. Issuance of patents has been denied by the CCPA only on software that is essentially algorithmic in nature. Thus, it is still unclear what types of software will ultimately be considered patentable if and when that broad issue is considered by the Supreme Court.

On the other hand, the courts have held that inventions are not unpatentable merely because they involve programming. For example, consider a microprocessor-based system that is programmed to operate with an array of sensors to monitor a physical parameter in a unique way and to process sensor-generated data in accordance with a stored program, generating machine-control signals. This system is patentable if it satisfies the three basic criteria of novelty, usefulness, and non-obviousness. Thus, patent protection is available to computer-related innovations involving programming so long as the invention is in the overall system and not solely in the program.

Because the costs involved in obtaining patent protection are high and the law of software protection is still uncertain, I do not recommend patents as an avenue of protection of programming by the personal computer experimenter or small businessman. However, if the invention involves more than just programming (eg: a complete system involving programming, or a new piece of hardware) and there is a significant commercial potential associated with the invention, then Letters Patent should be considered to increase the likelihood of success in the commercial environment.

Copyrights

A copyright is essentially the right of an author to control the copying of his or her work by others. It is applicable to computer software but not hardware. A copyright is easy and inexpensive to obtain. It must include the following comment at the start of the program:

< name of copyright owner> <date of first publication>,

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In order to perfect the copyright, as is necessary before a copyright infringer can be sued, the copyright must be registered with the Copyright Office by filling out a FORM TX. (The address is: United States Copyright Office, Library of Congress, Washington DC 20559.) After you fill it out, mail it with two copies of the program as originally published (or publically disseminated) and a \$10 registration fee.

If the program is on magnetic tape

or other non-readable form, a printout must also be deposited. Even if you do not register the copyright, you are required to deposit copies with the Copyright Office within three months of the date of first publication of the program with the copyright notice.

As a practical matter, however, there is no penalty for non-deposit in the absence of registration, unless the Copyright Office specifically demands a deposit. Details on software registration can be obtained directly from the United States Copyright Office or from an attorney specializing in intellectual property law.

The term of a copyright extends throughout the lifetime of the author plus 50 years. In the case of a work made for hire, the term is the earlier of two periods: 75 years from the year that the work (ie: program) was published, or 100 years from the year that the program was written.

Although the cost and effort of obtaining a copyright on software are minimal, and although there is virtually no time delay or uncertainty (as in patents), a copyright offers substantially less protection than a patent. First, the copyright covers the "expression" (ie: program listing) of software but not the idea, procedure, or concept underlying the software. A competitor could, for example, use the copyright owner's basic procedure or method of solution without infringing the copyright if a different but equivalent program is developed. Also, the copyright owner is provided no protection against competitors

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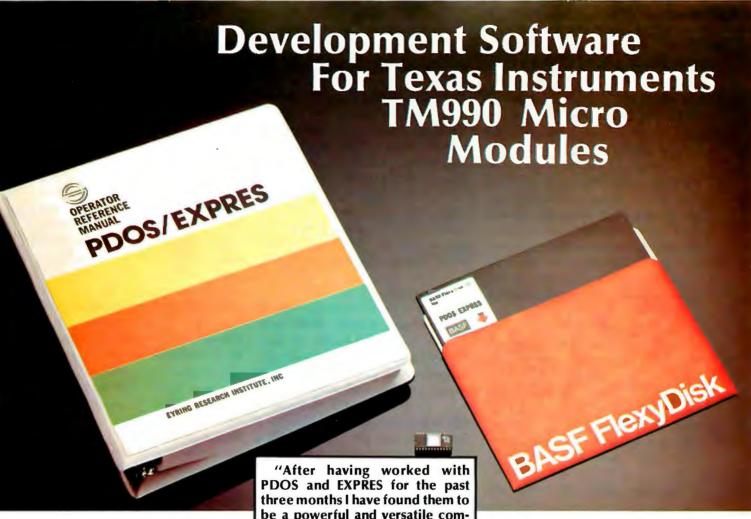
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who independently develop the same program; a copyright offers protection only against actual copying.

This may be enough protection for many computer programs. But the form of expression of a program is often critical and modification of that expression often destroys or substantially reduces its utility. I recommend that programmers routinely include the copyright notice in a comment statement at the start of each program prior to distribution, and postpone registration of the copyright until a lawsuit for copyright infringement is contemplated.

A word of caution concerning copyrights: there is presently some uncertainty whether, and to what extent, computer programming is a proper subject for copyright protection. An early attitude was that programs could not receive copyright protection because they are part of a machine rather than a literary work. Present sentiments, however, are that at least the "expression" of the program should be protectable by copy-

right. This issue may soon be settled because Congress is expected to consider subcommittee recommendations to amend the Copyright Act.

(Editor's Note: Source listings are unequivocably covered by copyright laws, but the extent of copyright protection as it is applied to programs in other forms is less clear. For further explanation, and a discussion of Supreme Court rulings regarding software patents, see "Washington Tackles the Software Problem," page 128.)

Trade Secrets

A trade secret is commonly defined as a formula, process, mechanism, compound, or compilation of data, not patented, but known only to certain individuals using it in business to obtain a commercial advantage. In order for there to be a trade secret that will be enforced by the courts, a secret must exist and there must be a duty on the part of all persons who learn the secret not to disclose it. Confidential relationships are generally established between employers

and employees or between businesses cooperating in a technical development by a type of contract known as a confidential disclosure agreement. For example, if you, a small businessman, wish to submit your unpatented innovation to a corporation for evaluation you may request that a corporate officer sign a confidential disclosure agreement. Such an agreement states that the corporation agrees to use your disclosure only for the purpose of evaluation and to disclose it outside the company only with your express written approval. The agreement will require the company to bind all its employees to confidentiality. However, the agreement must not be too restrictive to prevent the company from properly evaluating your innovation. Some companies may not be willing to sign a confidential disclosure agreement and, in fact, may even require you to agree to non-confidentiality before they will review an outside innovation.

A trade secret automatically exists between a patent applicant and the Patent and Trademark Office during the period of examination of the patent application. The Patent and Trademark Office is required by law to maintain the application in secrecy.

The Coca-Cola formula is an example of a successful trade secret which has never been patented and is known only to some internal personnel. For a trade secret to exist the subject matter must, in fact, be maintained in secrecy. But trade secrets are easy to lose. Once the secret becomes public, for example, legal protection is lost. It may become public through your own carelessness or through commonplace and legal competitive means, such as reverse engineering. A trade secret is not lost, however, if a competitor obtains the secret by unfair means, such as industrial espionage. The courts are filled with lawsuits involving piracy of trade secrets-including cases that involve theft of software and data by such means as tapping communication lines.

One advantage of trade secrets, in contrast with either patents or copyrights, is that the trade secret exists as

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long as the secret is maintained; it does not expire after a predetermined finite lifetime. There are no formal procedures, applications to fill out, or government fees to pay to establish a trade secret. Furthermore, there are no delays because the trade secret becomes enforceable as soon as it exists.

Unless you are in a position to maintain your software in secrecy and to bind all parties involved in confidentiality by contract, a trade secret is apt to be lost through inadvertence or by acceptable competitive efforts. For example, in the absence of restrictive licensing, there are no legal means to prevent a competitor from purchasing your software for the purpose of reproducing it for sale to his own customers. Of course, if the printout carries the copyright notice and the program is copied by the competitor verbatim, you will have a claim for copyright infringement following registration of your copyright with the Copyright Office,

Trade secret protection is at best very risky and can be lost for any number of reasons both inside and outside your control. In addition, there is some conflict between copyright law and trade-secret law since copyright protection is based upon publication, whereas trade-secret protection prohibits publication. Therefore, care must be taken to indicate that there is no presumption of publication of programs carrying the copyright notice that are distributed under restrictive licenses or confidential disclosure agreements. Even then, once the program is deposited with the Copyright Office, trade-secret protection may be lost.

Protection

The type or types of protection that should be considered for programs and computer-related developments depend upon several factors. These are:

 the nature of the development, that is, whether it is basically a mathematical algorithm of some other type of program or computer-based system merely involving programming

- the commercial importance of the invention
- the commercial lifetime of the invention
- the importance of exclusivity in the marketplace

Patent protection should be considered for hardware, or for computer-based systems, when the novelty involves more than merely the programming, if there is significant commercial potential and there is a commercial lifetime of at least several years.

Software should bear the copyright notice, despite uncertainties in the law, and I even recommend applying the copyright notice to printed-circuit boards to protect direct copying of circuit layouts. Trade secrets should be relied upon only when you are in a position to actually maintain your software or hardware systems in secrecy and bind your employees to secrecy and customers by contract; this is generally not practical where public sales are made. An old practice for maintaining circuitry in secrecy has been to embed the circuitry in epoxy, to prevent reverse engineering by inspection. It may even be necessary to embed small metal particles in the epoxy to prevent inspection by X-ray photography. Obviously, this approach is impractical for the small businessman working in the public market.

Whenever possible, software should be sold under restrictive licenses between you and your customers. Under the license terms, the software remains your property, while the customer is permitted to use it but not reproduce the program for use by others. A patent attorney will be able to draft a restrictive license to meet your particular requirements.

Most patent attorneys are also engineers who specialize in all areas of intellectual property, such as patents, trademarks, copyrights, and trade secrets; they are in a position to develop a portfolio of intellectual property protection suitable to your particular needs. I strongly recommend that you consult one before you attempt to market any product.

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GENERAL INSTRUMENT

Software Review

Dancing Demon from Radio Shack

Elizabeth Cooper and Yvon Kolya POB 22 Peterborough NH 03458

Radio Shack's latest addition to its games line is a fantastic graphics and sound game called Dancing Demon. The author of this well-designed gem is Leo Christopherson—the creator of Snake Eggs and Bee Wary, those wonderfully graphic but nonsensical games.

Dancing Demon is a fairly sophisticated music-generating program which uses carefully synchronized moving graphics and impressive sound.

Written in BASIC, the game places you in the role of agent/operator of

an ex-devil called the Dancing Demon. As his agent you must choreograph his dance steps to music you compose.

The documentation is careful to explain that the demon is rather dimwitted and understands only a special code for the music and dance steps. This code assigns one note to each letter of the alphabet. Covering a full two octaves (25 notes total) the "A" key equals low C and the "Y" key is equal to high C. The "Z" key is reserved for rests between notes.

After selecting the demon's music, you are given the opportunity to choose his dance steps. (If you wish, you can select the dance steps first; the order is up to you.) The same simplistic approach is also used for this procedure. The letter "A" represents Step 1, the letter "B" represents Step 2, and so forth to the letter "Z," a total of 26 different steps.

The instructions are clear and to the point; at times, they are clearly geared towards young children.

The program is as easy to understand and the documentation is clearly written. After CLOADing it and typing RUN, you see the main program menu. The menu options are:

- 1. Compose your own music
- 2. Create your own dance routine
- 3. Make the demon perform the program in memory
- 4. Save your show to tape
- 5. Load a show from tape
- 6. Make the demon perform the first preset show
- 7. Make the demon perform the second preset show

The last two options are usually the first ones chosen. These two opening numbers give a good example of the capabilities of the demon and are quite entertaining.

Continuing up the menu in reverse order, you have the option to LOAD (from tape) a show previously composed, or to save to tape a show you have just perfected. Both of these options are arranged simply so children should experience little difficulty.

Option three lets you play the show currently in memory. You are asked two questions: The first question asks for a speed factor, which determines how fast the music plays, and how fast the demon executes the dance routine. Any number between 1 (super fast) and 255 (very slow) may be entered.

The second question asks how many performances of this routine you wish to see. Again, you may answer with a number between 1 and 255.

After you've answered the questions the screen displays the theater stage, the curtain rises, and the demon starts his performance.

Option two lets you program the dance steps to be used by the demon. The steps have enough variety to be entertaining and yet the differences are subtle enough so that any combination of steps will result in a credible dance routine. Since the steps are designated by letters of the alphabet, you can amuse yourself by typing in actual sentences and watching how these are translated into movements by the demon. You can even type in the words to the song you've just

At a Glance-

Name of software package Dancing Demon

Type of package Game

Manufacturer Radio-Shack 1600 Tandy Center Fort Worth TX 76102

Price \$9.95

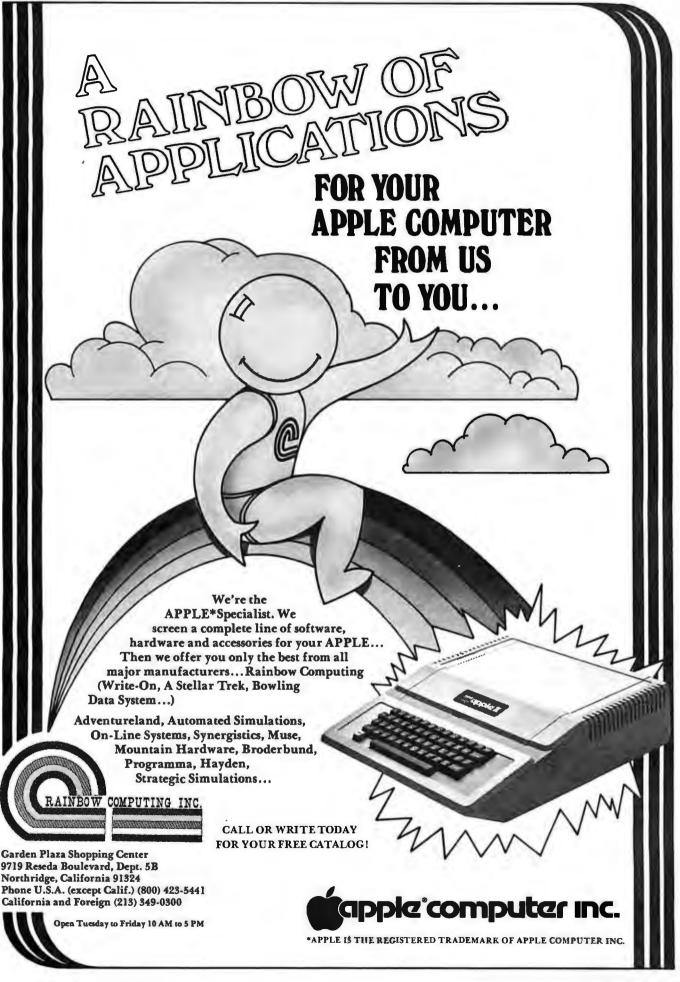
Format Cassette tape

Language used BASIC

Computer needed TRS-80, Level II BASIC, 16 K programmable memory

Documentation 13 pages, 8½ by 11 inches

Of interest to Children, parents and grown-ups who are kids at heart



entered into the music section of the program.

One very nice feature is the "preview." By pressing the space bar you can see the demon dance the routine as you have entered it so far. If you don't like it, you can easily change it. The only restriction is that you are limited to a maximum of 248 dance steps in the routine.

Once you're satisfied with the dance routine performed by the demon, you enter it into "permanent" memory by pressing the ENTER key.

This also returns you to the main menu. Finally, option number one lets you enter the music to which you want the demon to dance.

While the basic idea of the musical accompaniment seems quite simple, in actuality, it is considerably more difficult to create (or recreate) a musical melody than it is to design a workable dance routine. As with the dance steps, each note is designated by a letter of the alphabet. To include a rest, the "Z" key is used. What's confusing is the fact that there cannot

be a direct correspondence between the letters of the keyboard and the letters of the musical scale. This is because the sharps, flats, and octaves (ie: the notes low C, low C#, high C, etc) cannot all be matched to the keyboard letter "C"; instead, they are matched to the keyboard "A," "B," and "M" keys, respectively. Even for someone who already plays music of a more conventional sort, it's like learning an entirely new instrument. For those who read music, a chart matching the keyboard letters to their appropriate places on the musical staff might have been a very welcome addition to the documentation.

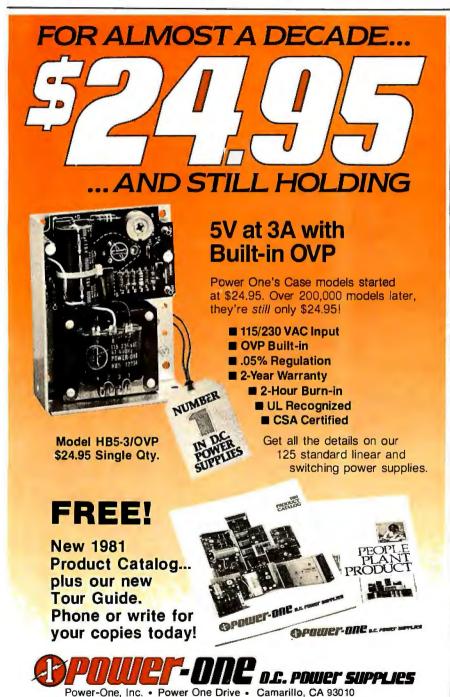
Then again, it might be easier to take the advice in the instructions and simply pick out tunes by ear. When you're programming music, each press of a key results in the appropriate note being played, and the appearance of that key's symbol on the sequence list.

To hear the sequence you've input so far, press the space bar. This is an excellent feature, since it is always encouraging to hear your progress up to this point, and it's easier to spot and correct mistakes. As in option two, when you're satisfied with the music sequence, press ENTER to have it added to memory, and to return to the main menu. You are limited to a sequence of 248 notes. There's no need to worry about having the same number of notes as you have dance steps. The music sequence repeats (if necessary) until all of the dance steps in the sequence have been executed.

Conclusions

Dancing Demon, Radio Shack's newest graphics and sound game, is an admirable addition to its game line. It combines an entertaining graphics routine with an equally amusing sound routine (including the clicks from the demon's tap-dance shoes). Because of the unusual combination of sophistication and simplicity, this game could be an excellent means of sparking and fostering the creativity of children.

The game sells for \$9.95 and, we feel, it should be purchased by anyone with children. We heartily recommend it.



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Wire-Wrapping and Proto-System Techniques

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The cost of microprocessor, memory, and peripheral devices has plummeted, while the details of computer circuit theory and design have become widely available. In combination, these conditions are enticing a greater number of hobbyists to build and experiment with computer circuits. However, the process of translating published circuits and personal circuit designs into functioning hardware can create unusual problems.

Whether you build a system from the ground up or expand an alreadyexisting system, your initial choice of wiring and prototyping techniques will have a substantial impact on both the effort required and the success of the project. Plugboard systems break a computer system into manageable and easily documented circuit blocks. For rapidity in wiring, assembling, and later modification of the project, wrapped-wire techniques best serve the computer hobbyist.

Wrapped-Wire Connection

A wrapped-wire connection is made up of six closely spaced turns of solid copper wire wrapped, under tension, around square, sharp-edged metal posts. Both the wire and wrappost edges become indented, forming a number of gas-tight contacts with a total resistance of less than three milliohms. An additional turn of the insulated wire at the start of the wrap process prevents wire breakage under conditions of extreme vibration, and also reduces the possibility of a short

circuit from the lowest turn of exposed wire to a nearby trace or ground plane on the circuit board.

The wrapped connection is made with a metal tube that has a central hole in one end for a wrap post and a smaller hole (alongside the first) that accepts a piece of wire. In conventional insulated wire wrapping, a piece of wire is cut to length and the ends are stripped of insulation. One end is inserted into the wire hole in the wrapping tool, and the tool is then placed over a wrap post. As the tool is rotated, wire is pulled from the hole at a 90 degree angle and wrapped around the post, creating enough drag and tension to make a good contact. This method requires a separate wire for every connection. It is also possible to connect a number of posts with a single unbroken strand of uninsulated wire- a process known as chaining. However, bare-wire chaining is suitable only for installation of ground buses or isolated jumper connections.

Fortunately, insulated wire chains can be made with special wrapping tools recently introduced by Vector Electronics.

Wire-Wrapping Tools

The Vector Electronics model P180 Slit-N-Wrap is a high-speed chainwrapping tool that eliminates wire cutting and stripping. A top-mounted wire spool holds 100 feet of #28 gauge nylon-polyurethane insulated wire (available in four colors). Wire exits the wire hole, and a sharp cutting edge slits the insulation to expose a portion of bare wire as you form the wrapped connection. The tool is supplied with two spools of wire and a P183 chisel knife and wire-forming tool, for routing wire and nipping off the beginning end-tail.

The nylon-polyurethane insulated wire resembles magnet wire, and it may be wrapped around an odd-sized terminal and soldered directly through the insulation. (However, you should exercise caution in avoiding the dragging or binding of wire against sharp wrap-post edges.) The thin but tough wire insulation barely increases wire diameter or stiffness, and as a result, the tool maneuvers smoothly on dense wirewrap boards.

A similar high-speed tool, the Vector model P184 Tefzel Slit-N-Wrap, chain-wraps #28 gauge Tefzel insulated wire. This tool is supplied with two 50-foot spools of wire in different colors. Tefzel insulation is relatively thick, allowing carefree wire wrapping and eliminating any chance of a short circuit, but the wire also handles somewhat more stiffly. Both Slit-N-Wrap tools must be rotated clockwise to slit the wire insulation, and both wrap their wire type conventionally.

The Vector P160-2A Dual-Way Wrap-N-Strap is a conventional tool that wraps #30, #28, and #26 gauge wire. Bare-wire chaining or strapping is possible by feeding wire down through the hollow handle. The

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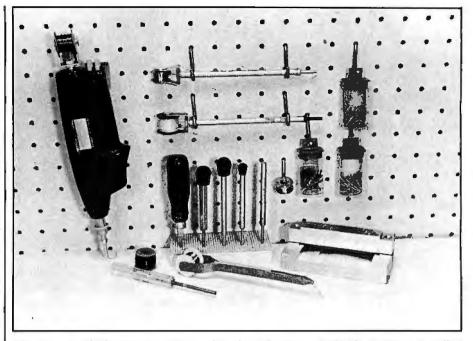


Photo 1: Available wire-wrapping tools include the Vector P180 Slit-N-Wrap, installed in a P160-4R cordless driver unit (left), the P160-2A-1 Dual-Way Wrap-N-Strap (top center), and the P184-Tefzel Slit-N-Wrap (below). The stand (center) displays five different pin-insertion tools. In the foreground (left to right) are the P160-1A Dual-Way unwrapping tool, P178-1 wiring pencil, and the P187 IDC fixture for assembling IDC ribbon cables.

P160-2A-1 wrapping tool is a similar instrument, but it has a top-mounted spool to hold the bare wire. Both tools offer a solution to the problem of inserting wire (especially the remaining end of a very short wire) into the wire hole. Each tool has a recessed tip with a cross-slot that allows wire insertion without up-ending the tool or fumbling about on the board. The Vector P160-1A Dual-Way unwrap tool has a retractable hood that catches the unravelled wire when you unwrap a connection.

Even chaining can become tedious if you wrap a large backplane or motherboard, but a powered wrapping tool can make this kind of operation less tiresome. Powered wrappers are versatile hand-held units that contain an electric motor and a hollow main spindle that accepts the handles of various manual Vector tools. These electrical tools can make a single wrap in seconds; chains can be wrapped as quickly as the tool is moved to the next wrap post. However, the powered wrappers are bulkier and less easy to handle when routing wire on a densely populated circuit board. The Vector model P160-4R wrapper (see photo 1) is

powered by rechargeable nicad batteries. The newer model P160-4R3 has a hand-fitting pistol grip. The P160-4T1, supplied with the P180 wrapping tool installed, is similar in design, but it operates off 110 V AC lines. The battery-operated P184-4T model, and the line-operated P184-4T1 Electro-Wrappers are supplied with the P184 Tefzel wire-wrapping tool installed.

Another recently developed wiring technique uses a wiring pencil. The pencil dispenses solder-thru insulated wire from a top-mounted wire spool. Instead of wrapping a connection, you simply loop several turns around a terminal and begin to solder. This technique permits assembly of lowprofile plugboards with low-profile solder-tail sockets. The Vector model P178-1 wiring pencil dispenses either #36 gauge or #32 gauge solder-thru wire and #30 bare tinned wire. The tool is supplied with one 400-foot bobbin of #36 gauge wire (available in three colors).

Wrap Posts and Accessories

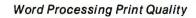
Vector can supply wrap posts in many styles, along with DIP (dual inline pin) wrap-post sockets of all

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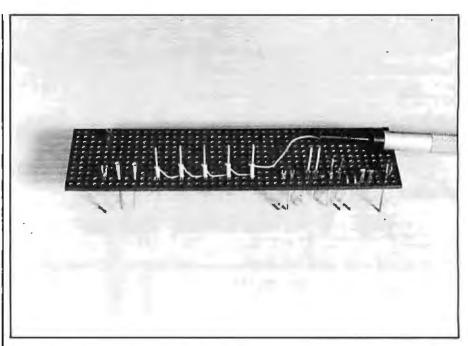


Photo 2: Rapid assembly of circuit boards demands insulated-wire strapping or chaining techniques, as demonstrated with the P184 Tefzel Slit-N-Wrap tool. The wide variety of board pins shown can handle any wiring situation.

sizes. At least four pin styles and several pin insertion tools will be needed to assemble a project. Wrap posts are 0.025 inches square (0.64 mm) and are push-fitted into 0.042 inch (1.07 mm) holes. The T-49 Klip Wrap post has a three-way fork (see photo 2) at one end for support of discrete components that may be snapped in place or soldered. You can install this pin with the Vector P156 insertion tool. For soldered installation of discrete components, the T-44 Miniwrap pin has a small slot at one end and is installed with the A13 hand tool. The K-32 J-pin passes through two holes and the short leg is bent to the board. Substitute DIP sockets can be made using these pins.

The Vector T46-5-9 pin is one of several pins that has a crossbar on the shank. The pins are installed with the aid of the P205 insertion tool, and crossbars are aligned to accept female IDC (insulation displacement connector) plugs of ribbon cables. The T46-4-9 pin is similar in design but single-ended, and it passes a card-finger pad or power plane to the other side of the board. Other single-ended board-feed-thru pins include the T46-4 and T51 pins. Typical of a family of pins having no crossbar, the T46-3 double-ended pin is inserted

with the P133A insertion tool. Use these pins when the laterally extending crossbar pins create a problem. To assemble sockets for small transistors or integrated circuits, you can use the R31 and R32 socket pins. Use the Vector MB45-20 perforated alignment block to back up the board and assure perpendicular installation of board pins. Photo 2 shows useful pin styles and a sample Tefzel-wire chained connection.

Although the use of Slit-N-Wrap chaining tools reduces time spent forming the wrapped connections, it can be tedious to wire-wrap a circuit that includes hundreds of connections. Much of the time is spent referring to the schematic and plugboard diagrams, locating the pins on the circuit board, forming and routing wires, and correcting wiring errors. A particular circuit board may have markings (eg: socket pin numbers) that can be helpful in wrapping your circuit, but these marks are quickly obscured on a crowded board with hundreds of closely spaced wrap posts. Correcting wiring errors can be time consuming, as the wire in question is often buried under several layers of wires. Make sure that you are properly oriented when you make the connections: it will reduce the

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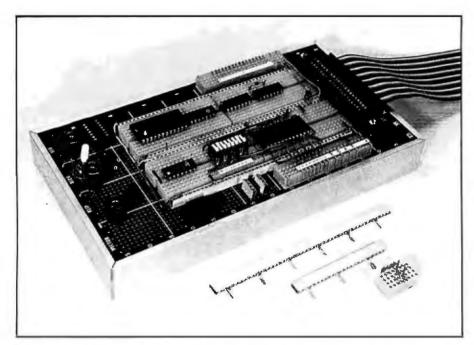


Photo 3: A DIP (dual in-line package) patchboard or breadboard, such as the Vector 51X patchboard, is indispensable to circuit development. This patchboard is top and bottom wirable and can be easily linked to a computer with an IDC ribbon cable.

amount of time devoted to the wiring operation.

To install a chained wire-wrap run correctly, push short lengths of insulation over each post as you identify it, then select the best route for the run. You should begin at the end that allows easy removal of the first wire anchor with a chisel knife. Remove the markers as you proceed, taking care to insert the tool on the marked pin. Check the completed wire run for errors before you pro-

Avoid taut wire runs that can result in wire breakage or bent wrap posts. When removing the tool from a wrap post, use the tip of the wrapping tool or the wire-forming tool to mold the wire to the board. An excellent wireforming tool can be made from the wooden handle of an artist's paint brush. Sharpen one end in a pencil sharpener and fashion a screwdriver blade at the other end. Use both the wrapping and the wire-forming tools as you form and route wire to the next wrap post. To reduce crosstalk, avoid bundling wire runs, and approach or pass the wire between socket pins perpendicular to the plane of the pin rows. To begin the next wrap, use the forming tool to press the wire to the board: do this slowly,

using no down-pressure on the first turn. If you use the P180 wrapping tool, start the wrap slightly above an etched plane. Wire breakage rarely occurs, but it is usually the result of a sudden start on a taut wire.

Pencil Wiring

When you assemble a board that uses solder-tail (low-profile) DIP sockets, use the pencil wiring technique. After you chain-wrap the interconnections, solder the looped turns with a soldering pencil heated to a temperature of 750 degrees F. The heat melts the nylon-polyurethane insulation, which allows the solder to bond the connection. The Vector P178-1 wiring pencil is supplied with #36 gauge solder-thru wire, but spools of #32 gauge solder-thru wire and #30 gauge bare wire can also be used.

Orbit the tip of the wiring pencil around the terminal or socket pin, placing the loops of wire somewhat above the board surface. Due to the additional soldering time required to melt the wire insulation, you should use soldering heatsinks to protect delicate components. If this is not possible, tin a portion of the wire before you form the loops (this premelts the insulation). You can obtain

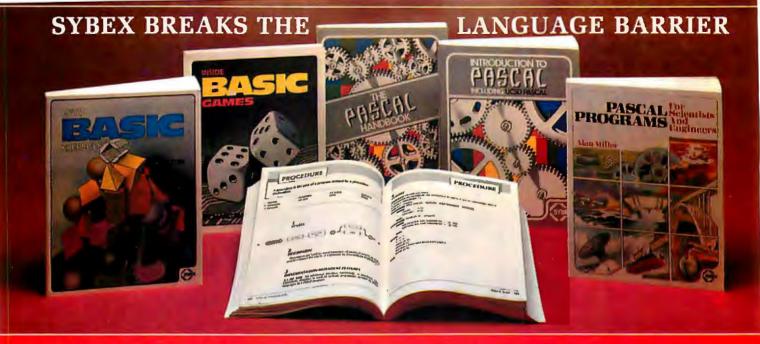
a satisfactory connection by solderwetting the loops on one side of the terminal or component post: this reduces soldering time.

You can use the Vector P179WS series of plastic wire spacers to route the wire neatly. The wire spacers are push-fitted into the board and have a number of wire-retaining slots topside. Low-impedance ground circuits may be obtained by running a second or third wire parallel to the first run, or you can pencil-wire the ground bus with Vector W30-4 #30 gauge tinned bare wire. Install discrete components on the T42-1 micro-clips or flea clips.

DIP Patchboard

The DIP patchboard or breadboard is a necessity for developing and verifying circuit designs. The breadboard includes strips and banks of tie points that accept DIP devices, jumper wires, and component leads. Photo 3 shows a Vector 51X DIP patchboard that, with the addition of an IDC 40-conductor ribbon cable, is modified to link up with a TRS-80 computer. Model 51X-GP is similar, but the supporting board has a ground plane. To make a large patchboard, you can install four 51X-GP-2 assemblies in the 43X-4 Multi-Conn chassis. A patchboard (including plugboards) can be assembled on any p-pattern board by inserting the large T66-96 Klip-Bloks, the T45-48 Klip-Bus, and similar components in any pattern. These unique systems can be wired from either side of the board. Wrap posts pass directly through the tie points to the other side.

A good ground system on the patchboard is imperative. Push long wrap posts through all device ground points and chain-wrap the pins on the bottom side to form a ground grid, Bypass the supply line with a 100 μ F electrolytic capacitor and a 1.0 µF tantalum capacitor, and bypass the supply pins of all monostables and flip-flops with a 0.1 µF disk capacitor to ground. One bypass capacitor for every pair of DIP packages should suffice for other devices. Use short jumper wires and keep the wires separated. You can measure the current drain of the patchboard with a meter, but be sure to short out or



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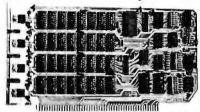
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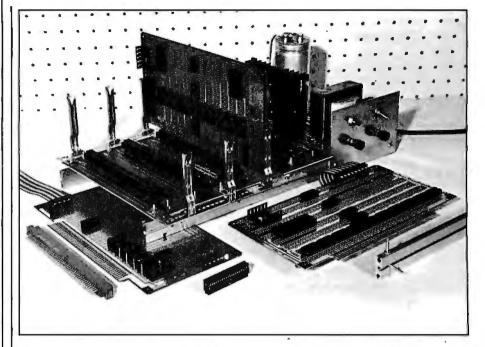


Photo 4: Low-cost open-frame S-100 bus mainframe uses a Vector 8803 motherboard and T169 T-struts, S-100 prototyping boards include the model 8800V in the mainframe, an 8804 Any-Dip board (right), and the 8802 pad board (left). Shown in the foreground (left to right) are the R681-2 plugboard receptacle, K52-40 female IDC connector, and T169 T-strut. The power supply (rear) bolts to T-struts supporting the S-100 motherboard.

remove the meter when you run operating tests.

Plugboard Proto Systems

Plugboard systems for the standalone microcomputer or for expansion of an existing system are easily assembled at low cost using Vector card-cage components. You can then add card receptacles to these openframe systems when needed.

An inexpensive S-100-bus system can be built using the components shown in photo 4, based on the Vector 8803 motherboard. The board accepts eleven Vector RS681-2 card receptacles that are easily soldered to the hot-tinned solder-masked board. A portion of the board includes printed-circuit traces for installation of either active or passive bus terminations.

Install the S-100 motherboard on a pair of Vector T169 T-struts (see photo 4) using the insulating spacers that are supplied, and secure it with SC4-28 hex-head screws (these slide into the strut). The BR27D card guides are mounted on the motherboard, on a length of B63-240 punched mounting plates. There is

ample room to the rear for installation of an S-100 mainframe power supply for the stand-alone system. The 8803 motherboard mounts directly on the T-struts of the Vector Pak VP1 and VP2 deluxe table-top microcomputer cabinets. These cabinets include card guides and a mounting plate for the power supply.

For prototyping or the assembly of system components, select from plugboards optimized for wire-wrapping or soldered-wiring techniques. The Vector model 8800V microprocessor board has a number of wide vertical bus bars on both sides that form the ground and supply planes. The connecting zig-zag buses between the bars accept board feed-thru pins. The supplied heatsink mounts on either end of the board which supports two on-card voltage regulators, one of which is prewired to the power plane. Device sockets are mounted vertically, in four rows and twelve columns, with labeled pin numbers. A connector for IDC ribbon cable may be installed at either end of the board. The Vector 8804 Any-Dip board (which is similar to the 8800V model in many respects) accepts

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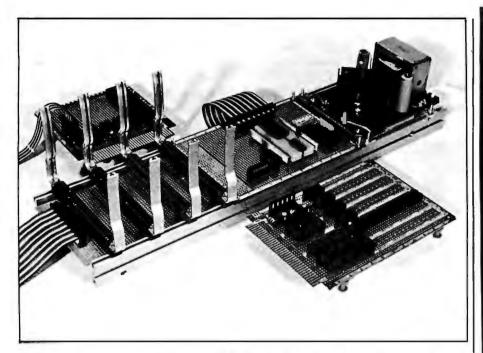


Photo 5: A system bus with fewer than one hundred lines can also be assembled using standard Vector components. The seventy-two-line combination system pictured here is a typical example. Primary components include the R636-1 receptacles, a 3677-7 clearance ground-plane board on the wire-wrapped backplane, and a Vector 8004 Circboard in the patchboard area. Plugboards include the model 4066-1 ground-plane board (top left) and the 4493 Any-Dip board with opposing power and ground planes. The system is powered by a Jameco model JE200 power supply.

sockets horizontally, in seven rows and ten columns, and its IDC cable connector resides anywhere along the top edge of the card. With sockets parallel to the card-finger array, this board allows easy wiring of card buffers and memory arrays.

You can choose from four S-100 plugboards that tend to favor pointto-point soldered wiring. The Vector 8801-1 plugboard has no circuit traces apart from card fingers. Sockets and connectors mount in any position, and you can use Vector T107 punched bus strips to assemble lowimpedance ground and supply buses. The double-sided 8801 plugboard has one tinned pad per hole that serves as a solderable anchor point for sockets, component wire leads, etc. The double-sided 8802-1 board is similar. but has two holes per pad and vertically mounted sockets. The Vector 8802 board also has two holes per pad, but the holes are plated through to the opposing pad. This unique board favors rapid and reliable anchoring of components, and with minimal risk of pad lifting.

You may find it advantageous to

use this prototyping system with a smaller user-defined system bus. Lines from the TRS-80 forty-line bus can be assigned so that you can place ground lines that alternate between signal lines, while retaining the same assignment for normal S-100 bus power-supply lines. Connect the ground on the plugboard, leaving the backplane unaltered. The resulting ground lines shield the signal lines. One prototyping sytem may then serve both the S-100 bus and the foreign bus if you are careful not to plug incompatible cards in simultaneously. The large S-100 boards generally provide more board space per dollar than small cards, but packing a number of smaller system modules on one S-100 card tends to complicate system documentation.

Plugboard systems with a user-defined system bus are easily assembled at low cost and in a manner similar to the assembly of the S-100 system. The system shown in photo 5 uses the R636-1 plugboard receptacle with seventy-two (36/72) contacts and mating BR27-1 card guides. Receptacle wrap posts pass

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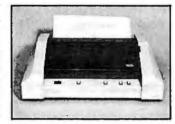
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through a length of 3677-7 clearance ground-plane board supporting pin rows so that you can plug in an IDC ribbon cable. To create a work area for a patchboard or other circuit, you can add a Vector 8004 Circboard with clearance ground plane, as shown, Alternatively, you can install the 8002 Circboard with interleaved buses for wire wrapping, the 8801 Circboard with buses and three-hole pads for any wiring method, or the 8803 pad-per-hole Circboard. A Jameco JE200 5 V, 1 A power supply fits the system neatly and powers the combination proto system. Plugboards that mate with this system include the Vector 4493 Any-Dip series and the 4066 series boards.

A system with a fifty-six-line bus can be assembled with the R656 plugboard receptacle and the Vector 4610 series plugboards. If you use the R644-3 receptacle with forty-four bus lines, you can choose from numerous plugboards in the Vector 4412, 3662, 3682, and 4494 board series. The 4609 plugboard can be adapted to the external bus system of the Apple II, PET, or Super-KIM machines, either as an open frame set-up or installed in a Vector card cage using the standard mounting hardware.

Give early consideration to the installation of ribbon cable links. IDC cables are readily available, and they come assembled in assorted lengths and a number of lines. You can also use Vector KS2-20 or KS2-40 female IDC plugs to assemble your own cables. The plugs mate with two rows of T49-5-2 wrap posts installed on p-pattern board. Use the P187 universal IDC fixture or its equivalent to press-fit the IDC connector to KW2-20-type twenty-line ribbon cable (use two lengths side by side on the KS2-40 connector). The IDC cable can be used for the links between the computer and proto-system, between plugboards, and to peripherals. You can also use the DIP-plug ribbon cable with male headers that fit standard DIP sockets of most sizes. It is best to use pre-assembled DIP cable. The Vector DIP interconnects are available in lengths of 12 inches (304 mm) and 24 inches (608 mm), and as single- or double-ended cables

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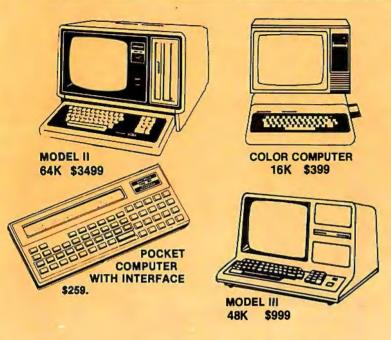
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With the aid of a short backplane and short connecting cable to the computer, the plugboard system can usually operate without bus line terminations. However, line terminations reduce line impedances, thereby reducing noise and crosstalk. The line termination consists of pull-up resistors that are placed at one end of the backplane and connected from each signal line to a noiseless regulated-voltage source of 2.6 V to 5.0 V.

The active line termination of the 8803 motherboard is made up of 270-ohm resistors connected to the 2.6 V source. On a pull-down to logic level 0 (approximately 0.4 V), the line termination current is (2.6 -0.4)/270 (approximately 8 mA), which can be easily handled by standard TTL devices. More than likely, the line drivers of your computer consist of 74LS devices which can drive (sink) 8 mA. This leaves no reserve drive for gates sensing the line, and for this reason you should push-fit the termination resistors on T49 Klip Wrap posts instead of soldering so that you can experiment with lower line-termination currents.

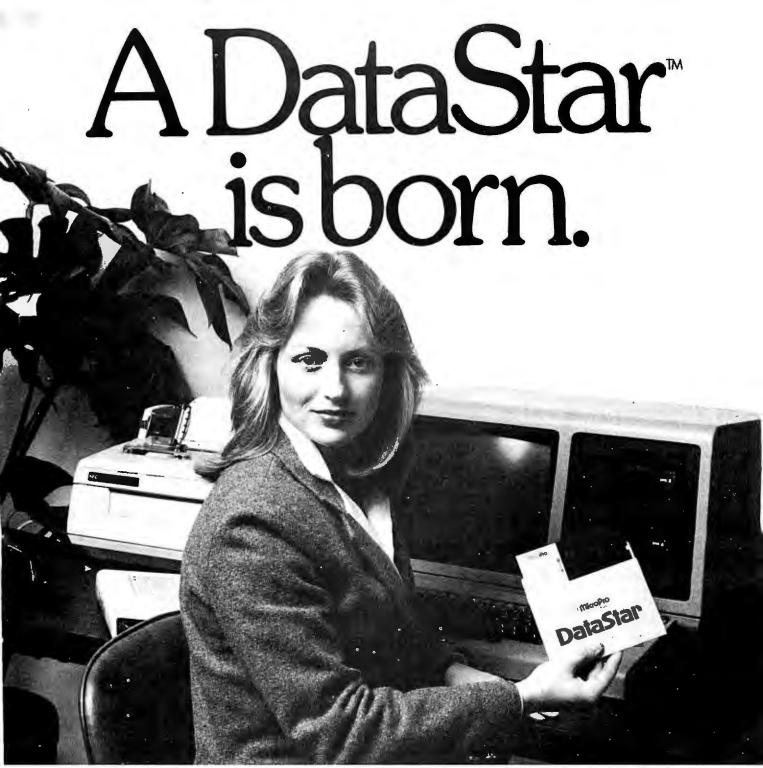
You can conserve supply current by using active line terminations. To obtain line-termination currents of approximately 4, 2, and 1 mA, use 560-ohm, 1100-ohm, and 2200-ohm resistors, respectively. For a smaller system, you can pull up the lines to the 5 V source and compute the termination current based on 5 V.

Plugboard Assembly and Test

Check for errors in the schematic diagram of the circuit, especially in the labeling of device-pin numbers. A pair of diagram sheets are supplied with the Vector plugboards so that you can determine the component and wiring placement for both sides of the board before you begin actual construction. Both sheets should be thoroughly labeled, especially with regard to each of the card fingers connected to the system bus. Observe how the data and address lines are

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	600 1 80 3 60 15 00 600 2 00 2 75 4.30	74LS27 — 45 74LS190 — 1.25
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grouped together in certain areas-it can help you determine the optimum placement of the associated integrated circuits. Use the plugboard itself for the preliminary layout of sockets and other components. Determine the locations of board feed-thru pins and all discrete components: don't wait until after you have begun to wire the board. It's a good idea to include extra ground feed-thru pins and to leave one socket position open near the card fingers for future additions. Draw the socket outlines on both layout sheets, show the positions of feed-thru pins and discrete components, and label them accordingly. Check any prewired card finger or voltage regulator position and make any changes by cutting traces.

Install all board pins, but omit the sockets so that you can use the board backup block. Insert T46-2-9 doubleended wrap posts in all card fingers, driving them in from the copper side of the pad hole. Though pins make excellent electrical contact with the pads, the connection can become erratic if you loosen or rock the pins excessively. Check for continuity with the ohmmeter, and solder if necessary. Many of the wire-wrapstyle plugboards are designed to accept the disk bypass capacitor by direct soldering to the etched planes. Install and solder the capacitors before you install the sockets.

Secure the sockets to the board using 5-minute epoxy cement. Press an index card against the tips of the wrap posts associated with the card fingers on the wiring side of the board. Mark and label the impressions with bus assignments, for reference when wiring. Label an unmarked socket position using MS10A pin-marking strips. Begin by chainwrapping the ground circuits to further reduce ground-return impedance. Wire the supply lines next and, as the last step, install any wiring which may be altered. Record your progress on the schematic diagram as you install and verify each wire run.

Before you install any integrated circuits, use the ohmmeter to verify all wiring topside from card fingers and from socket to socket. Check for

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bus- and supply-line short circuits. Insert a milliameter in the power supply line and energize the board. Check voltage-regulator outputs and voltage distribution. With the power off, insert integrated circuits one by one and observe the expected increases in supply current. If all is well, connect the ribbon cable to the proto system and check the voltages at the other end of the cable. Take care that the proto system's power supplies do not feed directly back to your computer!

At this point, the wise experimenter will perform static tests on at least a portion of the board logic (eg: port and memory decoders). Use jumper wires to program the input logic and verify the output. A patchboard with the entire system bus laid out and labeled on Klip Block linked to the system by ribbon cable is a handy aid for conducting static tests. These tests detect wiring and design errors, as well as defective integrated circuits.

Always turn off all power when in-

serting or removing connectors and plugboards. Connect the untested ribbon cable and proto system to your computer, but do not install the plugboards. If your computer fails to function, look for line shorts. Another possible culprit is the ribbon cable capacitance (or the cable may be picking up noise). Always use very short cables and be prepared to experiment with several lengths. As the final and most crucial test, insert the plugboard in the proto system for dynamic on-line tests. The most frequently encountered problems are the result of wiring errors or omissions, erratic or defective integrated circuits, and contaminated and erratic connectors.

An erratic integrated circuit device is difficult to pinpoint, but it can be forced to reveal itself. Allow the system to warm up thoroughly, and attempt to reproduce the observed erratic behavior. Then, spray each suspected device with integrated-circuit cooler. In many cases, this will temporarily restore the system to normal operation and isolate the troublesome component. Another approach is to substitute suspect integrated circuits with those that you know are reliable.

Once you resolve the frustrating circuit problem, you will gain a far greater understanding of the microprocessor, logic circuits, and test techniques. So start experimenting with computer hardware circuits made simply by wire wrapping and a plugboard system. It will lead to greater enjoyment of your hobby.

Due to a processing error the Lanier Business Products ad which appeared on page 27 of the April Byte had no Reader Service Number.

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Speeding Up TRS-80 Graphics

Ronald Bobo 3246 Gravois St Louis MO 63118

John Knoderer The Software Center 51 Florissant Oaks Shopping Ctr Florissant MO 63031

Many TRS-80 owners have probably, at one time or another, experimented with using DATA statements to store graphics information. This method can be highly efficient, but there's a catch. It is possible to store graphics as data in several different ways. Which is best?

In this article, we will examine some of the methods of storing a screen image as DATA statements, and, later, of recreating it on the video screen. Listings 1 thru 13 show the evolution of successively complex techniques.

In most cases, we will start with a picture onscreen (as provided by a run of listing 1). Many of the simpler sketching programs for the TRS-80 don't provide any way to store the images to disk, and the screen-reading programs used as examples in this article can be appended to a sketching program that will allow you to save your work. Let's look at the first method of saving screen images.

POINT Graphics

Every cell (graphics point) on the TRS-80 graphics screen can be turned on by a SET statement or turned off by a RESET statement. This method is used in listing 1 to draw a picture on the TRS-80 video screen. Another

Interested readers can call The Software Center at (314) 838-7785.

TRS-80 Level II command, POINT, returns a 1 or 0 based on the value of the cell given by the x (column number) and y (row number) parameters of the POINT statement.

The easiest way to store the video screen would be to examine and write an (x, y) number pair for each cell that is shown. Unfortunately, this is both time consuming and wasteful of disk storage. Due to the nature of most drawings, they are more easily approached as a series of horizontal

By PEEKing the appropriate memory locations, we can represent the contents of the screen as exactly 1024 numbers.

lines; this is done in listing 1 where a horizontal line of cells is SET to screen inside a do-loop that varies the x (column) coordinate of the SET statement. We can store each line of cells as a triad of numbers: y (row) number, beginning x (column) number, and ending x (column) number. Then we can later read the triad and recreate the line by executing a SET statement within a do-loop.

Listing 2 illustrates this process by creating the disk file of triads (lines 11000 thru 11050), closing it (line

11060), then opening it again and recreating the picture from a cleared screen (it does this by reading the disk data file in lines 12000 thru 12020, as discussed above). The data in this data file will be used by listing 3.

Data Files and POKE Graphics

To use these data files in other programs, the disk file of numbers must now be converted to DATA statements. However, you won't have to type them on the keyboard. Listing 3 will read the disk file from listing 2, convert the numbers to DATA statements complete with line numbers, and put them back onto disk in ASCII format, ready to be merged with a BASIC program.

Now that the numbers have been reconfigured as DATA statements, they can be merged with a short program that will use the DATA statements to set the graphics. This method is a bit faster than reading the data from a disk file. Listing 4 includes the DATA statements (lines 1905 thru 1960) generated by listing 3 (which contain the data generated by listing 2). Lines 100 to 130 read the data and set the graphics. Lines 200 to 210 generate hardcopy of the information on the screen for conversion to DATAPOKE statements. Line 300 creates a file and stores the data on disk.

Listing 4 creates (in line 300) a new Text continued on page 176

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Listing 1: TRS-80 graphics program using the traditional SET and RESET graphics.

10 CLS:CLEAR 200

20 FOR X=50 TO 95:Y=12:SET(X,Y):NEXT:FOR Y=13 TO 32:SET(50,Y):SET(95,Y):NEXT 30 FOR X=20 TO 50:Y=22:SET(X,Y):NEXT:Y=22:FOR X=37 TO 43:SET(X,Y):Y=Y-1:NEXT 40 FOR X=44 TO 49:SET(X,Y):Y=14:NEXT:FOR Y=22 TO 29:X=20:SET(X,Y):NEXT:FOR Y=29

TO 30:X=27:SET(X,Y):NEXT:FOR Y=27 TO 28:X=29:SET(X,Y):NEXT:FOR Y=26 TO 27:X=30:S

50 FOR Y=25 TO 26;X=31;SET(X,Y):NEXT:SET(31,25):FOR X=32 TO 38:Y=24:SET(X,Y):NEX T:SET(40,23):SET(41,24):SET(46,24):Y=25:FOR X=42 TO 47:SET(X,Y):Y=Y+1:NEXT:FOR Y =23 TO 30:X=48:SET(X,Y):NEXT

60 Y=25:FOR X=38 TO 44:SET(X,Y):SET(X+1,Y):Y=Y+1:NEXT:FOR X=45 TO76:Y=32:SET(X,Y):NEXT:Y=31:FOR X=76 TO B3:SET(X,Y):SET(X+1,Y):Y=Y-1:NEXT
70 FOR X=83 TO 90:Y=24:SET(X,Y):NEXT:Y=25:FOR X=90 TO 94:SET(X,Y):SET(X+1,Y):Y=Y

+1:NEXT:FOR X=96 TO 97:Y=31:SET(X,Y):NEXT

80 FOR X=33 TO 36:Y=27:SET(X,Y):NEXT:FOR X=85 TO 88:SET(X,Y):NEXT:FOR Y=30 TO 32 :X=30:SET(X,Y):NEXT:Y=30:FOR X=30 TO 32:SET(X,Y):SET(X+1,Y):Y=Y-1:NEXT 90 Y=28:FOR X=36 TO 38:SET(X,Y):SET(X+1,Y):Y=Y+1:NEXT:Y=32:FOR X=30 TO 33:SET(X,Y):

Y):SET(X+1,Y):Y=Y+1:NEXT:FOR X=33 TO 36:Y=35:SET(X,Y):NEXT 100 Y=34:FOR X=36 TO 38:SET(X,Y):SET(X+1,Y):Y=Y-1:NEXT:FOR Y=30 TO 32:X=39:SET(X ,y):NEXT:X=34:Y=30:SET(X,Y):SET(X+1,Y):SET(33,31):SET(36,31):X=34:Y=32:SET(X,Y):

SET(X+1,Y) 110 FOR Y=30 TO 32:X=82:SET(X,Y):SET(X+9,Y):NEXT:Y=30:FOR X=82 TO 85:SET(X,Y):SE T(X+1,Y):Y=Y-1:NEXT:Y=32:FOR X=82 TO 84:SET(X,Y):SET(X+1,Y):Y=Y+1:NEXT:FOR X=85

TO 88:Y=35:SET(X,Y):NEXT Y=34:FOR X=88 TO 90:SET(X,Y):SET(X+1,Y):Y=Y-1:NEXT:Y=28:FOR X=88 TO 90:SET(X 120 ,Y):SET(X+1,Y):Y=Y+1:NEXT:FOR X=86 TO 87:Y=30:SET(X,Y):Y=32:SET(X,Y):NEXT:SET(85

31):SET(88,31) 130 X=55:Y=15:SET(X,Y):SET(X,Y+1):SET(X+1,Y+2):SET(X+1,Y+3):SET(X+2,Y+4):SET(X+2 ,Y+5):SET(X+3,Y+6):SET(X+4,Y+5):SET(X+4,Y+4):SET(X+5,Y+3):SET(X+5,Y+2):SET(X+6,Y +1):SET(X+6,Y)

140 FOR Y=15 TO 21:X=65:SET(X,Y):NEXT:FOR X=69 TO 73:Y=15:SET(X,Y):NEXT:FOR Y=16
TO 21:X=71:SET(X,Y):NEXT:FOR X=77 TO 81:Y=15:SET(X,Y):Y=21:SET(X,Y):NEXT:SET(78

,18):FOR Y=15 TO 21:X=77:SET(X,Y):NEXT 150 FOR Y=15 TO 21:X=85:SET(X,Y):NEXT:Y=18:FOR X=86 TO 89:SET(X,Y):Y=Y-1:NEXT:Y=
18:FOR X=86 TO 89:SET(X,Y):Y=Y+1:NEXT

160 FOR Y=24 TO 29:X=55:SET(X,Y):SET(X+4,Y):SET(X+9,Y):NEXT:Y=25:FOR X=60 TO 62: SET(X,Y):SET(X+1,Y):Y=Y+1:NEXT:

170 FOR Y=25 TO 28:X=68:SET(X,Y):NEXT:FOR X=69 TO 71:Y=24:SET(X,Y):Y=29:SET(X,Y) :NEXT:SET(72,25):SET(72,28):SET(74,29)
180 FOR X=1 TO 1500:NEXT:PRINT@64,STRING\$(60, '):PRINT@128,STRING\$(60, '):FOR

X=5 TG 125:Y=0:SET(X,Y):SET(X,Y+1):Y=47:SET(X,Y):SET(X,Y-1):NEXT:FOR Y=8 TO 47:X =5:SET(X,Y):SET(X+1,Y):SET(X+2,Y):NEXT

185 FOR Y=0 TO 47:X=125:SET(X,Y):SET(X-1,Y):SET(X-2,Y):NEXT:FOR X=1 TO 1000:NEXT

Listing 2: Program to read data directly from the screen memory and store it to the disk as numbers representing a series of horizontal lines of graphic dots.

11000 OFEN'0",1,"GRAPHIC/DAT":FORY=0T047:X=-1

11010 X=X+1:IFX>127THEN11060

11020 IFFOINT(X,Y)=0THEN11010

11030 X1=X

11040 X=X+1:IFX>1270RF0INT(X,Y)=0THENPRINT#1,Y","X1","X:G0T011010

11050 GOTO11040

11060 NEXTY:CLOSE 12000 OPEN'I',1, GRAPHIC/DAT':CLS

12010 C=C+1:IFEOF(1)=OTHENINPUT#1,Y,X1,X2:FORX=X1TOX2:SET(X,Y):NEXT:GOTO12010

12020 GOT012020

20000 REM--ORIGINAL GRAPHICS ROUTINE FROM A SKETCH BY KARL WILLIAMSON, OVERLAND, MO. SET AND RESET GRAPHICS BY RON BOBO.

20005 REM-~ALL OTHER PROGRAMMING IN THIS SERIES BY JOHN KNODERER, COMP-U-TRS, 51 FLORISSANT OAKS SHOPPING CENTER, FLORISSANT, MO, 63031

20010 REM--LINES 11000 TO 11060 CONVERT SCREEN TO VALUES

Y, X1 AND X2 AND SEND TO DISK. FOR USE IN LINE 'FOR X=X1 TO X2:SET(X,Y):NEXT'

20020 REM--LINES 12000-12020 TEST THE NUMBERS CREATED BY 11000 65000 'TWO

Listing 3: This routine reads the data file generated by the program in listing 2 (and subsequent listings) and creates an ASCII file containing BASIC DATA statements.

13000 CLEAR9999:OPEN'I',1,'GRAPHIC/DAT':LN=1900:OPEN'0',2,'GRAPHIC/ASC
13010 LN=LN+5:X\$=STR\$(LN)+' DATA'

13020 IFEOF(1)THENPRINT#2, LEFT\$(X\$, LEN(X\$)-1):PRINTX\$CHR\$(8):CLOSE:END

13030 INPUT#1,Y:X\$=X\$+MID\$(STR\$(Y),2)+*,*:IFLEN(X\$)>237THENFRINT#2,LEFT\$(X\$,LEN(X\$)-1):PRINTX\$CHR\$(8):GOTO13010

13040 GOT013020

13900 REMARK--THIS CONVERTS NUMBERS ON DISK TO BECOME REGULAR BASIC DATA STATEME NTS WITH A LIMIT OF 240 CHARACTERS PER LINE

45000 CONVERT

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Listing 4: Recreation of a graphics picture. This listing shows how the DATA statements generated by listing 3 may be appended to a program that uses them to recreate the original graphics display.

READY, X1, X2: FORX=X1TOX2: SET(X,Y): NEXT: GOTO110

120 RESUME130 130 ONERRORGOTOO:GOTO 150 150 REMARK--THIS SECTION OF PROGRAM FROM LINE 100 TO LINE 130 IS PROGRAM LISTING NUMBER ZERO THAT WILL RECREATE THE GRAPHIC PICTURE OF LISTING 1. 190 'GOTO300 200 FORI=15360T016383:LPRINTPEEK(I);:NEXT:RETURN 210 REM--LINE 200 WILL GENERATE HARD COPY OF DATA FOR THE NEXT PROGRAM 300 OPEN'O',2, DATAPOKE':FORI=15360T016383:FRINT#2, PEEK(I):NEXT:CLOSE:STOP:REMAR THIS LINE WILL OUTPUT TO DISK 1905 DATA0,5,126,1,5,126,2,5,8,2,123,126,3,5,8,3,123,126,4,5,8,4,123,126,5,5,8,5 ,123,126,6,5,8,6,123,126,7,5,8,7,123,126,8,5,8,8,123,126,9,5,8,9,123,126,10,5,8,10,123,126,11,5,8,11,123,126,12,5,8,12,50,96,12,123,126,13,5,8,13,50,51,13,95 1910 DATA96,13,123,126,14,5,8,14,45,51,14,95,96,14,123,126,15,5,8,15,44,45,15,50 8,18,41,42,18,50,51,18,56,57,18,60,61,18,65,66,18,71,72,18,77,79,18,85,87,18

,51,15,55,56,15,61,62,15,65,66,15,69,74,15,77,82,15,85,86,15,89,90,15,95,96,15,1
23,126,16,5,8,16,43,44,16,50,51,16,55,56,16,61,62,16,65,66,16,71,72,16,77,78 1915 DATA16,85,86,16,88,89,16,95,96,16,123,126,17,5,8,17,42,43,17,50,51,17,56,57 ,17,60,61,17,65,66,17,71,72,17,77,78,17,85,86,17,87,88,17,95,96,17,123,126,18,5, 1920 DATA95,96,18,123,126,19,5,8,19,40,41,19,50,51,19,57,58,19,59,60,19,65,66,19 ,71,72,19,77,78,19,85,86,19,87,88,19,95,96,19,123,126,20,5,8,20,39,40,20,50,51,2 0,57,58,20,59,60,20,65,66,20,71,72,20,77,78,20,85,86,20,88,89,20,95,96,20,123 1925 DATA126,21,5,8,21,38,39,21,50,51,21,58,59,21,65,66,21,71,72,21,77,82,21,85,86,21,89,90,21,95,96,21,123,126,22,5,8,22,28,51,22,95,96,22,123,126,23,5,8,23,28 ,29,23,40,41,23,48,49,23,50,51,23,95,96,23,123,126,24,5,8,24,28,29,24,32,39,24 1930 DATA41,42,24,46,47,24,48,49,24,50,51,24,55,56,24,59,60,24,64,65,24,69,72,24 ,83,91,24,95,96,24,123,126,25,5,8,25,28,29,25,31,32,25,38,40,25,42,43,25,48,49,2 5,50,51,25,55,56,25,59,62,25,64,65,25,68,69,25,72,73,25,82,84,25,90,92,25,95 1935 DATA96,25,123,126,26,5,8,26,28,29,26,30,32,26,39,41,26,43,44,26,48,49,26,50 ,51,26,55,56,26,59,60,26,61,63,26,64,65,26,68,69,26,81,83,26,91,93,26,95,96,26,1 23,126,27,5,8,27,28,31,27,33,37,27,40,42,27,44,45,27,48,49,27,50,51,27,55,56 1940 DATA27,59,60,27,62,65,27,68,69,27,80,82,27,85,89,27,92,94,27,95,96,27,123,1 26,28,5,8,28,28,30,28,32,34,28,36,38,28,41,43,28,45,46,28,48,49,28,51,28,55,5 6,28,59,60,28,64,65,28,68,69,28,72,73,28,79,81,28,84,86,28,88,90,28,93,96,28 1945 DATA123,126,29,5,8,29,27,29,29,31,33,29,37,39,29,42,44,29,46,47,29,48,49,29 ,50,51,29,55,56,29,59,60,29,64,65,29,69,72,29,74,75,29,78,80,29,83,85,29,89,91,2 9,94,96,29,123,126,30,5,8,30,27,28,30,30,32,30,34,36,30,38,40,30,43,45,30,47 1950 DATA49,30,50,51,30,77,79,30,82,84,30,86,88,30,90,92,30,95,96,30,123,126,31,5,8,31,30,31,31,33,34,31,36,37,31,39,40,31,44,46,31,50,51,31,76,78,31,82,83,31,8 5,86,31,88,89,31,91,92,31,95,98,31,123,126,32,5,8,32,30,32,32,34,36,32,38,40 DATA32,45,77,32,82,84,32,86,88,32,90,92,32,95,96,32,123,126,33,5,8,33,31,33 ,33,37,39,33,85,33,89,91,33,123,126,34,5,8,34,32,34,34,36,38,34,84,86,34,88,9 0,34,123,126,35,5,8,35,33,37,35,85,89,35,123,126,36,5,8,36,123,126,37,5,8,37 1960 DATA123,126,38,5,8,38,123,126,39,5,8,39,123,126,40,5,8,40,123,126,41,5,8,41 ,123,126,42,5,8,42,123,126,43,5,8,43,123,126,44,5,8,44,123,126,45,5,8,45,123,126 46,5,126,47,5,126

65000 'FOUR

100 ONERRORGOTO120:CLS

110

Text continued from page 171:

data file, DATAPOKE, that represents the screen contents in another way. Actually, the contents of the screen are stored in the TRS-80 memory as 1024 contiguous bytes of memory, each byte representing six graphics cells (two cells wide by three cells high). By PEEKing the appropriate memory locations (decimal 15360 to 16383), we can represent the contents of the screen as exactly_1024 numbers, which are written to the DATA-POKE file, as shown in listing 4.

Now, using the DATAPOKE file just generated and the conversion program in listing 3, we come up with a new set of DATA statements. These are merged with another short routine to produce listing 5, which reads data and POKEs the values into video

To get all of these graphics characters on the screen we are now using 1024 different numbers, with an average of 3 to 4 bytes used per number for storage (including commas). In

return for the large amount of memory that is being used, we are only gaining a slight speed advantage over the original program. Let's look for something that will reduce memory usage.

Replacing Blanks with Tabs

Tab characters are stored in TRS-80 Level II BASIC as the value 192 plus the number of spaces to tab to the right. With this knowledge, we can combine a string of spaces into one character of memory by replacing the spaces with a tab character.

Listing 6 uses this information to take a different set of numbers off the screen. The program will generate a new set of numbers that may then be converted to DATA statements using the converison program. To list these same values to a printer, merely remove the END statement from line

Note that in listing 6, the computer was not told to store any of the figures for regular printable charac-

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Listing 5: This program takes the DATA statements generated by listing 4 and POKEs the information directly into the screen memory.

```
500 DEFINTI-N:CLS
520 FORI=15360T016383:READA:POKEI,A:NEXT
530 GOTO 530
550 REM--LINES 500 TO 520 READ DATA STATEMENTS AND POKE THE
VALUES INTO SCREEN MEMORY
2,32,32,32,32,32,32,32,32,32,170,191,149,32,32,170,191,149,32,32,32,32,32,32
1930 DATA135,32,32,191,32,138,181,32,186,133,170,149,130,171,151,129,170,151,131
,129,170,181,158,129,32,170,149,32,32,32,32,32,32,32,32,32,32,32,32,170,191,149,
,180,32,32,170,149,32,32,32,32,32,32,32,32,32,32,32,170,191,149,32,32,170,191
,149,32,32,32,32,32,32,32,32,32,188,140,140,140,140,143,188,140,140,140,1488,191
1940 DATA32,32,32,131,32,32,130,129,32,130,129,32,130,131,131,129,130,129,130,12
9,32,170,149,32,32,32,32,32,32,32,32,32,32,32,170,191,149,32,32,170,191,149,3
2,32,32,32,32,32,32,32,32,191,184,151,131,175,182,173,144,131,191,191,32
1945 DATA170,149,170,189,180,191,32,190,131,141,32,32,32,160,190,135,131,131,175
1950 DATA170,149,170,149,131,191,32,175,176,156,176,32,184,159,161,190,135,175,1
80,139,191,149,32,32,32,32,32,32,32,32,32,32,32,32,170,191,149,32,32,170,191,149
157,187,149,170,157,132,32,32,32,32,32,32,32,32,32,32,32,170,191,149,32,32,170,1
91,149,32,32,32,32,32,32,32,32,32,32,32,130,175,180,190,135,32,32,32,32,32,32,32
1975 DATA32,32,32,32,32,170,191,149,32,32,170,191,189,188,188,188,188,188,188,18
```

Listing 6: A routine that compresses a string of spaces into a TAB character that represents the number of spaces in the string.

```
600 OPEN'D',2, PRINTCHR':L=1:A=PEEK(15360):POKE16383,32:IFA<129THENA=32
    FORI=15361T016383:B=PEEK(I):IFB<129THENB=32
620 IFB=AANDA=32THFNL=L+1:GOTO660
630 IFB=32THENL=1:C=A:GOSUB690:A=B:GOTO660
    IFA=32THENC=192+L:GOSUB690:GOTO655
650 C=A:GOSUB690
655 A=B
660 NEXTI:END
690 PRINT#2,C:RETURN
695 REMARK LINES 600-690 OUTPUT TO DISK, LINES 900-960 OUTPUT TO LINEPRINTER 900 POKE16383,32:L=1:A=PEEK(15360):IFA<129THENA=32
910 FORI=15361T016383:B=PEEK(I):IFB<129THENB=32
    IFE=AANDA=32THENL=L+1:GOTO960
930 IFB=32THENL=1:LPRINTA;:A=B:GOTO960
940 IFA=32THENLPRINT192+L;:GOT0955
950 LPRINTA;
955 A=B
960 NEXTI:END
65000 'SIX
```

Listing 7: Program to display data stored in the compressed format.

```
B00 DEFINTI-N:ONERRORGOTOB30:CLS
820 READJ:PRINTCHR*(J);:GOTOB20
830 RESUMEB40
840 POKE 16383,149
850 GOTO 850
```

Listing 7 continued on page 180

ters (such as blanks, letters, or numbers) because these can be more efficiently printed using PRINT statements. If you have both graphics and alphanumeric characters on the screen, the programs shown here will treat alphanumerics as a series of blanks for DATA purposes.

The next routine, listing 7, displays the data from the DATA statements created using listing 3 and the data file from listing 6, PRINTCHR. This routine requires graphics characters on every line. If you go more than sixty-three successive blank spaces, you will get a function error, so we are assuming that graphics will be present on every line.

In the sample data in listing 7, the last item in the DATA statements would give us a function error, so we did not use it in this particular example. Instead, a 149 was POKEd into the space (16383).

One problem that must be solved concerns the method of ending the loop that contains the DATA statements. For example, the three BASIC statements in line 820 of listing 7 are an endless loop that reads an item from the DATA statement and prints it. If we plan to use the same routine for different sets of DATA statements, we need to get the program out of the loop after it has read the last item of data; if we do not, the program will end immediately with an out-of-data error.

There are several ways this problem can be approached. Although tedious, we could count the number of items in the data statements and put the READ statement in a do-loop. We could also append a certain flag value (one that would not otherwise be in a valid list of data) to the end of the data statements and put the READ statement in a loop that stops when it reads the flag value. Instead, we decided to use the ON ERROR GOTO option that is available in Level II BASIC.

In listing 7, the ON ERROR GOTO 830 (in line 800) is executed when the READ tries to read past the last data value. (Without this statement, the program would end.) The RESUME 840 statement at line 830 causes the program to continue, even after what would otherwise be a fatal error. The loop to itself at line 850 allows us to fill the entire video screen with the picture being displayed, without ending the program and scrolling the

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Listing 8: Routine to convert the graphics data to strings of characters.

```
1100 POKE16383,32:OPEN'0',2,"PRINTSTR':L=1:A=PEEK(15360):IFA<129THENA=32
1110 FORI=15361T016383:B=PEEK(I):IFB<129THENB=32
1120 IFB=ATHENL=L+1:GOT01160
1130 PRINT#2,L',"A:L=1:A=B
1160 NEXTI:END
1170 REMARK--PRINT OUT TO DISK
65000 'EIGHT
```

top two lines off the top of the screen.

Graphics Using STRING\$

From an examination of the DATA statements in listing 7 it is apparent that we still have a lot of repetition. This is especially true when we print a straight line or a solid area of graphics. In order to save even further on DATA items and to speed program execution, the DATA may be rearranged to allow the printing of strings of identical characters (in much the same way that we printed a line of "set" graphics points in listing 2).

The STRING\$(X,Y) command in Level II BASIC allows us to print X identical characters, each of which has an ASCII value of Y. When reading the video screen with PEEK statements, we will be looking for identical adjacent values. The data we print to a disk file (and later translate to DATA statements) will be a pair of numbers, the first number being the repetition factor and the second being the ASCII value of the character to be repeated. This method has been used to create the data file PRINTSTR in listing 8, and it displays graphics faster than previous methods.

Please note that in each of these programs that use PRINT for output purposes, the very last character on the screen (position 16,383) will not print, so if any SET, RESET, or POKE had been done into this area in the original program, it would be left blank. Your program could remedy this by POKEing 16383 with the proper value.

Listing 9 restores the graphics image to the video screen by reading the data items in the DATA statements (again created by the PRINT-STR file and listing 3). This program reads pairs of data items and prints them using STRING\$ in line 1420 to expand the pair of numbers to a string of proper length.

Listing 9 demonstrates that it is possible to extend the number of lines on which graphics are not required. However, they must still be present on at least every fourth line, because the length of each string must be less than or equal to 255, a limitation of Level II BASIC.

Combining Methods

Listing 10 (to create the data file FASTER) and listing 11 (to print the image from the DATA statements) refine the above method by storing a single data item instead of a data pair, when the character being repeated is a space (decimal value 32). Since the

Text continued on page 184



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Listing 9: Routine to display graphics data converted to strings of characters.

```
1390 CLEAR 3000
1400 DEFINTI-N:ONERRORGOTO1430:CLS
1420 READI, J:PRINTSTRING$(I, J); :GOTO1420
1430 RESUME1440
1440 POKE 16383,149
1450 GOTO 1450
1905 DATA2,32,1,170,1,191,1,159,56,143,1,175,1,191,1,149,2,32,1,170,1,191,1,149,
56,32,1,170,1,191,1,149,2,32,1,170,1,191,1,149,56,32,1,170,1,191,1,149,2,32,1,17
0,1,191,1,149,56,32,1,170,1,191,1,149,2,32,1,170,1,191,1,149,17,32,1,160,2,176
1910 DATA1,191,21,131,1,171,1,149,12,32,1,170,1,191,1,149,2,32,1,170,1,191,1,149
,16,32,1,184,1,135,2,32,1,191,1,32,1,138,1,181,1,32,1,186,1,133,1,170,1,149,1,13
0,1,171,1,151,1,129,1,170,1,151,1,131,1,129,1,170,1,181,1,158,1,129,1,32,1,170
1915 DATA1,149,12,32,1,170,1,191,1,149,2,32,1,170,1,191,1,149,14,32,1,160,1,158,
1,129,3,32,1,191,2,32,1,171,1,188,1,151,1,32,1,170,1,149,1,32,1,170,1,149,1,32,1
,170,1,151,2,32,1,170,1,159,1,180,2,32,1,170,1,149,12,32,1,170,1,191,1,149,2
1920 DATA32,1,170,1,191,1,149,9,32,1,188,4,140,1,143,1,188,3,140,1,188,1,191,3,3
2,1,131,2,32,1,130,1,129,1,32,1,130,1,129,1,32,1,130,2,131,1,129,1,130,1,129,1,1
30,1,129,1,32,1,170,1,149,12,32,1,170,1,191,1,149,2,32,1,170,1,191,1,149,9,32
1925 DATA1,191,1,184,1,151,2,131,1,175,1,182,1,173,1,144,1,131,2,191,1,32,1,170,1,149,1,170,1,189,1,189,1,191,1,32,1,190,1,131,1,141,3,32,1,160,1,190,1,135,2,13
 ,1,175,1,180,1,170,1,149,12,32,1,170,1,191,1,149,2,32,1,170,1,191,1,149,8,32
1930 DATA1,160,1,191,1,167,1,190,1,135,1,175,1,180,1,139,1,189,1,155,1,180,2,191
,1,32,1,170,1,149,1,170,1,149,1,131,1,191,1,32,1,175,1,176,1,156,1,176,1,32,1,18
4,1,159,1,161,1,190,1,135,1,175,1,180,1,139,1,191,1,149,12,32,1,170,1,191,1,149
1935 DATA2,32,1,170,1,191,1,149,8,32,1,130,1,129,1,191,1,153,1,183,1,157,1,187,1
 ,149,1,130,1,175,1,182,1,179,1,191,12,176,1,190,1,135,1,32,1,191,1,153,1,183,1,1
57,1,187,1,149,1,170,1,157,1,132,11,32,1,170,1,191,1,149,2,32,1,170,1,191,1,149
1940 DATA10,32,1,130,1,175,1,180,1,170,1,135,21,32,1,130,1,775,1,180,1,190,1,135
 15,32,1,170,1,191,1,149,2,32,1,170,1,191,1,149,56,32,1,170,1,191,1,149,2,32,1,1
70,1,191,1,149,56,32,1,170,1,191,1,149,2,32,1,170,1,191,1,149,56,32,1,170,1,191
1945 DATA1,149,2,32,1,170,1,191,1,189,56,188,1,190,1,191
65000 'NINE
```

Listing 10: Routine to generate a more compact graphics data file.

```
1500 POKE16383,32:L=1:A=PEEK(15360):IFA<129THENA=32 1505 OPEN'0',2,"FASTER"
1510 FORI=15361T016383:B=PEEK(I):IFB<129THENB=32
1520 IFB=ATHENL=L+1:GOT01560
1530 IFA=32THENLPRINT192+L;ELSELPRINTL;A;
1535 IFA=32THENPRINT#2,192+LELSEPRINT#2,L", "A
1540 L=1:A=B
1560 NEXTI:END
1570 REMARK--PROGRAM LISTING NUMBER TEN TO PRINT OUT LISTING FOR NEXT PROGRAM AN
D SEND IT TO DISK
1580 REMARK--IF HARD COPY IS NOT DESIRED, ELIMINATE LINE 1530
```

Listing 11: Routine to display data as created by listing 10.

65000 'TEN

1690 CLEAR 3000

```
1700 DEFINTI-N: ONERRORGOTO1730:CLS
1720 READI:IFI<192THENREADJ:PRINTSTRING$(I,J);ELSEPRINTCHR$(I);
1725 GOT01720
1730 RESUME1740
1740 POKE 16383,149
1745 GOTO 1745
1750 REMARK--PROGRAM NUMBER ELEVEN LINES 1600-1740
1905 DATA194,1,170,1,191,1,159,56,143,1,175,1,191,1,149,194,1,170,1,191,1,149,24
8,1,170,1,191,1,149,194,1,170,1,191,1,149,248,1,170,1,191,1,149,194,1,170,1,191,1,149,248,1,170,1,191,171,149,194,1,170,1,191,131
1910 DATA1,171,1,149,204,1,170,1,191,1,149,194,1,170,1,191,1,149,208,1,184,1,135
,194,1,191,193,1,138,1,181,193,1,186,1,133,1,170,1,149,1,130,1,171,1,151,1,129,1
,170,1,151,1,131,1,129,1,170,1,181,1,158,1,129,193,1,170,1,149,204,1,170,1,191
1915 DATA1,149,194,1,170,1,191,1,149,206,1,160,1,158,1,129,195,1,191,194,1,171,1
,188,1,151,193,1,170,1,149,193,1,170,1,149,193,1,170,1,151,194,1,170,1,159,1,180
 ,194,1,170,1,149,204,1,170,1,191,1,149,194,1,170,1,191,1,149,201,1,188,4,140
149,194,1,170,1,191,1,149,201,1,191,1,184,1,151,2,131,1,175,1,182,1,173,1,144
1925 DATA1,131,2,191,193,1,170,1,149,1,170,1,189,1,180,1,191,193,1,190,1,131,1,1
41,195,1,160,1,190,1,135,2,131,1,175,1,180,1,170,1,149,204,1,170,1,191,1,149,194,1,170,1,191,1,149,200,1,160,1,191,1,167,1,190,1,135,1,175,1,180,1,139,1,189
1930 DATA1,155,1,180,2,191,193,1,170,1,149,1,170,1,149,1,131,1,191,193,1,175,
76,1,156,1,176,193,1,184,1,159,1,161,1,190,1,135,1,175,1,180,1,139,1,191,1,149,2
1935 DATAI,157,1,187,1,149,1,130,1,175,1,182,1,179,1,191,12,176,1,190,1,135,193,1,191,1,153,1,183,1,183,1,187,1,187,1,149,1,170,1,157,1,132,203,1,170,1,191,1,149,194,
1,170,1,191,1,149,202,1,130,1,175,1,180,1,170,1,135,213,1,130,1,175,1,180,1,170
1940 DATA1,135,207,1,170,1,191,1,149,194,1,170,1,191,1,149,248,1,170,1,191,1,149
,194,1,170,1,191,1,149,248,1,170,1,191,1,149,194,1,170,1,191,1,149,248,1,170,1,1
 1,1,149,194,1,170,1,191,1,189,56,188,1,190,1,191
```

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Listing 12: Routine that converts screen data to the most compact, fastest form discussed in this article.

```
1800 POKE16383,149:L=1:A=PEEK(15360):IFA<129THENA=32
1805 OPEN'O',2, 'FASTEST'
1810 FORT=15361T016383:B=PEEK(I):IFB<129THENB=32
1820 IFB=ATHENL=L+1:GOTO1860
1830 IF A=32 THEN PRINT#2,192+L:ELSE IF L=1 PRINT#2,AELSEPRINT#2,L*,*A
1840 L=1:A=B
1860 NEXTI:END
65000 'THELVE
```

Listing 13: Routine to display the compressed data generated by listing 12.

```
1905 DATA194,170,191,159,56,143,175,191,149,194,170,191,149,248,170,191,149,194,
170,191,149,248,170,191,149,194,170,191,149,248,170,191,149,194,170,191,149,209,
160,2,176,191,21,131,171,149,204,170,191,149,194,170,191,149,208,184,135,194
1910 DATA191,193,138,181,193,186,133,170,149,130,171,151,129,170,151,131,129,170
,181,158,129,193,170,149,204,170,191,149,194,170,191,149,206,160,158,129,195,191
,194,171,188,151,193,170,149,193,170,149,193,170,151,194,170,159,180,194,170
1915 DATA149,204,170,191,149,194,170,191,149,201,188,4,140,143,188,3,140,188,191
195,131,194,130,129,193,130,129,193,130,2,131,129,130,129,130,129,193,170,149,2
04,170,191,149,194,170,191,149,201,191,184,151,2,131,175,182,173,144,131,2,191
1920 DATA193,170,149,170,189,180,191,193,190,131,141,195,160,190,135,2,131,175,1
80,170,149,204,170,191,149,194,170,191,149,200,160,191,167,190,135,175,180,139,1
89,155,180,2,191,193,170,149,170,149,131,191,193,175,176,156,176,193,184,159
1925 DATA161,190,135,175,180,139,191,149,204,170,191,149,194,170,191,149,200,130
,129,191,153,183,157,187,149,130,175,182,179,191,12,176,190,135,193,191,153,183,157,187,149,170,157,132,203,170,191,149,190,191,149,202,130,175,180,190,135
1930 DATA213,130,175,180,190,135,207,170,191,149,194,170,191,149,248,170,191,149
,194,170,191,149,248,170,191,149,194,170,191,149,248,170,191,149,194,170,191,189
,56,188,190,191
2000 DEFINTI-N: ONERRORGOTO2030:CLS
2020 READI:IFI<129THENREADJ:PRINTSTRING$(I,J);ELSEPRINTCHR$(I);
2025 GOTO2020
2030 RESUME2040
2040 POKE 16383,149
2045 GOTO 2045
2050 REMARK--PROGRAM NUMBER THIRTEEN TO EXECUTE PRINTOUT LINES 1900-2040
```

Text continued from page 180:

65000 'THIRTEEN

tab characters have a decimal value of 193 or greater, listing 11 can distinguish between tab values (to be printed using CHR\$) and number pairs (to be printed using STRING\$). This gives us a slight improvement in speed over the previous method.

A variation of this program comes to mind, since the number 1 is really not needed when using the STRING\$ function. If the length of the string is 1, we can PRINT CHR\$(176), instead of using STRING\$(1,176) as we would when using a number pair (see line 1910 of listing 11). That being the case, it is possible to rewrite the routine and, by adding one statement, tell the computer to go ahead and print out only 1 character.

Features of several of these programs may be combined. The space saver, which prints a series of spaces as the value 192 plus the number of spaces (as done in listings 6 and 7), may be combined with printing of a string of graphic characters using STRING\$ (see listings 8 and 9). By combining these with the length-1 technique discussed above, we have a slightly more complicated program.

It does, however, run a bit faster than its predecessor and uses much less memory in the DATA statements.

The final (and fastest) version of this program is given in listings 12 and 13. Using the three techniques just discussed, listing 12 writes data values out to the data file FASTEST. When this data is converted to DATA statements (by running listing 3), the program in listing 13 (which includes the data statements) uses them to recreate the original picture on the video screen.

Conclusions

These programs serve to illustrate alternative methods of using graphics on the TRS-80 Model I with Level II BASIC. These are not the only techniques that can be used, but are merely our suggestions for ideas you can try in some of your programs.

In some cases you will be sacrificing memory space for printout speed. The decision as to which of these methods is best for your particular program rests solely with you. The easiest way to find out is to put the various routines into programs and experiment with them.

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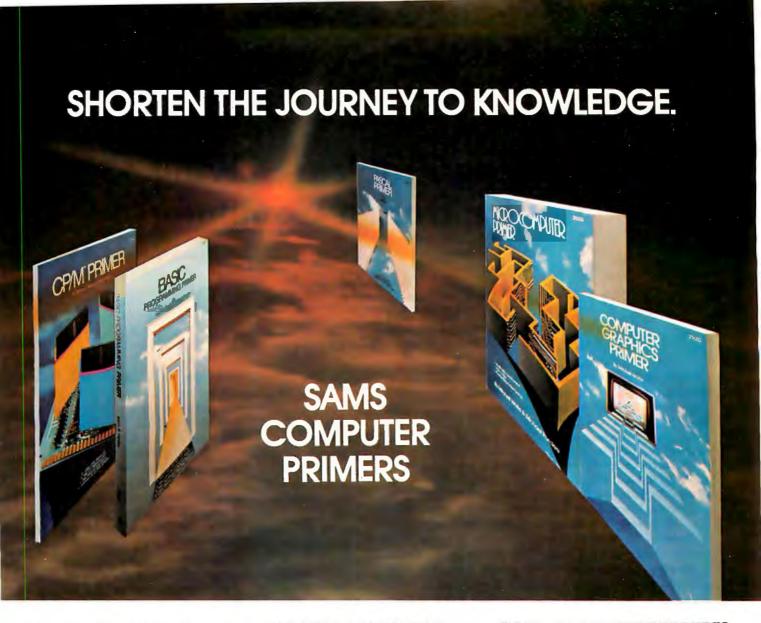
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Education Forum

Getting Problem-Solving Advice from a Computer

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Over the last three years, Paul Mellema and I have been at work on EMIL, an interactive computer program that we use to help teach our courses in formal logic. Since June 1979, we have been devoting our efforts to implementing a computerized "copilot" for EMIL that students can call on to solve problems.

The methods used to give our students advice are easily implemented and effective. The approach does not easily fit into the standard categories of educational computing (ie: record keeping, drill and practice, testing, games, simulation, etc). It is an approach that has potential for widespread application. The goal of this program is to help students develop and use skills and strategies needed to creatively solve problems that do not necessarily have only one solution. The program is Socratic in its style, because it asks students leading questions that help them analyze and resolve their difficulties.

In the study of formal logic, students are required to construct formal proofs. A proof is a series of statements leading to a conclusion. Each step of the proof is assumed to be true or derived from previous steps according to the rules of logic. The proof is intended to demonstrate that the conclusion follows logically from the assumptions.

Learning this type of thinking is valuable to students not only because it can lead to a mastery of logic, but because it also gives students experience in the kind of creative problem solving characteristic of mathematics, theoretical science, and many other disciplines and reallife pursuits.

Giving students practice in the creative solution of formal problems is important in education and particularly

so in the sciences. Scientific knowledge is too often presented as if it descended from heaven or was created by some form of superhuman intelligence. Very little effort is given to help students appreciate the thinking processes that go into the analysis and solution of scientific problems. There is a tendency to obscure the very human process of trial and error, of trying out strategies, of assessing failures, and of creating better lines of attack, which are all part of scientists' daily life. A course in logic gives students the opportunity to refine their problemsolving skills in an environment where the difficulty of the problems can easily be adjusted to their growing abilities.

In a traditional course in logic, where students' abilities vary widely, those who do not have an initial knack for problem solving are at a serious disadvantage. Even when strategies for proof building are carefully discussed in class, some students invariably complain that they cannot solve a new problem on their own in spite of understanding the lectures. Part of this difficulty is that some students cannot convert verbal explanations of techniques into strategies for dealing with new situations. Their problem is somewhat similar to that of a student driver who has mastered a lecture on how to operate a car, but cannot convert this knowledge into the appropriate series of actions for handling a real car on a real road. Driver training classes overcome this problem by using the guidance of a copilot who helps correct errors while the students practice the task.

Similar sorts of tutoring are very effective for helping students who cannot apply the verbal knowledge about logic to the construction of proofs. If students are asked to "think out loud" while attempting a proof, a gentle nudge here and there often leads to success. If they do not understand the rules or simply have not bothered to learn them, guiding them through a few proofs tends to straighten things out quickly, and it improves confidence and motivation. Just as in teaching most skills, effective methods involve letting students perform given tasks under guidance. Lecturing on the proper procedures and telling students to "go home and do likewise" is relatively ineffective.

About the Author

James Garson is a member of the Department of Information Engineering, University of Illinois at Chicago Circle. This article is a revised version of a paper he delivered to the National Educational Computer Conference, June 1980, in Norfolk VA. The work described was carried out under the National Science Foundation Grant Number SER79-00527. This article does not represent the views of that foundation. Another article by Mr Garson, "The Case Against Multiple Choice," can be found in The Computing Teacher, February-March 1980, page 29.



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Of course there are good reasons why tutoring is not widely used in introductory logic courses. These classes are usually quite large, so tutoring simply takes too much of the teacher's time. Besides that, grading formal proofs constructed by students is tedious, so teachers tend to give students relatively few exercises that require them to create such proofs. Even students who do well in logic generally do not get enough practice to develop very much skill. Often the teacher relies on exercises that require a single answer — exercises that ask students to give justifications for the steps of a completed proof. This does familiarize students with the rules, but it gives them no practice in the art of building up a proof.

Enter the Computer

Computers make it possible to simulate the tutoring situation. Students can enter their proofs at the terminal, and the computer can determine whether each line follows from previous lines and describe the difficulty if one does not. If students get lost, the computer can give advice on how to proceed.

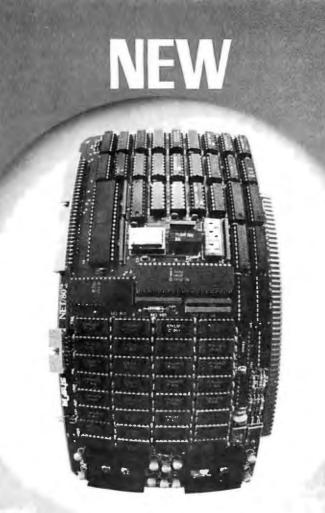
In 1976 we wrote a program called EMIL that lets students enter their proofs at the terminal and monitors their progress. The program has been used in a variety of courses at Notre Dame and has recently been adopted at Rutgers University. EMIL has several advantages over other proof-checking programs. First, there are a large number of logic textbooks, each with its own version of the rules of logic. Our program is the only one that lets a teacher supply the program with the set of rules used in his or her class, instead of forcing the use of the text with the set of rules written into the program. Second, the EMIL program is extremely gentle with students' input and generally repairs typing mistakes rather than complaining about them. This is important because many students are unfamiliar both with the terminal keyboard and the notation of logic. Third, the program lets students enter statements at the bottom (ie: end) of the proof so they can work the proof backwards if they desire to do so.

We allow and, in fact, encourage this because effective proof-building requires an analysis not only of the statements already derived, but of the statement to be proved as well. Often the proof can be considerably simplified by using the goal statement as a guide for determining the steps previous to it. Our program allows students to employ such strategies right at the terminal, instead of submitting a finished product to the computer for checking. The fourth advantage of our program is the main topic of this article: since September of 1979 EMIL has been giving students good advice on how to solve problems they find difficult. In this way, it is providing a good portion of what can be offered by a human logic tutor.

Programming Strategies

There are several distinct approaches to designing a computer program that can offer advice on formal proof construction. The first is simply to store a completed version of each proof and a list of comments that are intended to help students who ask for aid in deriving a given line. If the comments prove unhelpful, students can ask to see the next line of the stored proof or, indeed, any number of lines up to and including the entire proof.

This hint approach requires that a completed proof



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must be stored in the computer with appropriate comments for every problem students will work on. It also presupposes that there is only one reasonable sequence of steps that leads to the conclusion. If students approach a problem in an unusual way, there may not be enough similarity between their proofs and the stored proof for the computer to be of any help. Finally, it presupposes a top-to-bottom pattern of proof construction. But very often, from a given step in a proof, it is not at all apparent how to get to the conclusion. Such strategies must be explained with reference to what happens later in the proof. This sort of hint routine fails to help students appreciate global strategies that require knowledge not just of where the proof has been, but of where it is going. These are generally the most useful strategies.

Another technique is to write a program that allows the computer to generate a solution to students' problems and to recognize certain standard situations during the course of that solution. This strategy eliminates the need for storing a proof with commentary for each problem, since the computer generates its own solutions. But this strategy runs the risk of generating strange proofs that students are unlikely to recapitulate. Also, each formulation of the rules of logic would require its own customtailored program for generating proofs. Furthermore, the program to generate comments must be very carefully written to avoid misleading advice. Most importantly, this approach still does not help students to see global strategies; like the stored proof approach, it uses a top-tobottom pattern of proof construction. So, this approach also confines itself to giving advice only about the next line of the proof.

Another difficulty with both of these approaches to the design for an advice giver is that the program does not attempt to construct advice on the basis of whatever progress the student may have already made on the proof. This tends to discourage invention of novel, yet promising, partial solutions. It can devalue students' creative abilities and lower their self-confidence. It dampens students' engagement in the problem-solving process

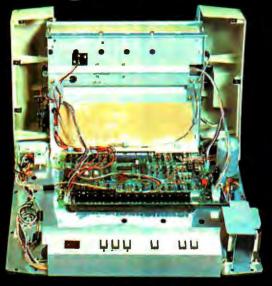
while reinforcing stereotyped solutions.

Our Approach

The third approach to the design of an advice giver, the one we have adopted, overcomes these problems by paying more attention to the techniques actually used by human logic tutors. One of the main things a human tutor should do is to provide students with effective problem-solving tools for analyzing situations and for breaking problems into simpler subproblems. The same tools can then be applied to these simpler problems. An effective tutor does not give a solution or even pieces of it. Instead, the tutor provides an apprenticeship in the art of asking relevant questions, whose answers lead students to see how problems can be broken down into more manageable parts. Questions like "Can you apply this rule to statements you have already derived?" and "What rule could be used to derive a statement of this type?", when presented in a coherent sequence, are very effective for helping students develop strategies to be used effectively in a wide variety of proof-building prob-

The central function of our advice-giving program is simply to ask students leading questions and then branch

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- 1. 'CAN YOU APPLY MP TO ANY PROVEN LINES' 'Y' 2 'N' 3 '*ANSWER YES OR NO'
- 2. 'APPLY MP TO THESE LINES' '*'
 3. 'WHAT IS THE MAIN CONNECTIVE OF YOUR GOAL FOR-MULA?' '&' 4 'V' 5 '->' 6 '*PLEASE ANSWER &, V OR ->'

Table 1: Sample records from the question file of our program that is designed to give advice to students concerning the construction of formal proofs in logic courses.

to new questions on the basis of the answers. Eventually, the program runs out of questions to ask, and specific advice is given on the basis of the information provided in the previous answers. (The questions can be thought of as being structured in a tree, with the path taken along the branches being determined by the students' answers and the advice for each situation being located at the tip of each branch.)

Programming the question-asking routines for our own advice giver was quite simple. Thus the main focus of our attention has been the creation of a file of questions with real pedagogical merit. Since the questions are not written into the structure of our program, modifying the question tree in response to what we learn about effective advice is a painless process that does not require any programming expertise.

Our question file has a very simple format. (See table 1.) Each record contains the text of a question followed by a list of acceptable answers. Each answer is followed by a number indicating which record to jump to in case the student responds with that answer. The last item in each record begins with a "*" (which indicates that there are no more acceptable answers) and contains text that is printed in case the student does not respond with one of the acceptable answers. Most of the questions we ask are answered with yes or no, but we found the use of other sorts of answers more convenient for certain questions. The text of the advice to be given is simply stored in the question file followed by "*". This indicates that this pseudoquestion has no acceptable answers, and the program should stop after printing the advice.

Expansion

We have built a number of improvements into this simple program. The first has to do with the fact that the sequence of the questions should vary depending on how much students have learned and how difficult their problems are. Our solution to this problem is to assign each problem a level number and to use this number to route the program to separate question trees for each level we have defined.

The second enhancement is motivated by the fact that we want to mention items in our questions that change during the execution of the program (for example, the last line number finished in the proof or the name of the rule to be used). Obviously the text of the questions in the file cannot mention specific line numbers or rule names. Our solution is to introduce variables that are replaced with the corresponding specific information just before the question is printed. We have adopted a convention that words beginning with "&" are variables, so a line of advice on our question file might read:

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"YOU SHOULD APPLY &RULE TO LINE &GNUM"

This directs the program to fill in the specific information about the rule name and line number, for example:

"YOU SHOULD APPLY MP TO LINE 5"

Although our advice-giving program was running with these two enhancements in September, we were still working on a central portion of the program the following January. We still had to program the most important improvement: the development of subroutines that can answer all the questions posed to students by the program and that can comment on any errors in students' responses. Though students are usually accurate in their responses, they occasionally make mistakes that can result in their receiving bad advice. But this is not the only reason for giving the computer the ability to monitor the correctness of students' responses.

Once students run the advice giver a number of times, they become bored with answering a number of seemingly pointless questions. The questions become pointless not because they are not needed in analyzing proofconstruction problems in general, but because a particular portion of the analysis is not needed for the problem being dealt with. When the computer is capable of answering the questions itself, we can decide which questions at particular levels of difficulty should be printed at the terminal, and those the computer should answer for itself by examining the proof being worked on.

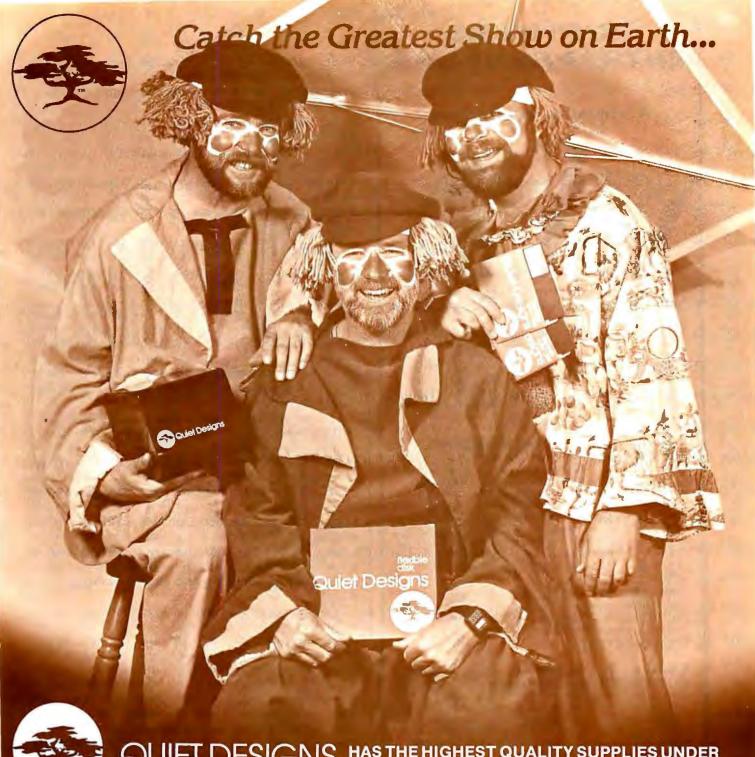
Experienced students may resent being asked any questions at all and may prefer the advice giver to merely print specific pieces of advice. However, we believe that for most students who need the advice giver in the first place, posing relevant questions is much more valuable to learning problem-solving skills than is obtaining advice.

Does It Work?

We now have a version of EMIL that answers all the guestions it poses. We also have a method for indicating which guestions are to be asked under the particular circumstances. There is a need to do more research on how obtrusive the advice giver ought to be in relation to students' progress and cognitive style. However, one of the advantages of our program is that we can easily control the circumstances under which questions are asked. In fact, our program allows the students to suppress the asking of questions if this bothers them.

There is a final reason for programming the computer so that it can answer all the questions: when this is done the program can traverse the question tree on its own and come up with relevant advice. Once advice is available, the program can follow it to construct proofs on its own. Judging from extensive tests of the program, our advice tree turns out to be highly, though not totally, effective for solving logic problems. It is capable of solving over 95% of the problems that we give to our students. This provides us with an important tool for improving our program. By running a large number of problems through our advice giver, we can determine the circumstances under which it is unable to do a proof. Then we use that information to create a more sophisticated version of our question file.

This approach to giving computerized advice has a



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wide range of applications. It can be used, for example, to help college students with their physics homework, to determine the identity of unknowns in qualitative chemistry, to help medical students learn diagnosis, and even to help people determine what is wrong with their cars or whether they should itemize their deductions. All it takes is a simple program to run the questions and a question file that is carefully constructed to reflect the best strategies that people actually use to solve the kind of problems at issue. Depending on the context of its use, some or all of the enhancements to the basic program we have developed could be used.

It is worth pointing out exactly how our advice-giving program differs from the traditional way in which the multiple-choice format is used in CAI (computer-aided instruction). These differences are not particularly striking from the programmer's point of view. In both cases. programs are designed to ask questions and to select new questions on the basis of the answers. The advice-giving program requires a more elaborate branching structure and may differ in being unable to evaluate responses. But the important differences are the ones that are obvious to the educator: these have to do with the educational goals of the program.

The standard objective for using multiple-choice techniques is to help students learn certain facts. In the case of the advice-giving program, the answers are not part of what is being taught. It is the sequence of questions representing an effective problem-solving strategy that we would like students to master. By repeatedly exposing students to questions that have been proven effective in problem analysis, they learn to develop efficient strategies that can be used over a wide range of problems. The whole process of adopting principles of problem analysis is a valuable exercise of problem-solving skills that can be applied to any domain where creative thinking is required.

We should stress that despite our emphasis on strategy learning as an objective to advice-giving programs, the programs are also effective in giving factual information. From our advice giver, our students learn about the rules of logic, their names, their operation, and their functions in proofs. Also important is that our program helps expose students to this information at the exact times when it is most useful: this is the context when they are most

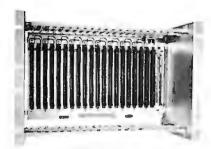
likely to be receptive to learning these facts.

Although the advice-giving program may not look very different from standard multiple-choice "courseware" to the programmer, it has radically different educational goals — the most important of which is the development of problem-solving abilities. Given the simplicity of the programming effort as compared to games and simulations, the advice-giving program is particularly attractive for educators interested in developing students' creativity.■

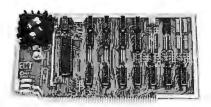
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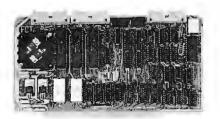
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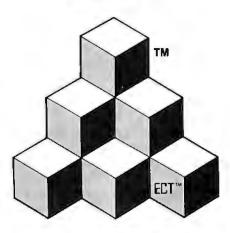


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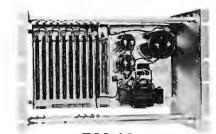




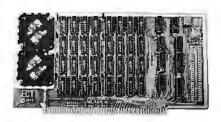
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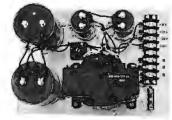


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A Chessboard Journey on the TI-59 Programmable Calculator

Michael Gilpin Michigan Technological University Houghton MI 49931

KTTOUR-59 (see listing 1) is a program for the Texas Instruments TI-59 that finds *Knight tours* on an 8 by 8 board. (A Knight tour is a journey on a chessboard where the Knight lands on each square exactly once.)

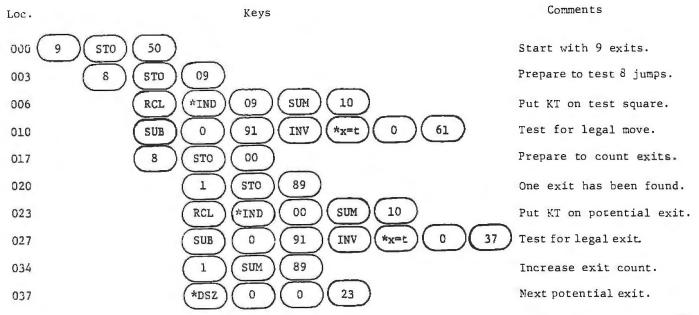
To begin, partition the calculator memory locations into 320 program lines and 90 addressable memory locations by pressing 9, *Op, 17. Then enter the program and press B. This initializes values in registers 00 thru 89 as shown in figure 1. The actual chessboard is represented by registers 11 thru 18, 21 thru 28, . . . 81 thru 88. After setting up this initial configuration, the program returns with the display value 0. Enter the initial square number and press C. The program will then move the Knight at

the approximate rate of one move every 33 seconds according to the Rule of Warnsdorf. That is, it will always move the Knight to a square having, at that point in the tour, a minimal number of entrances.

Execution stops with the display value 0 as soon as no additional moves can be found. Pressing D causes the program to flash each move in the format "square.move" (eg: "13.07" means the seventh move was made on square number 13). This allows the user to write down the complete tour on graph paper. If used in conjunction with the Texas Instruments PC-100A printer, a hard copy of the tour is produced using the same format. Then for a dif-

Text continued on page 202

Listing 1: KTTOUR-59, written for the Texas Instruments TI-59.



Listing 1 continued on page 200

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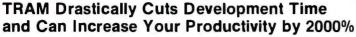
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Listing 1 continued: 041 +/-SUM 2 1 10 Return KT to test square. 046 50 RCL RCL 89 3€X* 0 60 Test for new minimum. XIL 054 STO 50 RCL 10 STO 20 New minimum and position. 060 *CP 061 *DSZ 9 0 06 Next test square. 2 065 1 +/-SUM 10 Return KT to last square. 10 070 RCL 10 RCL 20 *x=t 2 Stop if no move possible. 20 079 *IND 10 STO *IND 20 RCL RCL STO 10 1 SUM *IND 10 Move knight. 090 RST Look for further moves. *x≥t 07 10 0 1 091 RCL x2t 1 8 9 *x≥t 07 Test for correct range. XSL *IND RCL 10 INV SUB Return O for legal move. 105 *LBL Prepare board for tour. 109 8 0 STO 00 8 +/-*CMS *Op *IND 00 30 STO *IND STO 00 SUM 00 *DSZ 1 18 Fill border squares. 01 07 STO 03 130 2 STO STO x^2 02 STO 05 STO 04 7 STO 08 STO 06 +/-STO Load jump increments. 153 CLR R/S Make first move. *LBL 155 C *IND 10 RST 10 STO 20 1 STO Begin search. STO Display Routine. 165 *LBL D *Fix 8 STO 00 Prepare row index. 169 Prepare column index. 8 STO 09 172 00 0 RCL 09 RCL 1 175 *IND 89 RCL ÷ STO 89 = + 1 0 0 Pause Pause Pause *Prt Display "square.move". 75 *Adv Next column. 9 *DSZ 198 1 *DSZ 0 1 72 Next row. 203 207 INV *Fix R/S CLR

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10	11	12	13	14	15	16	17	18	19
-8	0	0	0	0	0	0	0	0	-8
20	21	22	23	24	25	26	27	28	29
-8	0	0	0	0	0	0	0	0	-8
30	31	32	33	34	35	36	37	38	39
-8	0	0	0	0	0	0	0	0	-8
40	41	42	43	44	45	46	47	48	49
-8	0	0	0	0	0	0	0	0	-8
50	51	52	53	54	55	56	57	56	59
-8	0	0	0	0	0	0	0	0	-8
60	61	62	63	64	65	66	67	68	69
- 8	0	0	0	0	0	0	0	0	-8
70	71	72	73	74	75	76	77	78	79
-8	0	0	0	0	0	0	0	0	-8
80	81	82	83	84	85	86	87	88	89
-8	0	0	0	0	0	0	0	0	0

Figure 1: Register initialization assignments. The values are assigned as shown for an 8 by 8 playing area. Usable squares are identified by a zero value; the board size can be reduced by manually assigning nonzero values to eliminate squares.

П	12	13	14	15
1	20	9	14	3
21	22	23	24	25
10	15	2	19	24
31	32	33	34	35
21	8	23	4	13
41	42	43	44	45
16	11	6	25	18
51	52	53	54	55
7	22	17	12	5

Figure 2: Example of a reduced-size board. The Knight tour shown here is the result of KTTOUR-59's version of the Rule of Warnsdorf applied to a starting position of 11.

Text continued from page 198:

ferent tour, press B, enter a new starting position, and proceed as before.

The program execution can be modified to find tours on subsets of the 8 by 8 board. Press B as before. Then enter a nonzero value (say 1) into any square you wish to eliminate before entering the initial square and pressing C. This works since the Knight is not allowed to move to squares containing a nonzero value. For example, press B and then store the value 1 into registers 16, 17, 26, 27, 36, 37, 46, 47, 56, 57, 61 thru 67, and 71 thru 77. Enter the initial position of 11 and press C. The result will be the 5 by 5 tour shown in figure 2. ■

Acknowledgments

M Kraitchik, le Probleme du Cavalier, Gauthiers-Villars et CIE, Paris, 1927.

Thanks are also due Professor William Woodruff, Grand Rapids, Michigan.

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BYTE May 1981

An Integer Math Package for the 8080

Bruce D Carbrey 109 Bucknell Trl Hopatcong NJ 07843

"How can you have a computer that doesn't know how to multiply?" People unfamiliar with microcomputers ask this question incredulously whenever I describe the limitations of arithmetic on my 8080-based system. Of course, if you work in BASIC, you may take arithmetic for granted; but if you are an assembly-language user like myself, you are probably painfully aware of the absence of 16-bit arithmetic on the 8080 microcomputer.

It is quite possible that you need multiple-byte arithmetic routines for your assembly-language programs. If program space is a problem (most floating-point routines use several K bytes of memory), or if 16-bit signed integer arithmetic is sufficient for your needs, then the arithmetic routines given in this article may be of interest. These routines run one order of magnitude faster than full floating-point routines; also, they occupy only 215 bytes, all of which may be in read-only memory if

Two additional routines provide conversion between ASCII (American Standard Code for Information Interchange) decimal character strings and the signed binary notation used by the arithmetic routines. These routines require an additional 175 bytes, including 2 bytes that must be in programmable memory.

Improve your 8080-based personal computer by adding these 16-bit arithmetic routines.

Design of the Arithmetic Routines

The arithmetic routines (given in listing 1) use the HL register pair as a 16-bit wide "accumulator." Subroutines performing dyadic operations (ie: those with two operands) expect to find one operand in the HL register pair and the other in the DE register pair. The result is returned in the HL pair. The arithmetic subroutines also set the sign and zero flags to reflect the value of the result returned in the HL register pair. (For example, if the result of an operation is decimal -11034, then the minus flag will be set and the zero flag will be cleared.) The information in the carry flag is invalid and should be ignored. The B, C, D, and E registers are restored by all routines except EDIVMOD (the division routine), which returns the quotient in the HL register pair and the remainder in the DE register pair, with the B and C registers restored.

Internally, values are represented in two's complement form, with the most significant bit acting as a sign bit. (See text box on page 225.) This representation is a simple extension of the 8-bit representation used for normal accumulator operations.

Unfortunately, this also leads to one small anomaly. The smallest representable number is -32,768, but the largest is only +32,767. (See the text box on page 226.) Thus, if you negate the value -32,768, an overflow will result. As a consequence of this fact, you may add or subtract two values that give a result of exactly -32,768, but if you try to multiply or divide two numbers that will yield an answer of exactly -32,768, an overflow will result because the multiply and divide routines work on absolute values internally.

All operations, including the string-to-numeric conversions, will

Text continued on page 226

NO MEMORY PARITY? Good luck!



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and character literals

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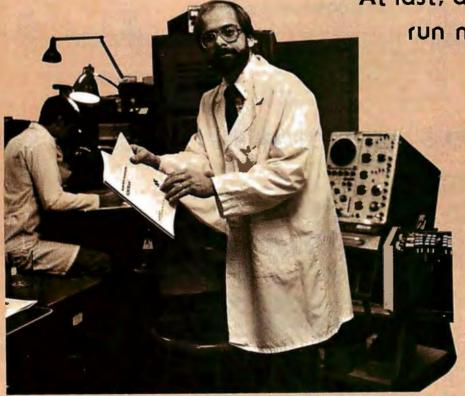
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97 98

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4000

4001

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4005

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AA

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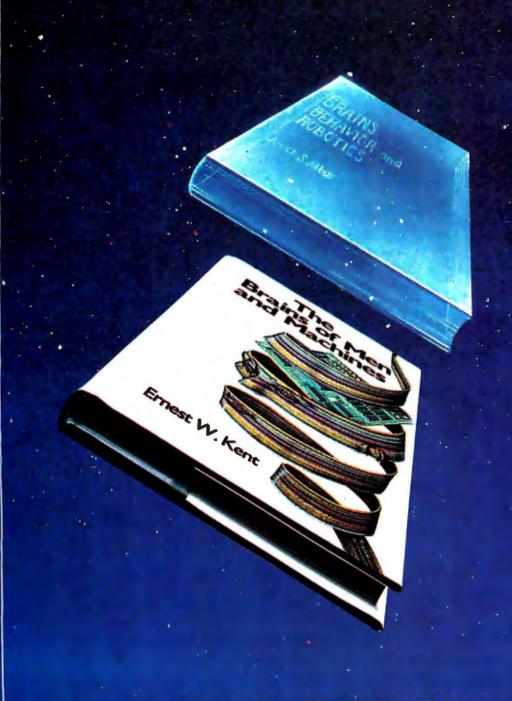
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RAR

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4000H
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**** EXTENDED ARITHMETIC SUBROUTINES FOR 8080 COMPUTERS *****
                   WRITTEN BY BRUCE D. CARBREY
                                                             REVISION 0
                                                                                  SEPT., 1977
                   THESE ROUTINES EXTEND THE ARITHMETIC CAPABILITIES OF
                   THE 8080 MICROCOMPUTER TO INCLUDE INTEGER ARITHMETIC
                   ON SIGNED, 16-BIT QUANTITIES, USING BINARY, TWOS-COMPLEMENT
                   ARITHMETIC.
                                    THE RANGE OF PERMISSABLE VALUES WITHOUT
                   OVERFLOW IS
                                   -32767 TO +32767
                   DECIMAL. TO USE A MATH ROUTINE. SIMPLY LOAD THE REGISTER(S) INDICATED WITH THE OPERAND(S). AND CALL THE APPROPRIATE ROUTINE. THE ANSWER WILL BE
                   RETURNED IN THE REGISTER(S) INDICATED. THE SIGN (S) A ZERO (Z) FLAGS WILL BE SET TO REFLECT THE VALUE OF THE RESULT IN THE SAME WAY AS FOR AN ORDINARY 8-BIT ADD.
                                                                   THE SIGN (S) AND
                   LOGIC IS PROVIDED FOR DETECTING OVERFLOW FOR ALL OPERATIONS.
                   WHICH RESULTS IN A CALL TO A ROUTINE NAMED OVERFLOW ( WHICH IS NOT SUPPLIED SINCE YOU MUST DECIDE WHAT YOU WANT TO DO IN CASE OF OVERFLOW--- PROBABLY PRINT A MESSAGE
                   AND JUMP TO YOUR MONITOR).
                   IN ADDITION TO THE MATH OPERATORS. TWO UTILITY SUBROUTINES
                   ARE PROVIDED FOR STRING-NUMERIC AND NUMERIC-STRING CONVERSION.
                   ENTRY
                                SUBROUTINE FUNCTION
                   EADO
                                (HL) = (HL) + (DE)
                   FSUR
                                (HL) = (HL) - (DE)
                   EMULT
                                (HL) = (HL) * (DE)
                    EDIVMOD
                                (HL) = (HL) / (DE) + AND (DE) = (HL) MOD (DE)
                   ESIGN
                                SET (S). (Z) FLAG TO REFLECT (HL). LEAVING (HL)
                               UNCHANGED.
                   ECMP
                                SET (S), (Z) FLAGS TO REFLECT (HL) - (DE), LEAVING
                                (HL) AND (DE) UNCHANGED.
                   DECBIN
                               CONVERT ASCII CHARACTER STRING REPRESENTING A SIGNED
                               DECIMAL INTEGER TO A SIGNED BINARY NUMBER.
CONVERT A SIGNED BINARY NUMBER TO AN ASCII STRING
                   BINDEC
                               REPRESENTING THE SIGNED DECIMAL VALUE OF THE NUMBER.
                              ************************
                    MATH PACKAGE EXECUTION TIMES IN MICRO-SECONDS:
                    ROUTINE TYPICAL
                                          WORST CASE
                    EADD
                                30
                                            54
                    ESUB
                                50
                                            74
                    EMULT
                                           517
                               370
                    EDIVMOD
                               680
                                          2500
                    ***** YOU MUST PROVIDE PATCHES TO THESE TWO ROUTINES... *****
         OVERFLOW
                     EQU
                                             WHERE TO GO AFTER OVERFLOW
         CONVERR
                     EQU
                                              WHERE TO GO ON STRING-NUMERIC ERROR
         SUBROUTINE EADD - ADD (HL) TO (DE), RESULT TO (HL)
                     (HL) = (HL) + (DE)
                   ON RETURN. SIGN. ZERO FLAGS WILL REFLECT RESULT. CY CLEARED.
                   A REGISTER CLOBBERED. B. C. D. E REGS RESTORED.
        EADD
                    MOV
                                 A.H
                                             TEST IF SIGNS ARE SAME OR DIFFER...
                     XRA
E680
                     ANI
                                 80H
                     DAD
                                             ADD, WITHOUT AFFECTING ZERO FLAG... SKIP OVERFLOW TEST IF SIGNS DIFFER
C20E40
                     JNZ
                                 ESIGN
```

TEST FOR OVERFLOW BY ...



KNOW THYSELF

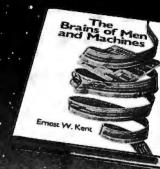
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"Humans are no longer limited to philosophic in-trospection in their strivings for self-knowledge. They can now attempt to analyze and under-stand the workings of the human mechanism." Emest W. Kent



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John Whitney is on the Faculty in the Department of Art at the University of California, Los Angeles.

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Scott Kim is a doctoral student in Computer Science at Stanford University and is a concert pianist and composer.

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Dr. James S. Albus is Project Manager with the National Bureau of Standards.

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                                                   OVERFLOW
                                                             CHECK FOR ARITH OVERFLOW
118
                                                             FALL THRU TO .....
119
                            SUBROUTINE ESIGN - SET (S) . (Z) FLAGS TO REFLECT (HL)
122
123
                                      A REGISTER CLOBBERED, ALL OTHERS RESTORED.
124
125
          400F
                    ΔF
                            ESIGN
                                       XRA
                                                             CLEAR FLAGS
                                                             SET FLAGS TO REFLECT HI BYTE RETURN IF HI-ORDER BYTE IS NON-0
                                       ADD
126
          400F
                    84
                                                   н
127
          4010
                    CO
                                       RNZ
                                                             ELSE, SEE IF L IS 0 TOO ...
128
          4011
                    85
                                       ADD
129
          4012
                    C8
                                       RZ
                                                              AND IF SO, RETURN
                                       XRA
130
          4013
                    AF
                                                   A
                                                             ELSE, FORCE FLAGS TO SHOW +
          4014
                    3C
                                       INR
131
132
          4015
                    C9
                                       RET
                            SUBROUTINE ESUB - SUBTRACT (DE) FROM (HL), RESULT TO (HL)
135
                                       (HL) = (HL) - (DE)
136
                                      ON RETURN, ZERO, SIGN FLAG REFLECT RESULT. CY CLEARED.
137
                                      A REGISTER CLOBBERED. B. C. D. E RESTORED.
138
          4016
139
                    05
                            ESUB
                                       PUSH
 140
          4017
                    EB
                                        XCHG
 141
          4018
                    CD3040
                                        CALL
                                                   COMP2
                                                             FORM 2S COMPLEMENT OF SUBTRAHEND ...
142
          4018
                    CD0040
                                        CALL
                                                   EADD
                                                              ... AND PROCEED AS IN ADDITION
143
          401E
                    DI
                                        POP
144
          401F
                                       RET
                            ECHS - CHANGE SIGN OF REGISTER (HL)
147
                                      (HL) = -(HL)
148
                                      ON RETURN, ZERO, SIGN FLAG REFLECT RESULT. CY CLEARED.
149
                                      A REGISTER CLOBBERED. B, C, D, E RESTORED.
150
151
          4020
                    7C
                            ECHS
                                       MOV
                                                  A.H
152
                    0680
                                                             CHECK FOR THAT ONE NASTY CASE...
...OF (HL) = EXACTLY -32768...
          4021
                                       SUI
                                                  80H
153
          4023
                    C22A40
                                       JNZ
                                                  ECHSGO
154
                    85
          4026
                                       ADD
                                                             ... WHICH CANT BE COMPLEMENTED RIGHT
                                                             ...AND WHEN DETECTED, ABORT
ELSE, FORM 2S COMPLEMENT IN (HL)
155
          4027
                    CC0000
                                       CZ
                                                  OVERFLOW
156
          402A
                    CD3040 ECHSGO
                                       CALL
                                                  COMP2
157
          402D
                    C30E40
                                       JMP
                                                  ESIGN
                                                             SET FLAGS AND RETURN
158
159
                                      SUBROUTINE COMP2 - FORM 2S COMPLEMENT OF (HL)
160
161
          4030
                    7C
                            COMPS
                                       MOV
                                                  A.H
                    2F
162
          4031
                                       CMA
163
          4032
                    67
                                       MOV
                                                  H.A
          4033
                    70
164
                                       MOV
                                                  A.L
165
          4034
                    2F
                                       CMA
                    6F
166
          4035
                                       MOV
                                                  L.A
167
                    23
          4036
                                       INX
                                                  н
          4037
168
                                       RET
                            SUBROUTINE EMULT - MULTIPLY (HL) BY (DE), RESULT TO (HL)
                                       ************
171
                                      (HL) = (HL) * (DE)
                                      ON RETURN, ZERO, SIGN FLAG REFLECT RESULT. CY CLEARED.
172
```

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Circle 171 on inquiry card.



```
Listing 1 continued:
                                       A REGISTER CLOBBERED. B. C. D. E RESTORED.
173
174
                    C5
                                        PUSH
          4038
175
                            EMULT
176
          4039
                    05
                                        PUSH
177
                    CD6F40
                                        CALL
                                                   RSLTSIGN FIND RESULT SIGN, ABS VAL OF OPERANDS
          403A
                    AF
                                        XRA
          4030
178
179
          403E
                    84
                                        ADD
                                                   HLSMALL
                                                              BRANCH IF (HL) LESS THAN 8 BITS
180
          403F
                    CA4840
                                        JZ
                    AF
                                        XRA
181
          4042
                    82
                                        ADD
                                                              ELSE, OTHER OP MUST BE .LT. B BITS ...
182
          4043
                                                   ח
                                                               ... OR OVERFLOW WOULD RESULT
                                                   OVERFLOW
                    C40000
183
          4044
                                        CNZ
                                        XCHG
                                                               (HL) NOW HAS AN OP WITH .LT. 8 BITS
184
          4047
                    EB
                                                               MOVE 8-BIT OR LESS MULTIPLIER TO A INITIALIZE PARTIAL PRODUCT
185
186
                            HLSMALL
                                        MOV
          4048
                    7D
210000
          4049
                                        LXI
                            XMLOOP
                                        STC
                                                               CLEAR CARRY ...
187
          404C
                    37
                    3F
                                        CMC
188
          4040
          404E
                    1F
                                        RAR
                                                               ROTATE MULTIPLIER RITE OFF END
189
                    025640
                                                   SHIFTOP
                                                              IF BIT SHIFTED-OUT WAS 0. SKIP
                                        JNC
190
          404F
                                                               ELSE. ADD MULTIPLICAND TO PARTIAL PROD.
191
          4052
                    19
                                        DAD
                                                   OVERFLOW
192
          4053
                    DC0000
                                        CC
                                                               ... WHILE CHECKING FOR OVERFLOW
193
                    EB
                            SHIFTOP
                                        XCHG
          4056
194
          4057
                    29
                                        DAD
                                                               SHIFT MULTIPLICAND LEFT 1 BIT ...
                                                              ... WHILE CHECKING FOR OVERFLOW
195
                    DC0000
                                                   OVERFLOW
          4058
                                        CC
196
          4058
                    EB
                                        XCHG
197
          405C
                    B7
                                        ORA
                                                               BRANCH TO TOP OF LOOP IF MULT IS NON-0
198
          4050
                    C24C40
                                        JNZ
                                                   XMLOOP
199
          4060
                    01
                                        POP
                                                   D
                                                               WHEN MULTIPLY DONE, RECALL (DE)
200
                            SIGNACL
                                        MOV
          4061
                    7C
                                                   A.H
                    07
                                                               MAKE FINAL OVERFLOW CHECK ...
201
          4062
                                        RLC
                                                              FOR VALUES BETWEEN32768 AND 65535 INCLUS.
                    DC0000
                                                   OVERFLOW
202
          4063
                                        CC
203
          4066
                    78
                                        MOV
                                                   A.B
                                                               THEN RECALL SIGN BYTE
204
          4067
                    17
                                        RAL
                    DC3040
                                                   COMP2
                                                               CHANGE SIGN OF RESULT IF IT IS TO BE -
205
          4068
                                        CC
          4068
                                        POP
206
                    Cl
                    C30E40
                                        JMP
                                                   ESIGN
                                                               SET FLAGS AND RETURN
207
          406C
208
209
                                       SUBROUTINE RSLTSIGN - COMPUTE SIGN OF RESULT FOR * AND /
210
211
                                       ON RETURN: (HL) = ABSOLUTE VALUE OF (DE): (DE) = ABS. VAL (HL):
                                       (B) = SIGN OF RESULT IN MOST SIGNIFICANT BIT.
212
213
                            RSLISIGN
                                        MOV
                                                              FETCH SIGN BYTE OF 1ST OPERAND
214
          406F
                    44
7C
                                                   B.H
215
          4070
                                        MDV
                                                               ... TO B AND ALSO TO A...
                                                   A.H
216
          4071
                    17
                                        RAL
217
          4072
                    DC3040
                                        CC
                                                   COMP2
                                                               ABSOLUTE VALUE OF (HL)
                                        XCHG
218
          4075
                    ER
                                                               2ND OPERAND ...
                    7C
219
          4076
                                        MOV
                                                               SIGN BYTE TO A ...
                                                   A.H
220
          4077
                    84
                                        XRA
                                                   ₿
                                                               RESULTANT SIGN ...
                                                               ...TO MSB OF REG B FOR LATER RECALL SIGN BYTE OF 2ND OP TO A
221
          4078
                    47
                                        MOV
                                                   B,A
          4079
                    7C
                                        MOV
                                                   A.H
222
223
          407A
                    17
                                        RAL
          407B
                                        JC
                                                   COMP2
                                                               ABSOLUTE VALUE, THEN RETURN.
224
                    DA3040
225
          407E
                    C9
                                        RET
                            SUB. EDIVMOD - DIVIDE (HL) BY (DE), QUO. TO (HL), REM. TO (DE)
228
                                       ON CALL: (HL) = DIVIDEND, (DE) = DIVISOR.
                                       ON RETURN: (HL) = QUOTIENT, (DE) = REMAINDER.
                            #
229
                                                                             CY CLEARED.
230
                                       FLAGS REFLECT VALUE OF QUOTIENT.
                                       A REGISTER CLOBBERED. B. C RESTORED.
231
                                       REMAINDER IS ALWAYS POSITIVE, REGARDLESS OF SIGN OF OPERANDS.
232
233
                    C5
                                        PUSH
234
          407F
                            EDIVMOD
                                                              IF DIVISOR = 0 ...
235
          4080
                    AF
                                        XRA
236
          4081
                    B3
                                        ORA
                                                   E
237
          4082
                                        ORA
                    CC0000
                                        CZ
                                                   OVERFLOW
                                                                .. THEN ABORT
238
          4083
                                                   RSLTSIGN
                                                               COMPUTE RESULT SIGN: SWAP DE, HL
239
          4086
                    CD6F40
                                        CALL
                                        MDV
                                                               INSURE THAT NEITHER OPERAND ...
          4089
                                                   A.H
                    7C
240
                                                               ... WAS THAT NASTY SPECIAL CASE...
241
          408A
                    82
                                        ORA
                                                   n
                                                              ...OF EXACTLY -32768...
...AND IF IT WAS, ABORT
SAVE RESULT SIGN BYTE
242
          408B
                    07
                                        RLC
243
          408C
                    DC0000
                                                   OVERFLOW
                                        PUSH
244
          408F
                    C5
                                                               MOVE DIVIDEND ( = REM) TO BC
                    4B
                                        MOV
                                                   C,E
245
          4090
          4091
                    42
                                        MDV
                                                   B.D
246
247
          4092
                    110000
                                        LXI
                                                   0,0
                                                               INITIALIZE QUOTIENT = 0 ...
                                                               ...ON TOP OF STACK (TOS)
          4095
                                        PUSH
                                                   D
248
                    05
                                                               NOW BC = REM. DE=DIV. TOS=QUO
          4096
                    EB
                                        XCHG
249
```


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Time between dates

1 RULE78 2 ANNUI

3 DATE

4 DAYYEAR 5 LEASEINT

6 BREAKEVN

7 DEPRSL 8 DEPRSY

9 DEPRDB

10 DEPRDDB 11 TAXDEP

12 CHECK2

13 CHECKBKI

14 MORTGAGE/A

15 MULTMON

16 SALVAGE 17 RRVARIN

18 RRCONST

19 EFFECT

20 FVAL

21 PVAL 22 LOANPAY

23 REGWITH

24 SIMPDISK

25 DATEVAL

26 ANNUDEF

27 MARKUP

28 SINKFUND

29 BONDVAL

30 DEPLETE

31 BLACKSH

32 STOCVALI 33 WARVAL

34 BONDVAL2

35 EPSEST

36 BETAALPH

37 SHARPEI

38 OPTWRITE

39 RTVAL

40 EXPVAL

41 BAYES

42 VALPRINF 43 VALADINF

44 CITLITY

45 SIMPLEX 46 TRANS

47 EOQ

48 QUEUE1

49 CVP

50 CONDPROF

51 OPTLOSS 52 FQUOQ

55 QUEUECB

58 CAPI

Day of year a particular date falls on interest rate on lease Breakeven analysis Straightline depreciation

Interest Apportionment by Rule of the 78's

Sum of the digits depreciation Declining balance depreciation

Annuity computation program

Double declining balance depreciation Cash flow vs. depreciation tables

Prints NEBS checks along with daily register

Checkbook maintenance program

Mortgage amortization table

Computes time needed for money to double, triple, etc.

Determines salvage value of an investment Rate of return on investment with variable inflows Rate of return on investment with constant inflows

Effective interest rate of a loan

Future value of an investment (compound interest)

Present value of a future amount Amount of payment on a loan

Equal withdrawals from investment to leave 0 over

Simple discount analysis

Equivalent & nonequivalent dated values for oblig.

Present value of deferred annuities

% Markup analysis for items

Sinking fund amortization program

Value of a bond

Depletion analysis

Black Scholes options analysis

Expected return on stock via discounts dividends

Value of a warrant

Value of a bond

Estimate of future earnings per share for company

Computes alpha and beta variables for stock Portfolio selection model·i.e. what stocks to hold

Option writing computations

Value of a right

Expected value analysis

Bayesian decisions

Value of perfect information Value of additional information

Derives utility function

Linear programming solution by simplex method Transportation method for linear programming

Economic order quantity inventory model

Single server queueing (waiting line) model Cost-volume-profit analysis

Cost-benefit waiting line analysis

Conditional profit tables

Opportunity loss tables

Fixed quantity economic order quantity model

NAME DESCRIPTION

53 FQEOWSH As above but with shortages permitted 54 FQEOQPB As above but with quantity price breaks

56 NCFANAL Net cash-flow analysis for simple investment 57 PROFIND Profitability index of a project

Cap. Asset Pr. Model analysis of project Circle 172 on inquiry card.

59 WACC 60 COMPBAL

61 DISCBAL 62 MERGANAL

63 FINRAT

64 NPV

65 PRINDLAS

66 PRINDPA

67 SEASIND

68 TIMETR

69 TIMEMOV

70 FUPRINF

71 MAILPAC

72 LETWRT 73 SORT3

74 LABELI

75 LABEL2

76 BUSBUD

77 TIMECLCK

78 ACCTPAY

79 INVOICE 80 INVENTS

TELDIR

TIMUSAN

83 ASSIGN

84 ACCTREC 85 TERMSPAY

86 PAYNET

SELLPR ARBCOMP

89 DEPRSF

UPSZONE 91 ENVELOPE

92 AUTOEXP

93 INSFILE

94 PAYROLL2

95 DILANAL

LOANAFFD 96

RENTPRCH

98 SALELEAS

99 RRCONVBD

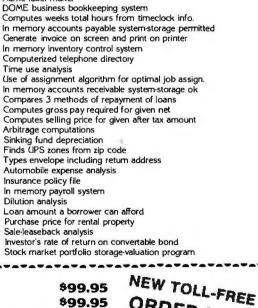
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```
LXI
                                                   H.1
                                                              INITIALIZE HOLD = 1
          4097
                    210100
250
251
                                                              LEFT SHIFT HOLD
                    29
                            DBLDIV
252
          409A
                                        DAD
253
          409B
                    E8
                                        XCHG
                                                              NOW BC=REM, DE=HOLD, HL=DIV, TOS=QUO
254
          409C
                    29
                                        DAD
                                                               LEFT SHIFT DIV
                    CDC940
255
          409D
                                        CALL
                                                   CMPBH
                                                               COMPARE DIV TO REM
                                                               NOW BC=REM, DE=DIV, HL=HOLD, TOS=QUO
                    E8
                                        XCHG
256
          40A0
                                                              BRANCH BACK IF DIV < REM
DUMMY XCHG TO MAKE LOOP WORK 1ST PASS...
                    D29A40
                                        JNC
                                                   DBLDIV
257
          40A1
          40A4
258
                    FR
                                        XCHG
259
          40A5
                    Eθ
                            HALVEDIV
                                        XCHG
                                                              NOW BC=REM, DE=DIV, HL=HOLD, TOS=QUO
                    CDCE40
260
          40A6
                                        CALL
                                                   DI VBY2
                                                              HOLD = HOLD/2 (RITE SHIFT)
                    CAC240
                                        JZ
                                                   DIVDONE
                                                               IF HOLD = 0, WERE DONE
261
          40A9
                                                              NOW BC=REM. DE=HOLD. HL=DIV. TOS=QUO
                                        XCHG
          40AC
                    FB
262
                                                              RITE SHIFT DIV
COMPARE DIV TO REM...
                    CDCE40
                                        CALL
263
          40AD
                                                   DIVBYZ
                    CDC940
                                                   СМРВН
          40R0
                                        CALL
264
                    FAA540
265
          4083
                                        JM
                                                   HALVEDIV
                                                              IF DIV > REM, BRANCH BACK
                                        MOV
266
          40B6
                    79
                                                   A,C
                                                              REM = REM - DIV...
267
          40B7
                    95
                                        SUB
          40B8
                    4F
                                        MOV
268
                                                   C.A
269
          4089
                    78
                                        MOV
                                                   A.B
270
          40BA
                    9C
                                       588
                                                   H
          40BB
                    47
271
                                       MOV
                                                   B.A
272
          40BC
                    E3
                                        XTHL
                                                              NOW BC=REM. DE = HOLD, HL=QUO, TOS=DIV
          40BD
                    19
                                        DAD
                                                   D
                                                              QUO = QUO + HOLD
273
274
          40BE
                    E3
                                        XTHL
                                                              NOW BC=REM, DE=HOLD, HL=DIV, TOS=QUO
275
          40BF
                    C3A540
                                        JMP
                                                   HALVEDIV
                                                              FNODO.
                    F1
                                       POP
                                                              GET QUOTIENT TO HL
277
          40C2
                            DIVDONE
                                                   H
                                                   E,C
          40C3
                    59
278
                                       MOV
                                                               MOVE FINAL REM TO DE
279
          40C4
                    50
                                        MOV
                                                   D.B
280
          40C5
                    Cl
                                       POP
                                                   Θ
                                                               RECALL SIGN BYTE FOR RESULT
281
          40C6
                    C36140
                                        JMP
                                                   SIGNRCL
                                                              COMPUTE FINAL SIGN OF RESULT AND RETURN
282
                                       INTERNAL SUBROUTINE CMPBH - COMPARE BC TO HL ...
283
284
285
          40C9
                    79
                            СМРВН
                                        MOV
                                                   A,C
          40CA
                    95
286
                                        SUB
                                                   L
287
          40CB
                    78
                                       MOV
                                                   A.B
288
          40CC
                    9C
                                        SBB
                                                   н
                                                              SIGN, ZERO NOW REFLECT (BC) - (HL) ...
289
                    C9
          40CD
                                       RET
290
291
                                       INTERNAL SUBROUTINE DIVBY2 - DIVIDE (HL) BY 2 (RITE SHIFT)
292
                                       KILLS PSW.
                                                    REMAINDER RETURNED IN CY.
293
294
          40CE
                    ΔF
                            DIVBYZ
                                        XRA
                                                              CLEAR CY
295
                    7C
          40CF
                                        MDV
                                                   A,H
296
          40D0
                    1F
                                        RAR
297
                    67
          40D1
                                       MOV
                                                   H,A
298
          40D2
                    70
                                        MDV
                                                   A+L
299
          40D3
                    1F
                                        RAR
                    6F
300
          40D4
                                        MOV
                                                   L.A
301
          40D5
                    B4
                                        ORA
                                                   Н
                                                              SET ZERO FLAG IF BOTH H AND L = 0
          4006
                    C9
                                        RET
302
```

```
305
                                    THIS ROUTINE CONVERTS A STRING OF ASCII CHARACTERS REPRESENTING
                                    A NUMBER TO A SIGNED 16-BIT NUMBER IN TWOS COMPLEMENT FORM. LEGAL RANGE OF CONVERTIBLE VALUES IS -32767 TO +32767.
306
307
                                    LEGAL FORM FOR STRING IS ...
308
309
310
                                    <BLANKS><SIGN><BLANKS><DIGITS><NON-DIGIT>
311
312
                                    WHERE
                                            <BLANKS> IS 0 OR MORE BLANKS.
                                            <SIGN> IS +, -, OR OMITTED, 
<DIGITS> IS A STRING OF 1 OR MORE NUMERIC DIGITS,
313
314
                                                      REPRESENTING AN INTEGER NOT EXCEEDING 32767.
315
316
                                            <NON-DIGIT> IS ANY NON-DIGIT CHARACTER (E.G., A BLANK).
317
                           4
                                    USAGE:
318
319
320
                                       CALL:
321
                                     (DE) = ADDRESS OF START-OF-ASCII STRING TO BE CONVERTED.
                           4
                                    ON RETURN TO CALLING PROGRAM ...
322
                                     (HL) = RETURNED SIGNED NUMERIC VALUE
323
                                     (DE) = ADDRESS OF TERMINAL CHARACTER OF STRING (<NON-DIGIT>)
324
                                    SIGN AND ZERO FLAGS WILL BE SET TO REFLECT VALUE IN (HL).
325
```

SUBROUTINE DECBIN - CONVERT ASCII DECIMAL TO BINARY NUMBER

Listing 1 continued on page 220

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```
Listing 1 continued:
```

```
CY CLEARED. A REGISTER CLOBBERED. B. C RESTORED.
326
327
                                      NOTES ON METHOD... B REG USED TO HOLD 3 FLAGS...
BIT 7="-" FLAG = MINUS SIGN ENCOUNTERED
328
329
                                      BIT 6= "SE" FLAG = SIGN ENCOUNTERED
330
                                      BIT 0 = "DE" = DIGIT ENCOUNTERED,
331
332
333
          40D7
                    C5
                            DECRIN
                                       PUSH
                                                   В
          4nD8
                    0600
                                       MVI
                                                              INITIALIZE FLAGS -, DE, SE
                                                  B . 0
334
          40DA
335
                    210000
                                       LXI
                                                   H,0
                                                              INITIALIZE RESULT
336
          40DD
                    1 A
                            AKLOOP
                                       LDAX
                                                  D
                                                              FETCH NEXT ASCII CHARACTER
          40DE
                    0630
                                       SUI
                                                   48
                                                              CONVERT CHAR TO BCD DIGIT IF POSSIBLE
337
                                                              SAVE (CHARACTER-48) IN C
338
          40E0
                    4F
                                       MOV
                                                   C,A
                                                              IS IT A DIGIT 0 THRU 9 ...
339
          40E1
                    FAFF40
                                        JM.
                                                   NOTDIGIT
                    FE0A
                                       CPI
          40E4
340
                                                   10
                    F2FF
                                        .IP
                                                   NOTDIGIT
341
          40E6
                                       PUSH
          4nE9
342
                    05
                                                   n
                                                              ... IF SO, SAVE BUFFER POINTER
343
          40EA
                    110A00
                                       LXI
                                                   D, 10
                                                              ... MULTIPLY PARTIAL RESULT BY 10...
                                       CALL
344
          40ED
                    CD3840
                                                   EMULT
                                                              ... (ALSO CHECKING FOR OVERFLOW) ...
345
          40F0
                    1600
                                       MVI
                                                   D,0
                                                              ... AND ADD IN VALUE OF DIGIT ...
          40F2
                    59
                                       HOV
                                                   E.C
346
          40F3
                    CD0040
                                                   EADD
                                       CALL
347
                                                              (HL) = (HL)*10 + DIGIT
                                                              RECALL BUFFER POINTER
          40F6
                                       POP
348
                    DI
                                                   D
349
          40F7
                    13
                                       INX
                                                   D
                                                              BUMP BUFFER POINTER
350
          40F8
                    3E01
                                       MVI
                                                   A , 1
                                                              ... SET "DIGIT ENCOUNTERED (DE) FLAG...
351
          40FA
                    B0
                                       ORA
                                                   В
352
          40FB
                    47
                                       MOV
                                                  B.A
          40FC
353
                    C3DD40
                                        JMP
                                                   AKLOOP
                                                              ... AND WERE READY FOR NEXT CHARACTER
354
355
                            .
                                      COME HERE FOR ANY CHARACTER EXCEPT 0,1,...9
356
357
          40FF
                    79
                            NOTDIGIT
                                       MOV
                                                   A.C
                                                              RECALL (CHAR-48)
          4100
                    FEF0
358
                                       CPT
                                                   -16
                                                              IS IT A BLANK. SET ZERO FLAG IF SO.
359
          4102
                    78
                                       MOV
                                                              RECALL FLAGS
360
          4103
                    0F
                                       RRC
                                                              TEST "DIGIT ENCOUNTERED" FLAG IN CY
                                                              IF DIGITS ENCOUNTERED PRIOR. WERE DONE ... ELSE, IF NOT BLANK TRY + OR -
                    DA6140
361
          4104
                                       JC
                                                   SIGNRCL
          4107
362
                    C20E41
                                        JNZ
                                                   TRYSIGN
                                                              ... ELSE IGNORE LEADING BLANK, AND PROCEED WITH NEXT CHARACTER
          410A
363
                    13
                                       INX
                                                   D
          410B
                    C3DD40
                                       JMP
                                                   AKLOOP
364
                            TRYSIGN
365
          41 0E
                    78
07
                                       MOV
                                                   A.B
                                                              TEST "SE" FLAG IN CY
          410F
                                       RLC
366
367
          4110
                    07
                                       RLC
368
          4111
                    DC0000
                                       CC
                                                   CONVERR
                                                              ELSE RECALL (CHAR-48)
369
          4114
                    79
                                       MOV
                                                  A.C
                    FEFD
                                                              IS IT "-" . .
370
          4115
                                       CPI
                                                   -3
371
          4117
                                                   TRYPLUS
                                                              ... IF NOT TRY FOR "+" SIGN...
                    C2
                                       JN7
                    3EC0
372
                                                              ... IF IT IS "-", SET SE AND - FLAG
          411A
                                       MVI
                                                   A,OCOH
373
          411C
                    RO
                                       ORA
                                                  В
374
          411D
                    47
                                       MDV
                                                  B,A
375
          411E
                                                              BUMP BUFFER POINTER
                    13
                                       INX
                                                  D
                    C3DD40
          411F
                                       JMP
                                                   AKLOOP
                                                              AND PROCEED WITH NEXT CHARACTER
376
377
                    FEFB
                           TRYPLUS
                                       CPI
          4122
                                                   -5
                                                              IS IT "+" CHARACTER...
                    C40000
378
          4124
                                       CNZ
                                                   CONVERR
                                                              IF NOT ITS AN ERROR
379
          4127
                    3E40
                                       MVI
                                                   A , 40H
                                                              IF IT IS "+", SET "SE" FLAG
380
          4129
                    B0
                                       ORA
                                                  В
381
          412A
                    47
                                       MOV
                                                  B,A
          4128
382
                    13
                                       INX
                                                              BUMP BUFFER POINTER
                                                  D
          412C
                    C3DD40
                                       JMP
                                                   AKLOOP
383
                                                              AND PROCEED WITH NEXT CHARACTER
384
                          SUBROUTINE BINDEC - CONVERT BINARY NUMBER TO DECIMAL ASCII STRING
```

```
387
                                              THIS ROUTINE GENERATES A STRING OF ASCII CHARACTERS
388
                                 #
                                              REPRESENTING A SIGNED DECIMAL INTEGER. THE STRING IS GENERATED LEFT-JUSTIFIED, WITH LEADING ZEROS SUPPRESSED.
389
                                             THE STRING WILL OCCUPY FROM 1 TO 6 CHARACTERS DEPENDING ON THE SIGN AND MAGNITUDE OF THE NUMBER DESIRED.

ON CALL: (HL) = SIGNED BINARY NUMBER TO BE CONVERTED.

(DE) = ADDRESS OF FIRST CHARACTER OF BUFFER WHERE STRING IS
390
391
392
393
                                              TO BE GENERATED.
394
                                              ON RETURN: (DE) = ADDRESS OF NEXT BYTE AFTER THE STRING
395
                                              WHICH WAS GENERATED. (A) = NUMBER OF CHARACTERS GENERATED.
396
397
                                              B. C. H. L RESTORED.
398
                                 BINDEC
                                                             В
399
            412F
                        C5
                                               PUSH
            4130
                        F5
                                               PUSH
                                                            Н
                                                                          SAVE HL
400
401
            4131
                        010000
                                               LXI
                                                            B,0
                                                                          B=MINUS FLAG, C= DIGIT COUNTER
```

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402	4134	E5	PUSH	н	
403	4135	29	DAD	Н	PUSH SIGN INTO CY
404	4136	E1	POP	H	RECALL UN-SHIFTED NO.
405	4137	D24241	JNC	DIVIOK	FALL THRU FOR - NUMBER
406	413A	3E2D	MVI	A , 45	ASCII MINUS SIGN
407	413C	12	STAX	D	INTO BUFFER
408	413D	04	INR	В	SET MINUS FLAG
409	413E	13	INX	D	AND BUFFER POINTER
410	413F	CD3040	CALL	COMP2	ABSOLUTE VALUE OF NUMBER
411	4142	EB DIVIOK	XCHG		N TO DE, BUFF ADDR TO HL
412	4143	228441	SHLD	BUFADR	SAVE BUFFER ADDRESS
413	4146	EB	XCHG	00, 70,	
414	4147	111027	LXI	D.10000	
415	414A	CD6E41	CALL	CNVTIDIG	FIND FIRST DECIMAL DIGIT
416	414D	11E803	LXI	D.1000	THE THE DECIME DIGIT
417	4150	CD6E41	CALL	CNVTIDIG	SECOND DEC DIGIT
418	4153	116400	LXI	D.100	SECOND DEC DIGITAGE
419	4156	CD6E41	CALL	CNVTIDIG	THIRD
420	4159	110A00	LXI	D•10	INTRDese
					4. 7 11
421	415C	CD6E41	CALL	CNVTIDIG	
422	415F	70	MOV	A+L	LAST DIGIT IS FINAL REMAINDER
423	4160	C630	ADI	48	CONVERT TO ASCII CHAR
424	4162	0C	INR	C	
425	4163	2A8441	LHLD	BUFADR	RECALL BUFFER POINTER
426	4166	E8	XCHG	_	
427	4167	12	STAX	D	INSTALL LAST CHARACTER INTO BUFFER
428	4168	79	MOV	A • C	RETURN CHARACTER COUNT IN A REG
429	4169	80	ADD	В	AND ADD 1 FOR MINUS SIGN IF MINUS
430	416A	13	INX	D	POINT TO NEXT DIGIT IN BUFFER
431	4168	F1	POP	Н	FINAL RESTORE FOR HL
432	416C	Cl	POP	В	RECALL B
433	416D	C9	RET		
434	416E	CD7F40 CNVT1DIG	CALL	EDIVMOD	DIVIDE REMAINDER BY 10**N
435	4171	EB	XCHG		NEW REM TO HL
436	4172	78	MOV	A,E	DIGIT TO A
437	4173	R1	ORA	C	IF NO NON-ZERO DIGITS SO FAR
438	4174	C8	RZ		AND THIS = 0, THEN SUPPRESS LEADING 0
439	4175	78	MOV	A,E	ELSE, RECALL DIGIT
440	4176	C630	ADI	48	CONVERT DIGIT TO CHAR
441	4178	n C	INR	С	UPDATE CHAR COUNTER
442	4179	EB	XCHG	_	or bring similar sources
443	417A	2A8441	LHLD	BUFADR	BUFFER ADDRESS LOAD
444	4170	77	MOV	M.A	STORE CHAR IN BUFFER
445	417E	23	INX	н	NEXT CHAR
446	417F	228441	SHLD	BUFADR	SAVE BUFFER POINTER
447	4182	EB	XCHG		SITE ST. IN I VANIEN
448	4183	C9	RET		
•					

* FOLLOWING MUST BE IN READ/WRITE MEMORY ...

450 451 4184 4186 DS END BUFADR TEMPORARY STORAGE FOR POINTER

--- SYMBOLIC CROSS-REFERENCE MAP ---

-SYMBOL-	-VALUE-	-R	-DEFINED-	-REFEREN	ICED-				
AKLOOP	40DD	#A	336	353	364	376	383		
BINDEC	412F	#A	399	29					
BUFADR	4184	# A	450	412	425	443	446		
CHIN	0000		7	13					
CHOUT	0000		8	27	36				
СМРВН	40C9	# A	285	255	264				
CNVTIDIG	416E	#A	434	415	417	419	421		
COMP2	4030	# A	161	141	156	205	217	224	410
CONVERR	0000		102	368	378				
DBLDIV	409A	# A	252	257					
DECBIN	40D7	*A	333	20	23				
DIVBY2	40CE	# A	294	260	263				
DIVDONE	40C2	# A	277	261					
DIVIOK	4142	#A	411	405					
EADD	4000	# A	110	142	347				
ECHS	4020	* A	151	*UNUSED					
ECHSG0	402A	#A	156	153					
EDIVMOD	407F	# A	234	434					
EMULT	4038	#A	175	25	344				
ESIGN	400E	*A	125	114	157	207			

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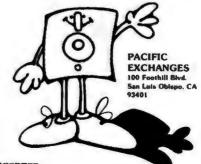
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HALVEDIV	40A5	*A		259	265	275						
HLSMALL	4048	#A		185	180							
INBUF	3038	# A		40	12	19						
MONI TOR	0000			9	35							
NOTDIGIT	40FF	* A		357	339	341						
OUTBUF	304F	*A		41	28	32						
OVERFLOW	0000			101	118	155	183	192	195	202	238	243
RSLTSIGN	406F	# A		214	177	239						
SHIFTOP	4056	* A		193	190							
SIGNRCL	4061	* A		200	281	361						
TEST	3000	* A		12	#UNUSED							
TEST1	3003	# A		13	18							
TEST2	3010	* A		19	16							
TEST3	302F	* A		33	38							
TRYPLUS	4122	*A		377	371							
TRYSIGN	410E	+A		365	362							
XMLOOP	404C	* A		187	198							
-COMMON	BLOCK-	- L	-									
ABSOL		01DF										

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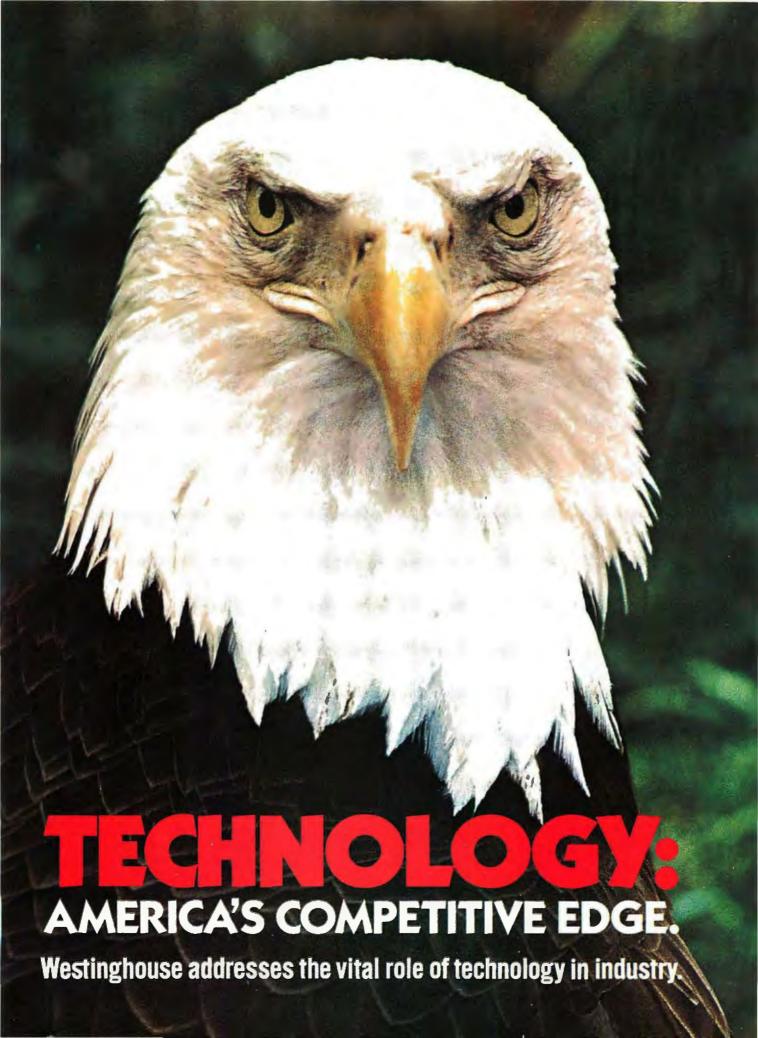
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Technology is the key to the world marketplace.

If we want to maintain America's competitive edge, we must make better use of present technologies, and encourage new ones.

Most of the firms and countries which have achieved conspicuous success in this world have done so because they possessed some special advantage. They had an edge over their competitors. In recent decades, America's competitive edge has been its technology. Our ability to originate and apply innovative scientific and engineering ideas earned us a commanding lead in the world market-place.

Things have changed

Unfortunately, that lead has dwindled. America's share of the world's manufactured goods market has eroded over the past 20 years, lost to foreign manufacturers. Not only have they captured part of what had been our share of the world market, but they are now successfully penetrating our own domestic markets.

What happened?

A look at a few statistics helps reveal some of the reasons for our reversals. Take patents. The number of domestic patent applications by Americans has been flat for several years. In contrast, the number of those filed here by foreign countries has been rising every year. In 1978, almost 37 percent of the patents granted went to foreign applicants. Or take the percentage of our Gross National Product going into industrial R&D. Over the past two decades, it has dropped precipitously.

What is needed

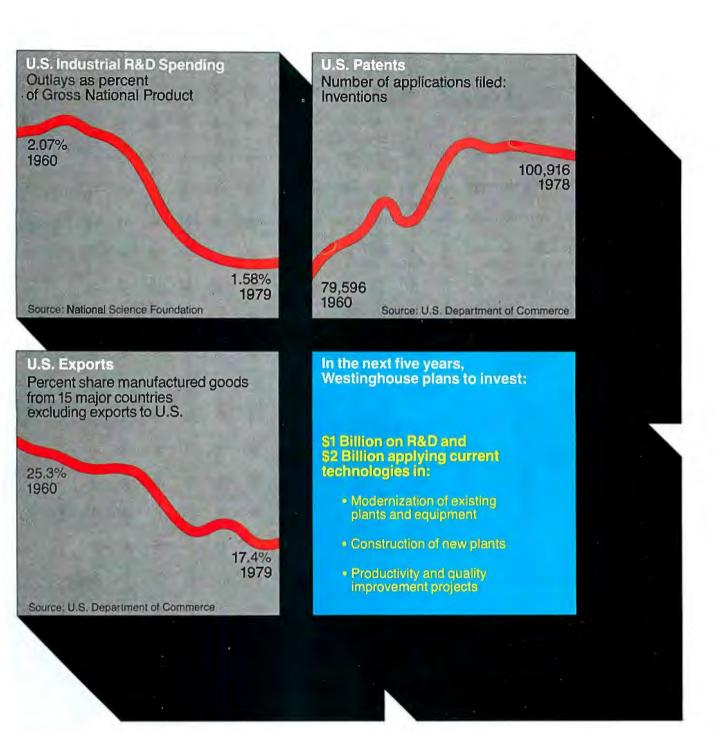
Fortunately, today Westinghouse and other corporations already have technologies which can help America maintain its technological leadership. And these same corporations are hard at work on technologies which can expand America's leadership. The problem lies in implementing those technologies. Because, while the development of new technologies costs a large amount of money, turning them into commercial realities requires far more.

A national commitment

Something else is needed: a united effort by industry, labor and government. Obviously, management should make a greater R&D effort to refine today's technologies, and develop new ones for tomorrow. Employes must realize that their cooperation is vital if America is to remain the most productive nation in the world. And our elected officials need to reestablish a sound economic foundation, because that is basic to all social progress. In particular, tax laws and monetary policy must be structured to allow industry to accumulate capital needed to apply available technologies, and invest in the development of still more advanced ones.

The Westinghouse role

At Westinghouse, we believe technology is vital to our nation, our customers, and our own progress. We're supporting that belief by ambitious R&D programs, by building and modernizing existing facilities, and by introducing innovative methods to improve both our own quality and productivity and that of our customers. Today's proven Westinghouse technologies are focused on key areas such as productivity, services, energy, and America's national security. These existing technologies, together with the ones we are developing for the future, represent our efforts to help maintain this nation's competitive edge. On the following pages are some examples.



WESTINGHOUSE TECHNOLOGY APPLIED TO ENERGY

Someday, Westinghouse technology will provide economical electricity from the sun, and clean gas from coal.

The fact that silicon photovoltaic cells can turn sunlight into electric current has been known for some time. The problem is the high cost involved. Westinghouse has invented a new dendritic web process that significantly reduces the cost of producing such cells. As a result, the U.S. Department of Energy's economic cost target now appears achievable. Westinghouse is working with the two largest electrical utilities in California to provide demonstration photovoltaic modules this year.

Advanced energy technologies

Westinghouse is involved in the advanced energy technologies that may play a role in this nation's energy future. For example, on the horizon are promising technologies like iron-nickel, and iron-air high power batteries. Also showing promise are fuel cells that chemically produce electricity. But until solar and other energy technologies become a reality, this nation will depend upon coal and nuclear power for its electricity. Westinghouse is focusing much of its effort on these two areas.

Clean gas from coal

Westinghouse has pioneered in coal gasification technology. Over the last decade we have developed a process to turn coal into a clean gas for power generation, and for industrial or synthetic natural gas applications. The process has the advantage that it can use virtually any type of coal, soft coal or hard coal. The environmental impact is minimal, regardless of the coal's moisture, sulphur, or ash content. With continued technical progress, Westinghouse coal gasification systems can be in commercial operation by the mid-1980's.

Nuclear technology

Nuclear power remains an economical and safe way of producing electricity. Westinghouse leads in the application of nuclear technology to generate electricity. And we are developing an advanced nuclear plant able to make more fuel thank uses.



WESTINGHOUSE **TECHNOLOGY APPLIED TO** SECURITY

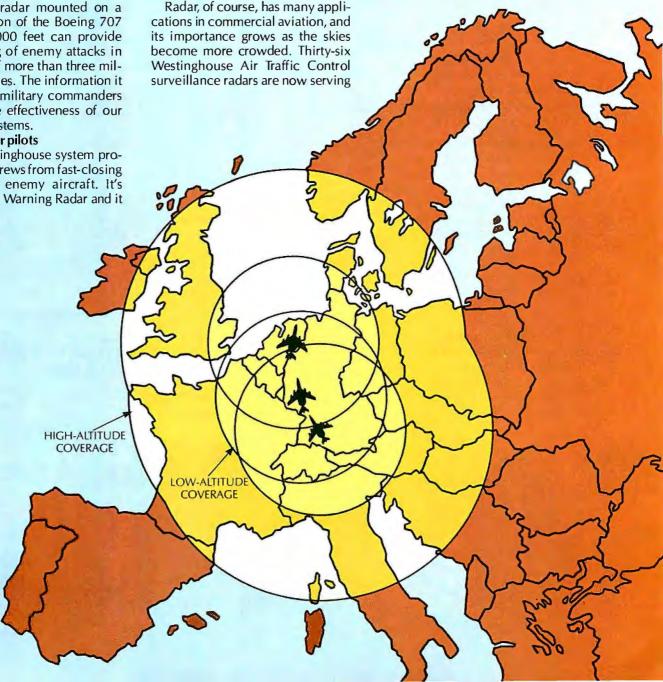
Today, Westinghouse Airborne Radar is one of our first lines of defense around the world.

It's called AWACS, an airborne warning and control system which provides long-range surveillance in an area at least 20 times greater than any surface-based system. It's already in use by our Air Force, and has been adopted by NATO. Just one AWACS radar mounted on a military version of the Boeing 707 flying at 30,000 feet can provide early warning of enemy attacks in an airspace of more than three million cubic miles. The information it helps give to military commanders multiplies the effectiveness of our air defense systems.

New safety for pilots

Another Westinghouse system protects aircraft crews from fast-closing missiles and enemy aircraft. It's called our Tail Warning Radar and it provides the pilot with accurate warnings to take evasive maneuvers. It also automatically triggers appropriate countermeasures. It's able to do all this in a split second, and with a phenomenally low false alarm rate.

the FAA, the Switzerland Federal Air Office, and the Canadian Department of National Defense. The FAA uses the radars in some of the nation's most heavily traveled areas. So, nearly all domestic commercial flights come under the surveillance of a Westinghouse radar at some point during their flight.



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How Westinghouse product can increase industrial

How to increase output per hour...
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with effective answers.
Here are several of special interest.

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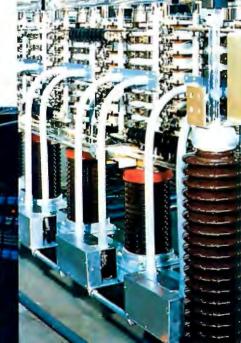
The Westinghouse Numa-Logic solid-state programmable controller uses microprocessor technology to provide more reliable operation for electrical control applications. It can economically replace as few as eight relays. It also has the capability to control the hundreds of sequences required by sophisticated, automated processes. The Westinghouse Numa-Logic system is being used in the machine tool, materials handling, textile, paper, steel-making and other industries to reduce downtime, give quick start-ups, and increase operational efficiency.

Factory computer systems

Also making major contributions to increased productivity are Westinghouse factory computer systems. They are capable of operating as many as 100 different machine tools simultaneously. They can also provide real time status and performance monitoring at four levels: maintenance, shop supervisor, middle and upper management. In application after application, downtime has been sharply reduced, and actual machine time has been increased up to 55 percent.

Power electronics

Solid-state static VAR generators are a key solution for utility and industrial system line problems because they provide system stability and improve power flow capability. Planning studies at a major utility concluded that 10 transmission lines with static VAR generators could deliver the power ordinarily requiring 16 lines. When it comes to industrial applications such as steel-making, VAR generators can improve the efficiency of power usage by improving the power factor and providing faster arc furnace melt times. One steel producer's productivity increased sufficiently to pay back the nearly \$2 million cost of the static VAR generator in 15 months.



and service technologies productivity today.

Applied Plasma Systems

Because of the skyrocketing costs of fossil fuels used to supply process heat or chemical reactions, many firms are searching for alternatives. The Westinghouse Applied Plasma Systems can efficiently fire high temperature industrial processes, and serve as a central heating device for a myriad of applications such as chemical processes, metals treating, and combustion replacement. This technology is already providing an efficient answer for blast furnaces and direct reduction iron-making processes. It uses a high temperature gas stream to transmit heat. Studies on the upgrading of existing blast furnace facilities demonstrate up to an 80 percent increase in the capacity of the facilities through the application of Applied Plasma Systems.

How to minimize downtime... As machines grow more complex, keeping them running takes specialists. To help you maximize productivity, Westinghouse can provide the same technological expertise in services as it does in products.

A remarkable worldwide service network

Because Westinghouse engineers, tests, and builds complex products and systems, we have the special skills, trained personnel, and necessary tools to maintain such equipment best; or to repair it in the least amount of time. Available to help you with either maintenance or repair are hundreds of trained Westinghouse field service engineers and specialist mechanics who use the most sophisticated on-site testing and repair equipment. And backing them up is a vast network of repair facilities.

Whether Westinghouse built it or not, we can service and repair almost anything from escalators and elevators, to steam turbines and nuclear power plants. Westinghouse can do an operation analysis and recommend an upgrading program, we can train your operators and service personnel, or we can do continuous monitoring of various operations. Whatever is needed.

Experience has taught us that a regularly planned and scheduled maintenance program greatly increases uptime and saves money. Westinghouse is equipped to provide programmed maintenance on a plant-wide basis. During scheduled shutdowns, a crew of Westinghouse field engineers and technicians can move in to do a complete analysis and topto-bottom overhaul of your entire facilities.





- To retain that competitive edge, we must make better use of the technologies we already have, and actively encourage the development of new ones.
- Westinghouse believes technology is vital to our nation, our customers, and our own growth.
- Westinghouse has technologies that increase manufacturing productivity, help meet our energy needs, and contribute to our national security.



Listing 2: Test program for the arithmetic subroutines. This program receives two numbers from the keyboard and displays their product. Note that the user must supply entry points to character input and output routines and to the system monitor (or any other program to be jumped to when this program ends).

		*	TEST PRO	GRAM D		OF 2 NUMBERS ENTERED FROM KEYBOARD	* * * * *
5				****	NOTE: YOU MUST	SUPPLY THESE 3 ENTRY POINTS: *****	
6			*				
7			CHIN	EQU	0	SUBROUTINE TO GET KEYBOARD CHAR. IN A REG.	
8			CHOUT	EQU	0	SUBROUTINE TO DISPLAY (A) AS ASCII CHAR.	
9			MONITOR	EQU	0	ENTRY TO SYSTEM WHEN PROGRAM DONE	
10			4				
11				ORG	3000H		
12	3000	213B30		LXI		INPUT BUFFER ADDRESS TO HL	
13	3003	CD0000	TEST1	CALL	CHIN	GET AN ASCII CHARACTER IN A REGISTER	
14	3006	77		MOV	M + A	STORE CHARACTER INTO BUFFER	
15	3007	FE0D		CPI		AND IF ITS A CARRIAGE RETURN	
16	3009	CA1030		JZ			
17	300C	23		INX		ELSE, ADVANCE TO NEXT BYTE OF BUFFER	
18	300D	C30330		JMP	TEST1	AND CONTINUE	
19	3010		TEST2	LXI		RECALL INPUT BUFFER STARTING ADDRESS	
20	3013	CDD740		CALL		CONVERT ASCII DECIMAL TO BINARY NUMBER	
21	3016	E5		PUSH		SAVE NUMBER	
22	3017	13		INX	D	ADDRESS OF START OF SECOND NUMBER STRING	
23	3018	CDD740		CALL			
24	301B	D1		POP		RECALL FIRST NUMBER	
25	301C	CD3840		CALL	EMULT	FIND PRODUCT IN HL	
26	301F	3EGA		MVI		ASCII LINE FEED	
27	3021	CD0000		CALL		START ANSWER ON NEW LINE	
28	3024	114F		LXI		OUTPUT BUFFER STARTING ADDRESS	
29	3027	CD2F41		CALL		CONVERT ANSWER TO ASCII STRING	
30	302A	AF		XRA	A	WARK END OF CIDING WITH A BUTE	
31	302B	12		STAX		MARK END-OF-STRING WITH 0-BYTE	
32	302C	214F30		LXI		RECALL START OF BUFFER	
33	302F	AF	TEST3	XRA			
34	3030	86		ADO	M	FETCH NEXT CHARACTER	
35	3031	CA0000		JZ		IF ITS 0-BYTE TERMINATOR. QUIT	
36	3034	C00000		CALL		ELSE, DISPLAY BYTE	
37	3037	23		INX		ADVANCE BUFFER POINTER	
38	303B	C32F30		JMP	TEST3		

Two's Complement of **Binary Numbers**

303B

40

Two's complement is a method of representing negative numbers in binary radix. It is only one of several methods of negative number representation, but it has the advantage of eliminating subtraction as a separate operation; subtraction can be performed by taking the two's complement of the subtrahend and adding it to the minuend.

INBUF

OUTBUF

The two's complement of a number is found by complementing every bit in the number (changing Is to 0s and vice versa) and adding 1 to the resulting value. For example, suppose we want to take the two's complement of the number 4 stored as an 8-bit value:

INPUT BUFFER FOR 2 NUMBERS

OUTPUT BUFFER FOR RESULT

00000100 4 in binary is: complementing each bit: 11111011 adding 1: -4 in two's complement: 11111100

(By the way, the numeral 11111011 is called the one's complement of

To show that subtraction can be performed using straight binary addition with two's complement, take the example of subtracting 4 from 7:

7 in binary is: 00000111 two's complement of 4: 11111100 1 00000011 adding, we get:

The carry, 1, is thrown away, and the result, 00000011, is decimal 3 in binary.

In two's complement, negative numbers always have a leftmost bit of 1; on the other hand, nonnegative numbers have a leftmost bit of 0. However, the absolute value of a negative number cannot be found by simply evaluating the lower bits; as before, you must complement the number and add 1.

These routines run an order of magnitude faster than full floating-point routines.

Text continued from page 204:

treat values outside the range of -32,768 to +32,767 as an overflow condition.

When an overflow is detected, a call is made to a subroutine called OVERFLOW, which is not provided because you will want to implement it

in a manner appropriate to your system. A simple error-processing routine would display an error message and jump to the system monitor. If desired, a more sophisticated error-processing routine could continue processing, because the top of the stack contains the return address to the routine where the overflow was detected. Similarly, you must provide an entry point called CONVERR, which will be called in the event of a string-numeric conversion error.

The string-numeric conversion routine, DECBIN, will convert any legitimate numeric decimal represen-

tation, including those with leading blanks or blanks between the sign and the leading digit. It will reject errors including two signs or an illegal character. Any nonnumeric character after the start of the number terminates the conversion, facilitating parsing of free-format data entries. This is illustrated by the sample test program of listing 2, which accepts two numbers on one line and prints their product on the next line.

The Largest and Smallest Numbers in Two's Complement Notation

Another property of two's complement numbers is that the absolute value of the largest positive number that can be represented is 1 less than the absolute value of the smallest negative number that can be represented. As an example, look at all the possible 3-bit two's complement numbers:

0 is 000; complementing and adding 1 gives 000 (or -0) 1 is 001; complementing and adding 1 gives 111 (or -1) 2 is 010; complementing and adding 1 gives 110 (or -2) 3 is 011; complementing and adding 1 gives 101 (or -3) -1 is 111; complementing and adding 1 gives 001 (or 1) -2 is 110; complementing and adding 1 gives 010 (or 2) -3 is 101; complementing and adding 1 gives 011 (or 3)

But we have one number left over, 100. Inasmuch as the most significant bit is 1, it must be negative. To find its absolute value, take its two's complement:

the number is: 100 complement it: 011 add 1: 1

its two's complement is: 100 which is binary for 4

Therefore, 100 in two's complement notation must be -4. But notice that, given three bits for the binary representation of signed numbers, there is no way to represent positive 4 in two's complement notation. The largest positive number that can be represented is one less than that.



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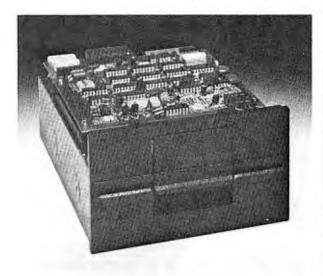
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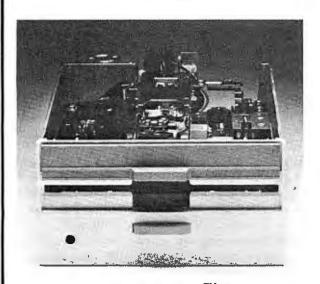
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Technical Forum

Print Your Own Bar Codes

UPC Bar Codes With the Centronics 737

John Anderson, 149 Cliffside Dr, Wilmington NC 28403

Hewlett-Packard's introduction of a less-than-\$100 bar-code reader will certainly increase interest in bar codes as a viable means of transporting program listings through the printed media. But reading bar codes is not enough. To maximize their usefulness, we must be able to generate them as well: only then will creative applications begin to emerge. There must be numerous instances where keyboard input to small-business data-processing systems can be replaced with bar-code input.

My interest in bar codes arose from a need for simple data entry in an educational application. The problem required easy generation as well as easy reading of bar codes. To generate bar code, you must be able to produce vertical lines and spaces of equal (or approximately equal) width. This can, of course, be done with a plotter or a high-resolution graphics printer. Or, it can be done with a low-cost, dot-matrix, proportional-spacing printer, such as the Centronics 737.

I had a Centronics 737, so I began to experiment with producing bar codes, and found that the printer can be used quite effectively. The Centronics 737 produces a high-density dot-matrix print in the proportional-spacing mode. With the concatenation symbol (|) as the basic vertical bar, the printer can be directed to backspace dot by dot, allowing the compression of vertical bars into a solid bar of variable width.

Text continued on page 276

PAPERBYTE® Bar Codes With Integral Data Systems Printers

Dr G Louis, OB/GYN Dept, St Michael's Hospital, 30 Bond St, Toronto M5B 1W8 Canada

The advent of Hewlett-Packard's low-cost bar-code reader, HEDS-3000, makes it possible to consider software distribution in machine-readable form via the printed page. The bar-code reader (described in Carl Helmers' editorial, "Bar Codes, Revisited...," April 1980 BYTE, page 6) can be interfaced to a computer for slightly more than \$100.

This article will describe a program that uses the graphics plotting option of an Integral Data IP-225 (or IDS-440) printer to produce bar code. (The IP-225 sells for about \$1000.) The format is the PAPERBYTE® format, described in Ken Budnick's book, *Bar-Code Loader* (Peterborough NH: BYTE Books, 1977).

In graphics mode, the Integral Data printers allow column by column control of the image printed. Each column is 7 dots high, and each dot is controlled by the corresponding bit in the byte of data sent. For example, if you send a question mark (hexadecimal 3F) to the printer while in graphics mode, a vertical bar of 6 dots is printed. If you send a NUL (0), the printer leaves a blank that is 1 dot-width across. This takes care of 0 bits and spaces. One bits (double-width bars) are simply printed as two question marks side by side. The bar-code loader program by Ken Budnick has software filtering to correct dropouts (white spots on the bars) and blotches (black dots in the spaces), and it also proves adequate to deal

Text continued on page 230

Editor's Note: When we put the Hewlett-Packard HEDS-3000 bar-code wand on the cover of the April 1980 BYTE, we believed that the only major obstacle to the widespread use of bar codes—lack of a reliable wand at an affordable price—had been eliminated. You couldn't make your own bar codes (we thought), but you could read them. In the January 1981 BYTE, we published an article that showed how to make HP-41C bar codes on an expensive Diablo 1650 printer (see "Generating Bar Codes in the Hewlett-Packard Format," by Thomas McNeal, January 1981 BYTE, page 148). But few people have such an expensive printer, and (we thought) most people still couldn't make their own bar codes.

We were wrong. The two articles above show two different formats of barcodes produced on two different dot-matrix printers. All of the work is done in the software; the hardware only has to generate a thin vertical bar and place it anywhere on a line. With the proper bar-code reading software, even bar codes made with dot-matrix printers can be consistently and reliably read....GW

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with the tiny white spaces left between the dots in the double-width bars. The only restriction is that the printer ribbon *must* be in good condition; otherwise, the contrast between bar and space will not be sufficient for a reliable wand reading.

The program in listing 1 prints bar code from data in memory with start and stop addresses specified by the user. Tiny Pascal as described by K-M Chung and H Yuen (see reference 2) and implemented by me in 8080 assembly language (see reference 4) was used for this routine. Those who are unfamiliar with Pascal should have little difficulty following the algorithm: readability is one of the most important advantages of Pascal. Two minor points may give some trouble to BASIC programmers: percent signs (%) associated with numbers or variables indicate that the number or variable is expressed in hexadecimal, and the CASE X OF... statement is used to choose from among options to be executed depending on the value of X. However, interested readers

Figure 1: Bar-code representation of part of listing 1, made on an Integral Data Systems IP-225.

BAR-CODE PRINTER -- SOURCE LIST -- 800624

Listing 1: Tiny Pascal source listing for a program that will generate printed bar codes from data in memory. Translation into BASIC or assembly language should prove fairly simple, even if the user is unfamiliar with Pascal.

●010 3 *** BAR-CODE PRINTER PROGRAM

```
POR INTEGRAL DATA IF-225 (440) WITH GRAPHICS
BY DR. G. LOUIS
OB/GYN DEPT., ST. MICHAEL'S HOSPITAL
30 BOND STREET, TORONTO, CANADA M5B 1W8
0020
0030
0040
0060
                            800501, LAST MODIFIED 800624
0080 CONST MAXBAR=400 ) MAX UNIT WIDTHS PER FRAME );
0090 PRINT=X85A ) DIRECTS OUTPUT TO PRINTER );
0100 NOPRINT=X84C ) NO OUTPUT TO PRINTER );
0110 DEL=127; CAN=24; FF=12; CR=13; TAR=9; ) ASCII CTL )
                           PLOT=3; PLTESCAP=3; NORMLPRT=2; ) PLOT MODE CTL )
CPI12=30 ) SET PRINTER DENSITY 12 CHAR/IN );
CPIMAX=31 ) SET MAXIMUM DENSITY FOR PLOTTING );
0120
0130
0140
0150
                           I ) GENERAL-PURPOSE INDEX ),
IPT ) CHARACTER INPUT ),
ABSFLAG ) TRUE IF ABSOLUTE ADDRESSING CALLED FOR ),
ORIGIN ) ADDRESS OF 1ST RYTE TO BE CODED ),
LASTBYTE ) ADDRESS OF LAST BYTE TO CODE ),
POINTER ) ADDRESS OF NEXT BYTE TO CODE ),
FRAMEID ) VALUE OF ID BYTE OF NEXT FRAME 3:
INTEGER;
0160 VAR
0170
0180
0190
0200
0210
0220
0230
                                 INTEGER
0240
0250
                            JOBNAME: ARRAY [53] OF INTEGER;
0240 FUNC WERAME (START STOP)
                  ) WRITE ONE FRAME BEGINNING AT START AND ENDING
AT STOP OR WHEN THE PAGE IS FULL, WHICHEVER IS
FIRST; RETURN THE ADDRESS OF THE BYTE FOLLOWING
0270
0280
0290
                 THE ONE LAST ENCODED )

CONST SYNC=296 ) FIRST BYTE OF EVERY FRAME );

VAR ABSCK ) TRUE IF AN ABSOLUTE ADDRESS IS WANTED );

BARCHT ) NUMBER OF UNIT WIDTHS IN FRAME );

CKSUM ) HEX CHECKSUM );

FRAMELEN ) NUMBER OF BYTES IN FRAME );
0300
0320
0330
0340
                                 I ) GENERAL-PURPOSE INDEX );
INTEGER;
0360
0380
0390
                  PROC WBYTE (VALUE);
0400
                      WRITE BAR CODE FOR 8 LSB'S OF "VALUE" > VAR BUF, I: INTEGER;
0420
0430
0440
0450
                       BEGIN
BUF := VALUE AND 255;
FOR I := 1 TO 8 DO BEGIN
WRITE (SPACE, BAR); IF BUF >
                                                                                                   127 THEN WRITE (DAR);
0460
                                     # (BUF SHL 1) AND 255 END
0480
                 FUNC SCANBYTE (VALUE);
) RETURN THE NUMBER OF UNIT WIDTHS NEEDED TO WRITE BAR CODE FOR S LSB'S OF "VALUE" )
VAR RUF, CNT, I: INTECER;
BEGIN
BUF := VALUE AND 255; CNT := 0;
FOR I := 1 TO 8 DO REGIN
CNT := CNT+2; IF BUF > 127 THEN CNT := CNT+1;
0490
0510
0520
0530
0540
```

```
) ONE SPACE + ONE BAR, + ONE MORE BAR IF BIT IS 1 ) BUF := (BUF SHL 1) AND 255 END;
SCANBYTE := CNT END;
 0580
 0590
 0600
              BEGIN ) WFROME )
ABSCK := ABSCLAG AND (START <= STOP);
WRITE (CPI12);
 0610
 0620
 0630
 0640
                   ABSFLAG THEN WRITE (START%)
ELSE WRITE (START-ORIGIN%);
               WRITE (TABLOPHMAX.FLOT); WBYTE (SYNC); FRAMELEN := 0;
IF ABSCK THEN BEGIN
CKSUN := (START SHR 8) + (START AND 255);
 0660
 0670
 0880
                        BARCHT := SCANBYTE (START SHR 8) + SCANBYTE (START)
 0690
                   END
ELSE BEGIN CKSUM :- OF BARCHT :- O END;
 0710
              ELSE REGIN CKSUM := 0; BARCNT := 0 END;

IF START <= STOP THEN REPEAT

I := MEM CSTART + FRAMELEN]; CKSUM := CKSUM + I;

BARCNT := BARCNT + SCANBYTE (I);

FRAMELEN := FRAMELEN + 1

UNTIL (BARCNT > MAZBAR-24) OR (START+FRAMELEN = STOF+1);

IF ABSCK THEN FRAMELEN := FRAMELEN+2;

CKSUM := 256 - ((CKSUM + FRAMELD + FRAMELEN) AND 255);

WBYTE (CKSUM); WBYTE (FRAMEID); WBYTE (FRAMELEN);

IF ABSCK THEN BEGIN

WBYTE (START SHR 8); WBYTE (START);

FRAMELEN := FRAMELEN-2 END;

FOR I := 1 TO FRAMELEN DO

WBYTE (MEM CSTART + I - 11);
 0720
 0730
 0750
0760
0780
 0790
0800
0810
0820
0830
              WBYTE (MEM CSTART + I - 1]);
WRITE (SPACE, BAR, PLTESCAP, NORMLPRT, CPI12, CR);
WFRAME := START + FRAMELEN
 0840
 0850
0870
               END
0880
0890 REGIN
                         3 *** MAIN PROGRAM *** 3
          CALL (NOPRINT); I :- 0;
WRITE (FF, 'KAR-CODE PRINTER', CR, CR, 'JOB NAME: ');
WHILE I < 53 DO BEGIN
 0900
0910
0920
              ILE I < 53 DU BEGIN

READ (IPT); CASE IPT OF

DEL: IF I > 0 THEN BEGIN WRITE (IPT); I := I-1 END;

CAN: WHILE I > 0 DO BEGIN WRITE (DCL); I := I-1 END
 0930
 0940
 0950
1100
              POINTER := WFRAME (POINTER: LASTBYTE);
1110
              POINTER := WPRAME (FURTHER) :: FRAMEID := FRAMEID := FRAMEID HOD 55) AND (POINTER <= LASTBYTE)
THEN BEGIN WRITE (FF); 1 := -1;
REPEAT I := 14; WRITE (JOBNAME [I])
UNTIL JOBNAME [I] = CR; WRITE (CR) END
1120
1130
1140
1160
1170 UNTIL POINTER > LASTBYTE;
1180 POINTER := WFRAME (FOINTER:0) ) WRITE EDF FRAME )
1190 WRITE (FF); CALL (NOPRINT)
1200 END. 3 MAIN PROGRAM 3
```

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Technical Forum-

should find it easy to adapt this routine to their own favorite languages and printers. Figure 1 shows the textually-encoded bar-code representation of a portion of listing 1.

The program need not be used exclusively for software distribution. Transfer of data of any kind between computers with incompatible mass-storage devices is easy if the source computer can create bar code and the recipient can read it. In addition, cheap, compact, archival storage of seldom-used information is possible if the length of files and frequency of use are such that entry via the wand is not unreasonably tedious.

Lest there be any doubt about the suitability of this program for use in software distribution, I will conclude by mentioning a recent experiment. I produced the barcode listing (partially reproduced in figure 1) and photocopied it on a high-quality electrostatic photocopier. Both the original and the copy were scanned five times with the bar-code wand. I counted the number of passes needed to read each line and calculated the average. For the original and the copy, 1.1 and 1.3 passes with the wand sufficed to obtain a good read. Total time to enter the code ranged from 10 to 15 minutes, but this time could be decreased if a portable drafting tool or a T-square were used instead of a ruler to guide the wand across the page. The most time-consuming step in the entry process involved alignment of the ruler. Clearly, it is perfectly feasible to use this method to distribute machine-readable code on paper.

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"I don't believe it! The guy who wrote this program didn't know what he was doing." How many times have you seen a program and said that? Well, I never thought I would say it while looking at the Microsoft multiplication routines written for Ohio Scientific's BASIC.

Multiplication routines written in software are slow. especially when accurate to 9 digits. Programmers are always trying to optimize mathematical routines for speed. That's why I was surprised that the main loop for the multiplication routine contained line after line of inefficient instructions.

To comprehend the problem, you need to understand how a software multiplication routine works. For multiplication of large numbers, the process is similar to the longhand method taught in school. The two numbers to be multiplied, the multiplier and the multiplicand, are stored in the floating-point accumulator and the alternate floating-point accumulator, respectively. These accumulators are usually 4 to 5 bytes in length and preferably located in page 0 memory. The low bit of the multiplier is checked to see if it is set: if it is, the multiplicand is added to the product (initially 0); if it is not, no addition occurs.

Next, both the multiplier and the product are shifted 1 bit right (or, alternately, the multiplier is shifted right and the multiplicand is shifted left) and the low bit on the multiplier is checked again. This process is repeated for each bit in the multiplier. Four bytes are required for 9 digits of precision: a great deal of bit shifting must go on. In fact, the bit shifting uses most of the time required for a multiplication routine.

Fortunately, there is a convenient instruction in the 6502 microprocessor for shifting several contiguous bytes 1 bit to the right. The ROR instruction shifts a byte 1 bit to the right, with the carry shifted into the high-order bit, and the low-order bit of the byte shifted into the carry. Successive executions of the ROR instruction on contiguous bytes will shift all of the bytes 1 bit to the right, with the low bit of 1 byte shifting into the high bit of the next.

Listing 1 contains a portion of the Microsoft multiplication routine for the 6502. It is part of the routine that shifts the product 1 bit right. This sequence is repeated four more times in the subroutine, and requires a total time of 85 µs (with a 1 MHz clock rate while assuming



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Listing 1: Section of the multiplication routine from Microsoft's disk BASIC, written for Ohio Scientific computers. This section can be replaced with a single ROR instruction (ROR \$73, where the dollar sign denotes a hexadecimal 73). The replacement accomplishes the same task in much less time.

LOC	CODE	MNEMONIC	TIME (us)
1946	A9 80	LDA #\$00	2
1948	90 02	BCC \$194C	3
194A	A9 80	LDA #\$80	2
194C	46 73	LSR \$73	5
194E	05 73	ORA \$73	3
1950	85 73	STA \$73	3

that, on the average, the instruction at hexadecimal 194A is executed only half of the time). This sequence is also in a loop that is repeated for all 8 bits of a multiplier byte, requiring a time of 680 µs for each subroutine call. Finally, the subroutine is called four (sometimes five) times for each floating-point multiplication. Thus, a total of 2.72 ms is used for each floating-point multiplication. However, the entire listing can be replaced by the single instruction (ROR \$73). This instruction requires only 5 μ s to execute, for a total time of 800 µs for each floatingpoint multiplication: a saving of 1,92 ms for each call to the multiplication routine.

My own tests with the changes have indicated that BASIC requires approximately 4.9 ms to complete a floating-point multiplication on a 9-digit number, whereas with the changes, it takes only 3.1 ms. This is an increase in speed of 37%!

Listing 2: Part of a routine accessed by the addition and subtraction routines in Ohio Scientific's disk BASIC. This section can be replaced by the single instruction ROR \$02, X.

LOC	CODE	MNEMONIC	TIME (uS	
1854	A9 00	LDA #\$00	2	
1856	90 02	BCC \$185A	3	
1858	A9 80	LDA #\$80	2	
185A	56 02	LSR \$02,X	6	
185C	15 02	ORA \$02,X	4	
185E	95 02	STA \$02.X	4	
		•		

Other routines that access the multiplication routines also execute more rapidly. For instance, the logarithm routine takes approximately 34.8 ms to complete a 9-digit logarithm; with the changes, it takes only 21.9 ms. This is also an increase in speed of 37%.

Similar mistakes were found in a section of the normalization routine (starting at hexadecimal 1854) accessed by the addition and subtraction routines (see listing 2). This sequence is repeated two more times. It can all be replaced by the instruction ROR \$02,X. Another interesting section of the routine occurs at hexadecimal 1879 (see listing 3). This can be replaced by the instruction ROR A, which takes only 2 us to execute. The actual increase in speed for the addition and subtraction routines with the changes installed was too difficult to measure since the routines are fairly rapid compared to the BASIC loops and other program segments used to test

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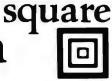
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Listing 3: Section from the normalization routine used by the addition and subtraction routines in Ohio Scientific's disk BASIC. This section can be replaced by the instruction ROR A.

LOC	CODE	MNEMONIC	TIME (us)
1879	08	PHP	3
187A	4A	LSR A	2
187B	28	PLP	4
187C	90 02	BCC \$1880	3
187E	09 80	ORA #\$80	2
1880	C8	INY	

them. I did notice that BASIC testing loops often executed approximately 10% faster with the changes. I attribute this to the faster addition routine.

I suspected that the division routines would also contain errors, but discovered that the ROL instruction was used wherever it was needed. (The ROR instruction isn't necessary in division.)

I immediately contacted Ohio Scientific and Microsoft to inform them of the problem. Both replied with an explanation that restored my faith in big-name software companies. Apparently, earlier versions of the 6502 microprocessor did not include an ROR instruction, but as customer demand grew, MOS Technology incorporated an ROR instruction in later versions of the 6502. Unfortunately, some of the earlier Ohio Scientific computers had already been sold with the old microprocessor. Therefore, Microsoft wrote its BASIC without any ROR instructions to make the software compatible with the earlier versions of the computer. Listings 1, 2, and 3 are actually macro expansions of the ROR instruction. [Macros are one-line pseudoinstructions placed in an assembly-language source listing. When processed, they are replaced by a (predefined) set of assembly-language instructions and assembled into machine language....GW] Microsoft assured me that this was done only for the KIM and Ohio Scientific computers. All other versions of 6502 BASIC were written using the ROR instruction.

For those who have later versions of Ohio Scientific computers and don't have BASIC permanently stored in read-only memory, there is a way to change Ohio Scientific's disk BASIC to use the ROR instruction. If you are using the OS-65D disk operating system; the program in listing 4 will permanently change your BASIC for 8-inch disks. It simply loads a part of the BASIC interpreter into memory, POKEs in the required changes, and stores the changed code back on disk. For 5-inch disks, statement 80 should be changed to read:

80 DISK!"CA 4200=03,1"

and statement 150 should be:

150 DISK!"SA 03,1=4200/8"

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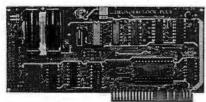
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OS-9™ MULTIPROGRAMMING **OPERATING SYSTEM**

true multitasking, real time operating system for timesharing, software development, database, process control, and other general applications. This versatile OS runs on almost any 6809-based computer.

- UNIX™ -like file system with hierarchical directories, byte-addressable random-access files, and full file security. Versatile, easy-to-use input/output system is hardware independent and expandable.
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Γ	0.20	Level	Two	\$495*	Level	One	\$195
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BASICØ9™ PROGRAMMING LANGUAGE SYSTEM

xtended BASIC language compiler/interpreter with integrated text editor and debug package. Runs standard BASIC programs or minimally-modified PASCAL programs.

- Permits multiple named program modules having local variables and identifiers. Modules are reentrant, position independent and ROMable.
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- Allows user-defined data types and complex data structures. Five built-in data types: byte, integer, 9 digit floating-point, string and boolean.
- Runs under OS-9[™] Level One or Level Two. □\$195*

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- Macro Text Editor
- Interactive Debugger

BASICØ9 and OS-9 are trademarks of Microware® and Motorola. UNIX is a trademark of Bell Laboratories.* Most software is available on ROM or diskette in versions for many popular 6809 computers. Contact Microware® for specific availability.

For compatible hardware see GIMIX ad page 142.



MICROWARE®

Microware Systems Corp., Dept. B2 5835 Grand Avenue, Des Moines, Iowa 50304 (515) 279-8844 • TWX 910-520-2535

System Notes

I have not been able to test these changes for the 5-inch systems, and I suggest that you exercise caution in using them. For systems that use the OS-65U operating system, the program in listing 5 should be used to change your BASIC.

Ohio Scientific often boasts of supporting the fastest BASIC of any of the popular personal computers, and it can give you a great sense of satisfaction to make it run even faster. I have run BASIC with these changes for four months and have noticed that all of my programs run faster than before, especially those loaded with mathematical equations. If you decide to incorporate these changes into your system, I suggest that you first try them on an old copy of your operating system to ensure that the changes work on your computer.

Listing 4: Program used with the OS-65D operating system and 8-inch disks. Beginning at hexadecimal location 4800, the progrant loads a portion of BASIC into memory, then POKEs the appropriate ROR instructions into the mathematical routines and stores the revised BASIC back on the disk.

```
REM DISK BASIC CORRECTION ROUTINE.
20 DATA 118,2,118,3,118,4,104,106,200,208,232,24,96
30 DATA 102,115,102,116,102,117,102,118,102,189,152
40 DATA 74,208,214,96
50 REM SET UP TOP OF MEMORY TO $47FF
60 POKE 132,255 : POKE 133,71 : POKE 128,255 : POKE 129,71
70 REM CALL IN A PORTION OF BASIC TO $4800
80 DISK!"CA 4800=04,1"
90 Al=18516 : REM 18516 = $4854
90 A1=18516 : REM 18516 = $4854

100 A2=18758 : REM 18758 = $4946

110 REM POKE IN THE CORRECTED CODE

120 FOR I=0 TO 12 : READ D : POKE A1+I,D : NEXT I

130 FOR I=0 TO 14 : READ D : POKE A2+I,D : NEXT I

140 REM SAVE THE CORRECTED BASIC BACK ON DISK
150 DISK! "SA 04,1=4800/B"
```

Listing 5: Program used with the OS-65U operating system. This program does the same thing as listing 4, but begins at hexadecimal location 7800.

```
10 REN DISK BASIC CORRECTION ROUTINE. OS-65U
20 DATA 0,36,0,0,0,2,0,120
30 DATA 118,2,118,3,118,4,104,106,200,208,232,24,96
30 DATA 118,7,118,3,118,4,104,106,200,208,232,24,96
40 DATA 102,115,102,116,102,117,102,118,102,189,152
50 DATA 74,208,214,96
60 REM SET UP USR FUNCTION AND PUT AND GET ROUTINES
70 POKE 8778,192 : POKE 8779,36
80 POKE 9432,243 :POKE 9433,40
90 POKE 9432,232 : POKE 9436,40
100 REM DISK ADDRESS = $1800 + $0C00, NUMBER OF BYTES = $0200
110 REM RAM ADDRESS = $7800

120 CB=9889 : FOR I=1 TO 8 : READ D : POKE CB+I,D : NEXT I

130 REM CALL IN A PORTION OF BASIC TO $7800
 140 ER=USR(0)
140 ER=USK()

160 Al=30804 : REM 30804 = $7854

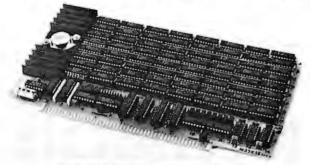
170 A2=31046 : REM 31046 = $7946

180 REM POKE IN THE CORRECTED CODE

190 FOR I=0 TO 12 : READ D : POKE Al+I,D : NEXT I

200 FOR I=0 TO 14 : READ D : POKE A2+I,D : NEXT I
 210 REM SAVE THE CORRECTED BASIC BACK ON DISK
 220 ER=USR(1):CLOSE
```

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32K STATIC MEMORY BOARD

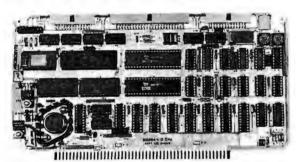
- •Fully S100 bus compatible, IMSAI, SOL, ALTAIR, ALPHA MICRO
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 •Commercially designed power bus; 7 ground bus bars; 0.1 uf
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 Inputs fully low-power Shottky Schmitt Trigger buffered on all address and data lines
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 •No DMA restriction

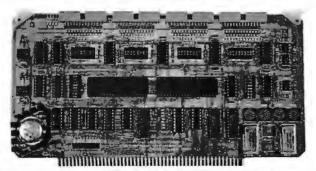
- No power consumption 800mA
 Fully warranted for 120 days from date of shipment

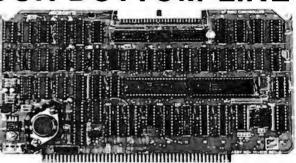


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- •A complete single board Z-80A CPU with serial/parallel interface
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- Dual RS-232 serial I/O ports using the Z80A-DART with individual baud rate selection (16 baud rates from 50-19,200 baud)

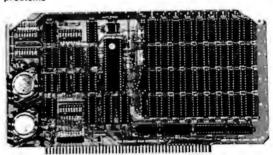
 **Up to 24 bit parallel I/O port—fully programmable Intel 8255A
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Book Reviews

Principles of Interactive Computer Graphics, 2nd Edition

William M Newman and Robert F Sproull McGraw-Hill, 1979 541 pages, hardcover \$25.95

Reviewed by Richard L Emery 559 Taos Ct Saginaw TX 76179

Is your computer a glorified scorekeeper? Was zapping your 10,000th Klingon your most creative accomplishment? Perhaps you have tried to do more, to be more creative. However, the books you found were either too simple ("See Dick run the program. Run, Dick, run.") or too technical ("The vec-

tored translation of a quadratic polynomial synthesizing imaginary roots and real constraints utilizing classical fourth-order Runge-Kutta numerical techniques...").

With the second edition of Principles of Interactive Computer Graphics, you can explore the special techniques of computer-generated graphics (see page 146 of the December 1977 BYTE for a review of the first edition). The first edition, published in 1973, discussed algorithms and hardware in reference to vector-drawing displays. because these were the most common type of display. At the time, raster-scan displays were available, but programmers mainly used them for data entry and interactiveprogram preparation. When experimenters needed inexpensive, human-readable output devices for microprocessor-based computers, the raster-scan method was developed for graphics use. Newman and Sproull recognize this and have included a section devoted to the software techniques needed to implement graphics capabilities on raster-scan displays. This section describes angle and line generation, solids generation, interactive computation, hardware, and language implementation.

Another major change is the use of Pascal to describe the algorithms. The first edition used a language called SAIL, which required the inclusion of a user's manual. Because of the wide use of Pascal, today's readers will more easily understand the material presented. Even those whose knowledge of Pascal is limited will comprehend the algorithms with little difficulty.

There are twenty-six chapters arranged in six parts. Part 1 discusses line drawing, point plotting, transformations, windowing, and clipping. This material is applicable to raster-scan and vectordrawing displays. In part 2, emphasis is on graphics packages-that is, groups of subroutines to be invoked by applications programs, Part 3 describes the man-to-machine interface, Here, the authors identify several input devices (keyboards, light pens, tablets, three-dimensional input) and methods to use them. In part 4, the following subjects dealing with raster-scan graphics are covered; fundamentals, solid-area conversion, interactive methods, and hardware.

Three-dimensional graphics techniques are more thoroughly examined in part 5, which includes perspective, shading, curved surfaces, and hidden-line/hidden-surface algorithms. Part 6 brings it all together by outlining various hardware display units, methods of user interfacing, and graphics languages. Two appendices are included. The first is a discussion of matrix- and vector-arithmetic operations: the second, homogeneous coordinate techniques. Many of the clipping, windowing, and transformation techniques require a fundamental knowledge of vector and matrix computation. These two appendices provide that knowledge, as long as you understand mathematical notation.

Although this book still is a basic tool for college- and graduate-level computer science courses, the novice or personal computerist will find it understandable. This book will spark your imagination and challenge your creative abilities. Once that challenge is accepted, zapping Klingons will be a bore.



Software for the Apple II and Apple II Plus*

BENEATH APPLE DOS

A Technical Manual

By Don Worth and Pieter Lechner

Become an expert on the intricacies of Apple's DOS (Disk Operating System). BENEATH APPLE DOS is the perfect companion to Apple's DOS 3.3 Manual. Containing eight chapters, three appendices, a glossary, an index, and over 160 pages, this manual will serve to completely fill in the many gaps left by Apple's DOS 3.3 Manual. Written for Apple users with DOS 3.3, 32 or earlier versions, any Apple disk user would welcome having this carefully written manual at his fingertips.

- . How DOS 3.3 differs from other DOS versions.
- How disks are protected.
- How to reconstruct a damaged diskette CATALOG
 How tracks are formatted.
- How to use the disk directly, without DOS.

- How to call DOS's file manager.
 How every routine in DOS works.
 How to customize DOS to your needs.
 How to overcome DISK 1/0 ERRORS.
 About the "secret" file types S and R.

- Large quantities of excellent diagrams and tables. Source listings of useful disk utilities.
- Glossary of over 150 technical terms
- Exhaustive description of DOS program logic.
- Handy reference card. Useful patches to DOS
- Many programming examples.

Book - \$19.95

CROSS-REF by Jim Aalto

Applesoft programmers will be delighted to have this cross reference utility program in their 'tool kit' of software aids. What can CROSS-REF do to speed and facilitate your Applesoft program development? Consider these functions:

VARIABLE CROSS REFERENCE FIND VARIABLE REPLACE VARIABLE

LINE CROSS REFERENCE FIND LINE NUMBER VARIABLE ONLY LISTING

LINE ONLY LISTING

Features that make CROSS-REF easy to use include:

· Written in machine language, occupies less than 3K

- Resides passively in memory while DOS or Applesoft is active.

 Can be loaded with your Applesoft program already resident.

 Very fast a VARIABLE CROSS REFERENCE for a 16K Applesoft program can start printing in 5 seconds.
 Contains printer format controls and headers for documentation.

Prints English language error messages.

Cassette - \$22.95 Diskette - \$24.95

LINKER by Don Worth.
Turn your Apple II or Apple II Plus into a powerful and productive software development machine with this superb linking loader/editor package. LINKER does the following and much more:

- · Dynamically loads and relocates suitably prepared machine language programs anywhere in RAM.
- Combines a main program with subroutines. You can assemble a subroutine once and then use it with as many main programs as you
- Produces a map of all loaded routines, giving their location and the total length of the resulting module.
 Contains a library of subroutines including binary multiplication and
- division, print text strings, delay, tone generator, and random

number generator.

Linker works with virtually any assembler for the Apple II. Requires 32K of RAM and one disk drive.

Diskette - \$49.95 Manual Only - \$19.95



FASTGAMMON' By Bob Christiansen. Sound, hi res, color, and musical cartoons have helped make this the most popular backgammon playing game for the Apple II. But don't let these entertaining features fool you — FASTGAMMON plays serious backgammon. Runs on any Apple II with at least 24K of RAM.

Cassette - \$19.95 Diskette - \$24.95

METEOROIDS IN SPACE"

By Bruce Wallace

We have taken our popular space game, formerly called Asteroids in Space, and made some important improvements. To accent these improvements we have given it a new name — METEOROIDS IN SPACE. Your space ship travels through a shower of deadly meteoroids. If your ship is hit, it will be destroyed, so you use your laser gun to blast the meteoroids. Big meteoroids shatter into smaller meteoroids when hit, and the smaller ones are usually faster and just as deadly. From time to time you will encounter an alien space ship whose mission is to destroy you, so you'd better destroy it first. All the action is displayed in fast, smooth, high resolu-



tion graphics, accompanied by sound effects. You now can control your ship using one of two options — the Apple game paddles or the keyboard. One of the game paddle buttons controls the laser fire. In METEOROIDS IN SPACE, the spaceship's velocity gradually decreases unless more thrust is applied, adding an element of control. Also new to this version is a hyperspace feature — translate instantly to another spot in the galaxy. The game is over when five of your ships have been destroyed. An additional ship is added for every 10,000 points you score. Runs on any Apple II with at least 32K of RAM and one disk drive.

Diskette - \$19.95

ASTROAPPLE" by Bob Male.

Your Apple computer becomes your astrologer, generating horoscopes and forecasts based on the computed positions of the heavenly bodies. This program offers a delightful and stimulating way to entertain friends. ASTROAPPLE produces natal horoscopes (birth charts) for each person based on his or her birth data. Any two people may be compared for physical, emotional, and intellectual compatibility. The program is written in Applesoft BASIC with machine language subroutines. It requires either RAM or ROM Applesoft and at least 32K of memory.



Cassette - \$14.95 Diskette - \$19.95



FRACAS** by Stuart Smith.

A fantastic adventure game like no other! Up to eight players can participate in FRACAS at the same time. Journey in the land of FAROPH, searching for hidden treasure while warding off all sorts of unfriendly and dangerous creatures. You and your friends can compete with each other or you can join forces and gang up on the monsters. Your location is presented graphically and sound effects enliven the battles. Save your adventure on diskette and continue it at some other time. Both integer or cassette and continue it at some other time. Both integer BASIC and Applesoft versions included. Requires at least 32K of RAM.

Cassette - \$19.95 Diskette - \$24.95

BATTLESHIP COMMANDER" by Erik Kilk and Matthew Jew.





A game of strategy. You and the computer each start out by positioning five ships of different sizes on a ten by ten grid. Then the shooting starts. Place your volleys skillfully — a combination of logic and luck are required to beat the computer. Cartoons show the ships sinking and announce the winner. Sound effects and flashing lights also add to the enjoyment of the game. Both Applesoft and integer BASIC versions are included. Requires at least 32K of RAM.

Cassette - \$14.95 Diskette - \$19.95

Also by Don Worth

BENEATH APPLE MANOR - Adventure. Uses Integer BASIC.

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BABBLE - Fun with words, sound, and graphics

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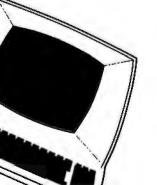


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Actively terminated, fully shielded motherboords handle clock speeds up to 10 MHz. Unkits have edge connectors and termination resistors oresoldered in place for easy assembly.

20 slot motherboard with edge connectors -Unkit \$174, A/T \$214 12 slot motherboard with edge connectors -Unkit \$129, A/T \$169 6 slot motherboard with edge connectors -Unkit \$89, A/T \$129

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CompuPro memories feature fully static design to eliminate dynomic timing problems, full conformance to all IEEE 696/S-100 specifications, high speed operation (4/5 MHz Unklt, 10 MHz A/T and CSC), low power consumption, extensive bypassing, and careful

thermal design. Unkit	A/T	CSC
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16K RAM 14 (extendedaddressing)\$279	5349	\$429
16K RAM 20-16 (extendedaddressingandbank select) \$319	\$399	\$479
24K RAM 20-24 (extended addressing and bank select), \$429	\$539	\$629
32K RAM 2O-32 (extended addressing and bank select) \$559	5699	5799

NEW! 64K RAM 17. Amozingly low power in a 64K fully static RAM board: draws less than 1.75 Watts typical, 2.5 Watts maxi it's fast, too: runs with 6 MHz Z-80° CPUs, or up to 10 MHz with 8086/88 family CPUs. Uses IEEE extended addressing protocol; also, user may turn off 2K windows from EOOO to FFFF in order to occommodate memorymopped peripherals/disk controllers. (The CompuPro disk controller con use the full 64K since it employs properly Implemented DMA techniques). \$1095 Unkit, \$1395 A/T, \$1595 CSC.

Most CompuPro products are ovaliable in Unklit form, Assembled/Tested, or qualified under the high-reliability Certifiled System Component (CSC) program (200 hour burn-in, more). Note: Unkits are not intended for novices, as de-bugging may be required due to problems suc Unklts at a flat service charge.

SYSTEM SUPPORT 1 \$295 Unkit, \$395 A/T, \$495 CSC

This multi-purpose S-100 board provides sockets for 4K of extended address EPROM or RAM (2716 pinout) with one battery backup socket; battery backup month/day/year/time crystal clock with BCD outputs; optional math processor (951) or 9512); full RS-232 serial port; three 16 bit interval timers (cascade or use independently); two interrupt controllers service 15 levels of interrupts; power fall indicator; and camprehensive owner's manual with numerous software examples. Conforms fully to all IEEE 696/\$-100 standards. (Add \$195 to the above prices for the optional 9512 math processor.)

COMPUTER ENCLOSURE 2

\$825 desk top version, \$895 rack mount version

includes fused, canstant valtage pawer supply (+8A at 25 Amps, +16V at 3 Amps, and -16V at 3 Amps); 20 slot shielded/active terminated motherboard; and rugged ali-metal enclosure with dual AC outlets on rear, heavy-duty line filter, circuit breaker, quiet ventilotion fan, and reset switch. Rack mount version includes slides for easy pull-aut from rack frame.

Also available: COMPUTER ENCLOSURE 1. Same as above, but less power supply and motherboard, \$289 desktop, \$329 rack mount.

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CompuPro CPU boords meet all IEEE 696/S-100 specifications (including timing). CPU 8085/88 uses two processors.on 8085 and 8088, to provide both 16 and 8 bit capability with a standard 8 bit bus.

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SOFTWARE

8088/8086 MONITOR-DEBUGGER: Supplied on single sided, single density, soft sectored 8" disk. CP/M" compatible. Great development tool; mnemonics used in debug conform as closely as possible to current CP/M* DDT mnemonics. \$35.

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Software Review

Super STEP

Stanley D Robbins, 249 Willow Ter, Sterling VA 22170

Super STEP is a machine-language utility that works with and is an extension of Radio Shack's T-Bug program. Super STEP allows you to run a machine-language program either by stopping at predefined locations (breakpoints) or stopping after each machine-language instruction is executed (single-stepping).

The TRS-80 video display shows a great deal of information that is useful during debugging, including the instruction currently executed, the contents of the top 5 bytes of the Z80 stack area, the status of all registers and status flags, and a user-specified area of memory. In addition, much of the information is printed twice in order to show these values before and after execution of the current machine-language instruction. Although it is not evident from the documentation supplied, Super STEP is not merely a utility that interrupts program execution after each instruction: it is a *simulation* (or *model*) that behaves like an actual Z80.

The instruction booklet that accompanies Super STEP creates the first impression—and that impression is not the best. The small type is difficult to read in good

At a Glance_

Name

Super STEP Z80 Processor Model

Type
Debugging utility for assembly-language programming (runs as an extension of Radio Shack's T-Bug

program)

Manufacturer Allen Gelder Software Box 11721 Main Post Office San Francisco CA 94101

Price \$19.95 Format Cassette tape

Language Machine language

Computer TRS-80 Model I, with Level II BASIC and 16 K bytes of memory

Documentation
Instruction booklet of
16 pages, 11.5 by 14
cm (4½ by 5½ inches)

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lighting; in reduced lighting (to facilitate reading of the TRS-80 video screen), the type is almost illegible.

The documentation is very detailed, but it took me a long time to fathom some of the obscure terminology. For example, the author, Alan Gelder, refers to "Z80 Processor Models" (plural), while more conventional terminology would refer to different "states" of the same model. An additional, but more aggravating, example occurs when he refers to "the left 1BH columns" and "the right 25H columns" of the TRS-80 video screen. After some thought, I realized that the H at the end of both "1BH" and "25H" referred to hexadecimal notation and that the author intended "1BH columns" to mean "(decimal) 27 columns" (hexadecimal 1B equals decimal 27). The video screen is a human interface and, as such, should be described with decimal values, not hexadecimal values.

Based on previous experience with a cassette-only system, I would assume that most (tape-oriented) assemblylanguage programmers have located their programs in memory to just above the top of the T-Bug program; in this way, they can use T-Bug while debugging their program. Since hexadecimal memory locations 4B00 thru 68FF are occupied by Super STEP, the user would be required to reassemble his programs to a location in memory above hexadecimal location 68FF in order to utilize this product (unless the program is relatively small and resides from hexadecimal locations 4980 to 4AFF). Of course, Allen Gelder Software also provides a product



called Super TLEGS; it enables the user to relocate Super STEP (as well as T-Bug) but costs an additional \$9.95. bringing the total to \$29.90.

The Super STEP program is loaded as follows: load Radio Shack's T-Bug software as a standard "system" tape (from BASIC, type SYSTEM, press the ENTER key, type TBUG, press ENTER, wait for the tape to finish loading); load Super STEP in the same way, using the file name "SPRSTP"; execute the machine-language program by typing a slash followed by the ENTER key (the TRS-80 should respond with a # sign); type S and press the ENTER key. (This procedure is described in the Super STEP booklet.)

At this point, Super STEP fills the video display with information: the right 37 columns fill with a display that shows the current contents of the Z80 (both the prime and unprimed sets of registers), an annotated display of the status byte that shows the flag settings, and some other information. The part of this display that I did understand was very impressive, but I was unable to decipher most of the information in the lower portion. The author describes this display in a photograph on page 3 of the instruction booklet, but his description is neither clear nor thorough.

I then used the T-Bug load (L) command to load a reassembled version (with a new starting address in memory) of the program that I wanted to debug. During the load of a program from tape, Super STEP improves upon the T-Bug loading procedure by displaying the name of the object program on the screen.

(Since Super STEP is an add-on package to Radio Shack's T-Bug program, many of the required commands are explained only in the Radio Shack T-Bug documentation. Consequently, familiarity with the T-Bug program—or at least its documentation—is necessary.)

I displayed a memory location via the memory (M) command. To advance the display to the next location, I depressed the SPACE bar (as directed in the Super STEP) instruction booklet), and the equivalent assembly instruction appeared to the right of the first byte of memory I had displayed (a feature that T-Bug doesn't offer); the following byte was then displayed on the following line (as in the normal T-Bug program). To single-step the Super STEP simulation model, depress the SPACE bar repeatedly. This will display memory one byte at a time and update the video display as each instruction is disassembled and executed.

While displaying memory, the semicolon (";") function allows you to view 16 bytes of memory simultaneously, versus the single-byte display of the normal T-Bug program. Another key determines whether this display is in hexadecimal or ASCII. Unfortunately, the display generated on the lefthand side of the video screen sometimes overwrites information on the righthand side. Although this information is correctly updated the next time an instruction is executed, the "garbage" characters remain in the spaces between information fields on the righthand side, making the screen harder to read.

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Exiting the "M(emory)" mode and reentering it at the entry point of my program, I depressed the ":" key to invoke the Super STEP trace function (ie: automatic singlestepping). I then watched my program "execute" for a while, instruction by instruction! The ":" (trace) function more than justifies the inclusion of the word "super" in the name of this product.

An additional feature is the ability to run Super STEP at two different speeds while tracing; at the slow speed, you can see individual instructions as they execute, while at the fast speed, only the registers of the display are readable.

While tracing a program, I found an error in the interaction between the halt ("Z") and trace (":") commands. Use of the "Z" key is supposed to immediately stop the automatic tracing of program execution. It does, but it may stop in the middle of a 2- or 3-byte instruction. The problem at this point was only aesthetic, but when I resumed tracing by pressing the ":" key, Super STEP took the next byte (in the middle of an instruction) and tried to interpret it as the first byte of a new instruction. This can result in the execution of an incorrect Z80 instruction.

A potential annoyance arises in the processing of a CALL or a RST (restart) instruction when tracing or single-stepping a section of a program: if the invoked subroutine is bug-free, it is irritating to slowly single-step through all the subroutine code to get back to the main routine that is being debugged. Super STEP tries to solve that problem via the "*" function. If this function is

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turned on, CALLs and RSTs will not be followed but will be "directly executed" (ie: the single-stepping is turned off during the execution); if this function is turned off, Super STEP will trace or single-step through all program code. However, this command is inconvenient when you want to step through some subroutines but not others. When I'm single-stepping through some code, I can't turn the "*" function on before a routine I don't want to trace by the time I see the CALL statement, I've already started single-stepping through the routine.

Some improvements come to mind. I would like to see some indication of interrupt status (enabled or disabled) on the video display. In addition, Super STEP would be greatly improved if the author provided three copies of the software (one each for the 16 K-byte, 32 K-byte, and 48 K-byte versions of the TRS-80) that would load in the top end of the computer's memory. It would be nice if Super STEP could be rewritten to include all of the T-Bug functions: it could then be sold as a stand-alone product. On the other hand, the additional time required to add such features is often unavailable to small software companies. If the author did incorporate these features, the necessary increase in price would probably be greater than the cost of T-Bug....GW]

Conclusions

- One of the most outstanding features of Super STEP is its ability to single-step or trace through any Z80 code, even routines in ROM; this power is due to the fact that Super STEP is a software program that simulates the Z80. so it has complete control of any program it is executing.
- On the negative side, the documentation for Super STEP is inadequate. I had to reread the instruction manual and experiment with the software in order to figure out how to use it. Users with less patience or machine-language experience will probably have trouble with this product.
- Overall, I think that the Super STEP package (in conjunction with the Super TLEGS program for an additional \$9.95) will be useful to the serious assembly-language programmer with a tape-based TRS-80. Its utility is decreased if you have a disk system (I don't know if you can save it to disk), but it still has some features that the TRSDOS DEBUG program (supplied with the TRS-80 disk operating system) doesn't have.

BYTE's Bugs

Problematic Problem Solving

The article entitled 'Machine Problem Solving" (November 1980 BYTE, by Peter Frey) has a bug on line 230 of the "Treasure Search" game. (See page 258, listing 1.)

The line should read:

230 X\$= RIGHT\$(STR\$ (B(I),1)):GOSUB 1000

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Software Review

Wordsmith

Mark Dahmke, 1515 Superior Apt 15, Lincoln NE 68521

The greatest compliment I can give Scion Corporation's Wordsmith is that I am using it to write this review. I have searched long and hard for a word processor that would give me the features and capabilities of a big-system full-screen editor.

I used to do all of my writing on an IBM 370 computer, using a full-screen editor and a batch program that read in my text and formatted it for a high-speed printer. The full-screen editor was adequate, but the batch program was painful to use because you couldn't see the results without running it (over and over). It was like using a compiler instead of an interpreter-you had to wait.

Wordsmith combines the features of a good, full-screen editor (one of the nicest I have used) in a "what you see is what you get" format, thus allowing text to appear on the screen exactly as you want it printed.

Wordsmith Overview

Wordsmith runs on an 8080- or Z80-based microcomputer with either CP/M or North Star disk operating systems. The distribution disk also supplies a customization program that allows the user to define the ASCII codes of the special-function keys, the location of the memory-mapped video display, and the printer interface.

Unlike many other word processors, Wordsmith is page-oriented, ie: page boundaries are maintained in the disk file. Scion's Screensplitter video display has 86 characters per line and 40 lines, but Wordsmith uses the top line as a "scoreboard" to keep track of cursor position (line and column numbers), current page, total number of pages, and the maximum number of pages that can be used within the disk file that is currently open. The file name (fully qualified by the conventions of the operating system in use) is also shown on the scoreboard. The right portion of the scoreboard is used to enter commands. Getting to the command line is easy-just hit Break, or the key you have assigned to that function. The command line then becomes active, shows a cursor, and awaits your input. Hitting Break again terminates command entry and executes the command. If no command is entered (ie: if you hit Break twice without entering a command), nothing will happen. Wordsmith has over seventy commands, not including those used for cursor movement (up, down, left, right, etc).

About the Author

Mark Dahmke is a consulting editor for BYTE magazine. He also operates a computer consulting business called MCD Consulting and is involved in the design of office automation systems. His interests include astronomy, science fiction, writing, and painting.

At a Glance ·

Wordsmith page-oriented word processor

Word processing

Manufacturer

Scion Corporation 8455-D Tyco Rd Vienna VA 22180 (703) 827-0888

Wordsmith word processor (CP/M or North Star): \$295; Screensplitter video board (86 characters by 40 lines) and firmware: \$395. Video subsystem (Wordsmith, Screensplitter board, firmware, 15-inch greenphosphor video monitor, and high-quality wordprocessor keyboard IBM Selectric II style): \$1795

Features

Wordsmith word processor (software) runs with a memory-mapped video display (the Screensplitter) with 86 characters per line and 40 lines. Wordsmith is completely reentrant and is written in 8080 assembly language

Operating System

CP/M 8-inch or North Star 5-inch (single-, double-, or quad-density) floppy-disk formats; also IMDOS, MDOS, CDOS (single-, double-, or quad-density formats)

Hardware

Any S-100 8080- or Z80-based microcomputer. Wordsmith will run in a CP/M system with only 16 K bytes of memory. The Scion Screensplitter memory-mapped video board is required.

Documentation

66-page manual, 21 by 27.5 cm (81/2 by 11 inches), for Wordsmith; 70-page manual for Screensplitter (same size)

Firmware

1 K bytes of video-display software in a 2708 EPROM

Audience

Anyone requiring highquality word-processing capability

Other Features

Wordsmith has many other features that make text entry less tedious. The tab-stop line allows you to set up any number of tabs in a given text file. When you enter the ET command, Wordsmith displays a reverse-video line just below the scoreboard. You can place a period wherever you want a tab stop, and Wordsmith will remember the tab-stop line (the line of periods) for each separate disk file. Once set up, the tab stops may be altered by entering the ET command again.

The hold area is a reserved area of memory that can be used to save up to an entire screen page (86 characters by 39 lines). Using this feature, any amount of text, from a single word or line to an entire screen page, may be copied to another part of the screen, another page in the file, or another disk file. Many commands are available for copying the held text back to the screen. For example, it may be put down "literally," meaning that it will be placed on the screen just as it was copied from the screen. The PF, or put-formatted command, will reformat the

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Last August, Chris Morgan and I went to Washington DC to see a networked office-automation system that Scion had installed in a congressman's office. The system, called Rosenet, consists of a network of Z80 microcomputers running a modified version of the North Star disk operating system. Each workstation also includes a Wordsmith video subsystem. All workstations are tied to a central microcomputer that maintains data bases and an electronic mail/memo system. The master system also provides printer and dial-up modem services to the workstations, which communicate with the master through RS-232C lines running at 19,200 bits per second...MCD

text in the hold area to fit a new shape or region of the screen. This allows you to work easily with "newspaper columns."

Up to 20 text windows may be defined on each page. Wordsmith keeps track of the windows on each page and the cursor location within each window. This extra information is stored in blocks at the end of the text file, which allows the file to be read in by an assembler or compiler without interference. A window may be any size, from 1 by 2 characters to a whole screen page. This feature is most useful in "cut and paste" operations. When several windows are defined on a page (the screen itself is called the base window), you can move from window to window by hitting the Cycle key. This moves you to the next window in the loop, and eventually returns you to the one you started at. When a window is active, it is im-



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possible to type text or move the cursor to a location outside the window. It's like having a miniature screen within a screen. Windows are also useful for setting up templates—files with no text, but with a window structure. A template might, for example, be set up to look like a standard letter format with header, body, closing, and so on. It is then a simple matter to fill in the blanks when writing the letter.

A large selection of cursor-movement commands is available, beginning with the obvious: up, down, left, right and home (move to the upper lefthand corner of the window). On the video-subsystem keyboard, typing Shift in combination with one of the cursor-direction keys causes movement of the cursor in increments of eight character positions, instead of one. Also included are: delete to end of line, move to end of line, delete character, backspace, insert blank, insert mode, delete left, and tab.

Line control and movement commands include: insert line, delete line, insert multiple lines, delete multiple lines, center line, hold multiple lines (in hold area), split line and join line, and search line for string.

Among the window control and movement commands are: open window, clear window, set mark, clear marks, open line window, open paragraph window, drop window, drop all windows, cycle (to next window), go to base window, jump window (to new location on screen), illuminate all windows (ie: set to reverse video), change size transparently, change size, fill window (from hold area), adjust window (right justify), hold window, put text literally (from hold area), put text formatted, erase window, search for string, and search and replace string.

Page Control and Movement

Pages may be inserted and deleted, up to the limit of pages allowed in a disk file. When a new file is created (using the new-file command), you must specify the number of pages you require. Other commands include: NP (flip to next page), PP (flip to previous page), PGn (go to page n), PG+n (go forward n pages), PG-n (go backward n pages), IP (insert page), DP (delete page), CP (reread current page off disk), SP (split page into two pages—split at the cursor), JP (join two pages), save and recall page templates (window structures).

Disk-File Management

Files can be created with the NF (new-file) command. For example, the command NF B:TEST-10 will create a file (under CP/M) on the B disk called TEST, with room for ten text pages. The command OF B:TEST will get the old file called TEST from the B disk. The page that was saved in the previous editing session will be redisplayed on the screen. CL (close file) ends an editing session and closes a file. Since text pages are not necessarily in sequential order in a file, the SQ (sequence file) command will sort them into order. (This is not needed for normal operations, except when Wordsmith files are being used to store programs or other information that will subsequently be read by another program, such as an assembler or BASIC compiler.) Other file-level commands include: SRFs (search file for string "s"), SUFs (substitute next occurrence) and SAFs (substitute all occurrences in file).

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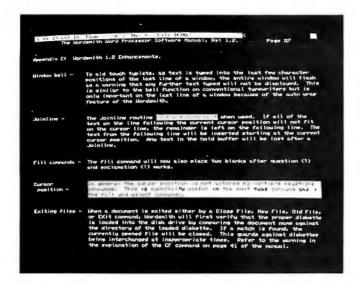


Photo 1: The Wordsmith word processor as displayed on the Screensplitter video board.

Printer Control

Scion supplies the intelligent printer interface of your choice. Printers currently supported include the Diablo 1610, 1620, 1650, NEC 1510, 1520, Qume Sprint 5, and any printer that accepts only carriage return, line feed, and form feed as control characters. A printed page may range from 1 to 255 lines in length. The user has control over the top margin, left margin, and number of lines per

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page. All hard-copy commands (except Type Window) begin printing from the cursor line and proceed through the file. The format for all five commands is:

(Command)-(t),(l),(h)

where t is the top margin (defaults to 4 lines), l is the left margin (defaults to 4 columns), h is the number of lines per page (defaults to 50).

If all defaults are used, Wordsmith will format output for an 8½- by 11-inch page. Control-S may be used to temporarily stop printing, and Control-K may be used to abort the print command.

The available printer commands are as follows:

•TCL (type continuous literally): The entire document is printed on the printer, starting on the current page and the current cursor line. Any blank lines at the bottom of a screen page will also be typed.

•TSL (type sheets literally): Wordsmith will pause after printing each page and await a carriage return from the keyboard. This permits use of single sheets of paper (eg: letterhead paper).

•TCC (type continuous compacted): Similar to TCL except that any blank lines at the end of each screen page will be ignored.

•TSC (type sheets compacted): Similar to TCC except that Wordsmith will wait for a carriage return at the end of each page.

•TW (type window): The current window is typed. This command is useful for cut-and-paste operations and for previewing portions of the document prior to final printing.

Wordsmith also allows the definition of page headers and footers. When a header or footer is set up, you may specify where it is to start (on what printed page) and, if page numbers are used, with which number it should begin. The page number will be inserted automatically anywhere in the header or footer where you have typed three pound signs (#) in a row. The page number will be left-justified within this field.

Software Problems

No software product is without its bugs, but Wordsmith is very reliable (it has never caused text to be lost). There are, however, some minor, annoying problems. First, the header and footer commands don't work properly if the default parameters are changed. Second, if no files are open and you issue a save-page command, the program may write over the file pointed to by the FCB (File Control Block) in the CP/M version. Otherwise, Wordsmith performs excellently, and the company, anxious to overcome any bugs, will often give you corrections over the phone (assuming you know 8080 assembly language).

Conclusions

The Wordsmith/Screensplitter combination forms one of the best word processors I have ever used, either on a microcomputer or a large system. The command repertoire is extensive, yet easy to use and learn. Many of its features are not available on word processors of any size or price.



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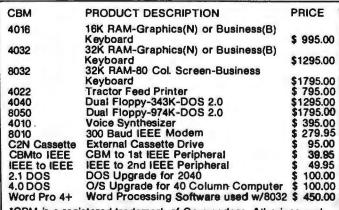
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BYTELINES

News and Speculation About Personal Computing Conducted by Sol Libes

n Apple III Emulation Mode: Axion Inc is working on a project that will allow an Apple II to run most of the software designed for the Apple IIIincluding the Apple III disk operating system, SOS. The product has its roots in another product recently introduced by Axlon, the Ax-Ion 256 Memory System. The unit consists of an interface card and a card cage that contains up to 256 K bytes of memory. There are separate versions for the Atari and Apple II, and one for the Apple III is in the works. Expressed simply, the unit can exchange 32 K-byte blocks of its memory for the top 32 K bytes in the 48 K machine connected to it.

Special disk-operating-system software included with the unit makes its operation transparent to the user. The hardware/software combination looks to the host computer like a large-capacity disk drive. Program files in the memory of the unit can be run as if they were on floppy disk, and data files can be accessed in both random and serial fashion. There are two advantages to this unit: one, information can be accessed in microseconds (as opposed to milliseconds or longer for floppydisk drives); and two, the increased main-memory space makes both existing and proposed programs that crowd the current 48 K-byte limit more feasible.

The Sunnyvale, Californiabased Axlon is working with Apple Computer to finalize the design of the Apple III emulation hardware/software combination. The proposed unit will include the Axlon 256 Memory System, a special hardware board, special software, and an 80-column adapter for the Apple II.

EEPROM is Coming: Several IC designers are predicting that the EEPROM (electrically erasable programmable read-only memory) will replace the ultraviolet-light EPROM within three to four years and may, perhaps, be used as nonvolatile main memory, Several companies are now putting finishing touches on these devices for introduction later this year. For example, Hitachi has announced the HN48016, a 16 K-bit EEPROM (2 K by 8 bits) that is pin-compatible with the popular 2716 UV-EPROM. It uses the same voltages, takes 10 ms per byte to program, and can be completely erased with a 1-second pulse. Data retention is claimed to be more than ten years. Intel has a similar device called the 2816. Prices and access times are comparable to their EPROM equivalents.

VI Icrosoft **Graphics Commands To** BASIC: Microsoft is offering OEMs who have hardware graphics capability an enhanced version of the popular BASIC-80 interpreter. The added commands will allow you to create lines, boxes, circles, curves, do object painting and relocation, and save all your work. Seven new commands have been added: CIRCLE, PAINT, GETSET, LINE, DRAW, PUT, and PRESET.

Continuing AMSAT OSCAR Activity: AMSAT, the Radio Amateur Satellite Corporation, has survived the loss of its Phase-IIIA OSCAR satellite. (See "BYTELINES," September 1980 BYTE, page 166.)

Construction of a new Phase-IIIB satellite is underway in Marburg, West Germany; Budapest, Hungary; and Washington DC. AMSAT has scheduled the satellite's launch for February 24, 1982 on a European Space Agency Arrianebooster flight.

As part of its planned use, the satellite will relay computer data by amateur radio operators in personal-computer networks.

For information on how to join AMSAT and receive Orbit magazine, write to AMSAT, POB 27, Washington DC 20044.

Details On 32-Bit Microprocessors: Intel released more information on its new 32-bit microprocessor, called the iAPX432. The microprocessor, under development for six years, features an object-oriented architecture that treats highlevel entities as elementary software components that can be easily manipulated. These entities include records, gueues, tasks, and collections of procedures.

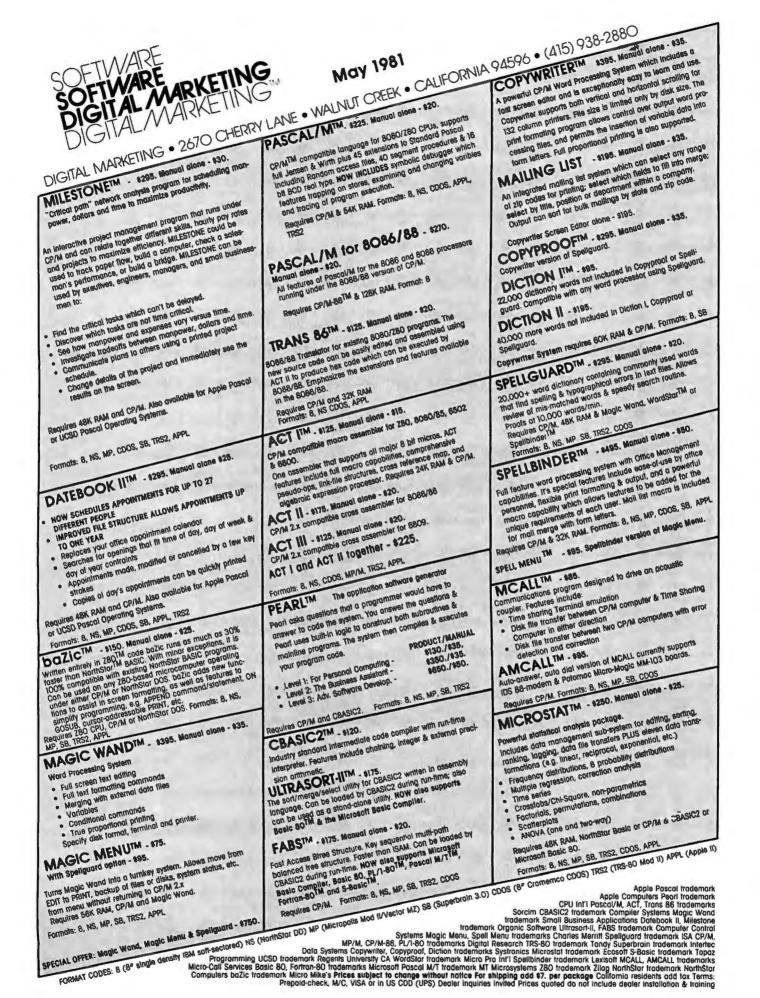
In its simplest form, the microprocessor consists of two integrated circuits. More processors can be added later to obtain multiprocessing without altering software. It is expected that samples will be available in the fall.

\$ 100,000 Computer-Chess Prize Offered: Carnegie-Mellon University (CMU) is offering a prize of \$100,000 to the first person to develop a computer program that can defeat the world chess champion. Dr. Hans Berliner, a computer scientist at CMU and a former world chess champion, heads the competitionrules committee. He feels that the prize may be won by 1990 or sooner, but certainly no later than the year

Last year a \$1000 CMU prize was won when Jack Gibson, a chess expert, was defeated by Belle, a computer-chess machine developed by Ken Thompson and Dr Joe Condon, researchers at Bell Laboratories, Murray Hill, New Jersey.

M Ini-Winchester Update: Five companies have announced 5-inch Winchester drives. The drives' storage capacity ranges from 1.8 to 12.3 megabytes (unformatted), and prices vary from \$690 to \$1600 (500-unit quantity prices). Most suppliers are now shipping evaluation units to OEMs (original equipment manufacturers), with limited production expected by late summer. Don't expect full production until next year.

The five companies which have already announced mini-Winchester hard disks are Shugart Associates, Seagate Technology, Irwin International, Tandon Magnetics, and New World Computer Company. The price leader appears to be Shugart, with its SA602 3.3-megabyte drive at \$660. The maximum storage leader is the 12.3-megabyte



Model 510 from Irwin. It costs \$1500, which includes integral tape-cartridge backup.

I improves The 99/4 Home Computer: Texas Instruments is determined to make its TI 99/4 home computer a success. TI has improved the competitive position of the 99/4 by substantial price cuts and software improvements, the two areas in which the machine fared poorly. The new list price for the console is \$649.95, a reduction of \$300, and the radio-frequency (RF) modulator's price has been reduced from \$75 to \$50.

TI has introduced a software-development system that includes UCSD Pascal and a ROM (read-only memory) module with an assembly-language debugger. The console has been modified and includes dual floppy-disk drives and RS-232C interfaces. TI has also announced third-party software-incentive programs for software developers. TI plans to introduce extended BASIC and memory-expansion capabilities in the TI 99/4. Regrettably, TI has not seen fit to improve the keyboard or make any substantial hardware improvements other than the addition of memory.

ore On Dalsy-Wheel Printers: Daisywheel printers are the most widely used printers for letter-quality hard copy, but the market is undergoing substantial change. Last year, the number of daisywheel-printer manufacturers doubled. More competition meant lower prices and increased performance. The new entries came from Olivetti, Fujitsu, Ricoh, C Itoh, and Pertec. Qume and Diablo still dominate the market, but competitors are

broadening their performance range from the traditional 45 to 55 cps (characters per second) to 15 to 80 cps.

The 45 to 55 cps range is dominated by Qume, an ITT subsidiary with 45% of the market, and Diablo, a Xerox subsidiary with 40% of the market. NEC also has a 10% market share, with the other companies dividing the remaining 5%. The prices of these machines should drop about \$1000, to \$2700 within the next two to three years. and the printer manufacturers will most likely introduce 30 cps versions selling for under \$2000. Look for the 30 cps machines by year-

Expect a price war between the manufacturers of the lower-speed 15 to 20 cps printers. Prices may drop to \$1200 or less by year-end. Those companies at loggerheads in this marketing war are Ricoh (which supplies Tandy), Olivetti, Pertec (which supplies machines made by Triumph-Adler), and C ltoh.

Fujitsu has already demonstrated an 80 cps daisy-wheel printer. Look for it in computer stores this summer. Qume, Diablo, and NEC are expected to introduce 80 cps machines, and some companies are working on 100 cps machines.

da On Microcomputers: At a recent ACM/SIGPLAN-sponsored meeting, TeleSoftware demonstrated the new Ada language on a 16-bit microcomputer. The compiler is 50 K bytes, supports run-time utilities, and produces pseudocode that runs directly on a Western Digital Pascal/Ada Microengine system. Tele-Software said that the Ada code could be converted to the native code of some other microprocessor by use of a simple p-code interpreter (p-code is the machine language executed by the Microengine). Ken Bowles, the developer of UCSD Pascal and founder of TeleSoftware, said the company also intends to provide Ada compilers for 8086, 68000-, and Z8000-based systems.

Western Digital will manufacture the Pascal/Ada Microengine for \$12,750. It will include 128 K bytes of programmable memory, five I/O ports, a 10-slot chassis, video-display terminal, dual floppy-disk drives, and a line printer. The basic system will cost \$6210. Western Digital also said that it soon expects to release a hard-disk controller, a cryptographic security module, a distributed multiprogramming operating system, and an X.25 packet-switching and local network product for the processor.

omputer Bulletin **Boards Grow in Popular**ity: There are over 200 CBBS (computer bulletin board systems) in this country and their number grows weekly. Anyone with a terminal, modem, and telephone can access them. (If you use an Apple computer, they are called ABBS.) Most CBBS and ABBS serve as message centers for computer clubs. Some systems distribute software; for this service, a caller needs a computer with modem-driver software for file transfers.

Other bulletin board systems serve special interests (eg: AMRAD's Blind Service CBBS 703-281-2222, the Family Historians' CBBS 703-978-7561, and Aviators' BBS 916-393-4459). For more information on all of these systems and how to access them, call the MAGMEDIA-80 CBBS (415) 573-8768.

Come The Japanese: Expect to see several Japanese personalcomputer systems in US stores this fall. Most of the systems will compete directly with the Apple II. Commodore PET, and Radio Shack TRS-80. They'll sell for the same price, perhaps slightly less, but offer extra features, NEC (Nippon Electric Company) will market the PC-8001 at the same price as the Apple II. (See "The NEC PC-8001: A New Japanese Personal Computer," by Michael Keith and C P Kocher, January 1981 BYTE, page 72.) Its features will match or exceed the Apple's, Matsushita (known in America as Quasar and Panasonic) and Sharp are also expected to have their systems on dealer shelves this fall. The Z80-based Sharp system is already on sale in England. One English distributor has already adapted CP/M for it.

hopping Via Computer: Comparison retail shopping by home computer appears to be the wave of the future. One of the first computerized retailers is Comp-U-Card of Stamford, Connecticut. It claims to have 1.5 million members, of which 5000 already have computer I/O capability. To become a member it costs \$18 per year, or \$9 if you come under a group plan. To access the service's base of more than 30,000 items, you call it either via a toll-free telephone number or a twoway cable TV hookup. Comp-U-Card presents product specifications, price, and delivery charges. You can order any item at a typical savings of 20 to 40% or just use the service to compare prices.

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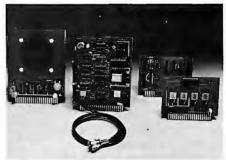


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	VP-44	RAM On-Board Expansion Kit—Fou 2114 RAM ICs, Expands VP-711	r				use of 2 Accessory Boards in eith I/O or Expansion Socket
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	VP-595	Simple Sound Board—Provides 256 programmable frequencies. For simple music or sound effects.				VP-710	Bytes RAM (avail. 7/80) Game Manual—Listing for 16 exciting games
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		Outputs to audio	\$	49	Er	rclosed	d is \$for items che
	VP-551	4-Channel Super Sound—Includes VP-576 and demo cassette. Require VP-550 and 4K RAM		74		•	state and local taxes \$_ □ check or □ money or
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☐ VP-565	EPROM Programmer Board—			Keyboards & Terminals	
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- VD 575	With software	\$ 99		encoded alphanumeric 8-bit parallel output	\$ 69
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	expansion sockets	\$ 59		Asynchronous serial output	\$ 99
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	use of 2 Accessory Boards in either				\$ 89
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□ VP-701	Floating point BASIC for	4 39	☐ VP-620	Cable—Connects VP-601/611 to VP-111/711	\$ 20
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	Bytes RAM (avail. 7/80)	\$ 49	U VF-025	VP-601/611	\$ 20
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				with built-in RF output	
Enclose	d is \$for items check	red plu	ıs shipping	g & handling charge of $\$3.00$	Э.
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panding: The need to access reference material has become much easier because of computerized database distributors. For example, a lawyer can access the Nexis system from Mead Data Central, 200 Park Ave, New York NY 10017, for a special keyed-word newssearch service. The cost is \$1 to \$1.50 per minute, plus a \$300 monthly charge. The initial sign-up charge is \$425. There are many lower-cost data-base services catering to the special needs of various professionals.

For information on database systems, consult the Directory of On-Line Data Bases, published by Cuadra Associates, 1523 Sixth St. Suite 12, Santa Monica CA 90401. The price is \$60 per year (four issues).

pomputer Makers To Market Private Software: If you develop software for the HP-85 desk-top system in your spare time, Hewlett-Packard has a plan for marketing it. Hewlett-Packard will pay a royalty for the software and offer to sell you a system at a discount. Burroughs has a similar plan.

On Robotics Shopping Spree: GM has ordered 25 robots for its transmission-machining lines at its Warren, Michigan, facility. This is the largest undertaking of its kind in the automotive industry. The robots will cost almost \$2 million. GM plans to buy as many as 1800 programmable robots between now and 1984.

In a related development, GM will use laser checking devices on its J-car-body assembly lines; the devices will check 20 to 30 points on each car for proper body fit and panel alignment. There will be no contact with the auto during this checking procedure.

T&T To Enter Computer Market: In a landmark decision, the FCC will allow AT&T (American Telephone & Telegraph) to enter the computer business. The decision requires AT&T to set up a separate subsidiary to offer terminals and computer-enhanced services. Industry pundits speculate that AT&T will position itself to capitalize on the marriage of the telephone and computer technologies.

sed Word-Processor Market Burgeoning: You can save quite a bit of money by buying a used word processor, IBM, Xerox, Lanier, and Vydec systems are becoming available as companies upgrade to newer, more powerful machines. In Minneapolis, Word Systems Inc specializes in selling used wordprocessor systems, although they are also available through many other dealers.

Extra-Life Printer Ribbons: Replacing printer ribbons is expensive. Here's how to revive worn-out closed-loop ribbons housed in plastic cases: carefully pry open the case without disturbing the ribbon. Spray the ribbon lightly (don't overspray) with an all-purpose lubricant such as WD-40, close the case, and let it stand overnight. The lubricant causes the ink from the moist unused portions of the ribbon to flow down into the dry areas of the ribbon. This renewal process can usually be repeated several times before the ribbon is completely exhausted.

Random News Bits: United States Robots, Conshocken, Pennsylvania, claims to have developed a five-jointed robot arm using seven microprocessors—one for each joint, one for math calculations, and one for overall coordination. The microprocessors do multiprocessing on a shared bus and memory system. ... Toshiba and Hitachi have demonstrated "pocketbook TVs" that typically use 120- by 160-element LCDs (liquidcrystal displays). Matsushita and Hitachi reportedly will introduce products next year using these displays. ...Interested in learning more about possible health hazards associated with CRT (cathode-ray tube) terminals? Then you should get a copy of the 16-page pamphlet entitled Health Protection for Operators of DCTs/CRTs. It's published by the New York Committee for Occupational Safety and Health, 32 Union Sq, Rm 404, New York NY 10003 (\$1 for individuals; \$3 for institutions).

Random Rumors:

Apple Computer may put off its plans to build 5-inch Winchester-disk drives for the Apple III and the rumored Apple IV. Apple has reportedly inked a contract for 10,000 six-megabyte ST-506 drives from Seagate Technology. Apple still plans to produce a hard-disk drive for introduction next year. ... It is rumored that Digital Equipment Corporation has developed a single-integratedcircuit version of the PDP-11 and that it exists in prototype form. No production plans have as yet been established. ... There is a lot of talk circulating that Xerox will soon release a version of the Smalltalk programming language and a complete book describing it. Most likely it will be released to

universities who presently have the Xerox Alto system (an experimental personal computer). ...Electronic News recently reported that IBM and Tandy were holding discussions on the possibility of IBM 3103 video terminals being sold through Radio Shack stores. ... According to a report issued by International Resource Development Inc (IRD) in Norwalk, Connecticut, IBM, Xerox, and Matsushita will introduce typewriters with voice input by 1983. IRD predicts that the typewriters will correctly recognize 93% of "typical business English as spoken by the average executive," and that the unit will have a video screen that displays the spoken words. Corrections and changes can be made on the screen....

MAIL: I receive a large number of letters each month as a result of this column. If you write to me and wish a response, please include a self-addressed, stamped envelope.

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MICROSOFT BASIC-86

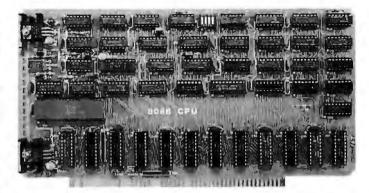
IT'S THE STANDARD — This BASIC is essentially identical to version 5 of Microsoft's BASIC interpreter, the accepted standard with widely available application programs. Programs distributed in CP/M® format are easily converted to the 86-DOS system. (CP/M is a registered trademark of Digital Research.)

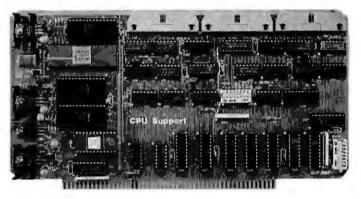
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267

Continued from page 20:

Resistible Puzzie

with resistance of 355/113 (a very close

approximation to π) with a minimum

number of unit-valued resistors. (See the

January 1981 BYTE, page 16.) He im-

proved greatly on W Lloyd Milligan's

26-unit solution (see the August 1980

BYTE, page 20) by presenting two 18-unit

solutions and asking if anyone could find a solution with 17 or fewer resistors.

method in favor of one based on

Diophantine equations (those having

positive, non-zero integer solutions), I

was able to come up with two different

15-unit solutions, (See figures 1a and 1b.)

essentially the only minimal solutions (ie:

I believe these two to be minimal, and

By abandoning their continued-fraction

John Moore revived the earlierpublished problem of creating a network



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except for other solutions created by trivial transpositions of series and parallel elements in one of these resistors) within

(1a)

the class of networks examined by this method and by the continued-fraction method (ie: all simple series-parallel networks).

But there are many more ways to connect a handful of resistors than just in simple series-parallel networks!

I looked for a solution with a bridge as a part of the total network. With the help of a TI-58 programmable calculator, I was able to find a 14-unit solution. (See figures 2 and 3 on page 270.)

Of course, with 12 or 13 resistances to connect together in an arbitrary fashion, much more complicated figures than bridges are possible. Unfortunately, the calculation of resulting network impedance, and the searching through the various configurations, becomes correspondingly complex. I suspect that the 14-unit solution can be improved upon.

(1b)

David F Smith 3033 Turk Blvd, #3 San Francisco CA 94118

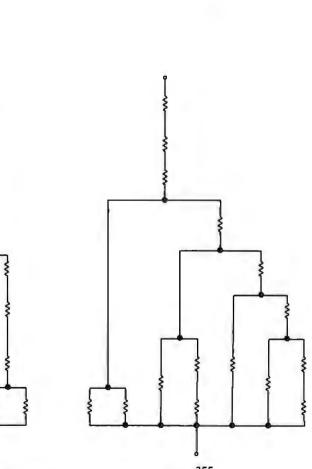


Figure 1: Two 15-unit networks with $Z = \frac{355}{113}$

OR EDIT





#5

#6









#7

#8





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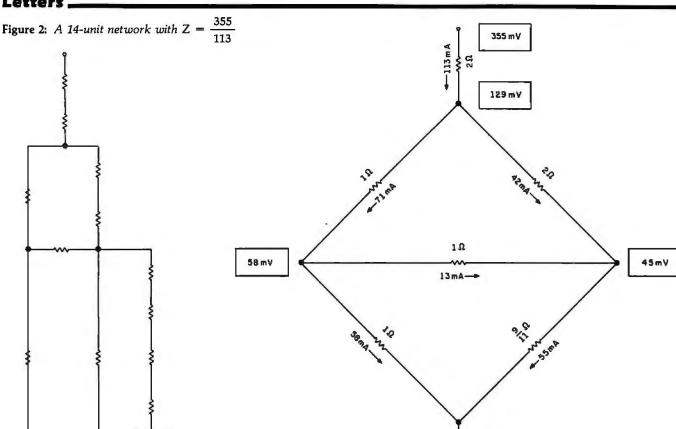


Figure 3: Voltages and currents in the 14-unit network with 355 mV across it.

0 m V

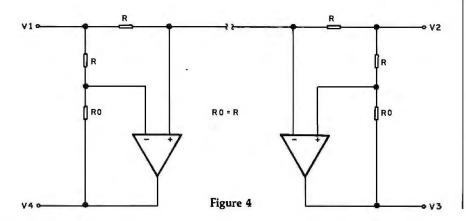
Easier Communication In Two Directions

Mark R Titchener's article "Communications in Two Directions" (June 1980 BYTE, page 96) presents a circuit to communicate bidirectionally on a single line; however, it requires too many components. An easier way to do it is shown in figure 4. This circuit will work for both analog and digital signals. Using standard op-amp theory, it is easily shown that V4 = V2 and V3 = V1. Line impedance can be compensated for by making RO variable.

R Gupta **Electrical Engineering** University of Auckland Private Bas, Auckland New Zealand

Smart Wheelchair Project

Steve Ciarcia's article "Home in on the



Rangel An Ultrasonic Ranging System" (November 1980 BYTE, page 32) was excellent. I would, however, like to make BYTE readers aware of another project that has incorporated the Polaroid Ultrasonic Ranging technology. The project was funded by the Veterans Administration Rehabilitative Engineering Research and Development Center of Palo Alto, California. The participants, Karen Altman, Rick Epstein, Leslie Gerding, Wayne Ledger, and Dave Parker, were graduate students last year at Stanford Mechanical Engineering.

The objective was to design, develop, and successfully fabricate a "smart" electronic wheelchair. Its construction included ten ultrasonic sensors, eight of which were used to detect approaching obstacles or the presence of a wall on either side of the chair. The remaining sensors were focused on the user's head from two angles.

The chair has many modes of operation: the most important is the headcontrol mode. Here, the user directs the movements of the chair by head motions. To move the chair forward, the user posi-

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In operation, the front-facing ultrasound sensors detect the presence of obstacles in the chair's path. When such an obstacle comes within a predetermined distance, the chair automatically slows and stops before running into it. If the

"obstacle" moves away, the chair will follow at a fixed distance.

Side sensors serve to detect walls. A mode to "follow that wall" enables a chair to travel parallel to the chosen wall at a fixed distance. Open doorways are detected and passed over, but a discontinuity of more than a few feet disables the wall-following mode and waits for further commands from the user.

A "cruise control" mode does not use any additional sensors, but instead relies on wheel-speed data obtained from two optical shaft encoders. Once in this mode,

the chair proceeds at a constant speed and heading despite changes in terrain.

A final mode allows the head to be moved without affecting the chair.

The user initializes the system to the range of his or her head motion by means of a "training" program that instructs the user to center the head, to move it to the left or right, and forward or backward. The program uses this information to calibrate the position/speed algorithm as well as set up a dead band around the user's rest position.

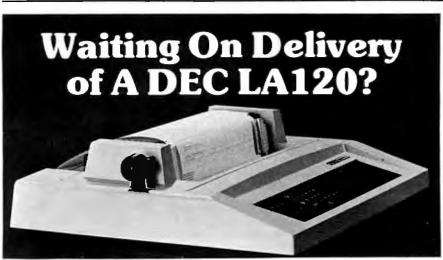
The hardware presently consists of a Z80 microprocessor, 64 K bytes of memory, and an external disk-drive system. Once the program is loaded, the disk is disconnected and the user drives off. The software executive is written in BASIC. with a majority of the actual real-time program coded in machine language and as arithmetic function calls. The listing consumes 40 pages.

The current construction phase will shrink the initial hardware and software configuration by one-third. A final design will capture the features on a single printed-circuit board.

The approach taken in pursuit of the interface between the ultrasound sensors and the microprocessor is considerably different from the method described in Steve's article. Since the Polaroid kits were not available at the time of construction, several new cameras were sacrificed to acquire the parts required. In addition, the computer interface was done not at the EDB level, but at the custom ultrasound board level. To perform a ranging, the computer generates a transmit request pulse via a convenient parallel output bit. The output from the board is then interrogated to start a software timing loop that is terminated by the received echo signal. The number of times the loop is performed gives a fairly precise measure of the range. Dividing this value by an appropriate factor will yield the range in whatever units are required. In the course of the project, a resolution of about a quarter of an inch was obtained over distances ranging from 9 inches to 20 feet (depending on surface characteristics).

Additional information about this ongoing project can be obtained by writing me at the address below.

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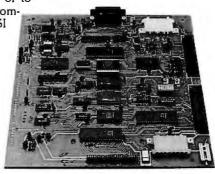
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dBASE II vs. the Bilge Pumps.

by Hal Pawluk

We all know that bilge pumps suck.

And by now, we've found out—the hard way—that a lot of software seems to work the

same way.

So I got pretty excited when I ran across dBASE II, an assembly-language relational Database Management System for CP/M. It works! And even a rank beginner like myself got it up and running the first time I sat down with it.

If you're looking for software to deal with your data, too, here are some tips that will help:



dBASE II vs. everything else.

dBASE II really impressed me.
Written in assembly language (with no

need for a host language), it handles up to 65,000 records (up to 32 fields and 1000 bytes each), stores numeric data as packed strings so there are no round-off errors, has a superfast multiple-key sort, and supports ISAM based on B* trees.

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(so when you've set up the formats, your secretary can do the work). Its report generator and user-definable full screen operations mean that you can even use your existing forms.

And if all this makes your mouth water, but you've already got all your data on a disk, that's okay: dBASE II reads your ASCII files and adds the data to its own database.

Right now, I'm using dBASE II with my word processor for budgeting, scheduling and preparing reports for my clients.

Next come job costing, time billing and accounting.



Tip #1: Database Management vs. File Handling:

Any list or collection of data is, loosely, a data base, but most of those "data base management" articles in the buzzbooks are really about file handling programs for specific applications. A real Database Management System gives you data and program independence (no reprogramming when data changes), eliminates data duplication and makes it easy to turn data into information.

Tip #2: Assembly Language vs. BASIC:

This one's easy: if you're setting up a DBMS, you're going to be doing a lot of sorting, and Basic sorts are s-l-o-w. Run a benchmark on a Basic system like S*-IV against a relational DBMS like dBASE II and you'll see what I mean. (But watch it: I've also seen one extremely slow assembly-language file management system.)

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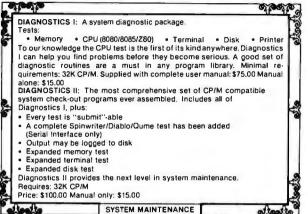
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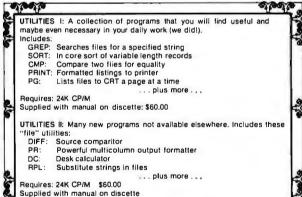
• left & right margin justification

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 underlining and backspace TFS lets you make multiple copies of any text. For example: Personalized form letters complete with name, address & other inser-tions from a disk file. Text is not limited to the size of RAM making TFS perfect for reports or any big job. Text is entered using CP/M standard editor or most any CP/M compatible editor.

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Technical Forum.

Text continued from page 228:

My application required that I code the 10 decimal digits (0 thru 9). I borrowed the 7-bit-per-digit bar code used in the UPC (Universal Product Code) to represent those digits. [Note that UPC bar codes, as shown in figure 1, have a different appearance from PAPER-BYTE® and other bar-code formats....GW | Each of the identifiers that is generated consists of 6 digits, thereby allowing the printer to operate close to the left margin. This was a distinct advantage for my application. The dot-backspacing feature of the printer reduces the dotposition counter by the amount the user specifies, returns the carriage to the left margin, and then back to the new position indicated by the pointer. Because of this method of printing, the time required to print a line increases disproportionately with its length. Thus, short lines are desirable.

The following procedure was used to generate bar codes with the Centronics 737:

•Set the proportional-spacing mode on the printer by issuing the command:

LPRINT CHR\$(27); CHR\$(17);

This can be done either in, or before running the program, but I suggest doing it in the program to avoid problems that arise in the monospacing mode.

- •Read the character codes into a binary array.
- •Use the INKEY\$ function to enter a character to be printed in bar code. Use the entered value to retrieve the binary code for the character from the array. The 1s and Os are values of the variable J, and are used as follows in the LPRINT statement:

LPRINT CHR(92 * J + 32);

If J=1, then CHR\$(124) causes a bar to be printed. If J=0, then CHR\$(32) results in a blank space.

• Backspace to the dot position immediately following the one just printed, by issuing the following printer command:

LPRINT CHR\$(08); CHR\$(4);

In my application, I placed equivalent Arabic numerals

800218 824009

Figure 1: Bar codes generated by a Centronics 737 dot-matrix printer and a TRS-80 computer, using the program in listing 1. The program also prints the equivalent Arabic numerals under



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Technical Forum

Listing 1: Bar-code generator. The program, written for the Radio Shack TRS-80 with Level II BASIC, generates bar codes for the decimal digits 0 thru 9 on a Centronics 737 printer.

```
10 DIM B(10,7)
       LOAD THE BINARY ARRAY
50 FORI=0T09:FORJ=1T07:READB(I,J):NEXTJ:NEXTI
        SET THE PROPORTIONAL SPACING MODE ON THE PRINTER
70
90 LFRINTCHR$(27); CHR$(17);
1 1 1 1
         EFGIN SIX-DIGIT INPUT LOOP
110
130 FORN=1TO6
150
         STROBE KEYBOARD FOR AN INPUT DIGIT
160
170 Y$=INKEY$:IFY$=""THEN170 ELSEI=VAL(Y$):A$(N)=Y$
180
         RETRIEVE BINARY CODE FOR THE DIGIT AND PRINT
200 4
         THE BAR CODE REPRESENTATION FOR IT.
210
220 FOR K=1TO7:J=B(I.K)
230 LPRINTCHR$(92*J+32); CHR$(08); CHR$(4); :NEXTK: NEXTN
250
         PRINT THE ARABIC NUMERALS
270 LPRINT" ":FORN=1TO6:LPRINTA$(N);:NEXTN
280
290 *
         EINARY CODE FOR DIGITS 0 - 9
300
310 DATA 0,0,0,1,1,0,1
320 DATA 0,0,1,1,0,0,1
330 DATA 0,0,1,0,0,1,1
340 DATA 0,1,1,1,1,0,1
350 DATA 0,1,0,0,0,1,1
360 DATA 0,1,1,0,0,0,1
370 DATA 0,1,0,1,1,1,1
380 DATA 0,1,1,1,0,1,1
390 DATA 0,1,1,0,1,1,1
400 DATA 0,0,0,1,0,1,1
410 END
```

after the 6-digit bar code to allow a quick check of the coded identifier. An example of bar codes generated with the Centronics 737 appears in figure 1.

The program in listing 1 was written for the Radio Shack TRS-80 using Level II BASIC. This is only a sample program that can be modified to suit your taste, but it demonstrates how you can generate bar codes on a lowcost printer.

BYTE's Bits

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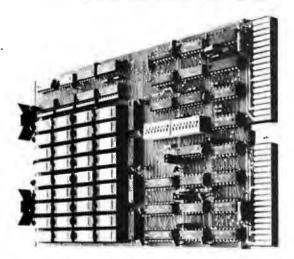
Fidelity Electronics Ltd has announced a free upgrading service for certain units of its Chess Challenger Electronic Game, Model CCX. The service is available only for units with serial numbers from 150871 to 174517.

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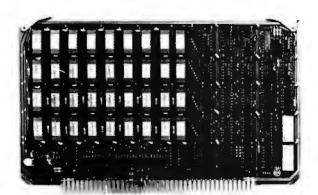
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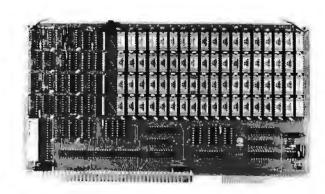
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Using Interrupts on the Apple II System

George M White Computer Science Department University of Ottawa Ottawa, Ontario K1N 6N5 Canada

The designers of the Apple II personal computer made a judicious choice of software/hardware tradeoffs. The most important software

A surprising feature of the Apple II's system software is that it makes little use of the 6502 interrupt system.

systems are stored in ROM (readonly memory) at high addresses where they are, for the most part, out of sight. Since the monitor, BASIC interpreter, and miniassembler are stored in ROM, they cannot be destroyed by user programs running

Acknowledgment

Most of this article was written while the author was enjoying the incomparable hospitalité of L'Institut de Recherche d'Informatique et d'Automatique in Rocquencourt, France.

out of control, nor can they be altered to produce strange results.

A surprising feature of the Apple II's system software is that it makes little use of the interrupt system of the 6502 microprocessor. However, the creators of the monitor have correctly assumed that some users might want to make use of interrupts, so they have provided several facilities to aid the user in doing so. The hardware and software facilities permit the user to write interrupt-service routines and to wire up interrupt generators that easily fit into one or more of the eight I/O (input/output) card slots, conveniently located under the Apple's removable plastic cover.

Interrupt-Service Software

Let's compare interrupt-service routines to program subroutines. In the BASIC language, subroutines are called at a known *location* within a program by a GOSUB statement. The subroutine is not executed (or shouldn't be) until the GOSUB statement is encountered during the course

len reasons lexy



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of the program. Subroutines are usually written to perform a specific action such as altering values of variables, I/O operations, etc.

Interrupt routines, on the other hand, are called at a specific point in time. An interrupt signal arrives, and the interrupt-service routine is called. There is no warning. The signal can arrive at any time, and the program being executed can be interrupted at any point.

The interrupt routine is a program like any other program. It can do everything an ordinary program can do, such as calculate numbers, manipulate strings, ring bells, or print messages on the console. Usually, the interrupt system found on microprocessors is used to control a computer peripheral device or to monitor and control external machinery.

The interrupt system can continuously watch the temperature of a furnace, the condition of a fire or burglar alarm, or the time of day. When something unusual happens, when the temperature goes too high or a burglar alarm sounds, the interrupt system alerts the computer to respond to the unusual condition and perform necessary actions.

However, the writing of such a program is a demanding task. The programmer must be aware of five aspects of interrupts that involve both the hardware and software of the system.

Necessary Conditions

1. There must be an external device capable of sending an interrupt signal to the computer.

The smaller systems used by novice BASIC programmers usually do not contain devices capable of generating interrupts. Even if they did, the BASIC language system available is not able to handle them directly, because most versions of BASIC do not recognize that interrupts exist.

The external device that sends the interrupt can be anything external to the processor and memory; it does not have to be physically located outside the computer box itself. Some common devices used as sources of interrupts are real-time clocks, terminals, and other computers. This list is not exhaustive. Anything capable of generating an electrical signalautomobile, household appliance, or burglar alarm—can be used as a source of interrupts.

2. The processor must be capable of receiving and acting upon the interrupt signal.

This implies not only that the signal must be wired into the computer with all its voltages having the correct values, but also that the processor must be set up to respond to the signal. We shall see later that the 6502 microprocessor can actually ignore some kinds of interrupts if the programmer has told it to ignore them.

Anything capable of generating an electrical signal—automobile, household appliance, or burglar alarm—can be used as a source of interrupts.

3. The processor must be able to tell which of several possible devices generated the interrupt.

If there is only one interruptgenerating device wired into the system, there won't be any problem identifying the source of the interrupt when it arrives. But if there are several interrupt sources-all trying to get the attention of the processor-the computer must have some way of telling which interrupt source is responsible for sending the signal, so it can take appropriate action.

4. The processor must respond to the interrupt by doing something.

When an interrupt signal arrives and is accepted by the computer, the program must perform an appropriate action (ie: "service" the interrupt). In some cases, this action is very simple, such as printing a character on a terminal. In other cases, the system may have to do something much more complicated, like placing a telephone call, sounding an alarm, or aborting the program it was executing.

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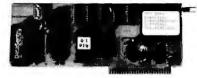
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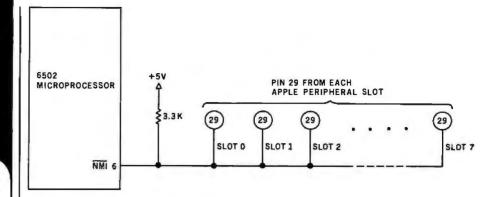


Figure 1: The 6502 \overline{NMI} signal and the Apple II peripheral slots. The \overline{NMI} signal is connected to pin 29 of each of the slots and is held high by the pull-up resistor shown. An interrupt is generated if the peripheral card in any of the slots presents a low impedance to ground to its pin 29.

5. After the service has been performed, the processor usually must return to the interrupted program and continue from the point of interruption.

When an Interrupt signal arrives and is accepted by the computer, the program must perform an appropriate action.

Usually (but not always), the interrupt has interfered with the execution of a program. After the interrupt has been successfully serviced, control should return to the interrupted program or process at the point of interruption without modifying the process in any way. Sometimes this program is nothing other than an endless loop waiting for interrupts to arrive.

Nonmaskable Interrupts

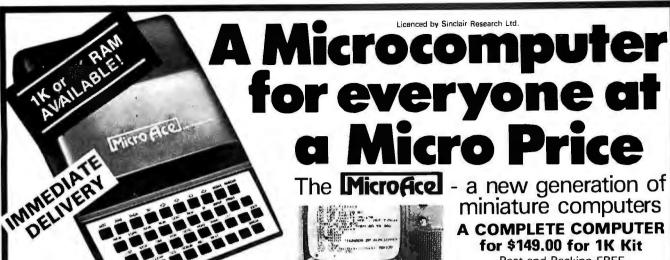
The Apple II has two separate interrupt lines entering its 6502 processor. They work somewhat differently.

Pin number 6 on the 6502 package is an active-low signal input called the nonmaskable interrupt, NMI. It is connected through the printed-circuit board to a pull-up resistor and to pin 29 in each of the eight I/O slots shown in figure 1.

If none of the circuit cards in the slots has anything attached to its pin 29, the potential at the $\overline{\text{NMI}}$ input observed by the 6502 is always held high by the pull-up resistor. This is the normal mode of operation. If a low impedance to ground is presented to pin 29 by any of the slots, the $\overline{\text{NMI}}$ line goes low, causing an interrupt condition to be generated in the 6502. This is the definition of the nonmaskable interrupt. This interrupt can be better understood by examining each of the five aspects presented earlier.

- 1. Any external device can generate an interrupt by presenting a ground (or low impedance to ground) potential to pin 29 in any of the I/O slots. Thus, the Apple II can have eight different interrupt sources, and they all may decide to interrupt at once.
- 2. The NMI signal is always recognized by the 6502 microprocessor, because it is nonmaskable. (Maskable interrupts will be discussed shortly.)
- 3. If there is only one device capable of sending the NMI signal, there is no question which device sent it. But if there are two or more interrupting devices, a problem arises. The 6502 microprocessor has only a single NMI input line, and every NMI signal goes there. In the Apple II, the processor can differentiate among several possible sources by polling the devices.

Polling is done by asking each device if it sent the interrupt signal or not. A program directs the computer



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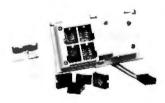
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to examine the status of each device which might have sent the interrupt. The details of this depend greatly on the way the devices are wired up, but in principle some of the 50 lines in the I/O slots can be used by the device to present logical flags or form data buffers. Examination of these signal lines by the program can then determine whether the device in question sent the $\overline{\text{NMI}}$ or not.

Daisy-chain inhibition of interrupts can be provided for in hardware by using control lines INT IN (pin 28) and INT OUT (pin 23) on the I/O slots, which are reserved for such a purpose. Various I/O devices can thereby have different priorities for interrupt servicing.

The Apple II's motherboard contains the wiring that links the boards together. This arrangement is shown in figure 2. Pin 28 (the INT IN line) of slot 0 has no connection, but pin 23 (INT OUT) of slot 0 connects to pin 28 of slot 1. Pin 23 of slot 1 connects to pin 28 of slot 2, and so on, up to slot 7. Pin 23 of slot 7 has no connection.

There are several methods for wiring the daisy chain, but in the most common configuration there is a low impedance (or a direct connection) on each interrupt-using card between INT IN and INT OUT. I/O cards have priority in interrupt service according to their physical position in the I/O slots. Cards in the lowernumbered slots have higher priority,

while cards in the higher-numbered slots have lower priority: it is not that the processor will process the I/O functions of the higher-priority cards before dealing with lower-priority cards if interrupts occur at the same time, but that the lower-priority cards are not permitted to generate an interrupt signal until the higher-priority device allows it.

In this scheme, I/O slots must be contiguously filled with cards so a continuous circuit, the daisy chain, is completed between the cards on the INT IN and INT OUT lines. I/O cards that do not use the interrupt system can be placed between cards that do if the noninterrupting cards have a jumper or connection between the contacts for pins 28 and 23 to maintain circuit continuity.

The highest-priority I/O card must reside in a lower-numbered slot than any other interrupt-generating card. The highest-priority card is special: it is responsible for placing a voltage indicating a high logic condition (usually +5 V) on the INT OUT pin for its slot. The lower-priority cards need not have this capability. They need only have the capability of opening the circuit between the INT IN and INT OUT pins for their slots.

Suppose, for example, that there are interrupt-generating I/O interface cards in slots 5, 6, and 7. The card in slot 5 must be capable of placing a potential of +5 V on the INT OUT connection. The card in slot 6 must

APPLE PERIPHERAL SLOTS

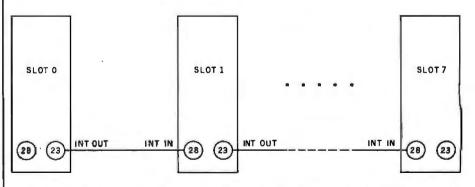


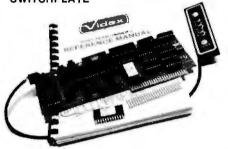
Figure 2: Using daisy chaining to create a priority system of interrupts. The INT OUT (pin 23 of each slot) and INT IN (pin 28 of each slot) signals are connected to each other to create a daisy chain that is broken by an interrupting slot. A peripheral device is not allowed to generate an interrupt unless it has highest priority or "permission" from higher-priority devices. Peripherals in lower slots have a higher interrupt priority than peripherals in higher slots. See the text for details.

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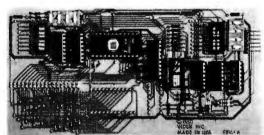
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Computer Furniture and Accessories, Inc. 1441 West 132nd Street Gardena, CA 90249 (213) 327-7710 have a low impedance from INT IN to INT OUT as a normal condition (so the card in slot 7 will be able to "see" the +5 V provided by the card in slot 5), and the cards in both 6 and 7 must be able to detect the absence of the +5 V potential on the INT IN line. The controlling circuitry of the slot-6 and slot-7 cards must recognize the absence of the INT IN high logic level and interpret it as denoting a condition in which the lower-priority cards are not permitted to generate an interrupt.

When the slot-5 device needs to interrupt the processor, it causes a low logic level to be placed on the \overline{NMI} line, pin 29, as previously described. At the same time, it removes the high logic level from the INT OUT line, pin 23. The slot-6 and slot-7 devices sense the low level on their INT IN pins, and they refrain from issuing an interrupt signal as long as this condition persists.

Meanwhile, the polling software in the processor polls the slot-5 card, as it has been set up to do first; the software polls the I/O cards in order of priority. Finding the slot-5 card needing attention, the software branches to the appropriate interrupt-servicing routine.

When the interrupt routine for the slot-5 device has finished its business, the interrupt condition is cleared, and control returns to the interrupted processing. At this point, the slot-5 card restores the +5 V potential to the INT OUT line, and the slot-6 and slot-7 cards can issue interrupts as necessary.

If the slot-6 card needs to issue an interrupt (and +5 V is present on its INT IN pin), it activates the NMI line in the same way. But because it is not the source of the +5 V on the INT IN/INT OUT path, it merely activates logic to create a high impedance between the INT IN and INT OUT pins for its own slot, thereby preventing the slot-7 device from seeing the +5 V INT IN level. In this way, the slot-6 card asserts its higher interrupt priority over the slot-7 card. When the slot-6 interrupt has been serviced by the processor, the low impedance is restored between the INT IN and INT OUT pins of slot 6, and the +5 V potential propagates once more along the motherboard traces to slot 7.

4. When an interrupt arrives at the 6502, the microprocessor responds by performing the following operations on its stack:

Push program-counter high byte Push program-counter low byte Push status register Jump via hexadecimal FFFA

Thus, the PC (program counter) and the status register are pushed (saved) onto the stack (the high byte of the PC is pushed first, then the lower byte, and, finally, the status register, P). After these stacking operations, the processor executes an indirect jump via hexadecimal memory location FFFA (ie: the location jumped to is the contents of FFFB (high byte) and FFFA (low byte) considered as a 16-bit number). In the Apple II computer, this is a ROM address, and Apple Computer Inc has set its contents to hexadecimal 03FB (remember that the lower byte contains the low-order address). Therefore, the system jumps to hexadecimal location 03FB and starts executing what it finds there. This area contains programmable memory, and it is the user's responsibility to start the interruptservice routine there. Unfortunately, this area is organized so there are only 3 bytes of memory actually available here. Because of this, the user must store a jump instruction in these 3 bytes that will direct execution to another area of memory, typically to the page beginning at hexadecimal location 0300 or to some higher area such as hexadecimal 0800 or 1000.

Generally, the first instructions in the interrupt-service routine are those to save the present value of the A, X, and Y registers on the stack. After that, the interrupt service is performed, and the A, X, and Y registers are restored. The routine should always be terminated with an RTI (return from interrupt) instruction. This instruction will unstack the status registers and program counter, and execution will continue from the point it had reached just before the occurrence of the interrupt. The inter-

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e, but avoid taking the poisoned portion. OTHELLO is the popular board game set to fully utilize the Atarf's graphics capability,
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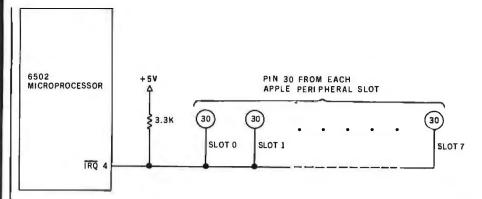


Figure 3: The 6502 \overline{IRQ} signal and the Apple II peripheral slots. The \overline{IRQ} signal is connected to pin 30 of each of the slots and is held high by the pull-up resistor shown. A maskable interrupt is generated if the peripheral card in any of the slots presents a low impedance to ground to its pin 30.

rupt-service routine itself must be written very carefully. It must, of course, perform whatever service you wish it to—such as printing a message on the console, ringing a bell, dialing a telephone, or turning on the furnace. But while it is doing these things, the service routine must not disturb any code used by the other routines stored in memory. The stacks should be in exactly the same state upon exit as they were when the service routine began.

5. The RTI instruction at the end of the service routine unstacks the status registers and program counter. This ensures that execution will continue from the point reached just before the arrival of the interrupt. Functionally, it is equivalent to:

Pop status register Pop program-counter low byte Pop program-counter high byte Execute next instruction

Maskable Interrupts

Pin number 4 on the 6502 chip is an input signal called the interrupt request, \overline{IRQ} . This is a *maskable* interrupt. In the Apple II, \overline{IRQ} is connected through the printed-circuit board to a pull-up resistor and to each of the eight I/O slots, as shown in figure 3.

This is the same scheme used for the \overline{NMI} except that the interrupt request will not be accepted if the interrupt-disable bit, I, in the status register, P, is set (ie: contains a 1). As before, this interrupt scheme can be

better understood by considering the five aspects of interrupts.

- 1. Any external device can generate an interrupt request by driving pin 30 on any I/O slot to ground potential. Once again, the Apple II can have eight different interrupt sources, and they all may decide to fire at the same time.
- 2. The 6502 microprocessor will respond to this request only if the interrupt-disable bit, I, in the status register, P, is cleared (ie: bit I must be a 0). This is done by executing a CLI (clear interrupt-disable bit) instruction any time before the arrival of the interrupt request. However, the 6502 will completely ignore the request if bit I has been set by executing an SEI (set interrupt-disable bit) instruction before the arrival of the interrupt.
- 3. Once again, the microprocessor is unable to determine the source of the interrupt. If there is only one device capable of sending an IRQ signal, there is no problem. If more than one device can do this, the same factors apply that were discussed earlier in the section on the nonmaskable interrupt, and polling can be used to determine which device caused the IRQ.
- 4. If bit I has been cleared and the IRQ signal arrives at the 6502, the following actions occur:

Push program-counter high byte Push program-counter low byte Push status register Jump via hexadecimal FFFE

Text continued on page 294



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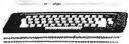
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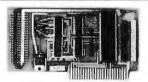


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Routines RNMI and RIRQ print the messages "NMI" and "IRQ", respectively, 255 times when the appropriate interrupt is generated. The short routines at hexadecimal 352 (decimal 850) and hexadecimal 354 (decimal 852) are meant to be called from BASIC to enable and disable, respectively, the maskable interrupt. See the text for details on generating the interrupts necessary to test these routines.

Listing 1: Assembly-language routines to test maskable and nonmaskable interrupts.

03FB- 03FE-	4C 21	00 03 03	1000 1010 1020 1030 1040 1050	***	.OR IMP .DA .OR	ERRUPT SYST \$3FB RNMI RIRQ \$300	******		*********
			1060 1070	•		ROUTINE	FOR	NMI	*******
			1080 1090	******	******	********	******	*****	*********
0300- 0301- 0302- 0303- 0304- 0305- 0307- 0306- 0311- 0313- 0316- 0317- 0319- 031B- 031C- 031F-	48 8A 48 98 48 A2 A9 20 A9 20 CA E0 D0 68 A8 68 AA	FF CE ED FD CD ED FD C9 ED FD	1100 1110 1120 1130 1140 1150 1160 1170 1180 1190 1200 1210 1220 1230 1240 1250 1260 1270 1280 1290	RNMI L1	PHA TXA PHA TYA PHA LDA ISR LDA ISR LDA ISR CPX BNE PLA TAY PLA TAX PLA	#\$FF #\$CE \$FDED #\$CD \$FDED #\$C9 \$FDED #0 L1	PRIN' PRIN' PRIN' REST	T "M" T "I" Ore re	TERS
0320-	40		1300	*****	RTI	*******	GO I	BACK	**********
			1310 1320	*					
			1330 1340	******	*****	ROUTINE	FOR	IRQ	*********
			1340	*****		*********	******		**********
0321- 0322- 0323- 0324- 0325- 0326- 0328-	48 8A 48 98 48 A2 A9	FF C9	1360 1370 1380 1390 1400 1410 1420	RIRQ L2	PHA TXA PHA TYA PHA LDX LDA	#\$FF #\$C9	SAVE PRIN	e regis T "i"	TERS
032A-	20	ED FD	1430		JSR	\$FDED			
032D- 032F-	A9 20	D2 ED FD	1440 1450		LDA ISR	#\$D2 \$FDED	PRIN	T ''R"	
0332- 0334-	A9 20	DI ED FD	1460 1470		LDA JSR	#\$D1 \$FDED	PRIN	T "Q"	
0337- 0338- 033A- 033C- 033D- 033E- 033F-	CA E0 D0 68 A8 68 AA	00 EC	1480 1490 1500 1510 1520 1530 1540		DEX CPX BNE PLA TAY PLA TAX	#0 L2	REST	ORE RE	GISTERS
0340- 0341-	68 40		1550 1560		PLA RTI		GO I	BACK	
J341-	40		1570	*****	VII	********	******	******	********
			1580 1590 1600	•	•••• Р	OUTINES	FOR	BASI	c
			1610	*****			*****	*****	*********
0352- 0353-	58 60		1620 1630 1640		OR CLI RTS	850	ENA	BLE INT	ERRUPTS
0354- 0355-	78 60		1650 1660 1670		SEI RTS .EN		DISA	BLE IN	TERRUPTS
SYMBOL	TARI	E							

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Circle 293 on inquiry card.



01460

Circle 86 on inquiry card.



Text continued from page 290:

As before, the program counter and the status register are placed on the stack, and the processor executes an indirect jump via hexadecimal location FFFE. This is again a ROM area in the Apple II and has been set by Apple Computer Inc to the value hexadecimal FA86 (or FA40 in the autostart version), which is an address in ROM. Thus, the processor starts executing at location FA86 (or FA40), where it finds the following instructions (a "\$" indicates a hexadecimal address):

STA \$45
PLA
PHA
ASL A
ASL A
BMI \$FA92 or BMI \$FA4C
JMP (\$03FE)

This section of code stores the accumulator at hexadecimal location 45 in page zero and checks to see if the fourth bit in the status register, the "break" bit B, is high or not. An interrupt request, IRQ, always forces this bit low, so the BMI instruction never succeeds and finally the indirect jump, JMP (\$03FE), is encountered. The hexadecimal address 03FE is in programmable memory, and the writer of the service routine must place the address of the routine here. Note that this is somewhat different from the way in which the NMI request is routed. For the IRQ interrupts, the address of the service routine rather than a jump instruction including an address must be stored in the 2 bytes, hexadecimal 03FE and 03FF. Also, remember that the lower byte of the 2-byte address must be stored first.

As before, the registers are usually stacked first, although this time the accumulator can be left alone, since it has already been stored at hexadecimal location 45 by the program in ROM. Then the interrupt service is performed, the registers are restored, and, finally, an RTI is executed.

5. The processor returns to its original program after it encounters the RTI. As before, this instruction will:

Pop status register
Pop program-counter low byte
Pop program-counter high byte
Execute next instruction

In principle, any program in any language can be interrupted by an external signal, and the interrupts can be serviced using the techniques described above. In microprocessor systems such as the Apple II, the interrupted program is usually a BASIC program, and the interrupt-service routines are usually written in assembly language. An example of such a service routine is shown in listing 1. It is assumed that there is only one device capable of generating an interrupt, that the service to be performed consists only of writing a message to the console, and that interrupts will not interrupt themselves.

To test this routine, a BASIC program should be written and executed. When you wish to enable the IRQ signal from your BASIC program, it is only necessary to execute:

CALL 850

and when you wish to disable the IRO, all you have to do is:

CALL 852

If you do not have any device in your I/O slots capable of generating an interrupt request, you can easily make one by bending a resistor with a pair of long leads so that the leads are about one-half inch apart. A 100-ohm resistor works well. Then very carefully connect pin 29 (for the NMI) or pin 30 (for the IRQ) through the resistor to the ground pin (pin 26) on any of the I/O slots. This technique is crude but effective, and will generate the interrupt request whenever you wish. The NMI signal will always set the interrupt system in motion, but the IRQ signal will be accepted only if you have executed the BASIC instruction CALL 850.

Once you have mastered the fundamentals of interrupt handling, the number of interrupts that can be serviced and the complexity of the service are limited only by the speed of the interrupting devices and ingenuity of the servicing programs.

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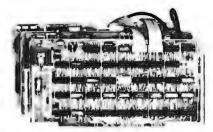
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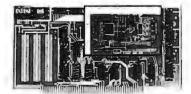
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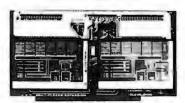
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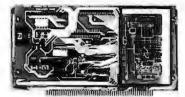
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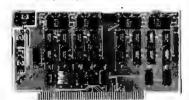
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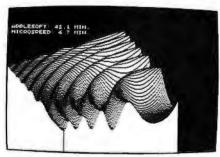
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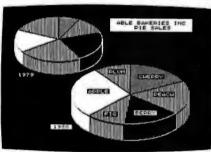
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Digital Plotting with the Apple II Computer

Dr Richard C Hallgren Department of Biomechanics Michigan State University East Lansing MI 48824

The first attempts to use personal computers in the research laboratory have met with considerable success. Not only are these machines functioning as computational tools, they are also being used with custom interface circuits to digitize analog signals and to process data using routines such as the fast Fourier transform (see "Fast Fourier Transforms on Your Home Computer" by W D Stanley and S J Peterson, on page 14 of the December 1978 BYTE).

In dealing with complex, timedependent waveforms and their spectrums, it is desirable to display the data as a function of either time or frequency. Plotting is possible with a data terminal such as the DECwriter II, but such methods are lacking when high-resolution plotting is required. The Hiplot digital plotter, manufactured by Houston Instrument, gives the small-system user a cost-effective means of obtaining high-quality digital plots. The plotter uses an 81/2by 11-inch sheet of paper and allows plotting within a 7- by 10-inch boundary. Reversible stepper motors are used to give bidirectional steps of either 200 or 100 steps per inch, amounting to a resolution of 0.005 or 0.01 inches per step. An RS-232C serial interface is a standard feature, which makes connecting the plotter to a computer an easy task.

The Hiplot accepts data in an RS-232C format consisting of 1 start bit, 8 data bits, and 2 stop bits. Since the computer manipulates 8 bits of

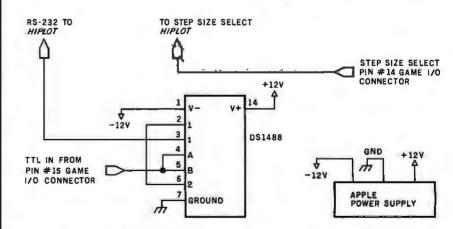


Figure 1: Schematic of Apple II TTL (transistor-transistor logic) to RS-232C interface utilizing only one line-driver integrated circuit, a DS1488.

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Listing 1: 6502 machine-language routine to perform functions of a UART (universal asynchronous receiver/transmitter) for transmitting RS-232C serial data through the hardware modification.

8000-	AO	09		LDY	#\$09	9 bits (1 start, 8 data)
8002-	18			CLC		
8003-	48			PHA		Save data byte
8004-	BO	05		BCS	\$800B	
8006-	AD	59	CO	LDA	\$C059	Output a space
8009-	90	03		BCC	\$800E	
800B-	AD	58	C0	LDA	\$C058	Output a mark
800E-	A9	03		LDA	#\$03	
8010-	48			PHA		
8011~	A9	04		LDA	#\$04	
8013-	4A			LSR		
8014-	90	FD		BCC	\$8013	Delay 1 bit time
8016-	68			PLA		
8017-	E9	01		SBC	#\$01	
8019-	DO	F5		BNE	\$8010	
801B~	68			PLA		Get data byte
801C-	6A			ROR		Rotate into carry bit
801D-	88			DEY		Decrement bit count
801E-	DO	E3		BNE	\$8003	Jump if more data
8020-	AO	02		LDY	#\$02	2 stop bits
8022-	AD	38	C0	LDA	\$C058	Output a mark
8025-	A9	03		LDA	#\$03	
8027-	48			PHA		
8028-	A9	04		LDA	#\$04	
802A-	4A			LSR		Delay 1 bit time
802B-	90	FD		BCC	\$802A	belay I bit time
802D-	68			PLA		
802E-	E9	01		SBC	#\$01	
8030-	D0	F5		BNE	\$8027	
8032-	88			DEY		Decrement bit count
8033-	D0	ED		BNE	\$8022	Jump if more stop bits
8035-	60			RTS		

parallel data at a time, we need a method to convert the parallel data to serial data. I decided to implement this conversion in software, instead of using a UART (universal asynchronous receiver/transmitter) to keep the system simple. The only things required are the software routine and a line driver to shift the TTL (transistor-transistor logic) voltage-level output from the Apple II to RS-232C levels for the Hiplot. A DS1488 quad line driver integrated circuit (see figure 1) is mounted on an Apple Hobby/Prototyping board and inserted into expansion slot 6 on the Apple motherboard. The Apple writes data to the line driver by toggling the latch circuit connected to the Apple game-I/O port. Accessing hexadecimal address C059 ("LDA \$C059" in listing 1) causes a 1 to be transmitted. Accessing hexadecimal address C058 ("LDA \$C058" in listing 1) causes a 0 to be transmitted. (In RS-232C communications, any voltage between +5 V and +15 V is called a space and represents a "high" signal or a digital 0; any voltage between -5 V and -15 V is called a mark and represents a "low" signal or a digital 1.)

Figure 2 on page 300 shows the flowchart for the software routine that replaces the UART; listing 1 (above) shows the program with comments. To reduce the plotting time to a minimum, I decided to operate the Hiplot at its maximum data rate of 9600 bps (bits per second). Executing the output routine loads the Y register with a count of nine and clears the carry bit. The routine then writes a mark (a digital 1 or a low signal) if the carry bit is cleared, or a space (the opposite of mark) if the carry bit is set, and loops for a time period equal to the time spacing between bits. The routine then shifts the data so the most significant bit goes into the carry bit and checks to see if all the data bits have been sent. If not, it loops to process the next bit. Otherwise, it transmits 2 stop bits and returns to the calling program.

Getting to the point where data can be transferred from the Apple to the Hiplot is only the first part of using the plotter. Since the plotter comes with no software, it is necessary to write routines which will generate axis systems and, if desired, alphanu-

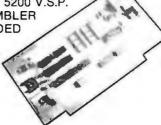
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33 35 39	360 390	3.6K 3.9K	36K 39K	360K	3.6M 3.9M

22	220	2.2K	22K	220K	2.2M
24	240	2.4K	24K	240K	2.4M
27	270	2.7K	27K	270K	2.7M
30	300	3K	30K	300K	3M
33	330	3.3K	33K	330K	3.3M
36	360	3.6K	36K	360K	3.6M
39	390	3.9K	39K	390K	3.9M
43	430	4.3K	43K	430K	4.3M
47	470	4.7K	47K	470K	4.7M
51	510	5.1K	SIK	5 10K	5.1M
56	560	5.6K	56K	560K	5.8M
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1N4742A	.30
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1N4747A	.30
1N4749A	.30

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2N2907A	.25
2N2369A	.25
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PN2907A	.19

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1N4001	12/1.00	07
1N4002	12/1.00	.07
fN4003	12/1,00	.07
1N4004	12/1.00	.07
1N4005	10/1.00	.08
1N4006	10/1.00	GO.
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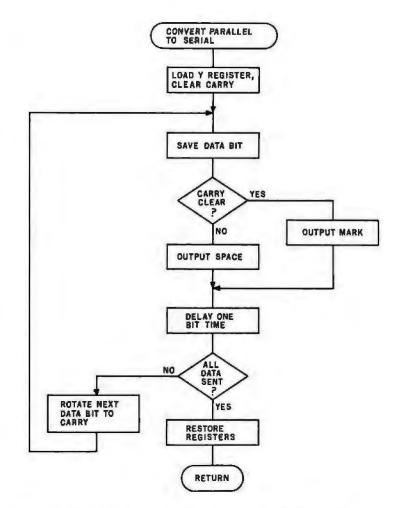
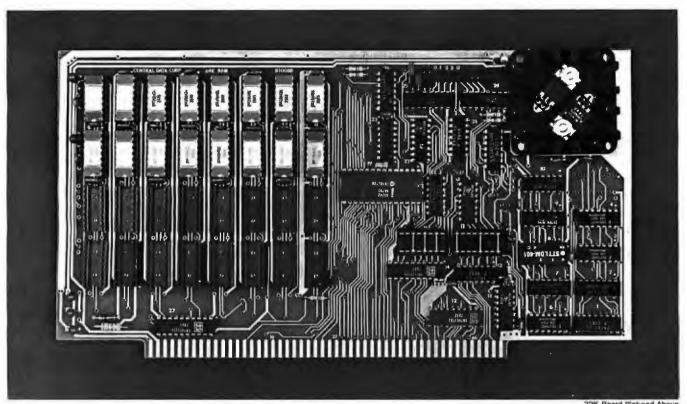


Figure 2: Flowchart for machine-language software UART in listing 1.

Listing 2: Machine-code command generator to select a specified plotter command before calling the UART subroutine.

8038-	48	PHA	Save accumulator
8039-	08	PHP	Save processor status
803A-	A9 70	LDA #\$70	Output 'p'
803C-	20 00 80	JSR \$8000	Jump to parallel to serial conversion
803F-	28	PLP	Restore processor status
8040-	68	PLA	Restore accumulator
8041-	60	RTS	Return
8042-	48	PHA	
8043-	08	PHP	
8044-	A9 71	LDA #\$71	Output 'q'
8046-	4C 3C 80	JMP \$803C	
8049~	48	PHA	
804A-	80	PHP	
804B-	A9 72	LDA #\$72	Output 'r'
804D-	4C 3C 80	JMP #803C	
8050-	48	PHA	
8051-	08	PHP	
8052 -	A9 73	LDA #\$73	Output 's'
8054-	4C 3C 80	JMP \$803C	
8057-	48	PHA	
8058-	80	PHP	
8059-	A9 74	LDA #\$74	Output 't'
805B-	4C 3C 80	JMP \$803C	
805E-	48	PHA	
805F-	08	PHP	
8060-	A9 75	LDA #\$75	Output 'u'
8062-	4C 3C 80	JMP \$803C	
8065-	48	PHA	
8066~	08	PHP	Listing 2 continued on wage 202

Listing 2 continued on page 302



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```
Listing 2 continued:
```

8067-	A9	76		LDA	#\$76	Output	'v'
8069-	4C	3C	80	JMP	\$803C	-	
806C-	48			PHA			
806D-	08			PHP			
806E-	A9	77		LDA	#\$77	Output	'w'
8070-	4C	3C	80	JMP	\$803C	_	
8073-	48			PHA .			
8074-	80			PHP			
8075-	A9	7 9		LDA	#\$79	Output	'y'
8077-	4C	3C	80	JMP	#803C	_	_
807A-	48			PHA		9,	
807B-	08			PHP			
807C-	A9	7A		LDA	#\$7A	Output	'z'
807E-	4C	3C	80	JMP	\$803C	-	

Listing 3: BASIC program to produce a plot of the voltage across a charging capacitor.

```
10 REM MAIN PROGRAM
```

- HOME : VTAB 12
- 14 PRINT "POSITION PEN IN LOWER LEFT HAND"
- 16 PRINT "CORNER. PRESS ANY KEY TO CONTINUE."
- GET AS
- 19 HOME
- GOSUB 1000 REM DRAW X,Y AXIS
- 30 REM EXPONENTIAL RISE
- 32 POKE 16293,0 REM SET RESOLUTION TO 200 POINTS PER INCH
- 34 Z = 0
- 36 CALL 32646: FOR I = 0 TO 10: NEXT I REM PEN DOWN
- FOR I = 0 TO 8.99 STEP .005
- 40 V = 5 * (1 EXP (I)) REM FIND CAPACITOR VOLTAGE
- 42 K = INT (200 * V)
- 44 IF K Z = 0 THEN GOTO 90 REM NO CHANGE IN PREVIOUS POTENTIAL
- IF K Z < 0 THEN GOTO 60 REM POTENTIAL IS DECREASING 46
- FOR J = 1 TO (K Z) REM POTENTIAL IS INCREASING
- CALL 82712 REM MOVE IN +Y DIRECTION
- 52 NEXT J
- 54 GOTO 70
- FOR J = 1 TO (Z K)
- 62 CALL - 32681 REM MOVE IN -Y DIRECTION
- 64 NEXT J 70 Z = K
- 90 CALL 32695 REM MOVE IN +X DIRECTION
- 92 NEXT I
- 94 CALL 32653 REM PEN UP
- 99
- 300 REM "1" 301 CALL - 32653: FOR I = 1 TO 8: CALL - 32674: Next I.
- 302 CALL - 32646
- 304 FOR I = 1 TO 8: CALL 32702: NEXT I
- 306 FOR I = 1 TO 26: CALL 32681: NEXT I
- 308 CALL 32653
- 310 FOR I = 1 TO 8: CALL 32667: NEXT I
- 312 CALL 32646
- 314 FOR I = 1 TO 16: CALL 32695: NEXT I
- 316 CALL 32653
- 317 FOR I = 1 TO 8:
- 318 FOR I = 1 TO 26: CALL 32712: NEXT I
- 319 RETURN
- 320 REM "2"
- 321 CALL 32653: FOR I = 1 TO 8: CALL 32667: NEXT I
- 322 CALL - 32646
- 324 FOR I = 1 TO 16: CALL 32695: NEXT I
- 326 FOR I = 1 TO 13: CALL 32681: NEXT I
- 328 FOR I = 1 TO 16: CALL 32667: NEXT I
- 330 FOR I = 1 TO 13: CALL - 32681: NEXT I
- 332 FOR I = 1 TO 16: CALL 32695: NEXT I
- 334 CALL 32653
- 336 FOR I = 1 TO 8: CALL 32667: NEXT I FOR I = 1 TO 26: CALL - 32712: NEXT I 337

339 RETURN

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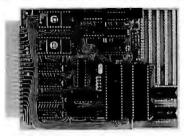
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already existing equipment, and provides an excellent nucleus when designing new products.

User communication is accomplished through the General's "Expeditor" system monitor. The Expeditor resides in a 2K EPROM, and features 15 commands and 18 utility routines to facilitate program development. An instructional user's manual is provided with every unit.

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16 Utility routines including Terminal I/O routines, Test and compare routines, Code check or convert

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Listing 3 continued: 340 REM "3" 341 CALL - 32653: FOR I = 1 TO 8: CALL - 32667: NEXT I

342 CALL - 32646 FOR I = 1 TO 16: 'CALL - 32695: NEXT I 344

FCR I = 1 TO 13: CALL - 32681: 346 NEXT I

348 FOR I = 1 TO 16: CALL - 32667: NEXT I 350 - 32695: NEXT I CALL

FOR I = 1 TO 16: FOR I = 1 TO 13: 352 - 32681: CALL. NEXT I - 32667:

354 FOR I = 1 TO 16: CALL 356 CALL - 32653

FOR I = 1 TO 8: 357 CALL - 32695: NEXT I

358 FOR I = 1 TO 26: CALL - 32712: NEXT I

359 RETURN 360 REM

361 CALL - 32653: FOR I = 1 TO 8: CALL - 32667: NEXT I

NEXT I

362 CALL - 32646

FOR I = 1 TO 13: 364 CALL - 32681: NEXT I

FOR I = 1 TO 16: 366 CALL - 32695: NEXT I

368 CALL - 32653

372 FOR I = 1 TO 13: CALL - 32712:

NEXT I 374 CALL - 32646

376

FOR I = 1 TO 26: CALL - 32681: NEXT I 377 CALL - 32653

378 FOR I = 1 TO 26: CALL = 32712: NEXT I

379 FOR I = 1 TO 8: CALL - 32667: NEXT I: RETURN

380 REM "5"

CALL - 32653: FOR I = 1 TO 8: CALL - 32695: NEXT I 381 382 CALL - 32646

FOR I = 1 TO 16: 384 CALL - 32667: NEXT I

386 - 32681: NEXT FOR I = 1 TO 13: CALL

388 FOR I = 1 TO 16: CALL - 32695: NEXT I

CALL - 32681: 390 FOR I = 1 TO 14: NEXT I

392 FOR I = 1 TO 16: CALL - 32667: NEXT I

CALL - 32653 394

396 FOR I = 1 TO 26: CALL - 32712: 397 FOR I = 1 TO 8: CALL -. 32695: NEXT I

399 RETURN

REM "6" 400

401 CALL - 32653: FOR I = 1 TO 8: CALL - 32667: NEXT I

402 CALL - 32646: FOR I = 0 TO 10: NEXT I

404 FOR I = 1 TO 26: CALL - 32681: NEXT I

406 FOR I = 1 TO 16: CALL - 32695: NEXT I

408 FOR I = 1 TO 13: CALL - 32712: NEXT I

ROR I = 1 TO 16: CALL - 32667: 410 NEXT I

412 CALL - 32653

414 FOR I = 1 TO 13: CALL - 32712: NEXT I

415 FOR I = 1 TO 8: CALL - 32695: NEXT I

416 RETURN

420 REM

CALL - 32653 422

424 FOR I = 1 TO 8: CALL - 32667: NEXT I

426 CALL - 32646: FOR I = 0 TO 10: NEXT I NEXT I

428 FOR I = 1 TO 16: CALL - 32695: 430 NEXT I

FOR I = 1 TO 26: CALL - 32681: 432 CALL - 32653

434 FOR I = 1 TO 26: CALL - 32712: NEXT I

436 FOR I = 1 TO 8: CALL - 32667: NEXT I

RETURN 439

469

REM "8" 440

442 CALL - 32653

FOR I = 1 TO 8: CALL - 32695: 444 NEXT I

CALL = 32646: FOR I = 0 TO 10: 445 NEXT I

446 FOR I = 1 TO 16: CALL - 32667: NEXT I

FOR I = 1 TO 26: CALL - 32681: NEXT I

448

450 FOR I = 1 TO 16: CALL - 32695: NEXT I

452 FOR I = 1 TO 26: CALL - 32712: NEXT I

454 CALL - 32653

456 FOR I = 1 TO 13: CALL - 32681: NEXT I

457 CALL - 32646

458 FOR I = 1 TO 16: CALL - 32667: NEXT I 460 CALL - 32653

FOR I = 1 TO 8: CALL - 32695: NEXT I 462

FOR I = 1 TO 13: CALL - 32712: NEXT I

RETURN

Listing 3 continued on page 306

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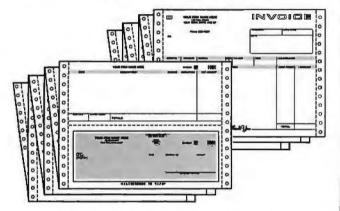
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```
480 REM "9"
    CALL - 32653
483
    FOR I = 1 TO 8: CALL - 32667: NEXT I
484
    CALL - 32646: FOR I = 0 TO 10: NEXT I
485
    FOR I = 1 TO 16: CALL - 32695:
                                     NEXT I
486
    FOR I = 1 TO 26: CALL - 32681:
                                     NEXT I
487
    CALL - 32653
488
    FOR I = 1 TO 13: CALL - 32712:
                                     NEXT I
489
    CALL - 32646
490
    FOR I = 1 TO 16:
                     CALL - 32667:
                                     NEXT I
    FOR I = 1 TO 13: CALL - 32712:
492
                                     NEXT I
493
    CALL - 32653
191
   FOR I = 1 TO 8: CALL - 32695:
                                    NEXT T
499
    RETURN
    REM "O"
500
    CALL - 32653
502
    FOR I = 1 TO 8: CALL - 32695: NEXT I
504
506
    CALL - 32646: FOR I = 0 TO 10: NEXT I
508
    FOR I = 1 TO 16: CALL - 32667:
                                     NEXT I
    FOR I = 1 TO 26: CALL - 32681: NEXT I
510
    FOR I = 1 TO 16: CALL - 32695: NEXT I
512
514
    FOR I = 1 TO 26: CALL - 32712: NEXT I
516
    CALL - 32653
518
    FOR I = 1 TO 8: CALL - 32667: NEXT I
519
    RETURN
999
    END
1000
     REM X AXIS
     POKE - 16294,0: CALL - 32653
1010
     FOR I = 1 TO 50: CALL - 32712: NEXT I
1012
     CALL - 32646: FOR I = 0 TO 10: NEXT I
1014
1016
     FOR I = 1 TO 1000: CALL - 34695:
                                       NEXT I
1018
     CALL - 32653
1100
     REM X AXIS SCALE
     FOR I = 1 TO 20: CALL - 32712: NEXT I
1110
1112
     CALL - 32646: FOR I = 0 TO 10: NEXT I
     FOR I = 1 TO 40: CALL. - 32681: NEXT I
1114
1116
     CALL - 32653
1118
     FOR I = 1 TO 5: CALL - 32681: NEXT I
     POKE - 16293,0
1120
1122
     GOSUB 480
1124
     POKE - 16294,0
1126
     FOR I = 1 TO 50: CALL - 32667:
                                      NEXT I
     FOR I = 1 TO 38: CALL - 32712: NEXT I
1128
1130
     CALL - 32646: FOR I = 0 TO 10:
     FOR I = 1 TO 26: CALL - 32681:
1132
                                      NEXT I
1134
     CALL - 32653
1146
     FOR I = 1 TO 50: CALL - 32667:
     FOR I = 1 TO 33: CALL - 32712:
1148
                                      NEXT I
1150
     CALL - 32646: FOR I = 0 TO 10:
                                      NEXT I
1152
     FOR I = 1 TO 40: CALL - 32681:
1154
     CALL - 32653
1156
     FOR I = 1 TO 5: CALL - 32681: NEXT I
     POKE - 16293,0
1158
1160
     GOSUB 440
1162
     POKE - 16294,0
1164
     FOR I = 1 TO 50: CALL - 32667:
                                      NEXT T
1166
     FOR I = 1 TO 38: CALL - 32712:
                                      NEXT I
1168
     CALL - 32646: FOR I = 0 TO 10:
                                      NEXT I
1170
     FOR I = 1 TO 26: CALL - 32681:
1172
     CALL - 32653
1174
     FOR I = 1 TO 50: CALL - 32667:
                                      NEXT I
1176
     FOR I = 1 TO 33: CALL - 32712:
                                      NEXT I
1178
     CALL - 32646: FOR I = 0 TO 10:
1180
     FOR I = 1 TO 40: CALL - 32681: NEXT I
1182
     CALL - 32643
     FOR I = 1 TO 5: CALL - 32681: NEXT I
1184
     POKE - 16293,0
1186
1188
     GOSUB 420
1190
     POKE - 16294,0
1192
     FOR I = 1 TO 50: CALL - 32667:
1194
     FOR I = 1 TO 38: CALL - 32712: NEXT I
1196
     CALL - 32646: FOR I = 0 TO 10: NEXT I
1198
     FOR I = 1 TO 26: CALL - 32681: NEXT I
1199
     CALL - 32653
                           Listing 3 continued on page 308
```

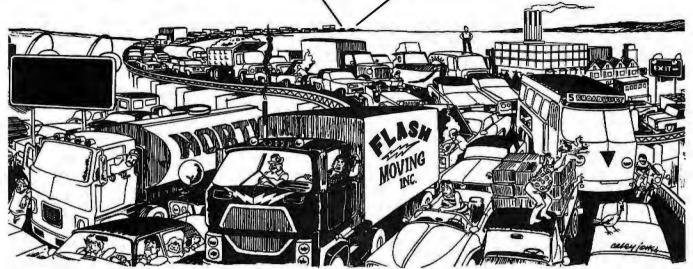
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																	Is car used in business (except to/from job):	ept	•		

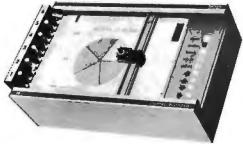
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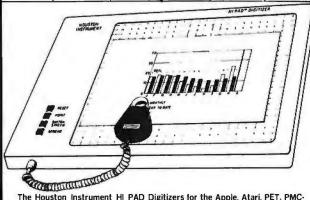
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```
Listing 3 continued:
1200
     FOR I = 1 TO 50: CALL - 32667:
                                       NEXT I
1202
     FOR I = 1 TO 33: CALL - 32712:
                                       NEXT I
1204
     CALL - 32646: FOR I = 0 TO 10:
                                       NEXT I
1206
     FOR I = 1 TO 40: CALL - 32681:
                                       NEXT I
1208
      CALL - 32653
1210
     FOR I = 1 TO 5: CALL - 32681: NEXT I
1212
     POKE - 16293,0
1214
     GOSUB 400
1216
     POKE - 16294,0
1218
     FOR I = 1 TO 50:
                       CALL - 32667:
                                       NEXT I
     FOR I = 1 TO 38: CALL - 32712:
1220
                                       NEXT I
1222
     CALL - 32646: FOR I = 0 TO 10:
1224
     FOR I = 1 TO 26: CALL - 32681:
                                       NEXT I
1226
     CALL - 32653
1228
     FOR I = 1 TO 50: CALL - 32667:
                                       NEXT I
1230
     FOR I = 1 TO 33: CALL - 32712:
                                       NEXT I
1232
      CALL - 32646: FOR I = 0 TO 10:
                                       NEXT I
     FOR I = 1 TO 40: CALL - 32681:
1234
                                       NEXT I
           - 32653
1236
      CALL
1238
                                      NEXT I
     FOR I = 1 TO 5:
                      CALL - 32681:
1240
     POKE ~ 16293,0
1242
     GOSUB 380
1244
     PUKE - 16294,0
1246
     FOR I = 1 TO 50: CALL - 32667:
                                       NEXT I
     FOR I = 1 TO 38: CALL - 32712:
1248
                                       NEXT I
     CALL - 32646: FOR I = 0 TO 10:
1250
                                      NEXT I
     FOR I = 1 TO 26:
1252
                       CALL - 32681:
                                       NEXT I
1254
     CALL - 32653
1256
     FOR I = 1 TO 50:
                       CALL
                            - 32667:
                                       NEXT I
1258
     FOR I = 1 TO 33: CALL - 32712:
                                       NEXT I
1260
     CALL - 32646: FOR I = 0 TO 10:
                                       NEXT I
1262
     FOR I = 1 TO 40: CALL - 32681:
                                       NEXT I
1264
     CALL - 32653
1266
     FOR I = 1 TO 5:
                     CALL - 32681: NEXT I
1268
     POKE - 16293,0
1270
     GOSUB 360
1272
     POKE - 16294.0
1274
     FOR I = 1 TO 50:
                       CALL
                            - 32667:
1276
     FOR I = 1 TO 38: CALL - 32667:
                                       NEXT I
1278
     CALL - 32646: FOR I = 0 TO 10:
                                       NEXT I
1280
     FOR I = 1 TO 26:
                      CALL - 32681:
                                       NEXT I
1282
     CALL - 32653
     FOR I = 1 TO 50: CALL - 32667:
1284
                                       NEXT I
     FOR I = 1 TO 33:
                      CALL - 32712:
                                       NEXT I
1286
1288
     CALL - 32646: FOR I = 0 TO 10:
                                       NEXT I
     FOR I = 1 TO 40: CALL - 32681:
1290
                                       NEXT' I
1292
     CALL - 32653
1294
     FOR I = 1 TO 5:
                      CALL - 32681:
                                      NEXT I
1296
     POKE - 12394,0
1298
     GOSUB 340
1300
     POKE - 16294,0
1302
     FOR I = 1 TO 50:
                       CALL - 32667:
                                       NEXT I
1304
     FOR I = 1 TO 38: CALL - 32712:
                                       NEXT I
1306
     CALL - 32646: FOR I = 0 TO 10:
                                       NEXT I
1308
     FOR I = 1 TO 26:
                       CALL - 32681:
                                       NEXT I
                            - 32667:
1312
     FOR I - 1 TO 50:
                       CALL
                                       NEXT I
     FOR I = 1 TO 33:
                       CALL - 32712:
1314
                                       NEXT I
1316
     CALL - 32546: FOR I = 0 TO 10:
                                       NEXT I
1318
     FOR I = 1 TO 40:
                      CALL - 32681:
                                       NEXT I
1320
     CALL - 32653
1322
     For I = 1 TO 5:
                      CALL - 32681: NEXT I
1324
     POKE - 16293,0
     GOSUB 320
1326
1328
     POKE - 16294,0
     FOR I = 1 TO 50:
1330
                       CALL - 32667:
                                       NEXT I
     FOR I = 1 TO 38: CALL - 32712:
                                       NEXT I
1332
1334
     CALL - 32646: FOR I = 0 TO 10:
                                       NEXT I
1336
     FOR I = 1 TO 26:
                       CALL - 32681:
                                       NEXT I
1338
     CALL - 32653
1340
     FOR I = 1 TO 50: CALL - 32667:
                                       NEXT I
     FOR I = 1 TO 33: CALL - 32712:
1342
                                       NEXT I
     CALL - 32646: FOR I = 0 TO 10:
1344
                                       NEXT I
1346
     FOR I = 1 TO 40: CALL
                             - 32681:
                                       NEXT I
```

Listing 3 continued on page 310

1348

CALL - 32653

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USR-330D \$339

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The
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DEC VT100 ... \$1668

Detachable keyboard. Separate numeric keypad with function keys. Business forms character set. Reverse video. Selectable double-size characters. Bidirectional smooth-scrolling. 80 cols or 132 cols. Split screen. Settable tabs. Line drawing graphic characters. Status line. Key-Click.



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5510 Spinwriter\$2754
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```
Listing 3 continued:
1350 FOR I = 1 TO 5: CALL - 32681: NEXT I
     POKE - 16293,0
1352
     GOSUB 300
1354
1356
     POKE - 16294,0
     FOR I = 1 TO 50: CALL - 32667:
1358
                                     NEXT' T
     FOR I = 1 TO 38: CALL - 32712:
1360
1362
     CALL - 32646: FOR I = 0 TO 10:
                                     NEXT I
1364
     FOR I = 1 TO 26: CALL - 32681:
                                     NEXT I
     CALL - 32653
1366
     FOR I = 1 TO 50: CALL - 32681:
1368
                                     NEXT I
1370
     FOR I = 1 TO 37: CALL - 32681:
1372
     CALL - 32646: FOR I = 0 TO 10: NEXT I
1373
     REM Y AXIS
1374
     FOR I = 1 to 700: CALL - 32712: NEXT I
     FOR I = 1 to 13: CALL - 32667: NEXT I
1376
     CALL - 32646: FOR I = 0 TO 10:
1378
                                     NEXT I
1380
     FOR I = 1 TO 26: CALL - 32695:
                                     NEXT I
1381
     CALL - 32653
     FOR I = 1 TO 44: CALL - 32681:
1382
     FOR I = 1 TO 43: CALL - 32667:
1386
    POKE - 16293,0
1388
     GOSUB 400
1390
     POKE - 16294,0
1392
     FOR I = 1 TO 6: CALL - 32681: NEXT I
1394
     FOR I = 1 TO 10: CALL - 32695: NEXT I
     CALL - 32646: FOR I = 0 TO 10:
1396
                                     NEXT I
1398
     FOR I = 1 TO 40: CALL - 32695:
                                     NEXT I
     CALL - 32653
1400
1402 FOR I = 1 TO 50: CALL - 32681:
     FOR I = 1 TO 33: CALL - 32667: NEXT I
1404
     CALL - 32646: FOR I = 0 TO 10:
                                     NEXT I
1406
     FOR I = 1 TO 26: CALL - 32695:
1408
                                     NEXT I
1410
     CALL - 32653
1412 FOR I = 1 TO 44: CALL - 32681: NEXT I
1414 FOR I = 1 TO 43: CALL - 32667: NEXT I
1416
     POKE - 16293,0
1418
     GOSUB 380
1420
     POKE - 16294,0
1422
     FOR I = 1 TO 6: CALL - 32681: NEXT I
1424
     FOR I = 1 TO 10: CALL - 32695: NEXT I
1426
     CALL - 32646: FOR I = 0 TO 10:
                                      NEXT I
1428
     FOR I = 1 TO 40: CALL - 32695: NEXT I
     CALL - 32653
1430
1432 FOR I = 1 TO 50: CALL - 32681: NEXT I
1434
     FOR I = 1 TO 33: CALL - 32667:
                                     NEXT I
     CALL - 32646: FOR I = 0 TO 10:
1436
                                      NEXT I
1438 FOR I = 0 TO 26: CALL - 32695:
                                     NEXT I
1440
     CALL - 32653
1442
     FOR I = 1 TO 44: CALL - 32691:
     FOR I = 1 TO 33: CALL - 32667: NEXT I
1444
1446
     POKE - 15293,0
1448
     GOSUB
1450
     POKE - 16294,0
     FOR I = 1 TO 6: CALL - 32681: NEXT I
1452
     FOR I = 1 TO 10: CALL - 32695:
1454
1456
     CALL - 32646: FOR I = 0 TO 10:
                                     NEXT I
     FOR I = 1 TO 40: CALL - 32695:
1458
                                     NEXT I
1460
     CALL - 32653
1462 FOR I = 1 TO 50: CALL - 32681:
                                     NEXT I
     FOR I = 1 to 33: CALL - 32667:
1464
     CALL - 32646: FOR I = 0 TO 10:
1466
                                      NEXT T
1468 FOR I = 1 TO 26: CALL - 32695:
1470
     CALL - 32653
1472
     FOR I = 1 TO 44:
                      CALL - 32681:
                                     NEXT I
                      CALL - 32667: NEXT I
     FOR I = 1 TO 43:
1474
1476 POKE - 16293,0
1478
     GOSUB 340
1480 POKE - 16294,0
1482 FOR I = 1 TO 6: CALL - 32681: NEXT I
1484 FOR I = 1 TO 10: CALL - 32695: NEXT I
1486
     CALL - 32646: FOR I = 0 TO 10:
1488 FOR I = 1 TO 40: CALL - 32695:
1490
     CALL - 32653
1492 FOR I = 1 TO 50: CALL - 32681: NEXT I
```

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Listing 3 continued:
```

```
1494 FOR I = 1 TO 33: CALL - 32667: NEXT I
1496
     CALL - 32646: FOR I = 0 TO 10:
     FOR I = 1 TO 26: CALL - 32695:
                                     NEXT I
1498
     CALL - 32653
1500
     FOR I = 1 TO 44: CALL - 32681: NEXT I
1502
     FOR I = 1 TO 43: CALL - 32667: NEXT I
1504
1506
     POKE - 16293,0
1508
     GOSUB 320
1510
     POKE - 16294,0
     FOR I = 1 TO 6:
1512
                     CALL - 32681: NEXT I
1514
     FOR I = 1 TO 10: CALL - 32695: NEXT I
1516 CALL - 32646: FOR I = 0 TO 10: NEXT I
1518 FOR I = 1 TO 40: CALL - 32695: NEXT I
1520
     CALL - 32653
1522
     FOR I = 1 TO 50:
                     CALL - 32681:
                                     NEXT I
     FOR I = 1 TO 33: CALL - 32667: NEXT I
1526
     CALL - 32646: FOR I = 0 TO 10: NEXT I
1528
     FOR I = 1 TO 26: CALL - 32695: NEXT I
1530
     CALL - 32653
     FOR I = 1 TO 44: CALL - 32681:
1532
                                     NEXT I
    FOR I = 1 TO 43: CALL - 32667: NEXT I
1536 POKE - 16293,0
1538
     GOSUB 300
1540
     POKE - 16294,0
1542
     FOR I = 1 TO 6: CALL - 32681: NEXT I
     FOR I = 1 TO 10: CALL - 32695: NEXT I
1546
     CALL - 32646: FOR I = 0 TO 10:
                                     NEXT I
1548
     FOR I = 1 TO 40: CALL - 32695:
1550
     CALL - 32653
1552
     FOR I = 1 TO 50: CALL - 32681:
                                     NEXT I
1554 FOR I = 1 TO 33: CALL - 32667:
1556
     CALL - 32646: FOR I = 0 TO 10: NEXT I
1558
     FOR I = 1 TO 26: CALL - 32695:
                                     NEXT I
1560
     CALL - 32653
     FOR I = 1 TO 26: CALL - 32667: NEXT I
1562
     FOR I = 1 TO 100: CALL - 32681: NEXT I
     CALL - 32646: FOR I = 0 TO 10: NEXT I
1566
1568
     FOR I = 1 TO 26: CALL - 32695: NEXT I
     CALL - 32653
1570
```

FOR I = 1 TO 13: CALL - 32667: NEXT I

FOR I = 1 TO 50: CALL - 32712: NEXT I

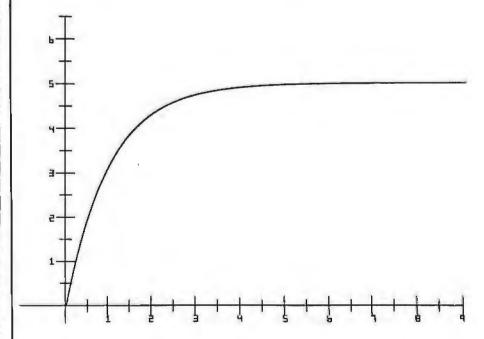


Figure 3: Sample plot of results obtainable with the information included in this article.

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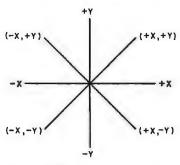
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- Y	t	8057
-x,-Y	U	805E
- X	v	8065
-X,+Y	w	806C
PEN UP	у	8073
PEN DOWN	z	807A

Table 1: Chart of plotter pen-movement commands and the vector notation associated with each command.

Text continued from page 298:

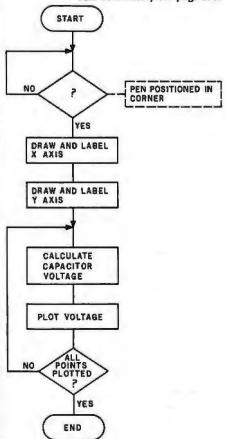


Figure 4: Flowchart for the BASIC program used to produce figure 3.

Table 1 shows plotter commands and the vector notation associated with each. Listing 2 on page 300 is a machine-language routine that generates the specified command characters. To execute a given command, a jump is made to the appropriate hexadecimal address, where the proper character is loaded into the accumulator. A call is then made to the parallel-to-serial subroutine, where the command character is transferred from the computer to the plotter.

Results with the digital plotter have been encouraging. Figure 3 on page 312 shows an actual plot made on the plotter. A #0 Rapidograph pen was used to produce a high-quality plot. The plot is a simulation of the voltage drop across a capacitor that is placed in series with a resistor and a fixed voltage source. Figure 4 shows the flowchart of the program, and listing 3 beginning on page 302 shows the program with comments.

At present, the Apple II and Hiplot digital plotter are being used for several projects that include the spectral analysis of breath sounds, muscle voltages, and neural characteristics. The two units working together provide a low-cost, high-quality record of the analysis of scientific data.

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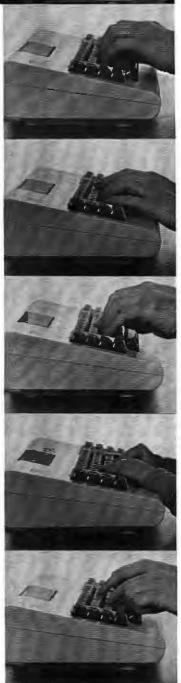
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Recursion and Side Effects in Pascal

Robert Morris and James Perchik University of Massachusetts **Boston Harbor Campus** Boston MA 02125

Two features of Pascal, recursion and side effects, often cause difficulties for beginners to the language. Although these features appear to address separate issues, they are not unrelated, and for this reason confusion over one often accompanies confusion over the other. Conversely, contemplation of one can assist in an understanding of the other. It is easier to comprehend both issues if you look at the management of variables that results from procedure calls. That will be the focus of this article.

Typically, the concept of recursion is illustrated with simple functions that are better written without recursion. We will adhere to that custom for the standard reason of comprehensibility. Readers who master recursion will find an excellent treatment of the subject (when and when not to use it) in Nikolaus Wirth's Algorithms + Data Structures = Programs, listed in the references.

Consider the easy problem of computing the factorial $n! = 1 \times 2 \times ... \times n$. Factorial is defined recursively as follows:

$$n! = n(n - 1)! \text{ if } n > 1$$

 $n! = 1 \text{ if } n = 1$

The following Pascal function computes the factorial function recursively:

FUNCTION fac(n: INTEGER): INTEGER;

BEGIN

IF
$$n = 1$$

THEN fac := 1

ELSE fac := fac($n - 1$)* n

END

Suppose that a main program contains the following calling sequence:

$$m := 3; y := fac(m)$$

The function "fac" is recursive. That is, "fac(3)" will call "fac(2)", which will call "fac(1)". We say that there are three activations of this function, with parameter values of 3, 2, and, finally, 1.

Each activation of a recursive function (or procedure) must have a separate location (called the stack frame) for its local variables, parameters, etc. In this way, one activation (say, "fac(2)") does not disturb the contents of another activation (say, "fac(3)"). As each activation begins, a new stack frame is created (or pushed) for its local variables. As that activation is completed, its stack frame is destroyed (or popped), and control returns to the previous activation. The "current" values of the local variables are then taken from the stack frame of the previous activation, which is now at the bottom of the (downward-growing) stack. [In a stack, only the item most recently placed there can be accessed. We call this the top of the stack if the stack is growing "up." Since the stack in this context is growing "down," we will refer to the item that can be removed as the bottom of the stack....GW

Snapshots of the stack are shown in figure 1. The global variables "m" and "y" (ie: those declared in the main program) are allocated storage in the stack frame of the main program, which is shown at the top frame of the stack. These variables are not duplicated with each activation of the function. A function or procedure may be able to directly access and modify a global variable. That, as you will see, can lead to surprising results.

Above and between the snapshots of the stack in figure 1 is the fragment of code (plus comments, in braces) which caused the changes to the stack. This information helps specify the time when each snapshot was taken.

At any point in time, there are two currently active frames that are of immediate interest. These two frames contain the values that are currently accessible; they are the top and bottom frames in figure 1. The top frame contains the (global) variables of the main program. The local frames are shown below it, growing downward. The bottom frame is the only local frame that is currently accessible (ie: belongs to the current activation). In addition to local variables, the stack frame contains the value of the function (marked "P" if it is pending further calculations) and the return address (so that control will be transferred back to the correct calling sequence). The



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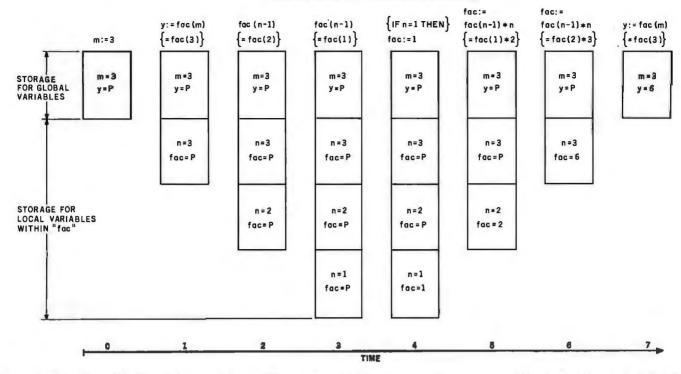


Figure 1: Execution of the Pascal statements "m := 3; y := fac(m)". The columns of boxes represent the stack at time t = 0, 1, 2, ..., 7. The statements above each column indicate the part of the function that is executed to give the stack illustrated below, and the comments in braces are used to clarify the statements being executed. The letter P indicates a pending calculation.

addresses have not been shown in figure 1.

Had the variables "m" or "v" occurred inside "fac" without a new declaration, these variables would be said to be global to the function, and then "fac" could access or change their values. When global variables are changed within a function, the function is said to cause side effects. Sometimes this is useful, but often it is dangerous, and should be used with caution.

When the program execution begins, the global frame is set up, and soon the variable "m" is assigned the value of 3 (see column 0 in figure 1). When the function call

"fac(m)" is reached, a stack frame for "fac" is set up (column 1) below the global frame, and the value, 3, of the argument "m" is assigned the parameter "n" and stored in the local stack frame. (This call by value is the default behavior in Pascal. The alternative method of passing values, variable parameters, will be discussed shortly.)

Now the value of "fac(n-1) = fac(2)" is required. In order to compute this, the function "fac" is called (recursively), this time with a parameter value of 2. A second local stack frame is set up with n=2 (column 2).

This activation will call "fac(1)", and its frame is set up

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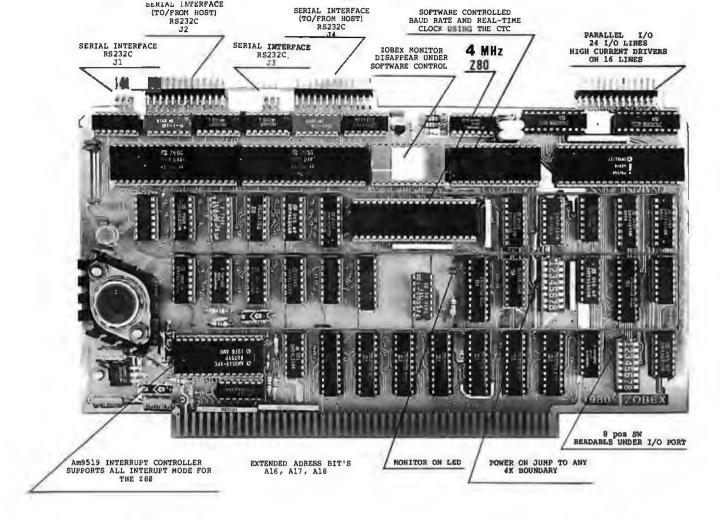
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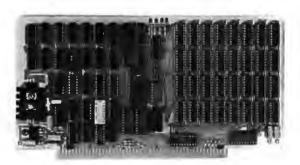
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at the bottom of column 3. Since n=1, this can be evaluated without further recursion: the answer is 1, and is stored in the variable "fac" in column 4. Now the previous invocation of "fac" (with n = 2) can complete its work. Its answer is $2 \times fac(1) = 2 \times 1 = 2$, which is assigned to the variable "fac" in column 5 (where the stack frame of "fac(1)" has been popped).

The unwinding process continues as control returns to the previous call of "fac" (with n=3), where the answer can now be computed as $fac=3 \times fac(2)=3 \times 2=6$, and stored in column 6. Finally, the answer is assigned to the global variable "y" in column 7.

Applications of Side Effects

Before we see how side effects can lead to unexpected trouble, we should point out that they can be used in many legitimate ways. For example, no useful language can exist without the statement READ(x). It may also be useful to have a function that includes the following code:

IF denominator = 0THEN write('attempt to divide by zero') ELSE quotient := numerator/denominator

The procedures read and write both have side effects—they affect the status of the (global) files input and output.

Another useful application of side effects occurs when each activation of a procedure computes only part of the answer and places it into the appropriate section of a



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global buffer. When all activations of the procedure have done their jobs, this buffer will contain the entire answer, which can then be worked on. Examples are the recursive algorithms for sorting arrays and for backtracking (see Wirth, Chapter 3 and page 79, listed in the references). This mechanism is not without risk, however, because procedures other than the one intended can inadvertently modify the global variable.

Some languages provide the appropriate mechanism, eg: "own" variables in ALGOL-60 or static storage in PL/I and C. These variables have "local name scope" (ie: they can not be directly accessed from outside the procedure). However, they are allocated storage only once. Thus, like global variables, new copies are not made with each activation of the procedure, so their values are retained from one activation of the procedure to the next. The loss of this feature in Pascal is generally overshadowed by the pleasant fact that Pascal is a simplification of ALGOL-60, whereas PL/I is a "complification."

A Faulty "fac" Function

Now we'll look at a modification of the factorial program, where a variable parameter is used. Although it looks very much like the first version of "fac", you will see that it computes the wrong answer:

FUNCTION fac2 (VAR n:INTEGER):INTEGER; **BEGIN**

$$n := n - 1;$$

 $fac2 := fac2(n) * (n + 1)$

END

END

Assume that it is called, as before, by the following se-

$$m := 3; y := fac2(m);$$

Note the keyword "VAR" in the function header. A variable parameter in Pascal does not copy the value of its argument onto the stack frame. Instead, a reference (ie: a pointer) to the argument (in this case, the variable "m") is placed on the stack frame. This method is known as "call by reference." There are times when you want to use this method-for example, when a large item like an array or file is a parameter, or when you want to change the value of a global variable. But disaster lurks, as we will indicate shortly.

The argument in a call by reference must also be a variable (see Wirth, page 71). This prohibits a call like "fac2(n-1)", since (n-1) is an expression, not a variable. Therefore, the variable "n" must be decremented in the ELSE clause. This appears to make the same mathematical calculation as in the previous version of the function "fac" because the multiplication is now by (n+1), the original value of n. In fact, it does not.

By having a variable parameter, "fac2" is able to get into the global variable "m" and (if you are not careful) change its value.



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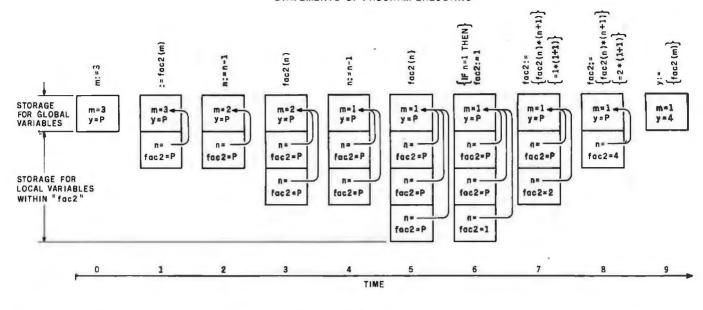


Figure 7: Execution of the Pascal statements "m := 3; y := fac2(m)". In this case, the variable "n" within the function "fac2" (listed in the text) points to the global variable "m" and can change its value; the arrows from "n" to "m" indicate this relationship.

Consider the stack diagrams for the function "fac2" (see figure 2). This time, each new instance of "n" gets a pointer to the variable "m" and the code "n := n-1" causes the global variable, "m", to be decremented by 1. Still, no values can be assigned to "fac2" until the stack starts to unwind, and when that happens, the value of "m" has been decreased to 1. Thus the multiplication is always by 2.

As you see, this function is not computing factorials at all, but 2^{m-1}. The problem arises because "fac2" is altering the value of its parameter, a situation to be avoided when not absolutely necessary. After the entire function terminates, the variable "m" will be left at 1, regardless of its initial value. The function "fac2" is exerting a side effect on "m".

Side effects can occur whenever a procedure accesses a global variable either directly or indirectly via a variable parameter. Side effects are avoided by the use of local parameters (declared within the procedure or function) and value parameters. Many side-effect errors are so easy to make and so hard to debug that language designers will prohibit certain dangerous constructs (or encourage the implementors to do so). (See *Pascal User Manual and Report*, page 79, listed in the references.) For example, the use of global variables (or parameters) for the control of "for" loops is prohibited by the CDC implementation of Pascal described in *The Pascal User Manual and Report* (pages 120 and 121, and error messages 155 and 180).

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Programmers should strive to write code that is clear, correct, verifiable, and easily transported to other implementations.

functions with side effects may occur if $f \times g$ is not equal to $g \times f$, at least if "f" or "g" is a function. Consider, for example, the apparently simple modification of the "fac2" function that is made by changing the key line to:

$$fac2 := (n + 1)*fac2(n)$$

The reader is invited to make a stack history as above. Assume that multiplications are performed left to right, and that the stack frame for "fac2" also allocates a location to hold the value of the expression (n+1) until after "fac2(n)" is computed, with the two values then being multiplied. (In practice, values of such expressions may be stored as temporary variables in registers.)

As a result of this single change, "fac2" will compute the correct value of factorial. What is the moral? Whenever the spectre of unplanned side effects rears its ugly head, discovery of the "correct" solution may be a matter of luck (and might depend on the implementation!). In any case, programs are certainly hard to debug whenever $f \times g$ and $g \times f$ are not equal.

There are, of course, simpler examples that illustrate this phenomenon. Consider the following function:

FUNCTION
$$f(VAR i:INTEGER):INTEGER;$$

BEGIN $f := i; i := i + 1 END;$

This function simply returns the value of its argument, but has the side effect of incrementing that argument.

The following sequence:

$$x := 1$$
; WRITE ($(x + 1)^*f(x)$); $x := 1$; WRITE ($f(x)^*(x + 1)$);

produces a printout of:

In this case, the printout (which would have been "2 2" if the order of multiplication had not mattered) vindicates our assumption that multiplication was performed left to right.

The order in which multiplications are performed is (deliberately) left unspecified by the semantics of most programming languages. For example:

$$x := 1$$
; WRITE($x*f(x)$); $x := 1$; WRITE($f(x)*x$);

produces a printout of:

2 2

and we must conclude that the value of the expression f(x) is evaluated before the value of the variable "x". This may be done for optimization reasons, in order to minimize register use. Furthermore, an optimizing compiler may choose not to evaluate f(x) at all in an expression like 0*f(x), since the answer is always zero. In that case, any side effects of the function "f" on "x" would not appear.

In short, the results of these examples can very well depend on the implementation! It is bad practice to write this kind of code, and programmers should strive to write code that is clear, correct, verifiable, and easily transported to other implementations. If you can avoid unnecessary side effects, you will be one step closer to this goal.

References

1. Jensen, K and N Wirth. Pascal User Manual and Report. Springer-Verlag, 1974.

 Wirth, N. Algorithms + Data Structures = Programs. Englewood Cliffs NJ: Prentice -Hall, 1976.

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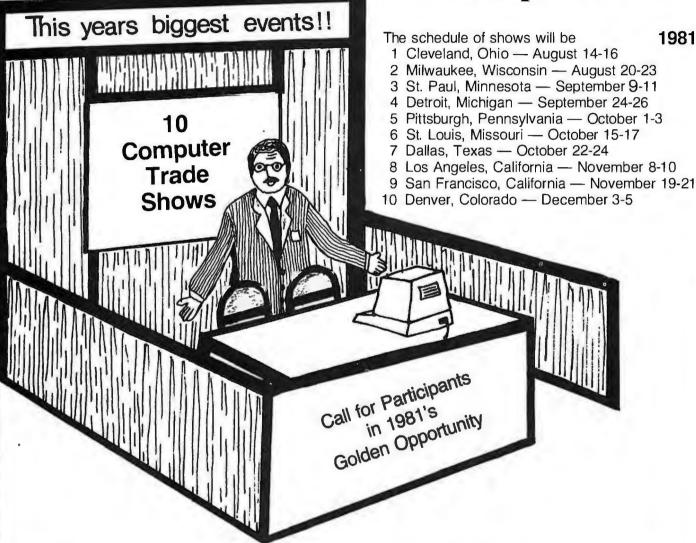
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About the Author

Aillil Ian Halsema has worked as a programmer since 1971. He is now a senior member of the programming staff at Xerox Corporation. He owns a Southwest Technical Products Corporation 6800 system equipped with 16 K bytes of memory, a CT-1024 video terminal, an AC-30 cassette tape interface, and an Okidata CP-110 printer.

cuits, which together form a hardware cycle counter producing nonmaskable interrupts. This cycle counter technique is the same as that used in Motorola's EXORciser development system, and allows stepping through programs in read-only memory.

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The built-in operator interface is designed for use with video terminals having displays in a format of sixteen lines of thirty-two characters each, although it will work with other types of terminals. Since each line of output is thirty-two characters in length, the interface routine will cause a single page of fifteen lines to be displayed, with the cursor at the bottom of the display (as illustrated in figure 1). A new address can then be entered. If

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Disassembler Tables

Almost half of the memory space taken up by the disassembler is used for two tables. The larger of the two is the packed-mnemonic table. Each entry in this table is 2 bytes long, with entries arranged in ascending operation code order. Those operation codes which are undefined (such as hexadecimal 00) are represented in the table by the FCB pseudo-operation mnemonic. Each entry is formed by dropping the fourth character of the mnemonic (either an A or a B as in LDAA), masking out the 3 high-order bits of each of the remaining characters, and packing them into 16 bits. The high-order bit of the 16 is used as a flag to specify an alternate entry in the smaller table. Note that this method of packing characters is valid only for character codes with the same high-order 3 bits. Numeric and alphabetic ASCII characters cannot be packed together. Figure 2 gives an example of mnemonic packing.

The smaller table is the format table. It defines the address mode, the fourth character of the mnemonic symbol, and the number of bytes in the input object code. The format table consists of thirty-two 1-byte entries with two entries for each possible value of the high-order nybble (ie: half-byte) of the input op code. The second entry of a pair is selected when bit 16 is set to the value 1 in the corresponding packedmnemonic-table entry.

This method of defining formats and mnemonics works for all but three mnemonic symbols. The PSHB, PULB, and BSR op codes are exceptions that must be handled differently in the program. A fourth exception is the FCB pseudo-operation which has its own format-flags byte outside of the table.

During execution of the disassembler, the op code is used as an index into the packed-mnemonic table, while the high-order nybble of the op code is multiplied by 2 and is used as an index into the format

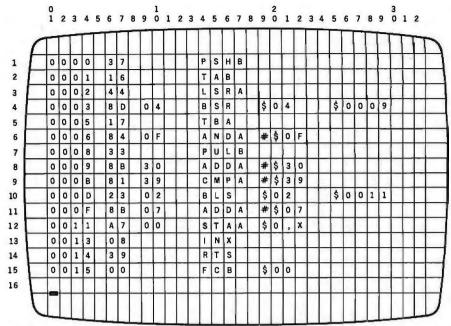


Figure 1: Example of disassembled code as it appears when output to a video terminal

Mnemonic to be packed: LDX

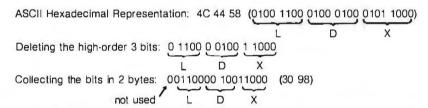


Figure 2: Forming an entry in the packed-mnemonic table. The three high-order bits are stripped from the ASCII representation of each character of the three-letter mnemonic. The 5-bit characters are packed into two 8-bit bytes, with one bit not used. The characters are restored to 8-bit form by adding hexadecimal 40 to the 5-bit value.

table. The packed mnemonic is unpacked, and the 3 high-order bits of each character are restored by adding hexadecimal 40 to each 5-bit value. The unpacked ASCII characters are stored in a line buffer along with the fourth character, if any, of the mnemonic.

The operand field is built using format table data indicating the length and address mode of the instruction. If an immediate-mode instruction is being processed, the operand is preceded by a "#" character. If the instruction uses relative addressing, the absolute effective address is calculated and is placed in the comments field of the output buffer. If the instruction uses indexing, the operand is followed by a ",X" sequence. All operands are in hexadecimal, All fields in the line start at fixed locations, making for easier user process-

Hardware Additions

The hardware cycle counter is connected to side A of the peripheral interface adapter. Figure 3 shows a schematic diagram of this. In my system, a Southwest Technical Products Corp (SwTPC) 6800, the peripheral interface adapter is on an MP-L parallel interface board which is connected to the system reset line. On power-up or reset conditions, data direction register A (DDRA) and I/O register A (lORA) cause logic 1 levels to appear on the MP-L's output lines. If applied directly to the counter, these levels would start the counter running and producing interrupts before the system could properly process them.

To avoid this condition, a 7404 hex inverter is used to complement the load, clear, and enable signals, and to keep the counter halted and cleared following power-up and system reset.

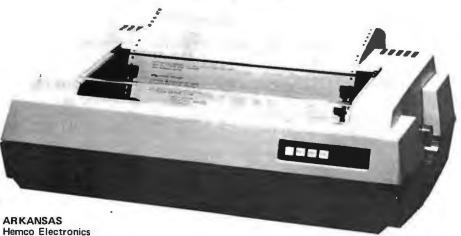
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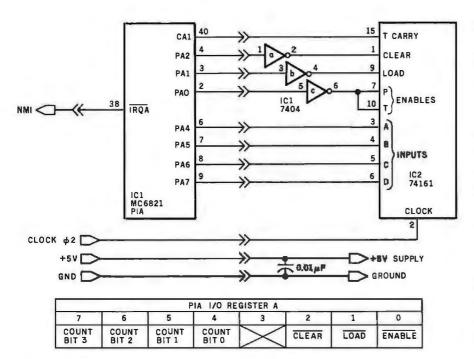


Figure 3: Schematic diagram of the hardware cycle counter. The DEMONS system uses the nonmaskable interrupt (NMI) in the 6800.

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From the program's viewpoint, the counter clear is off when IORA bit 2 is a 1, counter load is off when IORA bit 1 is a 1, and counter enable is on when IORA bit 0 is 0. IORA bits 4, 5, 6, and 7 are used to output the value to be loaded into the counter, leaving IORA bit 3 unused.

The 74161 device in figure 3 is a 32 MHz synchronous 4-bit counter whose carry output will go high for a period equal to one full machine cycle when a count of 15 is reached. By presetting the counter, the carry output can be made to go high after 1 to 15 clock cycles.

I built the prototype version of the cycle counter on a perforated circuit board and attached it to the MP-L board, which supplies power and clock signals. You can see this mounting technique in photos 1 and 2. This assembly plugs into the motherboard and I/O board slot 3, giving it the hexadecimal address range 800C through 800F. If the cycle counter is to be plugged into some other slot, DEMONS will have to have the new address of IORA patched in at hexadecimal locations 03E9, 03EA, 040B, and 040C. DEMONS uses the nonmaskable interrupt (NMI), so the interrupt-request acknowledge (IRQA) line must be wired to the NMI input on the cycle counter's peripheral interface adapter board.

How the Cycle Counter Works

Upon start-up DEMONS initializes the peripheral interface adapter and loads an initial value of 6 (count 9 phase-2 clock cycles) into the counter. The counter is started and a return from interrupt (RTI) instruction is executed. The counter will reach the terminal count value and toggle the CA1 line one cycle before the RTI instruction completes execution. Upon completion of the RTI instruction, the processor will recognize the interrupt, save the registers in the stack, and transfer control to the DEMONS interrupt routine via the previously set NMI vector address.

DEMONS' interrupt processor will test the cycle counter's peripheral interface adapter control register A to verify that it was entered as a result of a valid interrupt. If the cycle counter did not cause the interrupt, the instruction at hexadecimal location

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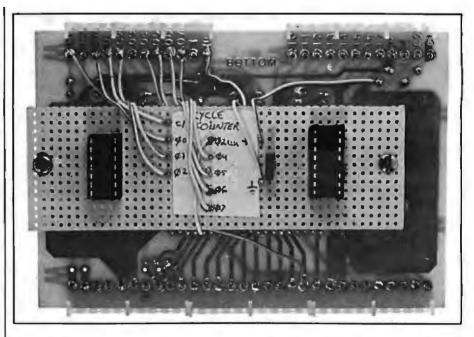


Photo 1: The cycle-counter circuit was constructed on a small piece of perforated board and mounted on the MP-L parallel interface board inside the SwTPC 6800.

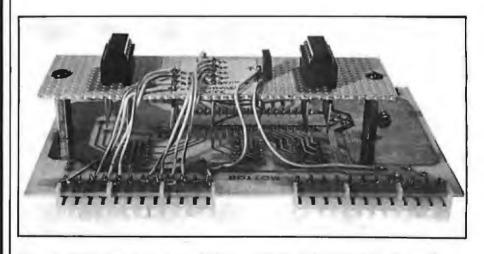


Photo 2: Shown here is the method of mounting the cycle-counter circuit board.

0411 will be executed. DEMONS is supplied with three no-operation instructions (NOPs) starting at this address. You should patch DEMONS to jump to another nonmaskable interrupt processing routine if the cycle counter is not the only source of nonmaskable interrupts.

If the interrupt is valid, the counter is halted, cleared, and reloaded with a value of 3. The registers are fetched from the stack and displayed on the terminal along with the next instruction to be executed, in this case the first instruction of the problem program. DEMONS then waits for the user to enter a command. If the *step* command is entered, the counter is started and a return from interrupt

(RTI) instruction is executed. Twelve phase-2 (ϕ 2) clock cycles later, the CA1 line is toggled, producing another nonmaskable interrupt. Since the RTI instruction takes 10 cycles to execute, the interrupt occurs during execution of the first instruction of the program that is being debugged. From this point on, interrupts will occur after the execution of the RTI instruction as *each* instruction of the program being debugged is executed.

Operational Modes

In step mode, DEMONS causes a single instruction of the program being debugged to be executed, and then seizes control of operations to

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Command	Description
S. Snnnn Tnnnn. Tnnnn,11 Cnn Bnn Ann	Step and execute from current address. Set hexadecimal address <i>nnnn</i> as the new current address. Set trace mode and break address <i>nnnn</i> . Break count set to 1. Set trace mode and break address <i>nnnn</i> . Set break count to 11. Set condition codes to hexadecimal value <i>nn</i> . Set B register to hexadecimal value <i>nn</i> . Set A register to hexadecimal value <i>nn</i> .
Xnnnn R	Set X register to hexadecimal value nnnn. Display registers.
D.	Display 14 instructions in disassembled form starting at the current address.
Dnnnn	Display 14 instructions in disassembled form starting at hexadecimal address nnn.
G	Exit from DEMONS and resume problem program execution at the current address.
Pnnnn,00 00	

Table 1: Summary and description of the DEMONS command set.

Dialogue at Terminal	Comments
*L *G P <i>1E00</i>	Command MIKBUG to load DEMONS from tape. Start DEMONS execution. Tell DEMONS where to start problem program being debugged.
CC B A X E1 00 00 3745	DEMONS displays registers.
1E00 BD 1E45 JSR \$1E45 : S. CC B A X E1 00 00 3745	DEMONS displays the next instruction. Operator commands an instruction step. DEMONS displays registers.
1E45 37 PSHB : <i>T1E5F,03</i> : S.	DEMONS displays the next instruction. Enter trace mode. Start tracing.

Table 2: Example of a typical user work session with DEMONS, with commentary. Characters in italics have been typed by the user.

	Simultaneous Interrupts	Processor Action
Early (PK) Mask	NMI and SWI NMI and IRQ IRQ and SWI	treats as IRQ handles NMI first handles SWI first
Later Masks	NMI and SWI NMI and IRQ IRQ and SWI	handles NMI first handles NMI first handles IRQ first

Table 3: Sequence of interrupt handling in the Motorola 6800 microprocessor. Parts produced during early production runs used the PK chip mask, and demonstrate unexpected behavior under certain interrupt conditions, most notably the simultaneous occurrence of a nonmaskable hardware interrupt (NMI) and a software interrupt (SWI). The PK series of 6800 branches to the IRQ (maskable hardware interrupt) vector location whenever this happens. (Parts of the PK series have the letters PK inscribed somewhere on the surface of the package; therefore they may be identified.) Later production runs of the 6800 processor used an improved chip mask, and devices from these later runs handle interrupts in a more logical manner.

The following rule holds true for all 6800 processors: in the case where the IRQ signal is overruled by one of the other two interrupts, the IRQ may be ignored and lost unless its interrupt signal has been latched. Fortunately, the IRQ signal from the peripheral interface adapter (PIA) is latched.

allow user input. At this point, the user can modify the program; alter the path taken through the program; change the contents of the condition code registers, index register, or either accumulator; display memory content in disassembled form; or enter the trace mode.

In trace mode, DEMONS continues to receive control following execution of problem program instructions, but the user is not given control (that is, a chance to input commands) until the break address (or breakpoint) is encountered and the break counter is decremented to 0. The user sets the break address and the break count. Once set, these cannot be cleared without going through DEMONS initialization or executing the program being debugged until the break address is encountered N times. The break address entered must always be the address of the op code (ie: first byte) of an instruction byte sequence. Once trace mode is selected, tracing will be started by entry of the step command. Using the trace feature, the user can avoid stepping through long loops and previously debugged code one instruction at a time. Table 1 shows the complete command set of DEMONS; table 2 shows an example of user interaction.

DEMONS may be exited by use of the GO function, which bypasses the counter start-up code, or by activating the system reset line (by hitting the reset switch).

Possible Problems

All debugging monitors have drawbacks; DEMONS is no exception. Since DEMONS relies on having the stack-pointer (SP) register properly set, code which uses the stack pointer as an index register must be bypassed using the step function. Any code that is synchronized with some external process or has critical timing requirements will be delayed by at least 130 machine cycles per instruction, causing possible errors. If a software interrupt (SWI) or regular maskable hardware interrupt (IRQ) occurs simultaneously with the cycle counter's nonmaskable interrupt (NMI), possible vectoring problems may occur. (Table 3 summarizes these effects.) Thus care must be

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I stated that the disassembler executes quickly, and will have to wait for input/output (I/O) operations when using terminals having data rates of up to about 3000 bits per second (bps). I calculated this figure by disassembling 128 instructions and noting the time required to complete this task (T1). The time required for I/O operations (T2) was determined from the following formula:

$$T2 = (C \times L) \times D$$

where:

C is the number of characters per line (32)
L is the number of lines in the test (128)
D is the time required to transmit one character (0.033 seconds at 300 bps)

The processor time required to disassemble the 128 instructions is then:

$$T_p = T1 - T2.$$

The disassembler is no longer I/O bound in speed of execution when $T_p = T2$ for the 128-line test. The system is I/O bound when $T_p < T2$, and is compute bound when $T_p > T2$.

taken when using DEMONS to avoid stepping through software interrupt (SWI) instructions. Likewise, I/O operations involving a regular maskable interrupt (IRQ) may not work correctly every time.

Other Considerations

Several extensions to DEMONS are possible. The *patch* function is not symbolic, but may be made so by using the disassembler tables in reverse and using a subset of the 6800 assembly language restricted to hexadecimal operands. This feature was not included in this version of DEMONS because of the need to avoid using excessive amounts of programmable memory. Another extension could be to allow the entry of

multiple addresses for the trace function to compare against. This feature would be useful if a situation arose in which the program under test could take several possible and unpredictable paths.

To use the disassembler in standalone mode, control should be passed to hexadecimal location 0000. The disassembler will reply by outputting a blank character to the terminal. Enter the four-digit hexadecimal address of the area of memory whose contents are to be displayed. The disassembler will issue home-up and erase-to-end-of-frame cursor commands to the terminal and will begin displaying lines of disassembled code. When 15 instructions have been displayed, the disassembler will pause

awaiting entry of the address of the next area of memory to be displayed. If a nonhexadecimal character is entered, MIKBUG will resume control.

DEMONS is started by transferring control to hexadecimal address 03CC. DEMONS will output the character P to the terminal and await entry of the four-digit hexadecimal address of the program to be debugged. Following entry of this address, the contents of the registers and the next instruction to be executed from the program being debugged will be displayed. DEMONS then issues a colon (:) as a prompt character and awaits entry of a command at the control terminal. If a format error is made while entering a command, DEMONS will output a question mark and again prompt for

The most efficient way to use DEMONS is to step through undebugged code a single instruction at a time, patching errors as they are encountered and correcting the contents of the registers when necessary, in an attempt to find as many bugs as possible in a single run. When the number of patches becomes unwieldy, or an unpatchable bug is found, or the last bug is found, only then should you reload the assembler and reassemble the problem program. This technique will reduce the number of times you have to load memory from your mass-storage device and so will increase productivity.

Listing 1: The main debugging routine of DEMONS, assembled in code for the 6800 microprocessor. This program uses the cycle counter, shown in figure 3, to generate interrupts that allow it to take command from the user program.

00100			NAM	DEMON		
00200		*				
00300		* AUTHOR: A.I. HALSEMA				
00400		* DATE:				
00500		* OBJECT	MACHINE	SWTPC 6800		
00600		* PROGRAM NAME: DEMON(S) VERSION 1.0				
00700		* DEBUG MUNITOR (SYMBOLIC) INITIALIZATION				
00800		*				
00900			THIS I	ROUTINE READI	ES THE PIA AND STARTS THE HARDWARE CYCLE	
01000		*			EQUEST THE STARTING ADDRESS OF THE CODE	
01100		*			H A 'P' PROMPT, IT ALSO REMOVES TRACE	
01200		* SETTINGS.				
01300			UPT	Ò		
01400	03CC		ORG	503CC		
01500		*				
01600	A075	XSAV	EQU	SA075	X-REGISTER SAVE AREA	
01700	A02F	STAK	EQU	SA02F	DEMON(S) STACK ADDRESS	
01800	A906	NMIV	Egu	\$A006	NMI INTERRUPT VECTOR ADDRESS	
01900	E1D1	DUTEEE	EQU	SEIDI	OUTPUT CHARACTER ROUTINE	
02000	A U 7 8	TFLAG	EQU	SA078	TRACE ACTIVE FLAG. 1= ACTIVE	
02100	AOOC	ADDR	EGU	SAOOC	ADDRESS STORAGE USED BY BADDR	
02200	800C	HCCPIA	EQU	8800C	CYCLE COUNTER PIA ADDRESS	
02300	AU7C	APPND	EQU	\$407C	APPENDAGE ADDRESS FOR DISASM	
02400	A077	LINES	EGU	\$A077	LINES FOR DISASM TO DISPLAY	
02500	01A4	APP	EQU	SUIA4	APPENDAGE ADDRESS IN DISASM	
02600	EOCA	OUT2HS	EQU	SEUCA	OUTPUT 2 HEX DIGITS AND SPACE	

Listing 1 continued on page 338

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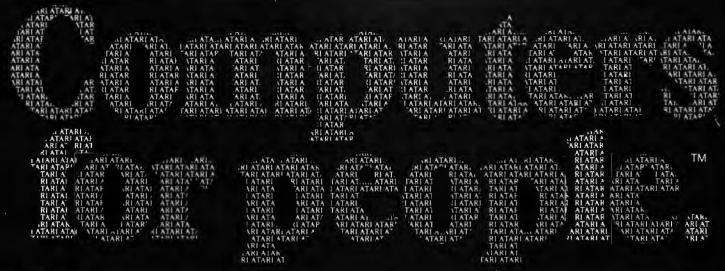
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               EOCS
                        OUTAHS
                                      Eau
                                               SECCE
                                                                       DUTPUT 4 HEX DIGITS AND SPACE
02800
               A079
                        TADDR
                                      EQU
                                               8A079
                                                                        TRACE RECUGNITION ADDRESS
02900
               ELAC
                         INEEE
                                      FOU
                                               SEIAC
                                                                        CHARACTER INPUT ROUTINE
03000
               EO7E
                        PDATA1
                                      Edu
                                               SEU7E
                                                                        PRINT BLOCK ROUTINE
03100
               0016
                        NEXTL
                                      EQU
                                               50018
                                                                          DISASSEMBLER FATRY POINT
03200
03300
                        * DEMON(S) START-UP ENTRY POINT
03400
03506
         OSCC BE AOSF START
                                               *STAK
                                                                        SET STACK ADDRESS
RESET TRACE FLAG
03600
         03CF 7F AU78
                                      CLR
                                               TFLAG
03700
         03D2 BD 047D UG
                                      JSR
                                               CURL
                                                                        ISSUE CK/LF
03800
         0305 86 50
                                               : IP
                                      LDAA
03900
         03D7 BD EIDI
                                     JSR
                                               DUTERE
                                                                        ISSUE PROMPT
                                                                        GET START OF PROBLEM PROGRAM
         03DA RD 0482
03DD 25 F3
04000
                                      JSR
                                               BADUP
04100
                                                                       BAD INPUT- TRY AGAIN
                                     BCS
                                               HG
         03UF ND 0561
                                               SETAD
                                     JSR
04200
04300
         03E2 CE 040A
                                               SINTER
                                                                       SET UP NMI VECTOR
                                     LDX
04460
         03E5 FF A006
                                               NMIV
                                      SIX
04500
         03E8 CE 800C
                                      LDX
                                               #HCCPIA
                                                                        INITIALIZE PIA/CYCLE COUNTER
04600
         03EB 6F 01
                                     CLR
                                               1 . X
                                                                       SELECT DOFA
04700
         03ED 86 FF
                                     LUAA
                                               #SFF
                                                                      AND SET UP ALL LINES TO OUTPUTS
UAROG
         03FF A7 00
                                     STAA
         03F1 86 04
04900
                                     LDAA
                                               1504
                                                                        SELECT TOPA
         03F3 A7 01
05000
                                     STAA
                                               1 . X
05100
         03F5 86 6B
03F7 A7 00
                                                                      TURN OFF COUNTER RESET
AND SET INITIAL COUNTER VALUE
                                     LDAA
                                               #86B
05200
                                      STAA
                                               Х
05300
         03F9 A6 UU
                        SETUP
                                     LDAA
                                               X
         03F8 84 F9
05400
                                      ANDA
                                               * SF 9
                                                                      TURN OFF COUNTER LOAD
05500
         03FD A7 90
                                      STAA
05600
         03FF 86 U7
                                     LDAA
                                               *507
                                                                         ENABLE CAT INTERRUPT ON LUA TO
05700
         0401 47 01
                                      STAA
                                                                       .HIGH TRANSITIONS
                                               1, 1
05800
         0403 A6 00
                                     LDAA
                                               #SFR
05900
         0405 84 FR
                                                                      START COUNTER
                                     ANDA
         0407 47 00
06000
                                     STAA
                                               X
         0409 3B
06100
                                                                       ON TO PROPLEM PROGRAM
                                     HTI
06200
                        * DEMON(S) INTERRUPT PROCESSOR AND OPERATOR COMMAND DECODING.
06300
                                     ENTERED ONLY UPON OCCURENCE OF NMI INTERRUPT,
LUCATION LAMELLED 'USER' ALLOWS FOR PATCHING IN JUMPS
TO FURTHER INTERRUPT PROCESSING IF MORE THAN ONE SOURCE
06400
06500
06600
                                     Ut WAI INTERMUPTS IS AVAILABLE.
06700
06800
06900
         040A CE HUOC INTRP
                                               * HCCPIA
                                                                       GFT PIA ADDRESS
                                     LDX
                                                                      . AND CHECK FUR CYCLE CHUNTER
07000
         U4UD 60 01
                                     TST
                                               1 . X
07100
         040F 28 03
                                     HMI
                                               HINE.
                                                                        . INTERRUPT
         0411 01
07200
                        USER
                                      NUF
                                                                       PATCH A JUMP TIJ SUME OTHER NMI
07300
         0412 01
                                                                        PROCESS HERE, RECAUSE THIS
                                     NUP
07400
         0413 01
                                     IVOP
                                                                        . INTERRUPT IS NOT FROM CYCLES,
                                                                         DISABLE COUNTED INTERPUPTS
07500
         0414 86 04
                                     LUAA
                                               #5U4
                        MINE
07600
         U416 A7 01
                                     STAA
                                               1 . X
         U418 86 3F
                                               8 S 3 F
                                                                      STOP COUNTER
07760
                                     LDAA
07800
         041A A7 U0
                                     STAA
                                               x
                                                                      HESET COUNTER- SET LOAD VALUE
                                               # 5 3 h
07900
         041C 86 3H
                                     LDAA
08000
         041E A7 00
                                                                         TO 3
                                     STAA
08100
         0420 A6 00
                                                                        DUMMY READ TO INSURE NAT OFF
                                     LDAA
08200
         0422 30
                                      TSX
08300
         0423 EM U5
                                                                      GET ADDRESS OF NEXT INSTRUCTION
                                                                      SET ADDRESS FOR DISASSEMBLER
TRACE MODE PUNNING?
08400
         0425 FF ADOC
                                     STX
                                               ADDR
08500
         0428 7D A078
                                     TST
                                               TELAG
                                                                        IF NOT- BRANCH
08600
         0428 27 OA
                                     REG
                                               NOT
                                                                        THACE FLAG SET- TEST ADDRESS
         042D BC A079
0430 26 52
                                               TADDR
08700
                                     CPX
                                     BNE
                                                                       BRANCH IF WHONG AUDRESS ADDRESSES ARE EQUAL COUNT HIF
                                               NOPE
08800
08900
         0432 7A AUTR
                                     DEC
                                               TELAG
                                                                        IF NUT ZEHO- GO THACE SOME MURE
09000
         0435 26 40
                                     HME
         0437 86 01
                                                                        SET NUMBER OF LINES FOR DISASM
09100
                                     LDAA
                                               * $ 01
         0439 A7 AU77
                                     STAA
                                               LINE.S
09200
                                                                       FO A CH LF AND DISPLAY REGISTERS DISPLAY INSTRUCTION
09300
         043C 80 28
                                     HSR
                                               REGS
09400
         043E BD 056E
                                     JSP
                                               SHOLIN
                                                                       GET COMMAND= ISSUE PROMPT
                        COMOR
                                               # 1 1
09500
         0441 85 3A
                                     LIDAA
                                               GUTHER
         0443 BU E1D1
                                     J.SR
09600
09700
         0446 HD E1AC
                                     JSR
                                               INDEED
                                                                        GET INPUT
                                                                        GET CUMMAND TANLE ADDRESS
09800
         0449 CH 0578
                                     LOX
                                               *COMTAR
                                                                        GET RECUGNITION CHARACTER
09900
         044C E6 00
                        TEST
                                      LUAB
                                                                       SAME AS IMPUT?
JF NOT- GU LOOK AGAIN
10000
         044E 11
                                      CHA
         044F 26 04
10100
                                      BNE
                                               MURE
                                                                      KOUAL- GET CURPENT ADDRESS
GO DU IT TO IT
         0451 EE 01
10200
                                     LITY
                                               LIA
10300
         0453 6E UI
                                     JAP
                                               X
10400
                        MORE
10500
                                      TSTR
                                                                        FOLND END OF TABLE?
         0455 50
10600
         0456 27 05
                                                                        YES- TELL OPERATOR
                                      HLG
                                               BAD
                                                                        NO- POINT TO NEXT ENTRY IN TABLE
10700
         0458 08
                                      INX
10800
         0459 08
                                      INY
                                      INX
10900
         045A UR
                                           TEST AND GO TEST IT TELL OPERATOR AND GIVE HIM ANOTHER CHANCE TO BO IT RIGHT
         0458 20 EF
11000
                                      BKA
                          INPUT 15
                                     BAD-
11100
                                               x 17
11200
         U45D 86 3F
                        BAD
                                     I,OAA
                                                                       DISPLAY QUESTION MARK
11300
         045F BD E1D1
                                      JSR
                                               DUTEEF
                        NEXT!
                                      BSR
                                                                       DO CARRIAGE RETURN/LINE FEED
11400
         0462 RD 19
                                               CURL
11500
         0464 20 DB
                                     HRA
                                               CUMBIN
                                                                       AND GO TRY AGAIN
```

Listing 1 continued on page 340



Atari graphics and sound stand in a class by themselves."

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"Its superiority lies in three areas: drawing fancy pictures (in color), playing music, and printing English characters onto the screen. Though the Apple can

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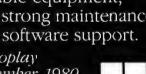
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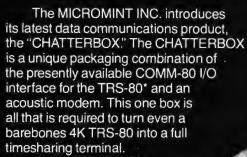


Computers for people:

For further information write: Atari Inc., Computer Division, 1196 Borregas Avenue, Sunnyvale, CA 94086

```
Listing 1 continued:
11600
11700
11800
                         * REGS- DISPLAY REGISTERS
11900
12006
         0466 80 15
                                               CURL
                                                                        DO CRALE
                        REGS
                                      HSP
                                                                        DISPLAY 'CC B A X' CHLF
         0468 CE 059A
                                      LDX
                                               *CCL
12200
         0468 BD E07E
                                      JSR
                                               PDATA1
12300
                                      TSX
                                                                        GET STACK ADDRESS
         046E 30
12400
         046F 08
                                                                        CORRECT FOR REGS RETURN ADDRESS
12500
         0470 08
                                      INX
12600
         0471 BD EUCA
                                               OUT2HS
                                      JSR
12700
         0474 BD EOCA
                                      JSB
                                               OUT245
                                                                        DISPLAY B
12800
         0477 HD EUCA
                                      JSR
                                               OUT2HS
                                                                        LISPLAY A
12900
         047A SO EUCH
                                      JSR
                                               UUT4HS
                                                                        DISPUAY X
13000
         047D CE 05A4 CURL
                                      LDX
                                               *CRLF
                                                                        DO CARRIAGE RETURN/LINE FEED
13100
         0480 BD E07E
                                               PDATA1
                                      JSR
13200
         0483 39
                                      RTS
                                                                        EXIT
13300
         0484 CE 800C NOPE
0487 7E 03F9
                                               *HCCPIA
13400
                                     LDY
                                                                        GET PIA ADDRESS
13500
                                               SETUP
                                                                        GO START COUNTER AND EXECUTE
                                      JMP
13600
13700
                         * GET HEX INPUT
13800
13900
         048A BD EIAC INHEX
                                      JSR
                                                                         GO GET CHARACTER
                                               INEEE
                                                                       HEX?
14000
         048D 80 30
                                      SUBA
                                               ....
14100
         048F 2B 10
                                      BMI
                                               BADHEX
                                                                        NO- JUMP
14200
         0491 81 09
                                      CMPA
                                               #809
                                                                         BETWEEN O AND $09?
14300
         0493 2F UA
                                      BLE
                                               OKHEX
                                                                        YES- UK
14400
         0495 81 11
                                      CMPA
                                               # 5 1 1
                                                                       A OR GREATER?
         0497 2B 08
                                      BMI
                                               BADHEX
         0499 81 16
                                      CMPA
                                                                       F OF LESS?
14600
                                               *816
14700
                                               BADHEX
                                                                        NO- ERROR
         049B 2E 04
                                      BGT
14800
         049D 80 07
                                      SUBA
                                               #$07
                                                                         ADJUST FOR A THROUGH F VALUES
14900
         049F OC
                        OKHEX
                                                                        CLEAR ERROR FLAG
                                      CLC
15000
         04A0 39
                                     RTS
                                                                        EXIT
15100
         U4A1 OD
                        HADREX
                                     SEC
15200
         U4A2 39
                                     RTS
15300
15400
                        *GET ONE BYTE OF HEX INPUT - EXIT WITH DIGIT IN A AND CARRY CLEAR IF OK.
15500
15600
         0443 8D E5
                                               INHEX
15700
         04A5 25 UA
                                               HADH
                                                                        BAD- NOT HEX- JUMP
                                      BCS
                                                                        MOST SIGNIFICANT DIGIT, SO LEFT
15800
         04A7 48
                                      ASLA
                                                                        . JUSTIFY IT
15900
         04AB 48
                                      ASLA
16000
         04A9 48
                                      ASLA
         04AA 48
16100
                                      ASI.A
         04AB
                                                                          AND SAVE IN BEREGISTER
16200
               16
                                      TAB
         U4AC BD DC
                                                                        GET LEAST SIGNIFICANT DIGIT
16300
                                               INHEX
         04AE 25 01
                                      BCS
                                               BADB
                                                                        IF INPUT IS BAD- JUMP
16400
16500
         0480 18
                                      ABA
                                                                        COMBINE BUTH IN A
16600
         0481 39
                        BADB
                                      RTS
                                                                        EXIT
16700
                           BADDR- HILLD ADDRESS, PESULT IN SACOC AND X IF GOOD, ELSE CARRY SET.
16800
16900
17000
         0482 8D EF
                                                                        GET HEX BYTE
                        BADDR
                                      BSR
                                               BYTE
                                                                        JUMP IF BAD
SAVE BYTE
         0484 25 00
                                      BCS
                                               ADBAU
17100
17200
         0486 87 AOUC
                                      STAA
                                               ADDR
17300
         0489 BD E8
                                               BYTE
                                                                        GET SECOND BYTE
                                      HSF
                                                                        JUMP IF BAD
SAVE IT
17400
         0488 25 06
                                      BCS
                                               ADHAD
         U48D 87 A00D
                                               ADDR+1
17500
                                     STAA
         U4CU FE AOOC
                                               ADDR
17600
                                      LUX
                                                                        GET IN X
                                                                        EXIT
                                     PTS
         04C3 39
                        ADBAD
17700
17800
                           TRACE PROCESSING, COMMAND FURMAT: TXXXX, OR TXXXX, NN WHERE T IS THE COMMAND CHARACTER
17900
18000
                                     XXXX IS A FOUR-DIGIT HEX ADDRESS
HN IS A TWO DIGIT TRAP COUNT=STF MAX.
TRACE MUDE IS SET WITH THIS COMMAND. STEP COMMAND STARTS HUN.
THE PROBLEM PROGRAM WILL HUN UNTIL DEMON(S) HAS ENCOUNTERED
18100
18200
18360
16400
                                      ADDRESS XXXX NO TIMES, WHEN IT WILL RETURN OPERATOR CONTROL.
ADDRESS XXXX MUST BE THE ADDRESS OF THE FIRST BYTE OF AN INSTRUCTION.
18500
18600
18700
                                      NOTE THAT ZERO IS A DISALLOWED VALUE FOR NN IN INPUT.
18800
                                      TXXXX, FURM SETS NN TO 01 AUTOMALICALLY.
                                                                        GO GET TPACE ADDRESS HAD INPUT
18900
         04C4 8D EC
                        TRACE
                                      BSR
                                               BADDH
         04C6 25 95
                                               HAD
19000
                                      BCS
                                               TADDE
19100
                                      STX
                                               INCOL
                                                                         GET PERIOD OF COMMA
19200
         U4CB BD E1AC
                                      JSR
19300
         04CE 81 2E
                                      CMPA
                                                                        PERTUD?
         04D0 27 OF
                                               SETONE,
                                                                        YES- SET TFLAG TO ONE
19400
                                      BEQ
                                               . .
         04D2 81 2C
19500
                                      CMPA
                                                                        CUMMA?
                                                                        NO- BAD INFUT
19600
         0404 26 97
                                      BNF
                                               HALL
                                                                        IT IS COMMA- GET LOOP COUNT
19700
         0400 80 CB
                                      HSK
                                               BYTE
19800
                                               BAD
         0408 25 83
                                      BCS
         040A 27 81
                                               BAD
19900
                                      HEQ
                                                                        SET IN TRACE FLAG
         04DC 87 AU78
                        SETT
                                      STAA
                                               TFLAG
20000
                                               NEXT 1
                                                                        GO GET A COMMAND
20100
         04DF 20 81
                         NEXTZ
                                      BHA
         04E1 86 01
                         SETONE
                                      LDAA
                                               .501
20200
         04E3 20 F7
                                      HHA
                                               SETT
20300
```

TRS-80 owners Explore new worlds with CHATTERBOX.



The CHATTERBOX includes a built-in programmable 50-19200 baud serial port, a Centronics compatible parallel printer port, a 300 baud acoustic originate modem, and a spare TRS-BUS expansion connector. It comes complete with power supply, ribbon cable and connector, user's manual, and smart terminal software for immediate operation. When the modem is in use, the complete data conversation is automatically routed to the serial output port and parallel port where it can be logged on a printer.

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```
Listing 1 continued:
                        * STEP COMMAND PRUCESSING, CAUSES TRANSFER OF COMTROL TO ADDRESS.

* COMMAND FURMAT: SXXXX OR S.

* WHERE XXXX IS A FOUR DIGIT HEX ADDRESS.
20400
20500
20600
                                               SXXXX FORM SETS NEW CUPREMT ADDRESS
20700
                                               S. FURM STARTS HUN AT CHARENT AUDRESS.
20800
20900
         0415 BD CB
                                     BSR
                                               BADDR
                                                                       GO GET ADDRESS
21000
                        STEP
                                                                       IF NOT HEX- ASSUME PERIOD
         04E7 25 98
                                     BCS
                                               NUPE
21100
                                                                       ELSE PUT ADDRESS IN STACK
21200
         0419 8D 76
                                     BSP
                                               SETAD
         04EB 7E 0437
                                     JMP
                                               NOT
                                                                       DISPLAY AND GET NEXT COMMAND
21300
21400
         U4EE UI
                                     NIJP
21500
         04EF 01
                                     NUP
21600
         04F0 01
                                     MUP
21700
         04F1 01
                                     NOP
                         * GO COMMAND PROCESSING, CAUSES EXIT FROM DEMON(S) WITH CYCLE COUNTER
21800
                                                      HALTED, USE WHEN FINISHED WITH DEBUGGING.
CONTPOL IS PASSED TO THE CURRENT ADDRESS REACHED
WHILE STEPPING OR TRACING.
21900
22000
22100
         04F2 3B
                                                                       EXIT FHUN DEMON(S)
22200
22300
                        * DISPLAY COMMAND PROCESSING. DISPLAYS 15 INSTRUCTIONS FROM MEMORY IN
22400
                                                            DISASSEMBLED FORM.
                                                            CUPMAND FURMS: DXXXX OR D.
WHEPE XXXX IS A 4 DIGIT HEX ADDRESS
D. CAUSES DISPLAY TO STAPT WITH THE CUPRENT
22500
22500
22700
                                                            INSTRUCTION
22800
         U4F3 7E 0450 DAR
22900
                                     JMP
                                               BAU
         04F6 46 UE
                                               SUE
                                                                       SET UP LINES FOR DISASA
23000
                        DISPLA
                                     LDAA
23100
         04F8 87 A077
                                               LINES
                                      STAA
23200
         04FB BD 85
                                     BSR
                                               BADDR
                                                                       GET ADDRESS
         04FD 24 06
04FF 30
23300
                                     RCC
                                               SHOW
                                                                       IF NO ERPOR ON ENTRY- BRANCH
23400
                                     15%
                                                                       GET ADDRESS FROM STACK
         0500 F.E 05
23500
                                     LDX
                                               5 . X
         0502 FF AUUC
                                                                       . AND SET FUR DISASSEMBLER
                                     STX
                                               AUDR
23500
23700
         0505 86 10
                                               *510
                                                                        HOME HP
                        SHOW
                                     LDAA
23800
         0507 BD E101
                                      JSR
                                               CUTEEE
23900
                                     ESH
                                               SHOLIN
         USUA BD 62
                                                                       GO DISPLAY INSTRUCTION
         050C 20 D1
                                                                       GET ANUTHER CUMMAND
24000
                                      BRA
                                               NEXT2
24100
                         * REGISTER DISPLAY.
                                               THE REGISTERS IN THE STACK APE DISPLAYED.
24200
                                               COMMAND FORMS R
24300
         050E BD 0466 SHUREG
                                     JSR
                                               REGS
                                                                       DISPLAY REGISTERS
2440U
24500
         0511 7E 0441 HACKUP
                                                                       GO GET ANDTHE COMMAND
                                      JMP
                                               COMON
24600
                         * SUPPORT SUBROUTINE - GET INPUT AND PREPARE X REGISTER
24700
24900
24900
         0514 8D 8D
                                      BSR
                                                                       GET INPUT FYTE
25000
         0516 25 04
                                      HCS
                                               GAG
                                                                       BAD VALUE?
25100
         0518 30
                                                                       SET VALUE IN STACK
                                      TSX
25200
         0519 08
                                      INX
                                                                       APJUST ADDRESS FOR BEING IN A SURRUUTINE
2530U
         051A 08
                                     INX
25400
         051B 39
                                      KTS
25500
         051C 31
                        GAG
                                      INS
         0510 31
25600
                                      INS
25700
         051E 20 D3
                                               DAB
                                     BRA
25800
25900
                         * SET CONDITION CUDE REGISTER. COMMAND FORM: CXX
                                                             WHERE XX IS A 2-DIGIT HEX VALUE THAT
26000
26100
                                                             THE CC REGISTER IN THE STACK WILL BE SET
26200
26300
26400
26500
         0520 BD F2
                        RSETC
                                     HSR
                                               BYN
                                                                       GET INPUT BYTE
         0522 A7 00
0524 20 B9
                        SETREG
                                     STAA
                                               NEXT2
26600
                                     BRA
                                                                       GO GET ANOTHER COMMAND
26700
26800
                        * SET B-REGISTER,
                                               COMMAND FORM: BXX
26900
27000
         0526 BD EC
                        RSETB
                                     BSR
                                               BYN
                                                                       GET INPUT BYTE
27100
         0528 08
                        BSETS
                                     INX
         0529 20 F7
                                               SETREG
27200
                                     BRA
27300
27400
                        * SET A-REGISTER,
                                               CUMMAND FORM: AXX
27500
27600
         052B 8D E7
                        RSETA
                                     BSR
                                               BYN
27700
         052D 08
                                     INX
27900
         052E 20 FR
                                     BRA
                                               BSETS
27900
                        * SET X-REGISTER,
                                               COMMAND FORMAT: XNNNN
                                                        WHERE NNNN IS A 4-DIGIT HEX VALUE
2B000
29100
28200
         0530 BD 04B2 RSETX
                                     JSR
                                               BADDR
                                                                       GET 4 DIGITS
         0533 25 BE
0535 30
28300
                                     BCS
                                                                       BAD INPUT?
                                               DAB
                                                                       NO- GET STACK ADDRESS
28400
                                     TSX
                                                                       SET X VALUE IN STACK
28500
         0536 09
0537 09
                                     DEX
                                     DEX
28600
28700
         0538 09
                                     DEX
         0539 RD 28
                                               SETS
```

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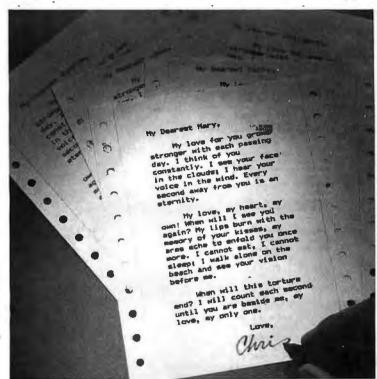
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```
Listing 1 continued:
                                                    NEXT2
28900
          053B 20 A2
                           NEXT3
                                          BRA
                              PATCH CUMMAND PROCESSOR, COMMAND FORMATI PXXXX, NN NN NW ,,,.(CR)
WHERE XXXX IS A 4-DIGIT HEX ADDRESS
AND NN IS A 2-DIGIT HEX VALUE
29000
29100
29200
                                         ENTER AS MANY 2 DIGIT VALUES AS NEEDED, THEN TERMINATE STRING WITH A CARRIAGE RETURN, EACH NN VALUE IS PLACED IN MEMORY AS IT IS ENTERED, UNLESS AN ERROR IS MADE. THEN THE NN VALUE CONTAINING THE ERROR IS REJECTED AND IS NOT STORED.
29300
29400
29500
29600
29700
29800
          053D BD 04B2 PATCH
                                                     BADDR
                                          JSR
                                                                                GET ADDRESS
                                                                                JUMP IF NOT HEX
SAVE X FOR LATER
29900
          U540 25 B1
                                                     DAB
                                          BCS
          0542 FF A075 GETMOR
30000
                                                     XSAV
                                          STX
30100
          U545 BD 04A3 GETS
                                          JSR
                                                     BYTE
                                                                                GET 2 DIGIT VALUE
          0548 25 0D
0548 FE A075
                                                                                JUMP IF NOT HEX RESTORE X
30200
                                          HCS
                                                     WHAT
30300
                                          LDX
                                                     XSAV
30400
          054D A7 00
                                                                                . AND STORE THE VALUE
                                          STAA
                                                     X
          054F 08
                                                                                POINT TO NEXT LOCATION SPACE BETWEEN INPUTS
30500
                                          INX
          0550 86 20
30600
                                          I.DAA
30700
          0552 BD E101
                                          JSR
                                                     COTEEE
30800
          υ555
                20 EH
                                          BRA
                                                     GETMIR
                                                                                GO GET MOPE INPUT
                                                                               INPUT NOT HEX- CAPRIAGE CODE?
JUMP IF YES
30900
          0557 88 DD
                           TART
                                          FORA
                                                     # SDD
          0559 27 E0
31000
                                          BEU
                                                    NEXT3
                                                                               COMMA?
31100
          U558 88 21
                                          ENRA
                                                     # 5 2 1
                                                                                JUMP IF YES
          055D 27 E6
055F 20 92
                                                     GE1S
                                          BEG
31200
                                                                                ELSE ERPOR IF HOT
                                          BHA
                                                     DAH
31300
31400
31500
                           * SUPPORT SURDUTINE - MOVE ADDRESS INTO STACK
31600
31700
          0561 30
                           SETAD
                                          TSX
                                                                                PUT THE ADDRESS IN THE STACK
31800
          U562 UB
                                          INX
31900
          0563 Bb ADOC SETS
                                          I.DAA
                                                     HOGA
                                          STAA
                                                     6.X
AUDH+1
32000
          0566 A7 06
          0568 H6 AUUD
0568 A7 07
32160
                                          LDAA
32200
                                          STAA
                                                     7 . X
          0560 39
                                          RTS
32400
32500
                              SUPPORT SUBROUTINE- SET APPENDAGE ADDRESS AND CALL DISASSEMBLER
32600
32700
          USBE CE OTA4 SHULIN
                                          LDX
                                                                                SET APPENDAGE FOR DISASSEMBLER
                                                     # APP
          05/1 FF AU7C
0574 HD 0018
32800
                                          STX
                                                     APPAIL
32900
                                                     NEXTL
                                                                                GO TU DISASSEMBLER
                                          JSR
          0577 39
33000
                                          HIS
                              COMMAND TABLE.
                                                 EACH ENTRY IS 3 HYTES LONG, THE FIRST BYTE IS THE ASCII COMMAND CHARACTER. THE NEXT 2 BYTES ARE THE PROCESS ADDRESS, THE TABLE IS TERMINATED WITH A BYTE OF ZERUS.
33100
33200
33300
33400
                                                                                STEP COMMAND
33500
          0578 53
                           COMTAB
                                          FCC
          0579 04E5
                                                    STEP
33600
                                          FIDE
                                          F'CC
                                                                                TRACE COMMAND
          0578 54
                                                     /T/
33800
          057C 04C4
                                          FDB
                                                     TRACE
          057E 52
                                                                                REGISTER DISPLAY
33900
                                          FCC
                                                     /R/
34000
          057F 050E
                                          FDB
                                                     SHUREG
                                                                                GO CUMMAND
34100
          0581 47
                                          FCC
                                                     161
          0582 04F2
34200
                                          FDB
                                                     GO
34300
          0584 44
                                          FCC
                                                     101
                                                                                DISPLAY CUMMAND
34400
          0585 U4F6
                                          FDB
                                                     DISPLA
                                                                                SET CONDITION CODES
34500
          0587 43
                                          FCC
                                                     101
                                                     RSETC
          0588 0520
                                          FOB
34600
34700
34800
                                          FCC
                                                                                SET A-REGISTER
                                                     /A/
          058A 41
          058B 052B
                                                     RSETA
                                          FD8
34900
                                                                                SET B-REGISTER
          U58D 42
                                          FCC
                                                     18/
          058E 0526
                                                     RSETB
35000
                                          FDB
35100
          0590 58
                                          FCC
                                                     /X/
                                                                                SET X-REGISTER
                                                     RSETX
35200
          0591 0530
                                          FDB
35300
          0593 50
                                          FCC
                                                     191
                                                                                PATCH
                                                     PATCH
                                          FDB
35400
          0594 053D
                                                                                SPACE FOR PATCHING AN ENTRY
35500
          0596 OU
                                          FCB
                                                     0.0.0
35600
35700
          0597 00
          0598 00
35800
          0599 00
                                          FCB
                                                     0
                                                                                END OF TABLE
35900
                            * REGISTER IDENTIFICATION
                                                                                REGISTER ID LINE
36000
           059A 43
                           CCL
                                          FCC
                                                     /CC B A X/
          059R 43
059C 20
36100
36200
36300
          0590
36400
          059E 20
36500
          059F 20
36600
          05A0 41
36700
          05A1 20
36800
          U5A2 20
          05A3 58
36900
37000
          05A4 0D
                            CRLF
                                          FCB
                                                     SUD, SUA, 4
                                                                                CARRIAGE RETURNALINE FEED
37100
          05A5 0A
37200
          05A6 04
37300
                                          END
```



We're known for our fine print.

Epson.

The type you get out of most printers you wouldn't send to your maiden aunt, much less use for your *important* correspondence. And up to now, in order to get a dot matrix hardcopy you could really call correspondence quality, you had to spend on the high side of a thousand bucks.

Not any more.

The Epson MX-80 challenges any dot matrix printer anywhere to match our type at our price. Or even come close.

Our emphasized print mode gives you a tack-

sharp, clean, easy-to-read face with true descenders—at a fraction of the price of daisy wheel printers. We give you a user-defined choice of twelve different weights and sizes of letters in 40, 80, 66 or 132 columns. We give you adjustable tractors so you can do anything from labels to memos to manuscripts. Fast and clean.

But if you think print quality is the only thing we have

to sell, you're wrong. The MX-80 may be the most revolutionary printer to come out in the past ten years.

For starters, it features the world's first disposable print head—after it's printed between 50 and 100-million characters, just throw it away. A new one costs less than \$30 and you can change it yourself with one hand. Plus, the MX-80 prints bidirectionally and 80 CPS with a logical seeking function to minimize print head travel time and maximize throughput. Finally—and this is the

best part—you can buy an MX-80 right now for less than \$650.

And that's what we call a lot of fine print for the money.

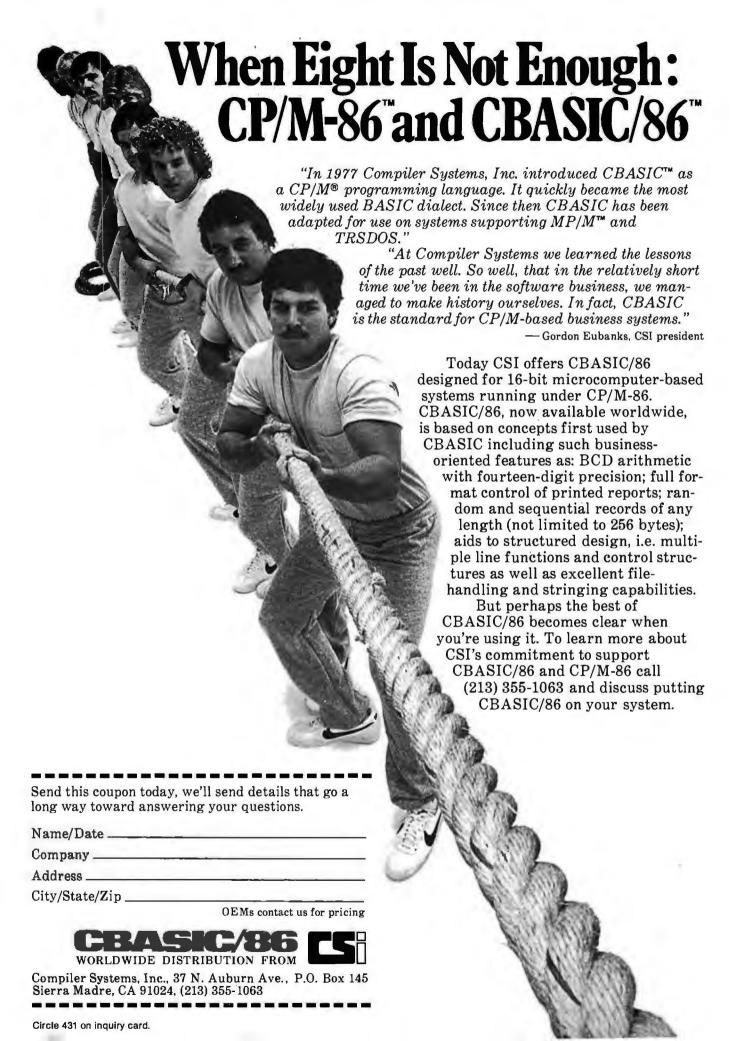




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Listing 2: The disassembler routine included as part of DEMONS. The packed-mnemonic table and format table occupy much space.

```
DISASM
00100
                                    NAM
00200
                        * AUTHOR: A.I. HALSEMA
00300
00400
                        * DATE: 10-28-1977
00500
                        * OBJECT MACHINE: SWTPC 6800
00600
                        * PROGRAM NAME: DISASSEMBLER VERSION 1.1
00700
00800
                                    OPT
                                              0
         0000
                                              $0000
00900
                                    DRG
              E047
                        BADDR
                                    EQU
                                              SE047
01000
                                    EQU
01100
               AOUC
                                              SAUOC
                        ADDR
01200
               E07E
                        POATA1
                                     EQU
                                              SE07E
01300
01400
                        * OPERATOR INTERFACE
01500
                                                                      GET DUMP ADDRESS FROM UPERATOR VIA MIKBUG
         0000 BD E047 START
                                              BADDR
01600
                                     JSR
         0003 86 OF
0005 87 A077
                                                                      SET # OF LINES TO DUMP
01700
                                     LDAA
                                              #15
                                     STAA
                                              LINES
01800
01900
         0008 CE 0169
                                    LDX
                                              *ERASE
                                                                      ERASE CRT SCREEN
         000B BD E07E
02060
                                    JSR
                                              PDATA1
02100
         000E CE 01A4
                                     LDX
                                              * APP
                                                                      SET APPENDAGE RUUTINE ADDRESS
02200
         0011 FF A07C
                                              APPND
                                    STX
02300
         0014 8D 02
                                    BSR
                                              NEXTL
02400
         0016 20 E8
                                    BRA
                                              STAPT
                          DISASSEMBLER, ENTER WITH DISPLAY START ADDRESS AT $400C.
02500
                                           NUMBER OF LINES TO DISPLAY AT LABEL 'LINES'.
02600
                                         SET ADDRESS OF APPENDAGE ROUTINE AT AOTC
02700
                                           ENTER VIA JSR.
02800
                                         EACH TIME A LINE IS READY, APPENDAGE RECIEVES CONTROL, VIA JSR. RETURN VIA RIS. EXIT WITH $400C CONTAINING ADDRESS OF NEXT
02900
03000
                                          INSTRUCTION, LOCATION SAOAA CONTAINING 32
BYTES OF ASCII TEXT TERMINATED BY CR, LF, 04,
03100
03200
                                           LOCATION 'LINES' WILL CONTAIN ZERO,
03300
                                                                      GET ADDRESS OF DATA
03400
         0018 FE AOOC NEXTL
                                    LDX
                                              ADDR
                                                                      GET DATA BYTE
         00 1B A6 90
                                    LDAA
03500
03600
         001D CE A06B
                                    LDX
                                              # WORKA
         0020 A7 03
                                     STAA
                                              BYTE, X
                                                                      SAVE IT
03700
03800
         0022 5F
                                    CLRA
                                                                      NOW MULTIPLY BY 2
                                    ASLA
03900
         0023 48
         0024 59
0025 AB 02
0027 E9 01
                                     ROLP
04000
                                                                      ADD MNEMONIC TABLE ADDRESS
                                     ADDA
                                              TAD+1.X
04100
04260
                                              TADAX
                                     ALCH
                                              HASE+1,X
                                                                      SAV THE DISPLACEMENT INTO TABLE
         0029 A7 07
                                     STAA
04300
04400
         0026 E7 Ub
                                     STAR
                                              BASE, X
04500
                                    LDAA
                                                                      GET HIGH OFDER NYABLE
         002D A6 03
                                              BYTE . X
                                    LSRA
04600
         002F 44
         0030 44
04700
                                     LSRA
                                                                      RIGHT JUSTIFY IT
04800
         0031 44
                                     LSRA
                                                                      AND MULTIPLY BY 2
                                     ANDA
                                              ARPE
04900
         0032 H4 FE
         0434 5F
                                    CLRB
05000
                                     ADDA
                                                                      ADD HASE OF FLAG TABLE
         0035 AB 05
                                              FAD+1.X
05100
         0037 E9 04
05200
                                    ADCB
                                              FAD, X
         0039 A7
                  (19
                                     STAA
                                              FLAGA+1.X
                                                                      SET POINTER INTO FLAG TABLE
05300
05400
                                     STAB
         003B E:7 0B
                                              FLAGA, X
05500
         003D CE A04A
                                     LDX
                                              *LINE
                                                                      BLANK THE DISPLAY LINE
         0040 C6 18
0042 86 20
056GU
                                    LDAR
                                              # 27
05700
                                     LDAA
                                              # 520
                        BLOP
05800
         0044 A7 04
                                     STAA
                                              4 , X
05900
         0046 OF
                                     INX
                                     DECH
06000
         0047 5A
         0048 26 FA
06100
                                              BLOP
                                    BNE
         004A 86
                  04
                                                                      SET EOL MARKER
                                     LDAA
                                              # 4
06200
06300
         004C A7 05
                                     STAA
                                              5 , X
         UU4L CE
                                              SUDUA
06400
                  ODOA
                                     LLDX
                                                                      SET CRLF IN LINE
         0051 FF A068
                                     STX
                                              LINE+30
06500
06600
         0054 FE 4071
                                     LDX
                                              WHAS
                                                                      GET THE PACKED MNEMONIC
06700
         0057 Ab
                  0.0
                                     LDAA
                                              X
                                                                      AND EXPAND INTO DISPLAY LINE
                                              #57F
         0059 84 7F
06800
                                     ANDA
         005a Et 01
06900
                                     LDAB
                                              1 . X
         005D 44
                                    LSRA
07000
         U05E 56
07100
                                     RORB
         005F 44
07200
                                     LSPA
07300
         0060 56
                                     RORR
         0061 RR
0063 67
07400
                  40
                                     ADDA
                                              #$40
07500
                  A058
                                     STAA
                                              UPER
07600
         0066 54
                                     LSRH
         0067 54
07700
                                     LSRH
07800
         0068 54
                                     LSRB
                                     ADDH
07900
         0069 CB 40
                                              # 540
08000
         0068 F7 A059
                                     STAB
                                              UPER+1
08100
         006E 46 01
                                     LDAA
                                              1 , X
08200
         0070 84 1F
                                     ANDA
                                              * 51F
         0072 88 40
0074 87 A05A
08300
                                     ADDA
                                              # $ 40
                                     STAA
                                              OPER+2
08400
08500
         0077 A6 00
                                     LDAA
                                                                      GET HI BYTE AGAIN
         0079 81 18
                                     CMPA
                                              ....
                                                                      TEST FOR FCB MNEMONIC
08600
08700
         007B 26 05
                                     BNE
                                              NFC
                                                                      NOT FCB
                                              #FFLAG
                                                                      IS FCB- SET FCB FLAG ADDRESS
09800
         0070 CE 03CB
                                     LD X
         0080 20 26
08900
                                     BRA
                                              DFF
```



```
Listing 2 continued:
09000
         0082 F6 A06E NFC
09100
                                                                            TEST FOR EXCEPTIONS
                                      LDAB
                                                WBYT
         0085 C1 37
0087 26 05
09200
                                      CMPH
                                                #537
                                                                          PULB?
09300
                                       BNE
                                                TPSH
09400
         0089 CE 03B5
                                       LDX
                                                *PULH
         008C 20 1A
008E C1 33
09500
                                       BRA
                                                OFF
09600
                         TPSH
                                      CMPB
                                                **33
                                                                         PSHB?
         0090 26 05
0092 CE 03B5
09700
                                       BNE
                                                THSR
09800
                                      LDX
                                                #PULH
09900
         0095 20 11
0097 CB 8D
                                       BRA
                                                OFF
10000
                                       EORB
                                                # 58D
                         TBSR
                                                                          BSR?
         0099 26 05
009B CE 03AF
                                       BNE
                                                SET
10100
10200
                                       LDX
                                                #BSR
10300
         009E 20 08
                                       BRA
                                                OFF
10400
10500
               OGAO
                                      EQU .
                         SET
                                                *
WFLG
         00A0 FE A073
00A3 85 80
00A5 27 01
00A7 08
10600
                                      LDX
                                                                          GET FLAG ADDRESS
                                                                         TEST FLAG BIT
BIT IS OFF
BIT IS ON- POINT TO 2ND FLAG
GET THE FLAG
10700
                                      BITA
                                                ....
10800
                                       BEQ
                                                OFF
10900
                                       INX
         00A8 A6 00
                         DFF
                                       LDAA
11000
         00AA B7 A06B
                                                FLAGD
                                                                          AND SAVE IT
                                       STAA
11100
                                                                         POINT ASCII ADDRESS IN LINE GET CURRENT ADDRESS
11200
         OOAD CE AOAA
                                       LDX
                                                #AADR
11300
         00B0 B6 A00C
                                       LDAA
                                                ADDR
                                                                          CONVERT TO ASCIT
11400
         00B3 8D 2A
                                       BSR
                                                CVASC
         0085 B6 A00D
11500
                                      LDAA
                                                ADDR+1
                                                                         SAME FOR LOW BYTE
11600
         00B8 8D 25
                                       BSR
                                                CVASC
                                                WBYT
                                                                         GET CURRENT BYTE
LEAVE SPACE BETWEEN ADDR.+ DATA
         OOBA B6 A06E
                                       LDAA
11700
         00BD 08
11800
                                       INX
                                                                          CURRENT BYTE TO ASCII
11900
         00BE 8D 1F
                                       BSR
                                                CVASC
         00C0 F6 A06B
00C3 C4 03
00C5 F7 A07B
12000
                                      LDAB
                                                FLAGD
                                                                          GET FLAG DATA
12100
                                                                          SAVE ONLY INSTRUCTION LENGTH
12200
                                       STAB
                                                SIZE
                                                                          SAVE IT
         00C8 08
00C9 FF A075 CLOP
00CC FE A00C
12300
                                       INX
                                                XSAV
12400
                                                                         SAVE POINTER INTO DISPLAY LINE
                                      STX
                                      LDX
                                                ADDR
                                                                         GET DATA ADDRESS
         00CF 08
12600
                                       INX
         00D0 5A
                                                                         COUNT BYTES
12700
                                      DECB
         00D1 27 10
12800
                                                NMR
                                                                          NO MORE
                                       BEQ
         00D3 A6 00
                                                                         GET DATA
12900
                                       LDAA
13000
         00D5 FF A00C
                                                ADDR
                                       STX
                                                XSAV
CVASC
13100
         00D8 FE A075
                                      LDX
13200
         00DB 8D 02
                                       BSR
                                                                         AND PUT IN DISPLAY LINE AS OBJ.
         00DD 20 EA
00DF 7E 018C CVASC
13300
                                       BRA
                                                CLOP
                                       JMP
13400
                                                TOASC
                                                                          X NOW POINTS TO NEXT DATA
13500
13600
         OOE2 FF AOOC NMR
                                       STX
                                                ADDR
         00E5 B6 A06B
                                      LDAA
                                                FLAGD
                                                                         GET FLAG DATA
13700
         00E8 85 40
                                       BITA
                                                #540
                                                                         SET REGISTER A?
         00EA 27 07
00EC 86 41
                                                NOTA
13800
13900
                                       LDAA
                                                                          YES- ADD TO ASCII MNEMUNIC
14000
         QUEE B7 AUSB SETR
                                       STAA
                                                OPER+3
14100
         00F1 20 08
                                       BRA
                                                FORM
         00F3 85 80
00F5 27 04
00F7 86 42
                                                                         SET REGISTER B?
14200
                         NOTA
                                       BITA
                                                ....
                                                                          NO- NO REGISTER SYMBOL
14300
                                                FORM
                                       BEG
                                                * 1B
14400
                                       LDAA
14500
         U0F9 20 F3
                                                SETK
                                       BRA
                                                                          POINT ARGUMENT POSITION IN LINE
14600
         OOFB CE AOSE FORM
                                                 *ARG
                                       LDX
         00FE 86
                                                                         GET FORMAT CODE
14700
                   A06B
                                       LDAA
14800
         0101 44
                                       LSRA
14900
         0102 44
                                       LSRA
15000
         0103 84 07
                                       ANDA
                                                                         INHERENT FURMAT
                                                DISPLY
         0105 27 OF
0107 4A
15100
                                       BEQ
15200
                                      DECA
                                                                         RELATIVE FURMAT
         0108 27 1A
010A 4A
                                                REL
15300
15400
                                       BEQ
                                       DÈCA
         010B 27 3D
                                                                         INDEXED FORMAT
15500
                                                IND
                                       BEQ
15600
         010D 4A
                                       DECA
         010E 27 46
                                       BEQ
                                                IMM
                                                                          IMMEDIATE FORMAT
15700
15800
         0110 80 03
                                      SUBA
                                                # 3
                                                                         FCB?
15900
         0112 27 49
                                      DE:O
                                                FCBFR
         0114 8D 57 DOMY
0116 FE A07C DISPLY
0119 AD 00
                         DOMV
                                                                          NUNE OF THE ABOVE- MUST BE EXTENDED OR DIRECT
16000
                                       BSR
                                                SETM
                                                                         GET APPENDAGE POUTINE ADDRESS
                                                APPND
16100
                                       LDX
16200
                                       JSR
                                                 LINES
                                                                          COUNT LINES
16300
         0118 7A A077
         011E 27 03
                                       BEQ
                                                                          ALL DONE?
         0120 7E 0018
                                                 NEXIL
                                                                   NO- DO NEXT LINE
GO AWAIT NEXT COMMAND
16500
                                       JMP
                         FIN
                                       RTS
16600
         0123 39
                         * FORMAT ARGUMENT FIELD FOR A RELATIVE INSTRUCTION
16700
                                                                         SET'S AND MOVE BYTES
POINT TO DATA
         0124 8D 47
                                                SETM
16800
                                       BSR
                         REL
         0126 FE A00C
16900
                                       LOX
                                                ADDR
         0129 09
17000
                                       DEX
         012A 4F
                                       CLRA
                                                                         CALCULATE EFFECTIVE ADDRESS OF
17100
         012H E6 00
                                                                           RELATIVE INSTRUCTION
17200
                                       LDAB
         012D 2A 01
17300
                                       BPL
                                                POS
         012F 43
                                       CUMA
17400
         0130 FR AUSD POS
                                                 ADDR+1
                                       ADDR
17500
         0133 B9 A00C
                                                 ADDR
17600
                                       ADCA
17700
         0136 01
                                       NUP
17800
         0137 01
                                       NOP
17900
         0138 01
                                       NOP
18000
         0139 CE A064
                                       LOX
                                                 #ABS+1
```

TOASC

BSR

Listing 2 continued on page 350

013C 8D 4E

18100

PMC-80 Expanded



Use all standard peripherals and existing software

When you buy PMC-80 you get hardware and software compatibility with the most popular microcomputer system in, the world—that means thousands of disk and cassette based programs and all kinds of peripherals are instantly available!

PMC-80 has configurations that give the computer enthusiast a way to grow from a STARTER system in affordable increments. Begin at a low \$675 for the basic 16K level II system and grow to the complete 48K memory system pictured above with two floppy disks for less than \$3000.

FASTLOAD option inputs short programs as fast as "disk" from ordinary,

standard format cassettes. Fast, reliable and economical!

PMC-80 COMMUNICATOR option provides interface to modems and parallel port printers. Take your pick of peripherals for communication with electronic bulletin boards and low cost timeshare services via phone lines from your home or business.

PMC-80 EXPANDER option provides the most powerful configuration with a total of 48K memory, provision for 4 mini-floppies, printer interface, RS-232C communications interface, plus a slot for the popular S-100 boards.

Sold through computer stores.

Personal Micro Computers, Inc.

475 Ellis Street, Mountain View, CA 94043

(415) 962-0220

```
Listing 2 continued:
18200
         013E 17
                                      THA
         013F 01
18300
                                      NOP
18400
         0140 01
                                      NUP
         0141 80 49
                                               TOASC
18500
                                      BSR
         0143 86 24
0145 87 A063
18600
                                      LDAA
                                               . ! 5
                                               ARS
18700
                                      STAA
                                               DISPLY
18800
         0148 20 CC
                                      BRA
                         * FORMAT ARGUMENT FIELD FOR AN INDEXED INSTRUCTION
18900
19000
         014A BD 21
                                                                        SET S AND MOVE BYTES
                         IND
                                      ASR
                                               SETM
         014C 86 2C
014E A7 01
                                                                        APPEND , X TO FIELD
19100
                                               ..,
                                      LDAA
19200
                                               1 . X
19300
         0150 86 SR
                                      LDAA
19400
         0152 A7 02
                                      STAA
                                               2, X
                                      BPA
19500
         0154 20 CO
                                               DISPLY
                         * FURMAT ARGUMENT FILLO FOR AN IPMEDIATE INSTRUCTION
19600
19700
         0156 86 23
                         IMY
                                      LDAA
                                               2 1 2
                                                                        PRECEED FIELD WITH A .
         0158 A7 00
19800
                                      STAA
                                               X
         015A 08
19900
                                      INX
         015B 20 B7
20000
                                      BRA
                                               DOMV
                         * FORMAT ARGUMENT FIELD FOR AN FCB PSEUDO
20100
20200
         015D 86 24
                                                                      MUVE DATA FUR FCB
                         FCBFR
                                      LDAA
                                               # 18
20300
         U15F A7 00
                                      STAA
20400
         0161 FE AU4F
                                      LDX
                                               LINE+5
20500
         0164 FF A05F
                                      STX
                                               ARG+1
         0167 20 AD
20600
                                               DISPLY
                                      ARA
         0169 10
20700
                         EPASE
                                      FCB
                                               $10,516,504
20800
         016A 16
20900
         016B 04
21000
         016C U1
                                      NOP
21100
                         * GENERAL ARGUMENT FIELD FORMATTING
         016D 86 24
21200
                         SETM
                                      LDAA
                                               #18.
                                                                        SET DOLLAR SIGN
         016F A7 00
0171 BF A073
21300
                                      STAA
21400
21500
                                               WFLG
                                                                        PREPARE TO MOVE BYTES
                                      STS
         0174 8E A051
                                               #LINE+7
                                      LDS
         0177 F6 A07B
                                      LDAR
                                                                       GET OBJECT INPUT SIZE
21600
                                               SIZE
21700
         017A C1 03
                                      CMPR
                                                                       IF SIZE= 3, MOVE 4 BYTES UF ASCII
21800
         017C 26 01
                                               DLOOP
                                      BNE
21900
         017E 5C
                                      INCB
22000
         017F U9
                         DLOOP
                                      INX
22100
         0180 32
                                      PULA
         0181 A7 00
22200
                                      STAA
                                               X
         0183 5A
22300
                                      DECB
22400
         U184 26 F9
                                               DLOOP
                                      BNE
22500
         0196 BE
                  A073
                                      LDS
                                                                       RESTORE SP
                                               WFLG
22600
         0189 39
                                      RTS
22700
         018A 01
                                      NOP
22800
         0188 01
                                      NOP
                         * CONVERT CONTENTS OF A TO ASCII AND STORE AT ADDRESS POINTED TO BY X.
22900
23000
23100
                         * RETURN WITH X INCREMENTED AND B UNCHANGED.
         018C 37
                                     PSHB
TAB
                         TOASC
                                                                      SAVE B
         018D 16
018E 44
23200
                                                                      COPY A
23300
                                                                      GET LEFT NYBBL
                                      LSRA
         018F 44
23400
                                      LSRA
23500
         0190 44
                                      LSRA
23600
         0191 44
                                      LSRA
         0192 8D 04
23700
                                      BSR
                                               ASC
                                                                     CONVERT TO ASCII AND STORE
         0194 17
23800
                                      TRA
         0195 84 OF
23900
                                      ANDA
                                                                     GET RIGHT NYBBL
                                               #aF
24000
                                      PULA
         0197 33
                                                                    RESTORE B
24100
         0198 88 30
                         ASC
                                      ADDA
                                               #530
                                                                      CONVERT A DIGIT TO ASCII
24200
         019A 81 39
                                               ....
                                      CMPA
         019C 23 02
019E 8B 07
24300
                                      BLS
                                               טם
24400
                                      ADDA
                                               #87
24500
         01AU A7 00
                         υU
                                      STAA
24600
         01A2 08
                                      TNY
24700
         01A3 39
                                      RTS
24900
                         * APPENDAGE FOR LINE DISPLAY
24900
         01A4 CE A04A APP
                                     LDX
                                               #LINE
                                                                        GET ADDRESS OF TEXT.
         01A7 BD E07E
25000
                                      JSR
                                               PDATA1
                                                                     DISPLAY THE LINE
25100
                                      RTS
25200
25300
                         * PACKED MNEMONIC TABLE
25400
                            MNEMONICS (ALPHA ONLY) ARE TRUNCATED TO THE 5 LOW ORDER BITS AND STORED 3 IN 16 BITS. THE HIGH ORDER BIT OF THE 16 IS USED AS A FLAG WHICH, IF SET, INDICATES THAT THE SECOND FORMAT FLAG
25500
25600
25700
25900
                            BYTE OF A PAIR SHOULD BE USED.
25900
         01AB 1862
                                      FOB
                         MTAB
                                               81862
                                                                        FCB
                                                                                  00
26000
         01AD 39F0
                                      FDB
                                               $39F0
                                                                        NOP
                                                                                  01
26100
         01AF 1862
                                      FDB
                                               $1862
                                                                        FCB
                                                                                  02
2620U
         01B1 1862
                                      FDR
                                               $1862
                                                                        FCB
                                                                                  03
         0183 1862
26300
                                      FDH
                                               $1862
                                                                        FCB
                                                                                 04
                                      FI)A
26400
         0185 1862
                                               $1862
                                                                                  05
06
                                                                        FCE
         0187 5030
26500
                                                                        TAP
                                      FDB
                                               $5030
2660u
         0189 5201
                                               $5201
                                      FDB
                                                                        TPA
                                                                                  07
26700
         01BB 25D8
                                      FDB
                                               $2508
                                                                        INX
                                                                                  08
26800
         01BD 10B8
                                      FDB
                                               $1088
                                                                        DEX
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26900
         018F 0096
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                                                                        CLV
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27000
         01C1 4CH6
                                               $4CB6
                                      FDB
                                                                                 OH
27100
         01C3 0D83
                                      FDB
                                               SOU 83
                                                                        CLC
                                                                                  UC
         01C5 4CA3
                                      FOB
27200
                                               84CA3
```

Listing 2 continued on page 352

SEC

00

4MHZ, DÖÜBLE DENSITY, CÖLÖR&B/W GRAPHICS. THE LNW80 COMPUTE



When you've compared the features of an LNW80 Computer, you'll quickly understand why the LNW80 is the ultimate TRS80 software compatible system. LNW RESEARCH offers the most complete microcomputer system at an outstanding low price.

We back up our product with an unconventional 6 month warranty and a 10 days full refund policy, less shipping charges.

FEATURES	LNW80	Pf4C-80**	TRS-80* MODEL III
PROCESSOR	4.0 MHZ	1 ,8 I4HZ	2.0 NHZ
LEVEL II BASIC INTERP.	YES	YES	LEVEL III BASIC
TRS80 MODEL 1 LEVEL II COMPATIBLE	YES	YES	NO
48K BYTES RAM	YES	YES	YES
CASSETTE BAUO RATE	500/1000	500	500/1500
FLOPPY DISK CONTROLLER	SINGLE/ DOUBLE	SINGLE	SINGLE/ DOUBLE
SERIAL RS232 PORT	YES	YES	YES
PRINTER PORT	YES	YES	YES
REAL TIME CLOCK	YES	YES	YES
24 X 80. CHARACTERS	YES	NO	NO
VIDEO MONITOR	YES	YES	YES
UPPER AND LOWER CASE	YES	OPTIONAL	YES
REVERSE VIDEO	YES	NO	NO
KEYBOARD	63 KEY	53 KEY	53 KEY
NUMERIC KEY PAD	YES	NO	YES
B/W GRAPHICS, 128 X 48	YES	YES	YES
HI-RESOLUTION 8/W GRAPHICS, 480 X 192	YES	NO	NO
HI-RESOLUTION COLOR GRAPHICS (NTSC), 128 X 192 IN 8 COLORS	YES	NO	NO
HI-RESOLUTION COLOR GRAPHICS (RGB), 384 X 192 IN 8 COLORS	OPTIONAL	NO	NO
WARRANTY	6 MONTHS	90 DAYS	90 DAYS
TOTAL SYSTEM PRICE	\$1,664.00	\$1,840.00	\$2,187.00
LESS MONITOR AND DISK DRIVE	\$1,200.00	\$1,375.00	***

COMPARE THE FEATURES AND PERFORMANCE

LNW80

- BARE PRINTED CIRCUIT BOARD & MANUAL \$89,95

The LNW80 - A high-speed color computer totally compatible with The LNWBO - A high-speed color computer totally compatible with the TRS-BO*. The LNWBO gives you the edge in satisfying your computation needs in business, scientific and personal computation. With performance of 4 MHz, ZBOA CPU, you'll achieve performance of over twice the processing speed of a TRS-BO*. This means you'll get the performance that is comparable to the most expensive microcomputer with the compatibility to the world's most popular computer (TRS-BO*) resulting in the widest software base.

FEATURES:

- KES: TRS-80 Model 1 Level II Software Compatible High Resolution Graphics RGB Output 384 x 192 in 8 Colors NTSC Video or RF MOD 128 x 192 in 8 Colors Black and White 480 x 192
- 4 MHz CPU

- 4 MM2 CFU 500/1000 Baud Cassette Upper and Lower Case 16K Bytes RAM, 12K Bytes ROM Solder Masked and Silkscreened

LNW SYSTEM EXPANSION

BARE PRINTER												
AND MANUAL						1	4-			6	¥	\$69.95
WITH COLD CO	10	IMC	-	ror	10							COA OF

The System Expansion will allow you to expand your LNW80, TRS-80*, or PMC-80** to a complete computer system that is still totally software compatible with the TRS-80* Model 1 Level II.

FEATURES:

- 32K Bytes Memory 5" Floppy Contro
 - 5" Floppy Controller Serial RS232 120ma 1/0

- Serial RSSS (20Ma 1/0
 Parallel Printer
 Real Time Clock
 Screen Printer Bus
 On Board Power Supply
 Solder Masked and Silkscreened

KEYBOARD

The Keyboard Kit contains a $63\ \text{key}$ plus a $10\ \text{key}$, P.C. board, and remaining components.

LNW RESEARCH

ORPORATION 14661-C MYFORD RD. TUSTIN CA.92680

Circle 219 on inquiry card.

LNDoubler

- Assembled and Tested \$149.00

Double-density disk storage for the LNW Research's "System Expansion" or the Tandy's "Expansion Interface". The LHDoublerTM is totally software compatible with any double density software generated for the Percom's Doubler***. The LHDoublerTM provides the following outstanding features.

- . Store up to 350K bytes on a single 5" disk
 . Single and double density data separation
 . Precision write precompensation circuit
 . Software switch between single and double density
 . Hardware override into single density only
 . Easy plug in installation requiring no etch cuts, jumpers
 or soldering
 . 35, 40, 77, 80 track 5" disk operation
 . 120 day parts and labor Warranty
 *** Doubler is a product of Percom Data Company, Inc.

Micro Systems software's double density disk operating system. This operating system contains all the outstanding features of a well developed DOS, with ease in useability.

LNW DATA SEPERATOR

- Assembled and Tested \$17.95

The LNW Data Separator provides you with a reliable and inexpensive means of solving your disk data read error problems for your 5" single density drives. Compatible with both the LNW System Expansion and Tandy's Expansion Interface. Some soldering is

CASE

The streamline design of this metal case will house the LMMST. LNN System Expansion, LNN80 Keyboard, power supply and fan, LNDoublerTH, or LNW Data Separator. This kit includes all the hardware to mount all of the above. Add \$12.00 for shipping

PARTS AVAILABLE FROM LNW RESERARCH 4116 - 200ns RAM

	chip	set	-	-		4			4			4					\$26.00
	3 chip	set		4													\$33.50
1	5 chip	set								-	*			*			\$64.00
2	4 chip	set															\$94.00
3	2 chip	set		4	4											. :	\$124.00
LNW80 "Start	uo na	rts	50	t.II		N	∦RI	n-1	1	_							\$82.00
LNW80 "Video				-	ı	N	WB.	0-3	,		1	7	1	-	1		\$31.00
LNW80 Transf										1	Ī	ï		ï			\$18.00
LNW80 Keyboa										1			,	Ĭ	i	,	\$16.00
								=			-			-		-	

40 Pin computer to expansion cable \$15.00
System Expansion Transformer \$19.00
Floppy Controller (FD1771) and UART (TR1602) . . \$30.00 VISA & MASTER CHARGE ORDERS & INFO. NO. 714 - 552 - 8946 ACCEPTED Add \$3.00 for shipping IUSIIN CA. 92680 ACCEPTED Add \$3.00 for shipping SERVICE NO. 714-641-8850

Listing 2	2 contin	uod.						
27 300		0D89	FDB	s0089		CLI	0 €	
27400		4CA9	FDB	\$4CA9		SEI	0F	
27500	OICE	4C41	FDB	84C41		SHA	10	
27600		0C41	FDB	SOC 41		CBA	11	
27700		1862	FDB	\$1862		FCB	12	
27800 27900		1862 1862	FDB	\$1862 \$1862		FCB FCB	13	
28000		1862	FDB	51862		FCB	15	
28100		5022	FDB	\$5022		TAB	16	
28200		5041	FDB	55041		TBA	17	
28300		1862	FDB	\$1862		FCB	18	
28400 28500		1021	F DB	\$1021 \$1862		DAA FCH	19 1A	
28600		0441	FDA	50441		ABA	18	
28700		1862	FDB	\$1862		FCB	1 C	
28800		1862	FDB	\$1862		FCB	1 D	
28900		1862	FDB	\$1862		FCB	1 5	
29000 29100		1862 0A41	FDB FDB	\$1862 \$0A41		FCB BRA	1 F 20	
29200		1862	FDB	81862		FCB	21	
29300		0909	FOB	\$0909		виі	22	
29400		0993	FDB	80993		BLS	23	
29500		0863	FDB	\$0863		BCC	24	
29600 29700		0873 09C5	FDB FDB	80873 809C5		BCS BNE	25	
29800		08B1	FDB	\$09C5		BEQ	26 27	
29900		OAC 3	FDB	SOAC 3		BVC	28	
30000	01FD	OADS	FOB	SOAD3		BVS	29	
30100		OAOC	FDB	BOAOC		BPL	2 A	
30200		09A9	FDB	\$09A9		BMI	2B	
30300 30400		08E5	FDB FDB	808E5 80994		BGE BLT	2C 2D	
30500		08F4	FDB	808F4		BGT	2E	
30600		0985	FDB	60985		BLE	2F	
30700		5278	FDB	\$5278		TSX	30	
30800		25D3	FDB	\$25D3		INS	31	
30900		CZAC	FDB	8CZAC		PUL A	32	EVERDETON
31000 31100		42AC 10B3	FDB FDB	\$42AC \$10B3		PUL B DES	33 34	EXCEPTION
31200		5313	FDB	\$5313		TXS	35	
31300		C 2 6 8	FDB	6C268		PSH A	36	
31400		4268	FDB	\$4268		PSH H	37	EXCEPTION
31500		1862	FD₽	81862		F.CR	38	
31600		4A93	F.DB	64A93		HTS	39	
31700		1862	FDB	\$1862		FCB	3 A	
31800 31900		4A89 1R62	FDB FDB	\$4A89 \$1862		FCB	3 A	
32000		1862	FDB	\$1862		FCB	30	
32100		5C29	FDB	\$5C29		1 A w	3E	
32200		4EE9	FDH	S4EE9		SWI	3F	
32300		39A7	FDB	S 38A 7		NEG A	40	
32400 32500		1862	FDB FDB	\$1862 \$1862		FCB	41	
32600		ODED	FDB	SUDED		FCB COM A	43	
32700	_	3272	FDB	\$3272		LSP A	44	
32800		1862	FDB	\$1862		FCR	45	
32900		49F2	FDB	549F2		ROF A	46	
33000		0672 066C	FUB	\$0672		ASE A	47	
33200		49EC	FDB	\$066C \$49EC		ROL A	48	
33300		10A3	FDB	\$10A3		DEC A	4 A	
33400		1862	FUB	\$1862		FCB	4 B	
33500		25C3	FDA	825C3		INC A	4C	
33600		5274	FDB	85274		TST A	40	
33700 33800		1862 0D92	FDB	\$1862 \$0D97		FCP CLF A	4E 4F	
33900		3HA7	FDB	S38A7		INFIG B	50	
34000	0240	1862	FDB	\$1862		FCB	51	
34100		1862	LOR	\$1862		FCB	52	
34200		ODED	FDB	SODED		COM B	53	
34400		3272 1862	FDB	\$3272 \$1862		LSP B FCB	54	
34500		4912	FDB	849F2		ROR B	55 56	
34600		0672	FDB	\$0672		ASR B	57	
34700	025B	066C	FD8	6966C		ASL B	58	
34800		49EC	FDB	849EC		ROL B	59	
34900 35000		10A3	FDH FDB	\$10A3		DEC B	5 A	
35100		25C3	FDB	\$1862 \$25C3		FCB INC B	5 B 5 C	
35200		5274	FDB	65274		TST B	5 p	
35300	0267	1862	FDA	\$1662		FCB	5E	
35400		UD92	FDB	\$0192		CLR B	5F	
35500 35600		38A7 1862	FDB FDB	\$38A7 \$1862		NEG, X	60	
35700		1862	FDB	\$1862		FCB FCB	61	
35800		ODED	FDB	SODED		COM.X	63	
35900	0273	3272	FDB	\$3272		LSR, X	64	
36000		1862	FDB	\$1862		FCH	65	
36100 36200		49F2 0672	FDB FDB	849F2 \$0672		POR,X ASR,X	66	
36300		066C	FDB	\$066C		ASL,X	68	
					+			Listing

Listing 2 continued on page 354

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4008 (8K RAM - 40 Column)	\$ 795
16K "B" (16K RAM - 40 Column)	995
32K "B"(32K RAM - 40 Column)	995
32K "N" (32K RAM - 40 Column)	1295
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CZN Cassette	95
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ATARI 400 (8K RAM)......\$499.00 ATARI 400 (16K RAM)630.00 ATARI 410 RECORDER89.95 ATARI 810 DISK DRIVE599.95

NEECO also carries all available ATARI software

ALTOS COMPUTER SYSTEMS

	RAM	DISK	
ACS 8000-IS	64K	250K	\$2840
ACS 8000-28	64K	500K	3500
ACS 8000-1	64K	500K	3840
ACS 8000-2	64K	1M	4500
ACS 8000-4	64K	2M	5600
ACS 8000-5	64K	1M	5990
ACS 8000-6 Mul2	- Mu	lti-Use	er
(14.5 M-Winchester)	112K	1M	10,670
(29 M-Winchester)	112K	1M	11,870
ACS 8000-6 Mul4	Mult	i-User	
(14.5 M-Winchester)	208K	1M	11,960
(29 M Winchester)	208K	1M	13,160

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32K " II+1430 APPLE DISK w/3.3 DOS......650 APPLE DRIVE Only495

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Allows 3 CPU's (Expandable to 8) to access

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Each Additional CPU \$199 (up to 8) **DIABLO 630** 45 CPS, Letter Quality RS-232 Port \$2,710

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a single Commodore Disk.



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55 CPS **Letter Quality** High Reliability

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36500	027F 10A3	FDB	810A3		6A
36600	0281 1862	FDB	81862		6B
36700	0283 2503	- FDB	825C3	INC,X	6C
36900 36900	0285 5274 0287 29BU	FDB FDB	85274 \$2980	TST,X JMP,X	6D 6E
37000	0289 0D92	FDB	80D92	CLR.X	6F
37100	028B 38A7	FDB	838A7	NEG	70
37200	028D 1862	FDB	\$1862	FCB	71
37300	028F 1862	FDB	81862	FCB	72
37400	0291 ODED	FDB	SODED	COM	73
37500	0293 3272	FDB	83272	LSR	74
37600	0295 1862	FDB	\$1862 849F2	FCB ROR	75 76
37700 37800	0297 49F2 0299 0672	FDB FDB	\$0672	ASR	77
37900	029B 066C	FDB	\$066C	ASL	78
38000	029D 49EC	FDB	849EC	ROL	79
38100	029F 10A3	FDB	B1UA3	DEC	7A
38200	02A1 1862	FDB	81862	FCB	78
38300	02A3 25C3	FDB	\$25C3	INC	7C
38400	02A5 5274	FDB	\$5274	TST	7D
38500 38600	02A7 29B0 02A9 0D92	FDB FDB	\$29B0 \$0D92	JMP CLR	7E 7F
38700	02AB 4EA2	FDB	64EA2	SUB A	80
38800	OZAD ODBO	FDB	8 ODBO	CMP A	81
38900	02AF 4C43	FDB	84C43	SBC A	82
39000	02B1 1862	FDB	81862	FCB	83
39100	02B3 05C4	FDB	805C4	AND A	84
39200	02B5 0934	FDB	80934	BIT A	85
39300	0287 3081	FDB	#3081 ************************************	LDA A	86
39400 39500	02B9 1062 02BB 15F2	FDB FDB	81862 815F2	FCB EOR A	87 88
39600	02BD 0483	FDB	\$0483	ADC A	89
39700	02BF. 3E41	FDB	63E41	ORA A	8A
39800	02C1 0484	FDB	60484	ADD A	88
39900	02C3 8E18	FDB	88E18	CPX	8C
40000	02C5 0A72	FDB	80A72	BSR	8D EXCEPTION
40100	02C7 B093	FDB	\$B093	LDS	8E
40200	0209 1862	FDB	\$1862	FCB SUB A	8F 90
40300 40400	02CB 4EA2 U2CD ODBO	FDB FDB	84EA2 80DB0	CMP A	91
40500	02CF 4C43	FDB	84C43	SBC A	92
40600	02D1 1862	FDB	81862	FCB	93
40700	02D3 05C4	FUB	805C4	AND A	94
40800	02D5 0934	FDB	60934	BIT A	95
40900	0207 3081	FDB	83081	LDA A	96
41000	02D9 4E81	FDB	\$4E81	STA A EOR A	97 98
41100 41200	02DB 15F2 02DD 0483	FDB FDB	\$15F2 \$0483	ADC A	99
41300	02DF 3E41	FDB	63E41	CIRA A	9A
41400	02E1 0484	FDB	80484	ADD A	98
41500	02E3 8E18	FDB	\$8E18	CPX	9C
41600	02E5 1862	FDB	81862	FCB	90
41700	02E7 B093	FDB	8B093	LDS	9E
41900	02E9 CE93 02E6 4EA2	FDB FDB	SCE93 84EA2	STS SUBA, X	9F A0
42000	02ED ODBO	FDB	\$0DB0	CMPA, X	A1
42100	U2EF 4C43	FDB	8 4C 4 3	SECA,X	A 2
42200	02F1 1862	FDB	\$1862	FCB	A 3
42300	02F3 05C4	FDB	805C4	ANDA, X	A 4
42400	U2F5 U934	FDB	\$0934	BITA, X	A5
42500	02F7 3081	FDB	\$3081	LDAA, X	A6
42600 42700	02F9 4E81 02FB 15F2	FDB FDH	\$4E81 \$15F2	STAA,X EORA,X	A7 A8
42900	02FD 0483	F DB	80483	ADCA, X	A9
42900	02FF 3E41	FDB	\$3E41	(IRAA, X	AA
43000	0301 0484	FDB	\$0484	ADDA, X	AB
43100	0303 BEIR .	FDB	68E18	CPX,X	AC
43200	0305 AA72	FDB	SAA72	JSR, X	AD
43300	0307 8093	FDB	SH093	LDS, X	AE
43400 435J0	0309 CE93 0308 4EA2	FDB FDB	SCE93 S4EA2	STS,X SUBA	AF BO
43500	030D 4EA2	FDB	SOUBO	CMPA	b1
43700	030F 4C43	FDB	\$4043	SACA	82
43800	0311 1862	FDB	\$1862	FCB	83
43900	0313 05C4	FDA	\$05C4	ANDA	в4
44000	0315 0934	FPB	80934	HITA	B5
44100	0317 3081	FDB	\$3081	LDAA	86
44200	0319 4581	FDB	\$4E81 \$15F2	STAA	H7
4430U 44400	031B 15F2 031D 0483	FDB FDB	\$15F2 \$U483	EURA ADCA	B9
44500	031F 3E41	FDB	83E41	OHAA	BA .
44600	0321 0484	FUB	\$0484	ADPA	hB
44700	0323 BE18	FDB	\$8E18	CPX	BC
44800	0325 AA72	FUR	SAA72	JSR	HO
44900	0327 5093	FUB	\$8093	LDS	BE
45000	0329 CE93	FDB	5CE93	STS	BF CO
45100 45200	0325 4FA2 0320 0DBu	FDB FDB	\$4EA2 \$0DB0	SUPA	CI
45300	032F 4C43	FDB	6 4 C 4 3	SBCB	C2
45400	0331 1862	FDB	\$1862	FCH	C 3



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16- 49

50-99

100-499

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Assembled & Tested

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16K \$250.00

32K \$275.00

48K \$300.00

64K \$325.00

\$299.00

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EXPANDO RAM II

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RAM BOARD

SBC-100 KIT

SBC-200 KIT

VDB-8024 KIT

KITS

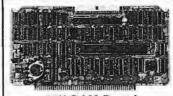
KITS

 A completesingleboard Z-80A CPU with serial/parallel interlace with serialiparallel interlace Fully compatible with the proposed IEEE S.100 Bus Standard 2.80A CPU (AMHz version of lite Z.80) 158 instructions—supersel of and upward compatible from the 8080's 73 instructions.

upward compatible from with compatible was a support of the compatible was a support of the compatible was a support of the compatible with compatible with compatible with compatible with compatible with which compatible with which compatible with compatible was a compatible with compatible with compatible was a compatible with compatible with compatible with compatible with compatible was a compatible with compatible with compatible was a compatible was a compatible with compatible was a compatible was

selectable sub-owalt state insertion selectable auto-walt state insertion or extending MT., MRGQ. IORQ* and/or on board ROM Doubt 8-52 senal I/O ports using the 260A-DART with individual based rates alection (from 50-19, 200baud) - Up 10.2 bit parallel I/O port—fully programmable Intel 2554.

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64K RAM Board

Assembled & Tested Call for Price MICROBYTE

Fully \$100 bus compalible 64K x 8 bit dynamic RAM Low power

*Low power + 8 VDC #: 700 ma + 16 VDC #: 100 ma -- 16 VDC #: 25 ma *Built im-parity with LED indicator and vector interrupt *Memory addressable in four 16K

Memory addressesshanks
shidden refresh
Gold contacts for high reliability
22-hour Burn-n
3/emory mappedvia OIP switch
sbuttin programmable write-protect
Programmable control portfor panly
and bank control

MICROBYTE Ouad RS-232C Senal Ports One 20mA Gurrent Loga Port -Fully IEEE S-100 Bus Compatible -Asynchronous Communications with 280A-Darlitra or Synchronous Communications with 280A-Darlitra or Synchronous Communications with 280A-

with 28th Gaintra or Synchronous Communications with 28th ASI/ODIAN Foll Set of Modern Control Signals, including RI (Ring Indicator) Feasily Configurable Io Any Type of Terminal Interface (IV) Servicing Environments' (I) Policet (IV) Bus Vector, 133/260 Mode 2 Vector Olfsboard Interrupt Capability

2114 L-2/200 NS

\$3.60 ea.
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\$3.00 ea.
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74LS240)	1.25 ea
74LS241	١ ,	1.10 ea
74LS244	١	1.25 ea
74LS373	3	1.25 ea
74LS374	1	1.25 ea
8T245 .		1.45 ea

S.D. SYSTEM / S-100 BOARDS

\$389.00

to within 16M byte nt m

State of the art NECTORN.
State of the art NECTORN.
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Obtained the art Nector and the art Nector art Nector and the
eryto eliminate possible noise operation VCC supply for data reco-operation

VERSAFLOPPY I KIT

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DISK CONTROLLER FOR 8" & 51/4" DRIVES

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NEW DOUBLE DENSITY DISK CONTROLLER FOR 8" &

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Special Receive Conditions:

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450 NS.

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ATARI 800 (NEW 16K VERSION)

2.5 MHz/Z-80 CPU WITH SERIAL

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BOARD WITH KEYBOARD I/O

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& PARALLEL I/O PORTS

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10% OFF SOFTWARE WITH PURCHASE

ATARI SOFTWARE Description Price

Educational Sys. ROM \$19.95

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\$12.95

\$12.95

\$12.95

\$15.95

\$12.95

\$16.95

\$16.95

\$17.95

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\$250.00

\$210.00

NOVATION CAT 300 BAUD, AUTO ANSWER/ACOUSTIC \$149.00 ea. NOVATION D-CAT 300 BAUD/DIRECT CONNECT \$169.00 ea. (OPTIONAL BS232

MODEMS

CABLE \$22.00) **MONITORS**

AMDEK 100 12" B&W \$129.00 SANYO VM5012 12" B&W \$260.00 **AMDEK** 13" Color \$375.00 IN STOCK

1.99 100 Up **14 PIN** .10 .09 **16 PIN** .12 .11 18 PIN .15 .13 20 PIN .23 .21 24 PIN .26 .24 .30 28 PIN .28 40 PIN .40 .38 (BURNDY/TIN SOLDERTAIL)

PRINTERS **CENTRONICS** 737-1

ANADEX DP8000 ANADEX DP9500 ANADEX DP9501 **TEXAS INST 810** BASE 2 800 MST

CALL FOR PRICE & DELIVERY

Z80A Z80A CTC	\$ 8.95 \$ 7.95
Z80A DART	\$13.95
Z80A S10	\$24.95
8255 AC5	\$ 5.95
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320T5	.80	340T5	.70
320T12 .	.80	340T12 .	.75
78L12			.25

RS-232 CONNECTORS

	DB25P	DB25S	
1-9	2.90	3.80	
10.24	2.75	3.70	
25 Up	2.40	3.60	
Data P	hone Ho	od 1.00	

MAIN/FRAME & DISK DRIVE CABINETS from INTEGRAND

MODEL X5 — Desktop Mainframe - 5 Cards - Small Power Supply
Cabinet size 9 4" w x 16" d x 7 5" h Cabinet painted dove grey, front panet is black. No optional colors! 5-position
onlinetroard, 5-connection is raidled, card cage with all guides. Reset switch on front panet Power switch, 4 DBZ5 outsit
1 BMC mounting note, 70CFM 1an, EMI filter, 6' power cord, line fuse, and clamped flat cable exit on rear panet. PX/5
power supply (4-Bg)0/A. - 16glof 5A. - 16glof 5A) Power supply (1 as removable module)

M DDEL 700D — Horizontal besktop Disk/Cover – 2 Eight Inch Drives – Drives Horizontal Cabinet size 20" w x 23" o x 7 5" h. Cabinet janiet dove givey, frost panel to black. Mounting for 2 eight-inch Shejar SASOIR Filoppy bis Drives for mechanical equivalent). Drive mounting brackets supplied. Drives not supplied 70CFM lan 6" increwire inc cord, pover switch, line tuse. EMI filter and ctamped flat cable exit on rear panel. P794 power supply; 4-594A, -24:895A—6A peak. "509 75A. All voltages regulated Power supply; 4-594A.

HODEL 8000 — Desktop Main/Frame - 15 cards - Standard Power Supply is a limitation indust.

2255
Cabinel size: 17" w x 20.5" d x 7.5" h. Cabinel paniled dove grey. Front panel is black (other color schemes optional).
15-postion IEEE compatible motherbase'r evil accept 10th in terminatorists, logicianal, card cage with all guides. Reset switch on front panel. Power switch, 50825 cutous, 2 8HC mounting hotes, 70CFM tail, EMI tiller, 6" power cord, line lists, and clamped that cable sail on rear panel. Pool power supply (+8@15A, +16@3A, -16@3A). Power supply is a removable module Motherbased connections optional

MODEL, 70005 — Vartical Desktop Disk/Cover – 2 Eight Inch Orhee - Orhee Vertical
Cabend stor. 13.5" w. x2" d.x 11"h. Cabend stor. 2 Eight Inch Orhee - Orhee Vertical
Cabend stor. 13.5" w. x2" d.x 11"h. Cabend plated dover grey, Kont panel is black. Mooaling for 2 eight-inch Shepart
SABOR Floppy Rock Orivor. 5 or mechanical on, redens). Over moonthing bracker's suppled Drives net supplied 705FM [a. 6]
E Thise-wire land cord, power switch, line true. Eith filter and champed fait cabe sci on sear panel. P794 power supply
+5gA4. x. x245A—56 pase. 75g. 75a. All violations regulation Provide supply is a membrable module.

ATARI OPTIONAL ACCESSORIES

MODEL	# DESCRIPTION	F	PRICE
810	Disk Drive System	\$	499.00
815	Disk Drive System	\$	1199.00
820	40-col. Dot Matrix Printer	\$	349.00
822	40-col. Thermal Printer	\$	349.00
825	80-col. Dot Matrix Printer		750.00
830	Acoustic Modem		159.00
850	Interface Module		175.00
CX853	16K RAM Module		140.00
410	Cassette Recorder	\$	60.00

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- Double-sided/Single-Double Density •IBM-compatible/1.2 Mbytes/Disk
- •Fast 3 ms. Track to Track
- •154 Tracks/Daisy Chain 4 Drives
- •ISO Standard Write Protect Programmable Door Lock

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DISKETTES	Part #	Price Box of 10
Verbatim 51/4" (soft)	MD525-01	\$26.50
Scotch 51/4" (soft)	744-0	\$33.00
Scotch 5 1/4" (10-sec)	744-10	\$33.00
Scotch 5 1/4 " (16-sec)	744-16	\$33.00
Memorex 51/4" (soft)	3421	\$24.00
Scotch 8" DS (soft)	743-0	\$49.95
Maxell8" DS/DD	FD-2D	\$65.00

SHUGART **SA 801R**

8" Sgl.-Sided, Sgl./Dbl. Density Disk Drive Call for Price & Delivery

New 16K RAM MODULE FOR ATARI 800 Computer Systems

> \$89.95 each 1 YEAR WARRANTY

```
Listing 2 continued:
45500
         0333 05C4
                                      FDB
                                               $ U5C4
                                                                        ANDB
                                                                                  C4
                                                                                  C5
45600
         0335 0934
                                      FD8
                                                                        6118
45700
         0337 3081
                                               s3u81
                                      FUB
                                                                        LDAB
                                                                                  C6
45800
         0339 1862
                                      FOR
                                               $1862
                                                                        FCB
45900
         0338 15F2
                                      FDB
                                                $15F2
                                                                        EORB
                                                                                  C 8
46000
         0330 0443
                                      FOR
                                               SU483
                                                                        ADCR
                                                                                  C9
         033F 3E41
46100
                                      FUH
                                               $3E41
                                                                        ORAR
                                                                                  CA
46200
         U341 U444
                                      FDB
                                               60484
                                                                        ADDH
                                                                                  Сb
46300
         0343 1862
                                               $1862
                                      FUB
                                                                        FCB
                                                                                  CC
46400
         0345 1862
                                      FDB
                                               $1862
                                                                        FCB
                                                                                  CD
46500
         0347 8098
                                               SHU98
                                                                        LOX
                                      FUB
                                                                                  CE.
46600
         0349 1862
                                      FDB
                                               $1862
                                                                        FCB
                                                                                  CF
46700
                                                                        SUBB
         UJ4H 4EA2
                                      FOR
                                               S4EA2
                                                                                  DO
46800
         0340 00B0
                                               SODBO
                                                                        CMPB
                                      FUB
46960
         U34F 4C43
                                      FIDE
                                               84C43
                                                                        SHCB
                                                                                  D2
                                                                        FCB
47060
         0351 1862
                                      FDH
                                               51862
                                                                                  D3
47100
         0353 0504
                                      FDB
                                               505C4
                                                                        ANDB
                                                                                  114
47200
         0355 0934
                                      FDB
                                               50934
                                                                        BITB
                                                                                  05
47 300
         0357 3081
                                      FDB
                                                                        LDAN
                                               $ 3081
                                                                                  06
47400
         0359 4EB1
                                      FUB
                                                                        STAB
                                                                                  D7
                                               84E81
47500
         0358
               15F2
                                      FDB
                                               $15F2
                                                                        EORB
                                                                                  D8
                                                                                  D9
47600
         U35D 0483
                                               50483
                                                                        ADCH
47700
         035F 3E41
                                      FDB
                                               $3E41
                                                                        URAB
                                                                                  DA
47800
         0361 0484
                                      FOR
                                               $0484
                                                                        ADDB
47900
         0363
              1862
                                      FOR
                                               $1862
                                                                        FC
                                                                                  DC
48000
         0365 1862
                                      F Dis
                                               $1862
                                                                        FCB
45160
         0467 8498
                                      FDB
                                               SHU98
                                                                        LDX
                                                                                  DE
         0364 CE4H
48200
                                               SCE98
                                      FDB
                                                                        STX
                                                                                  DE
48300
         0368
               4EA2
                                      FOR
                                               S4EA2
                                                                        SUBB, X
                                                                                  EU
46400
         0360 0080
                                               SOURO
                                      FDE
                                                                        CMPB, X
                                                                                  £1
                                                                        SBCB, X
48500
         036F 4C43
                                      FOB
                                               84643
46600
         0371 1862
                                      FOB
                                               $1962
                                                                        FCB
                                                                                  E3
                                               605C4
48700
         0373 05C4
                                      FDB
                                                                        ANDB . X
                                                                                  F: 4
                                      FOB
                                                                        BITB,X
48860
         0375 0934
                                               50934
                                                                                  ₹5
46900
         0477 3081
                                      FOR
                                               SBURT
                                                                        LDAH.X
                                                                                  F. 6
49000
         0379 4EA1
                                      FDB
                                               S4EH1
                                                                        STAB.X
                                                                                  E7
49100
         0378 15F2
                                               $15F2
                                                                        EORB, X
                                      FDB
                                                                                  €8
         U37D U483
49260
                                               SU483
                                                                        ADCB . X
                                                                                  E9
49300
         037F 3E41
                                               83E41
                                                                        ORAH, X
                                      FOB
49400
         0381 0484
                                      FOR
                                               50484
                                                                        ADDB , X
49500
         0383 1862
                                      FDB
                                               $1862
                                                                        FCB
                                                                                  FC
49600
         0385 1862
                                      FOB
                                               $1862
                                                                        FCB
                                                                                  R.D
49700
         0347 BO98
                                      FDB
                                               SBU98
                                                                        LDX . X
                                                                                  ME
49800
         0389 CE96
                                      FOB
                                               SCE98
                                                                        STXAX
                                                                                  F.F
49900
         0388 4EA2
                                      FUB
                                               S4EA2
                                                                        SUBB
         038D 0D80
50000
                                      FUB
                                               SOUBO
                                                                        CMPB
         038F 4C43
                                      FUB
                                               54C43
                                                                        SBCB
                                                                                  F 2
50100
50200
         0391 1862
                                      F'DB
                                               $1862
                                                                        FCB
56300
         0393 0504
                                      FDB
                                               505C4
                                                                        ANDB
50400
         0395 0934
                                      FDH
                                               50934
                                                                        BITH
                                                                                  F 5
50560
                                      FOB
                                               S3081
                                                                        LDAB
                                                                                  Fb
                                      FDB
         11399 4E81
                                                                        STAB
50600
                                               S4E 81
                                                                                  F 7
50700
         039B 15F2
                                      FDB
                                               515F2
                                                                        ERRE
50800
         0390 0483
                                      FDB
                                               S0483
                                                                        ADCB
                                                                                  F 9
50900
         039F 3F41
                                      FDB
                                               53E41
                                                                        URAH
                                                                                  FA
                                                                                  FC
51000
         U3A1 0484
                                               50484
                                                                        ADDB
                                      FDB
51100
         03A3 1862
                                      FDB
                                               $1862
                                                                        F.CR
51200
51300
         03A5 1862
03A7 8098
                                                                                  FD
                                      FOR
                                               $1862
                                                                        FCB
                                      FDB
                                               SH098
                                                                        LDX
                                                                                  F €.
                                      FDB
         03A9 CE98
                                                                        STX
                                               SCE98
51400
51500
                                      FIRMAT FLAGS
51600
51700
                             FLAGS ARE I BYTE DUNG AND ARRANGED IN PAIRS.
                         *
51800
5190u
                            mil: 75543210
                                  PHOFFFSS
52000
                            WHERE: HER REGISTER CODE, USBORE, 1SA, 2SB
FFFS ADDRESS MODE, OSLEHFRENT, 1SHELATIVE, 2STADEXED
52100
52200
                                                           BETOMORIATE, 4=EXTENDED, 5=01Pt.C1
b= NOGE (FCo PSEUDO)
52306
52400
52500
                                     85= SIZE OF INSTRUCTION IN HYTES.
                         .
52606
52700
         UBAR UT
                         FTAR
                                      FULL
                                               501
                                                                        ODMOF INHERENT
         USAC UD
52800
                                      FCB
                                               v
                                      FCH
                                                                        10-1F INNERENT
                                               51)1
52400
         O SAD UI
                                      FCB
53000
         UJAE UO
53100
         USAF UB
                                      FCB
                                               506
                                                                                 REDATIVE (USED HY 6SR)
                         HSK
                                      FCH
53200
         0350 00
         03H1 01
                                               Sul
                                                                        30-3F
                                                                                INHERENT
53300
                                      FCH
                                                                        PULA AND PSHA
40-4F INHERE
53400
         0362 41
                                      FCB
                                               541
                                                                                INHERENT
53500
         0383 41
                                               $41
53600
         0384 00
                                      F'CB
                                      FCB
                                               $81
                                                                        50-5F INHERENT (USEU AY PULB)
53700
                         PULB
         0385 81
53800
                                      FCB
         0386 00
53400
         AU Tatu
                                      FCB
                                               SUA
                                                                        60-6F INDEXEXED
54000
         0388 00
                                      FCH
54100
         U369 13
                                      FCB
                                               513
                                                                        70-7F EXTENDED
54200
         USBA UG
                                      FCB
                                                                        H 0 - HF
                                               5 4 F
                                                                                              IMMEDIATE
543011
         U 385 4E
                                      FCA
         OJHC OF
                                      FCB
                                                                        CPX, LDS
54400
                                               SUF
```



NOT ANYMORE!

No this isn't a "Hard Disk". We used to call it that, sometimes. But somebody muddled the water.

"Hard Disk", unfortunately, now calls something else to mind. That little bitty guy with no backup capability and no way of switching media? It's a "Hard Disk" to work with, all right, in business applications. Some even say "Impossible Disk".

We'd like to avoid confusion between our Cameo database solution and the one that doesn't work so well. The Cameo DC-500 subsystem employs a decade-proven **cartridge** disk. Our backup capability is built in, and takes four minutes. The ability to switch applications (by exchanging the removable cartridge) means you can use your computer for more kinds of work. A ten megabyte (5 fixed + 5 removable) subsystem costs \$5995, for your **TRS-80*** (Mod. I or II), Apple*, Heath H89^{T.M.} or S-100 computer.

So call us "The Cartridge Disk Guys", please, and call us soon. We'll show you the really cost-effective solution to microcomputer database storage.



^{*} TRS-80 is a registered trademark of the Tandy Corp. *Apple is a registered trademark of Apple Corp.

Listing	2 contin	uod.					
54500	u38D			FCH	850	90-9F	DIRECT
54600	UBBE			FCB	516	CPX, LDS, STS	DIMECI
54700	UBBF			F.CH	54A	AO-AF	INDEXED
54800	03Cu			FCB	SUA	CPX, JSR, LDS, STS	INDEXED
54900	0301			FCH	\$53	60=BF	EXTENDED
55060	0301			L.C.R	S13	CPX.JSP.LDS.STS	EATENDED
55100	03C3			FCB	SHF	CO-CF	IMMEDIATE
55200	U3C4			FCB	SUF	LOX	IMMEDIATE
55306	03C5			FCB	5 y b	DO-OF	DIRECT
55400	0305			FCB	S16		DIRECT
55500	0307			FCB	SHA	LDX, STX	2. INC.455
55600	0307			FCB		RO-EF	INDEXED
55700	0309				SOA	LDX, STX	
				FCB	593	F.O = F.E.	EXTENDED
55800	UJCA	1.5		FCH	s 1 3	I,DX,STX	
55900			*	F1	- 4.0		
56000	OBCB	19	FFLAG	FCB	s19	FCB FLAGS	
56100			*				
56200	AO4A			UPG	SAU4A		
56300	AU4A		LINE	RMA	32		
5640C	AUDA			FCB	0.4		
56500		AUDB	WORKA	ស្នប	*		
56600	AU6B	CF DIAB		+ LDX	AMTAB	MNEMUNIC TABLE HA	SE
56700		AUBC	WTAD	EQU	FLAGD+1		
56800	AOOE.	CE 03AB		LDX	#FTAB	FLAG TABLE BASE	
56900		AübF	WFAD	EUU	WHTYT+1		
57000		0000	WHAS	FDB	O		
57100		0000	WFLG	FDB	0		
57200		0000	XSAV	FDB	0		
57300	A077	00	LINES	FCB	U		
57400		FO	BYTE	ROO	wBYT=kURKA		
57500		01	TAU	EQU	wTAD=wUKKA		
57600		Ü6	BASE	Ersti	wBA5=*URKA		
57700		04	FAD	EQU	WF'AD=WORKA		
57800		6.9	FLAGA	FOU	WFLG-AURKA		
57900		AU4A	AADR	EQU	LINE		
58000		A058	UPER	EWU	LINE+13	MNEMUNIC PUSITIO	N
58100		A 1163	ABS	EUU	LINE+24	ABS. ADDRESS FOR	RELATIVE MODES
58260		AUSE	ARG	EQU	LINE+19	ARGUMENT POSITION	
56300	A078	00 .		FCB	0,0,G		
58400	AU7B	0υ ,	SIZE	FCB	0		
58500		0000	APPND	FDB	60	COMPLETION APPEND	AGE ADDRESS
58600				END			



Listing 3: Cross-references for symbols used in the disassembler source code of listing 2.

	=					
APP	=01A4	QUUE				
APPND	=AU7C	0011	0116			
ASC	=0198	0192				
HLUP	=0044	0048				
BSR	=03AF	009B				
CLOP	=00C9	0000				
CVASC	COODF	0063	SHOO	OURE	9000	
DISPL	=0116	0105	0144	0154	0167	
Droub	=017F	1117C	0184			
DUMV	■0114	015B				
ERASE	≈0169	0.008				
FCBFR	=015D	0112				
FFLAG	=03CB	0070				
FIN	=0123	OLIE				
FLAGD	=A06B	OJAA	00C0	00E5	OOFE	
FORM	=00FB	OGF1	00F5			
FTAB	=03AB	AU6E				
IMM	=0156	UIUE				
IND	=014A	0168				
LINE	= AUGA	0030	0051	0161	0174	01A4
LINES	=A077	0005	0118			
MTAB	=01AB	AU6B				
NEXTL	=0018	0 4 1 4	0120			
MFC	#00B2	0u78				
NMR	=00E2	0001				
NOTA	=00F3	UNEA				
OFF	=00A8	0080	008C	0095	009E	UDAS
OU	=01A0	019C				
POS	=0130	0120				
PULB	=0385	6:189	0092			
REL	=0124	0108				
SETM	=016 D	0114	0124	014A		
SETR	=00EE	COFS				
SIZE	≈AO7B	UOC5	0177			
START	■00 00	0016				
TBSR	=009/	0090				
TOASC	=018C	OUDF	0130	0141		
TPSH	=008F	0087				
WBAS	=A071	0054				
WBYT	*A06E	OUBA				
WFLG	■ A O 7 3	CADO	0171	0186		_
XSAV	=A075	UUC9	Oube			

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Build a Super Simple Floppy-Disk Interface, Part 1

James Nicholson and Roger Camp 1046 Gaskill Ames IA 50010

For personal-computer users, a floppy-disk system represents the ultimate in mass storage because of its speed and capacity. The floppy-disk controller described in this article provides all the capabilities found in commercial systems, yet it is simple and economical because it requires only ten integrated circuits. Fundamental software will be provided (in the second part of this article) to control and perform data transfers, and discussion of file structuring and alternate hardware will give the experimenter ideas for improvements.

This system uses the FD400, an 8-inch floppy-disk drive manufactured by the Pertec Computer Corporation, and the popular Western Digital 1771 floppy-disk controller integrated circuit (which allows such special features as variable block size, soft sectoring, IBM compatibility, and much more). Although the specifics shown are for microcomputers based on the MOS Technology 6502 microprocessor, the controller could be adapted to other microprocessors with some care at a few crucial

points. The 6502 offers some speed advantages and a programming ease not afforded by the others.

Fundamentals

The data recorded on floppy disks is logically arranged in concentric rings called tracks, with each track composed of blocks of data called sectors. The computer must be able to

This controller is simple and economical because it requires only ten integrated circuits.

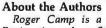
tell where a sector begins, and there are two ways of doing this. Each sector can be distinguished by its position relative to holes punched in the disk (this is called hard sectoring), or it can be distinguished by special sequences of information recorded on

the disk (soft sectoring). In either case, the disk has one hole that is used as an index to signal the start of the first sector on all tracks.

The most common 8-inch floppydisk format provides for 77 tracks of 26 sectors each, with 128 bytes recorded in each sector. The address of each sector, in the form of a track number (0 through 76) and a sector number (1 through 26), is recorded on the disk at the start of the sector itself.

The disk drive has two motors: one that spins the disk at 360 rpm (revolutions per minute), and one that moves the head from track to track on command. Each drive also has a printedcircuit board to control both motors. The inputs and outputs of this circuit board (see figure 1) follow a standard set by Shugart Associates, manufacturer of one of the first popular floppy-disk drives.

A single pulse on either the STEP-IN line or the STEP-OUT line moves the head one track toward the center of the disk (track 76) or toward the



Roger Camp is a Professor of Electrical Engineering at Iowa State University. He is the author of several technical papers and patents, and his most recent book is Micro-Processor Systems Engineering.

James Nicholson, currently Project Manager, Business Recovery Planning, has been involved in large data-center activities for Donnelley Marketing. He has designed and built several microcomputer systems in the last five years.

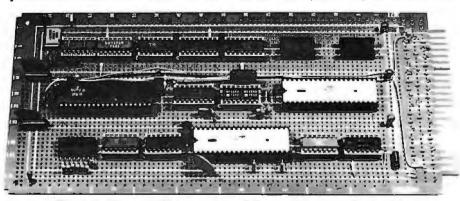


Photo 1: The authors' wire-wrapped floppy-disk controller board.



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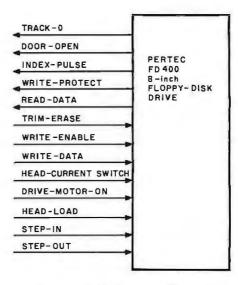


Figure 1: Input and output lines available for controlling a Pertec FD400 8-inch floppy-disk drive. These signals are the same as those found on any Shugart-compatible drive, so nearly any drive may be substituted for the FD400.

outside (track 0), respectively. When the head is positioned over track 0, the outermost track, the TRACK-0 output is activated. To turn on the spindle motor, the DRIVE-ON input must be activated, and the disk door in the front of the drive must be closed (this deactivates the DOOR-OPEN output line). As the disk rotates, a photoelectric sensor in the drive detects the index hole in the disk; this generates the INDEX signal that allows the system to begin counting sectors at the first one.

To read data, the HEAD-LOAD line is activated to force the head to contact the rotating disk surface. A mixture of data and clock bits are then detected and amplified by the drive's electronics; these appear as logic levels on the DATA-READ output at the rate of 250 K-bits per second.

To write data on the disk, the head must be loaded, the WRITE-ENABLE line must be activated, and the data must be sent to the drive on the WRITE-DATA line. (This must occur with very specific timing.) If the WRITE-PROTECT output has been activated, the drive has detected the presence of a write-protect notch in the disk's envelope.

Obviously, communication at this level between a disk drive and a microcomputer is possible but not desirable. The microcomputer would spend much of its time catering to the needs of the disk rather than computing. The purpose of the FD1771 (actually a microprocessor in its own right) is to act as a high-level com-

munications interface between the two.

When instructed to seek (move the head) to track 30, the 1771 will generate the appropriate number of STEP-IN or STEP-OUT pulses to move the head from its current position, wherever it may be, to track 30. Another example of the 1771's capabilities is the process of reading a specific sector: the 1771 will search a given track for the proper sector address; when located, the data following the address is transferred to the microprocessor. Simultaneously, the 1771 can maintain synchronization with the disk drive and check for errors. Therefore, using the 1771 floppy-disk controller circuit results in a greatly simplified hardware and software design.

Software must be an integral part of the design of any computer subsystem—a subroutine of about 256 bytes is required to communicate the proper commands to the disk controller. Additional software is required to handle complex data-file structures (this software and various structuring techniques will be discussed in part 2 of this article).

Disk Format

Figure 2 schematically describes the format of recorded data on a soft-sectored disk. The pulse generated by the index hole passing the sensor provides a physical reference point to determine the beginning and the end of a track. The diagram represents 16 256-byte sectors (the authors' choice for format) rather than the usual 26

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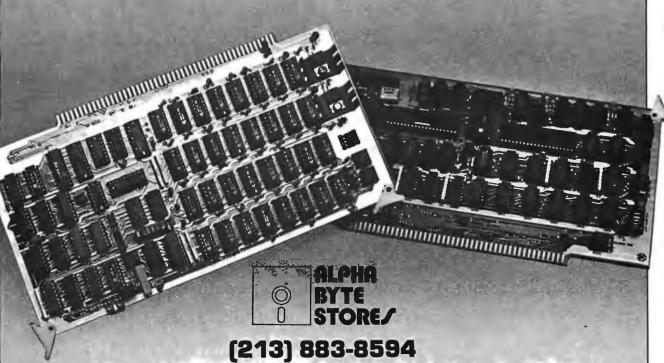
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sectors containing 128 bytes.

The disk rotates once every 166.67 ms, which allows the drive to read 41,665 bits of information; that is, a byte every 32 μs. Each track contains 5208 bytes (divided into data and control bytes), as well as gaps between sectors. (The gaps are required to allow sufficient time to turn writehead current on and off without destroying valid data.)

The IAM (index-address mark) that provides a recorded indication of the beginning of the track has 16 sectors recorded after it. The sectors consist of two records: the ID (identification record) and the DATA (data record). The ID contains information on the track number and the sector number of the DATA that follows. Each of the records begins with an AM (address mark). In addition, each record is ended with a 2-byte CRC (cyclicredundancy-check) code.

Each byte of data recorded on the disk consists of interleaved clock and

data bits. The clock bits convey information used for synchronization and for the identification of AMs. AMs always have clock bits corresponding to hexadecimal C7 (D7 in the case of the IAM); all other bytes of information have clock bits corresponding to hexadecimal FF. In other words, some clock bits are omitted in AMs. This scheme allows the data bits of a dataaddress mark (hexadecimal FB) to be distinguished from a hexadecimal FB recorded as data.

Figure 2 also illustrates that these data and clock bits are recorded as a single stream. When reading from the disk, the 1771 separates the data and clock bits (although our system uses discrete components to achieve greater reliability).

As a general rule, the larger the sector, the greater the total amount of data that can be recorded on one disk. This is due to the reduced amount of area necessary for gaps and indexing information. Using 16

256-byte sectors, 315,392 bytes of data can be recorded. The usual configuration of 256-byte sectors allows tracks with only 15 sectors; however, it has been found that sufficient space is available to reliably record 16 sec-

Western Digital's 1771 Floppy-Disk Controller

This device is essentially a microprocessor dedicated to the specific task of controlling disk drives (see figure 3). It has five programmable registers and accepts a number of commands through various combinations of them. For economic reasons, there is a desire to connect multiple drives to a single 1771, but, since the device "remembers" the track the head was last positioned to, switching from one drive to another would place an added burden on the driving software. A case can be made for complete duplication of the controller electronics for each disk drive.

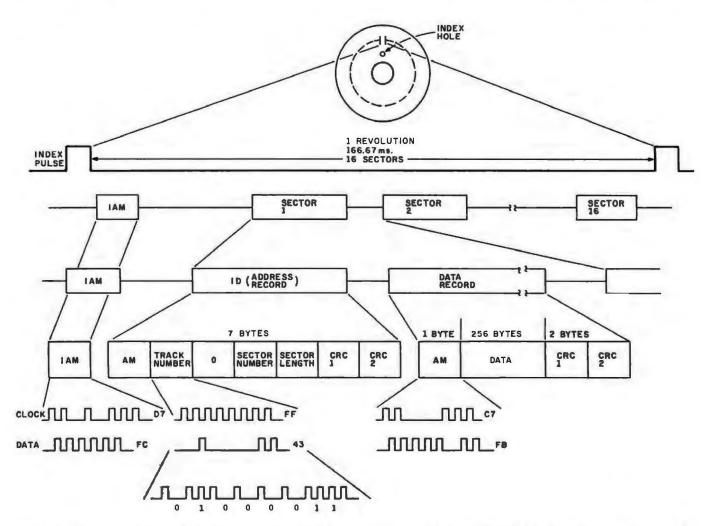


Figure 2: The format of recorded data on one track of a soft-sectored floppy-disk drive. The IAM (index-address mark) marks the beginning of each track. See the text for details.

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The registers in the 1771 that can be programmed by the user are the data, track, sector, and command registers-there is also a status register that can be read from but not written to. These 8-bit registers form the basis for software control of any disk drive:

•Data register: In disk-reading operations, this register receives 8 bits of data in parallel from the disk via the shift register. The data is held until the computer can accept it, allowing the shift register to be ready for the next byte. During disk-writing

COMMAND

DATA OUT BUFFERS

TRACK

SECTOR

operations, 8 bits of data are transferred in parallel from the computer to this register and held until they can be accepted by the shift register for transfer to the disk. When executing the seek command, the data register holds the address of the desired track. Track register: This register holds the track number of the current head position. The value is incremented by one for every track the head is stepped in (toward track 76), and decremented by one for every track the head is stepped out (toward track 0). The contents of the register are compared with the track number recorded

in the ID field of sectors on the disk.

 Sector register: During read or write operations, the contents of this 8-bit register are compared with the sector number recorded in the ID field of sectors on the disk. The contents should not be changed while the device is busy.

 Command register: This register holds the command currently being executed. The register should not be loaded while the 1771 is busy unless the current command is to be overridden (this action causes an interrupt to be generated). The eleven commands understood by the 1771 are divided into four types, shown in table 1, according to the way their flag bits are

• Status register: Information about the status of the controller can be read from this register. The meaning of the status bits may change depending on the current command.

defined.

STATUS

Registers are accessed by placing the proper logic levels on the A0, A1,

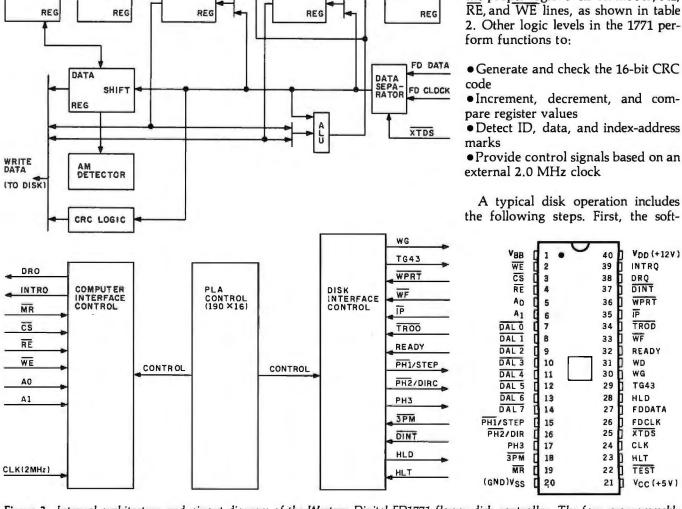


Figure 3: Internal architecture and pinout diagram of the Western Digital FD1771 floppy-disk controller. The four programmable registers and eleven commands of the 1771 allow any microprocessor to control a disk subsystem using high-level instructions, thus removing a significant burden from the disk-driving software. See table 1 for a summary of the commands.

DATA



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TYPE	COMMAND Restore Seek Step Step In Step Out Read Command Write Command Read Address Read Track Write Track Force Interrupt	BITS 7 6 5 4 3 2 1 0 0 0 0 0 h V r1 r0 0 0 0 1 h V r1 r0 0 0 1 u h V r1 r0 0 1 0 u h V r1 r0 0 1 1 u h V r1 r0 1 0 0 m b E 0 0 1 1 1 u h V r1 r0 1 0 0 m b E a1 a0 1 1 0 0 0 1 0 0 1 1 1 0 0 1 0 0 1 1 1 1	
$\begin{array}{c} h=0, \ \text{Do no} \\ V=\text{Verify flag} \\ V=1, \ \text{Verif} \\ V=0, \ \text{No v} \\ r_1r_0=\text{Steppin} \\ r_1r_0=11 \ \text{gi} \\ u=\text{Update flag} \end{array}$	d flag (Bit 3) head at beginning of load head at beginning (Bit 2) y on last track erify g motor rate (Bits 1 through 0) wes 40 ms step time ug (Bit 4) tie track register	BIT VALUES FOR TYPE II m = Multiple Record flag (Bit 4) m = 0, Single record m = 1, Multiple records b = Block length flag (Bit 3) b = 1, IBM format (128 to 1024 bytes) b = 0, Non-IBM format (16 to 4096 bytes) a ₁ a ₀ = Data Address Mark (Bits 1 through 0) a ₁ a ₀ = 00, FB (Data Mark) a ₁ a ₀ = 01, FA (User defined) a ₁ a ₀ = 10. F9 (User defined) a ₁ a ₀ = 11, F8 (Deleted Data Mark)	
(b)	(c)	
s = 0, Syr	nize flag (Bit 0) chronize to Address Mark not synchronize to Address Mark	BIT VALUES FOR TYPE IV Io thru Io = Interrupt Condition flags (Bits 3 through 0) Io = 1, Not Ready to Ready transition Io = 1, Ready to Not Ready transition Io = 1, Index pulse Io = 1, Inmediate interrupt E = Enable HLD and 10 ms Delay E = 1, Enable HLD, HLT and 10 ms delay E = 0, Head is assumed engaged and there is no 10 ms delay	
		(e)	

Table 1: The high-level instructions of the FD1771 disk formatter/controller device. When one of the instructions defined by table 1a is loaded into the command register of the FD1771, the FD1771 executes one or a series of actions. Bits represented by a letter within a command are defined in the bit-value tables for that type of instruction, tables 1b through 1e.

ware coordinating the disk operation checks to see if the controller is busy from the last command. If it is not, the software writes the desired command into the command register. If data is to be transferred as each byte is assembled (or disassembled) by the shift register, the controller sends a DRQ (data request) signal. When the

operation is completed, the controller sends an INTRQ (interrupt request) signal. The status register can then be checked by the controlling software for seek, write protect, busy, or CRC errors.

Controller Hardware

The schematic diagram for the

A1 0	A 0	Register Affected During Read (RE = 0, WE = 1) Status Register	Register Affected During Write (RE = 1, WE = 0) Command Register
ŏ	1	Track Register	Track Register
1	Ó	Sector Register	Sector Register
1	1	Data Register	Data Register

Table 2: Access to registers within the Western Digital FD1771 disk formatter/controller device. The FD1771 has five internal registers: command, data, sector, status, and track. A given register is read or written by placing the appropriate values on lines A1 and A0 and pulling down either the READ-ENABLE (RE) line for a read operation, or the WRITE-ENABLE (WE) line for a write operation. The sector and track registers specify the sector and track when these parameters are needed by a given command byte. The command register, when filled, causes one of eleven highlevel instructions to be executed (see table 1). Data passes between the computer and the disk drive through the data register. After a command has been executed by the FD1771, the status register must be read before another command can be executed.

floppy-disk controller is given in figure 4. In addition to the 1771 and the 6520 PIA (peripheral interface adapter), circuitry is included for read/write control, clock and data bit separation, head loading, and inversion of various signals as required by the FD400 disk drive.

Three gates convert the DIR (direction) and STEP signals from the 1771 into the STEP-IN and STEP-OUT signals needed by the FD400 disk drive. The HEAD-LOAD signal is conditioned by a simple one-shot (monostable multivibrator) and an inverter; this guarantees a fixed 40 ms pause allowing the head to load and settle. Once the interval has passed, a signal is sent to the 1771 to acknowledge the fact.

The data-separator and clock circuit was designed by Steve Christiansen of Iowa State University. This circuit contains four of the ten integrated circuits in the system. (If the disk drive you intend to use has sepa-

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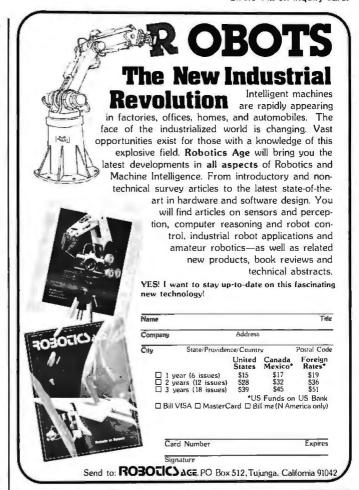
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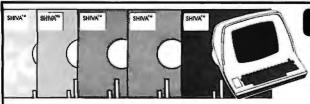
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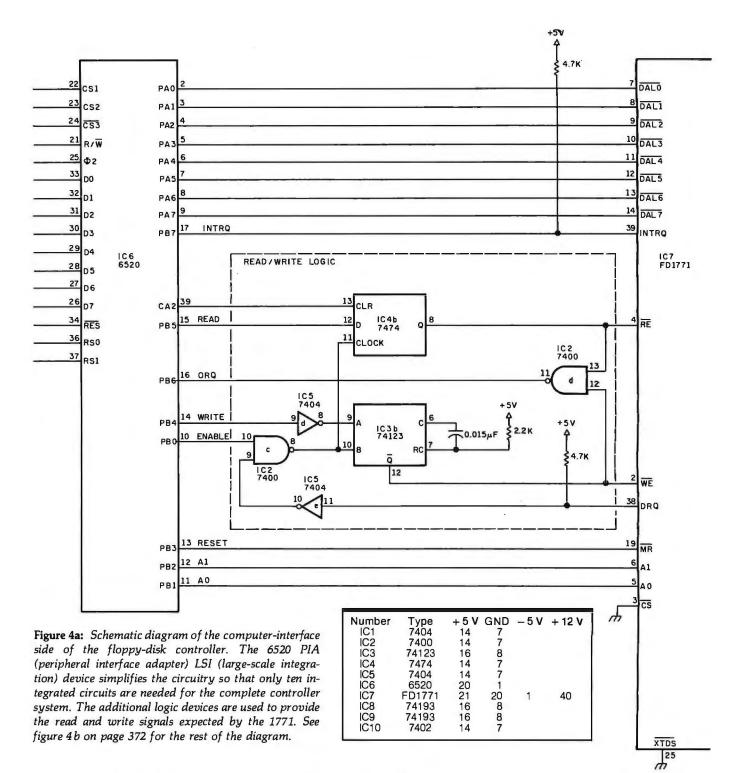
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rated clock and data signals, you may be able to eliminate some of the circuitry shown. Remember that the 1771 requires a 2.0 MHz clock.)

The clock part of this circuit is a conventional TTL (transistor-transistor logic) crystal oscillator which also drives a divide-by-two stage to produce the 2.0 MHz clock signal. The data-separator part of the circuit inverts the raw signal from the disk drive and gates it out as data or clock information, depending on the state

of the QD output of IC9.

There is a certain difficulty in determining, from a serial-bit stream, which bits are clock and which data (the two are interleaved, and some of the clock bits may be missing). The solution relies on the fact that, at most, three clock pulses will be omitted; if four in a row are omitted, the data and clock outputs are switched by the external data-separator circuit.

The read/write circuitry is very compact and plays a major role in the

simplicity of the system. It is a subtle solution to a timing problem; the obvious approach of using the outputs of the 6520 to control RE and WE (the read- and write-enable lines) as input for the DRQ (data-request line) is too slow. The indicated circuitry using the ENABLE line causes each DRQ signal to automatically generate another RE or WE signal as required.

The 6520 has 20 programmable I/O (input/output) pins (see figure 5),

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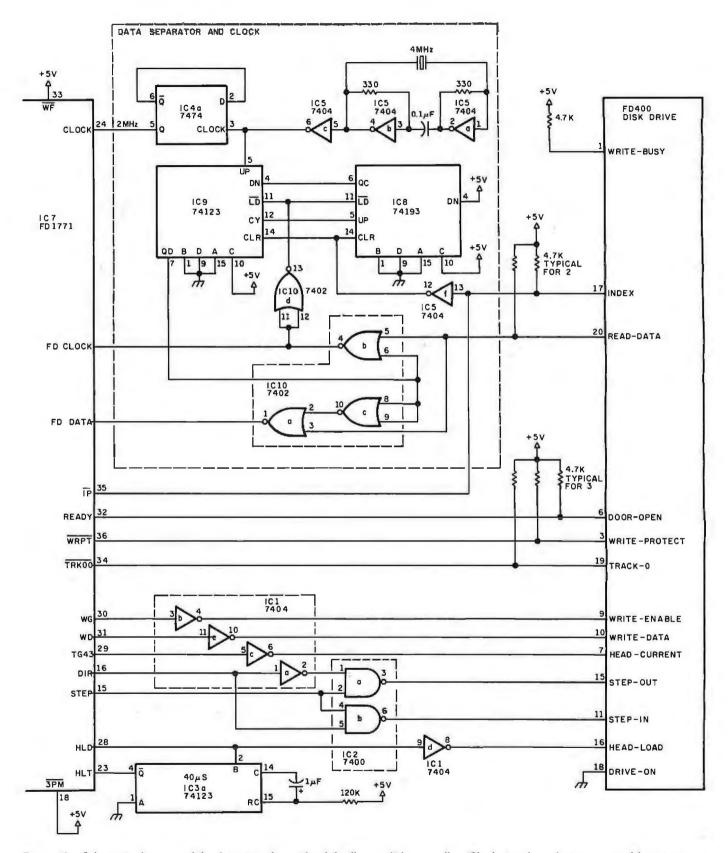


Figure 4b: Schematic diagram of the drive-interface side of the floppy-disk controller. Clock signals and minor control functions are provided for by the additional circuitry, as well as the separation of recorded data from recorded synchronization pulses.

of which only 17 are used in this system to interface with the 1771. The A port is programmed as eight bidirectional data lines, and is connected to

the 1771's data lines, while the B port pins are programmed as necessary to provide control lines. The data lines of the 6520 can be connected to like lines on the microprocessor, while its three device-select lines can be connected to match whatever addressdecoding scheme is appropriate. The

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0	0	X 0	0	X	Read or Write, DDRA Write into ORA			
0	0 1	1 X	1 X	×	Read or V			
1	0	X O	X X X	0 1	Read or Write DDRB Write into ORB Read from B-side input pins Read or Write CRB			
1	0	1 X	X	1 X				
X = d	on't care							
		Contro	l Register	Bit Desig	nations			
	7	6	5 4	1 3	2	1 0		
CRA	IRQA1	IRQA2	CA2 C	ontrol	DDRA Access	CA1 Control		
CRB	IRQB1	IRQB2	CB2 C	Control	DDRB Access	CB1 Control		

Control	of	CA2	Output	Modee

Bit 5	CRA Bit 4	Bit 3	Mode	Description
1	0	0	"Handshake" on Read	CA2 is set high on an active transition of the CA1 interrupt input signal and selow by a microprocessor "read A data" operation. This allows positive control of data transfers from the periphera device to the microprocessor.
1	0	1	Pulse Output	CA2 goes low for one cycle after a "reac A data" operation. This pulse can be used to signal the peripheral device that data was taken.
1	1	0	Manual Output Manual Output	CA2 set low CA2 set high

Table 3: Control codes for the 6520. This device offers 20 pins that may be programmed (either individually or in groups) as input, output, or bidirectional lines.

6520 controls and modes are listed in table 3.

Construction Notes

The prototype floppy-disk controller was built on a Vector 3677 wire-wrap board (see photo 1). There are no special layout considerations, but adequate power supply bypassing must be observed (i.e., 0.1 µf capacitors across the supply and ground pins of each integrated circuit). A 16-pin DIP (dual in-line package) socket is used to connect the controller to a ribbon cable from the disk drive (use proper terminations).

Debugging

The read/write circuit can be debugged by using a microcomputer. Move the DRQ input (IC5, pin 11 in figure 4) from the 1771 to a convenient 6520 output. With the microcomputer running a diagnostic program, check to see that the WE pulse (IC3, pin 12 in figure 4) is about 14 µs.

The data separator can be checked by using a single-pulse input signal in

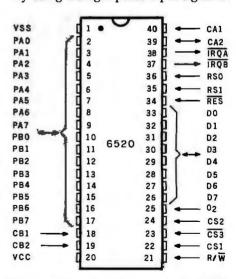


Figure 5: Pin description of the MOS Technology 6520 PIA. Use of this particular device allows easy interfacing of a disk controller to a 6502-based computer. One I/O port handles control signals; the other is used to transfer parallel bytes of data.

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lieu of the 4.0 MHz crystal oscillator signal. The output of IC9 should count through the full range of 0 through 15, starting at 4, while IC8 should count from 4 through 8.

The INTRQ and DRQ signals were connected to PB6 and PB7 of the 6520 because powerful testing instructions are available for these pins. If problems occur in this area, these instructions will come in handy.

Testimonials

This system has been built by several people and has been proven to work with minimal debugging, using wire-wrap, Slit-N-Wrap, and Super Strip techniques. The circuits are not the simplest possible; we have interfaced a 5-inch disk drive to the KIM

and AIM systems using only three integrated circuits. The newer versions of the 1771, which allow the controller to be connected directly to data and address buses, do not need a 6520; but there is a case for isolating the microcomputer from the disk con-

troller through a 6520. Whatever route you choose, this basic design will provide reliable, trouble-free operation.

In Part 2, next month, we will look at the software needed to use this controller.

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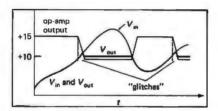
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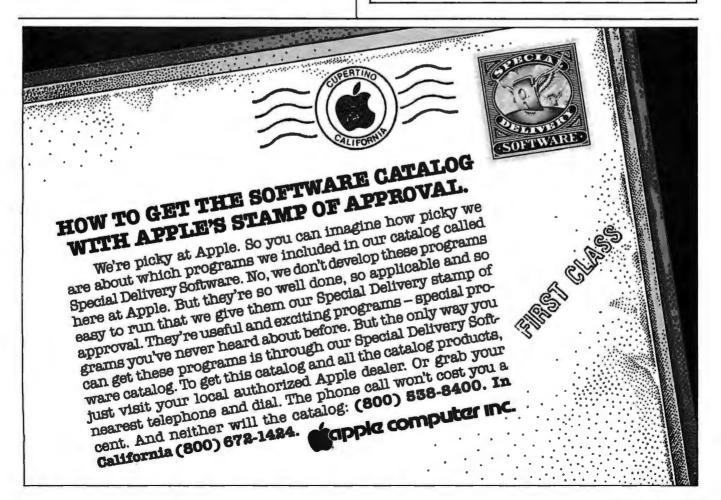
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Technical Forum

Favorite Benchmarks and Other Programs

In the July 1980 BYTE, Carl Helmers wrote a Technical Forum entitled "Some More on Performance Evaluation" (page 216), in which he requested readers to send in benchmark routines that are "appropriate to the typical language and operating-system environment of the contemporary small computer." The following submission from David I Wilcox, of Mansfield, Pennsylvania, is one of the most noteworthy.

While in college, I was shown a simple way to calculate the number of decimal digits a computer retains in its internal representation of floating-point numbers. If:

A = 1./3.

then, by computing:

 $abs(log_{10}(abs(1.-(A+A+A))))$

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the number of digits of accuracy is obtained.

The choice of 1./3. is deliberate because it is an infinitely repeating rational number in the binary-number system. Therefore, a difference between 1. and the sum A+A+A must exist in any attempt to represent 1./3. with a finite number of bits.

If the machine does not have the common logarithm function available, then compute:

$$1./(abs(1,-(A+A+A)))$$

The number of digits of accuracy is approximately the exponent of the result expressed in scientific notation. Better yet, use a calculator or math tables to find the common logarithm of the result.

The number of digits of accuracy available generally depends on both the machine and the language. This method offers a quick, in-the-store check of the actual number of digits used by a given system to represent floating-point numbers.

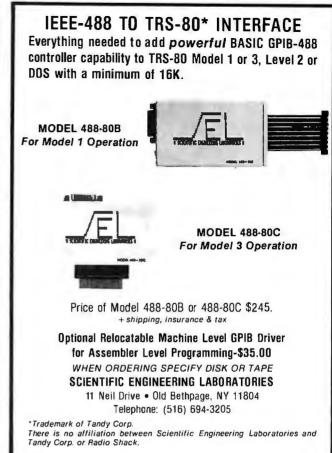
However, other letters we received bearing the "Favorite Benchmarks" title contained still more programs written in Pascal or BASIC that shaved minutes or seconds from the prime-number-generating program used as a benchmark in Carl Helmers's article. Although we appreciate the attempt at participation represented by these letters, they missed the point expressed by Carl Helmers in that article: "...the goal of the exercise was not to code the most efficient algorithm. It was, rather, to code an algorithm that takes a measurable amount of time while performing a certain group of calculations." The same algorithm (preferably embedded in a common computer language) can then be run on several computers and the times compared as performance indices of the respective language/machine combinations.

For example, the benchmark given by David Wilcox, above, results in a number (calculated in this case, not timed with a stopwatch) that can be used to compare, say, an Atari 400 with a Commodore PET; the comparison being made is one of digits of accuracy.

One prime-number-generating benchmark sent to us gave two times, one for execution of the program using a video terminal and another using a printer. In my opinion, such a benchmark confuses the issue under consideration (computer speed in generating a given set of prime numbers). Unless a benchmark is trying to measure the efficiency of a given computer in displaying numbers, the interval being timed should end as soon as the first display is printed. This assumes, as was done in the prime-number benchmark, that all results are stored and the printing is done after the computation being measured has finished. In fact, I sometimes bracket the part of the program being measured with print statements that say BEGINTIMING and END TIMING. This allows me to isolate the function being evaluated....GW







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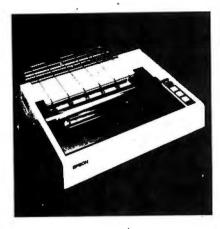
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Book Reviews

Travels in Computerland, or Incompatibilities & Interfaces

by Ben Ross Schneider Jr Reading MA: Addison-Wesley Publishing Company 1974. Softcover, \$6.50

Reviewed by

Jonathan Jacky 6551 5th Ave NE Seattle WA 98115

How many seemingly impractical projects have been attempted only because someone thought, "That should be a trivial exercise for a computer"? So it seemed to theater historian Ben Ross Schneider Ir, when he proposed organizing a data base from The London Stage, an eleven-volume, 8000-page calendar of eighteenthcentury theater performances. As Schneider envisioned, "It would be like having an index to every kind of thing in the book, only the computer would even turn the pages and take notes for you."

As he became involved in the project, Schneider soon realized that what is conceivable for the computer is sometimes not easily accomplished. He learned that the system which saves the scholar months of repetitive clerical work may well require several times that much effort to get running. Schneider recounts his experience in Travels in Computerland, an entertaining book that gives a true-to-life case study illustrating information-retrieval techniques. It is the best account of an ambitious computing project I have read.

Schneider describes the problems of creating a computer-accessible data base from source text intended for human readers. He intended his data bank to produce, for

example, listings of every role an actor played during his career. That meant sorting all the entries in The London Stage by actor—but The London Stage was not arranged by actor; it consisted of theater programs arranged chronologically. Each program included many items: titles, roles, actors... To enable the computer to identify each item, they must be clearly delimited and follow each other in undeviating order.

Schneider believed that the syntax and typography of The London Stage satisfied these conditions, but programmer Will Daland recognized otherwise: "Too much variation," he explained. "A computer can't tolerate as much ambiguity as a human... The human being uses an immense store of experience to resolve ambiguities."

So they faced the mammoth task of recopying the entire text to better reveal its contents to the machine.

'The structure of The London Stage, which we had to describe before we could analyze it by machine, continually evaded us. To retrieve what was in it we had to know what kinds of things were in it and how this information was arranged. It was like nature itself. We always thought we knew more about it than we actually did."

Eventually they found the precise form in which the text would be presented to the computer, but only after Schneider learned to view his specialty from a new perspective. At one point he was startled when Daland, in trying to allow for all conceivable possibilities, suggested a plausible variation in eighteenth-century casting practices that had never occurred to Schneider. He recalls: "This episode is an example of how computer methods, by imposing logic, increase one's comprehension of one's subject. And that is

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why Will, who never studied it for an hour, could teach me something about theater history."

The book vividly conveys the day-to-day feel of the project. The reader shares Schneider's dismay when, as deadlines approach and seemingly banal practical problems threaten to scuttle the project, the drama scholar must become a reluctant expert in the countless technical aspects of computing.

Because this work was done in the premicroprocessor era (about ten years ago), some of the problems seem very dated; inestimable difficulty resulted when terminals capable of producing lower-case characters proved to be unavailable. Other problems are perennially familiar; Schneider ruefully recalls the time invested in "persuading data-processing firms to meet declared standards, and explaining to sales representatives what their products were." In a final. ironic twist, humanist Schneider realizes that his achievement is poorly accepted and little understood by fellow scholars because he neglected to communicate effectively with them.

This book should be required reading for anyone planning to apply a computer to an intricate real-world activity, be it business or research. The nature of Schneider's project, his unusual perspective and lively writing, and particularly his vivid characterizations and keen appreciation of the way personalities shape projects, recommend the book to those on the fringes of the computing world. Travels in Computerland, or Incompatibilities & Interfaces is especially relevant to those technologically innocent people who think that computers are for doing math, and wonder how anyone could think a machine can help him appreciate a work of art.■

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Ask BYTE

Conducted by Steve Ciarcia

Here's LED in Your Display

Dear Steve.

I enjoyed your article "Self-Refreshing LED Graphics Display" (October 1979 BYTE, page 58), and think I can use such an output display. My present system is a KIM-1 computer with an 8 K-byte memory board. I use the KIM-1's keypad and LED display for input and output, but I'm having difficulty expanding the display board.

Your design is an 8 by 16 display, but I would like to expand that to 8 by 64; then I could have a small amount of alphanumerics and graphics.

Near the end of your article, you mentioned that to expand on your design, simply add more memory and column decoders. Please be more specific. Would I have to use six address lines, and spread this out over four 74154 1-of-16 decoders? I assume a total of eight 7489 memory devices would be needed. How do I tie this stuff together? Would this affect the refresh and scan rates? Could I substitute LStype logic circuits in your design?

Charlie Timbers

There are several ways to expand the basic 8 by 16 display into an 8 by 64. The easiest thing to do is to make four of the basic units, then change the addressing to be

four blocks of sixteen, for a total of sixty-four 8-bit output ports. To accomplish this, the address decoding presently done by IC1 and IC2 must be changed. Figures

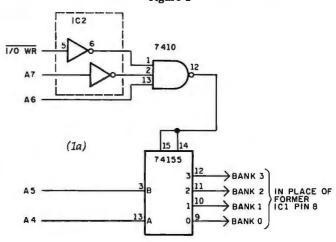
1a and 1b should help.

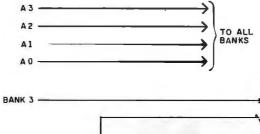
You can use LS TTL (transistor-transistor logic) devices for those integrated circuits that have an equivalent. Some don't. ...Steve

8 BY 16

8 BY 16

Figure 1





(1b)

8 BY 16

8 BY 16

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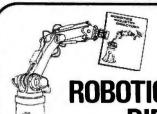
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We use Maxell MD1 singlesided 5-inch floppy disks on our Apple II. We noticed that the disks have a head-access slot on both sides, so we decided to experiment. We cut a write-enable notch on the opposite edge, and, to our surprise, we were able to write a full 16 sectors on the other side of a single-sided disk. A scan of the disk showed the written data to be in good shape and no physically damaged areas. So, what is the difference between single-sided and double-sided disks-and why purchase a double-sided disk at the extra cost?

Michael Berch John Oswalt Berkeley CA

The answer to your question involves how disks are manufactured and tested, rather than any physical difference between them. Both sides of a disk are usually capable of data storage, but, on a single-sided disk, only one is guaranteed.

When a disk is made, it is tested for data retention. This means writing data (usually at a higher density than you will ultimately use) onto the disk and reading it back. If all goes well, the disk is certified. On double-sided disks, both sides are checked and certified.

As a matter of economics, some manufacturers do not bother to check more than what is required to meet production schedules. For example, if 100,000 disks are made and 10,000 must be certified as double sided, the manufacturer may stop testing both sides after getting 10,000 good ones. This often requires checking 15,000 disks to get the 10,000 that pass. Most often, the 5000 rejects end up in the single-sided



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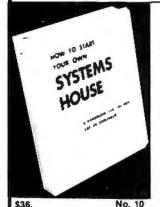
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stock (presuming that one side was good). The remaining 85,000 are only checked for one good side.

In your case, one of the following situations may

- 1. Both sides were checked, but the manufacturer decided to put the disk in a singlesided envelope anyway.
- 2. The second side was untested by the manufacturer.
- 3. The second side failed the manufacturer's data test, and the disk could only be certified as single sided.

In the first case, you are handed a golden opportunity. Cut another access hole and use the other side. In the second and third cases, you are playing the odds. Of course, all three are merely conjecture, since the manufacturer doesn't specify the performance capabilities of the uncertified side.

I suggest that you only use the modified disks for noncritical storage. While it may appear that your experiment has always worked in the disks you've tried, this may be more of a testimonial to the quality of that particular manufacturer's product than a general axiom for all disk users. ... Steve

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Ask BYTE-

my area) for my display? John Ramler Alexandria VA

I was hoping someone would ask that auestion.

Videocassette recorders have an input jack that is normally intended for use with a TV camera. In general, a camera has a 1-volt peak-topeak output signal into a 75-ohm load. Most computers with a straight video output try to conform to this specification, so they should be compatible.

To make sure, I connected

the output of an Apple II to the camera input of a Magnavox videocassette recorder. The camera/tuner and VTR/TV switches were set to camera and VTR. respectively. In my opinion, it worked well. However, it was necessary to reduce the TV's color-control setting to keep the letters from running together. Once adjusted properly, it made a satisfactory monitor.

An additional benefit of this technique is that you can record anything on the screen. ...Steve

Simple **Case Conversion**

Dear Steve,

I read Roger L Degler's "A Lowercase to Uppercase Converter," and it seems I have a similar problem. (See the September 1980 BYTE, page 326.) I own an uppercaseonly keyboard, but I would like to use lowercase on my video-interface board. Is there some sort of uppercaseto-lowercase converter I could put between my keyboard and video board and still have an operational shift key? I'm sure many BYTE readers have the same problem.

Andrew Meyer White Plains NY

To get lowercase codes from a keyboard that has uppercase-only output, it is necessary to make the fifth bit high (assuming 7-bit ASCII code), so that an "A" (1000001) becomes an "a" (1100001), and so on.

If your keyboard output is DTL (diode-transistor logic), RTL (resistor-transistor logic), or TTL (transistortransistor logic), it can be modified a number of ways. One method is the way Roger Degler suggested in his article. Another way, simpler but much less sophisticated. is shown in figure 2. You'll note that pressing the "shift key" causes bit 5 to be high. ...Steve

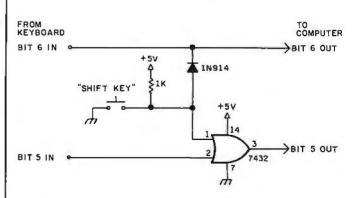
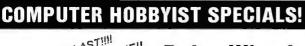


Figure 2





Daisy Wheel

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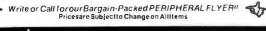
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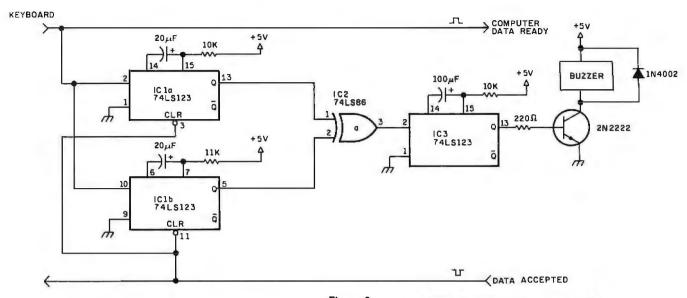


Figure 3

Where'd You **Get Those Beepers?**

Dear Steve.

I'm connecting a keyboard to a parallel port. I need a simple circuit that beeps if a pulse does not happen on the Data Accepted line within a set period of time after the pulse on the Data Ready line.

Can you help me? David Smith North Bergen NJ

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Data Accepted pulse. When a key is pressed, the resulting Data Ready strobe fires IC 1a and IC 1b. IC 1a is "set" for the longest time you will allow before signaling a missing Data Accepted pulse (perhaps 50 ms). IC 1b is set a few microseconds to a few milliseconds longer than 1a (it only has to be 50 ns longer).

When these two one-shots fire, they open a timing window for the Data Accepted strobe. If it is received within the period allowed by 1a, then 1a and 1b are reset (no beep). If, however, no Data Accepted pulse is received, 1a will time-out before 1b. The opposite logic outputs of the two one-shots are then sensed

IC			
Number	Туре	+ 5 V	GND
IC1	74LS123	16	8
IC2	74LS86	14	7
IC3	74LS123	16	8

by an Exclusive-OR, IC 2, which fires IC 3. IC 3 is a oneshot set for 200 ms and connected to a beeper. As long as the Data Accepted pulse is received within 50 milliseconds, you should never hear it....Steve

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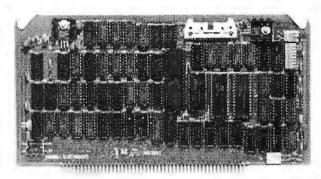


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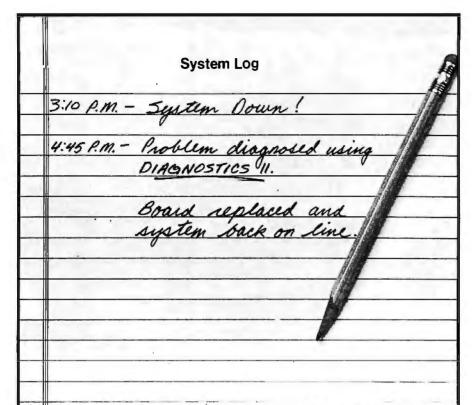
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Apple

Astro-Scope, horoscope for the Apple II. Floppy disk, \$30. Astro-Graphics Services Inc, POB 28, Orleans MA 02653.

E, Applesoft editing utility for the Apple II Plus. Cassette, \$14.95. Apollo Software Company, 318 Harvard St, Suite 10, Brookline MA 02146.

Electronics I, electronics-design application programs for the Apple II. Floppy disk, \$29.95. Howard W Sams & Company Inc, 4300 W 62nd St, POB 558, Indianapolis IN 46268.

Electronics II, electronicsdesign programs for the Apple II. Floppy disk, \$29.95. Howard W Sams & Company Inc (see above).

Electronics III, electronicsdesign programs for the Apple II. Floppy disk, \$29.95. Howard W Sams & Company Inc (see above).

Masterdos, disk customizing programs for the Apple II Plus. Floppy disk, \$29.95. Masterworks Software Inc POB 7000-285, Rolling Hills Estates CA 90274.

Micro-Painter, color drawing program for the Apple II. Floppy disk, \$34.95. Datasoft Inc, 16606 Schoenborn St, Sepulveda CA 91343.

1981 Tax Preparer, IRS tax-preparation aid for the Apple II. Floppy disk, \$99. Howard Software Services, 7722 Hosford Ave, Los Angeles CA 90045.

Reversal, graphics strategy game for the Apple II (plays Othello, a trademark of CBS Inc). Floppy disk, \$34.95. Hayden Book Company Inc, 50 Essex St, Rochelle Park NJ 07662.

Sex-O-Scope, horoscope for the Apple II. Floppy disk, \$30. Astro-Graphics Services Inc (see above).

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Software Received_

electronic-design program for the TRS-80. Cassette, \$24.95. Howard W Sams & Company Inc, 4300 W 62nd St, POB 558, Indianapolis IN 46268.

Arcade-80, arcade-like graphics game for the TRS-80. Floppy disk, \$24.95. Datasoft Inc, 16606 Schoenborn St, Sepulveda CA 91343.

Cosmic Fighter, graphics arcade game for the TRS-80. Cassette, \$17.95. Big Five Software, POB 9078, Van Nuys CA 91409.

Descriptive Statistics & Regression Analysis, statistics package for the TRS-80. Cassette, \$24.95. Howard W Sams & Company Inc (see above).

Football Classics, graphics strategy game for the TRS-80. Floppy disk, \$24.95. Datasoft Inc (see above).

Genealogy, genealogy program for the TRS-80 Model II. Eight-inch floppy disk, \$250. John J Armstrong, 3700 Whispering Pine Rd #47B, Mobile AL 36608.

Iago, graphics strategy game for the TRS-80 (plays Othello, a trademark of CBS Inc). Cassette, \$19.95. Datasoft Inc (see above).

Plotting Graphs for Line Printer, graphing program for the TRS-80. Cassette, \$24.95. Howard W Sams & Company Inc (see above). Plotting Graphs for Video Display, graphing program for the TRS-80. Cassette, \$24.95. Howard W Sams & Company Inc (see above).

Real-Estate, real-estate program for the TRS-80 Pocket Computer. Cassette, \$24.95. Radio Shack, 1300 One Tandy Center, Ft Worth TX 76102.

Other Computers

Atari Character Generator, graphics utility for the Atari 400 and 800. Cassette, \$15.95. Datasoft Inc, 16606 Schoenborn St, Sepulveda CA 91343.

C Compiler Version 1.4, programming language for the CP/M system. Eight-inch floppy disk, \$145. B D Software, Cambridge MA 02139 (distributed by Lifeboat Associates, 1651 Third Ave, New York NY 10028).

Chest of Classics, collection of games for the Sinclair ZX80. Cassette, \$9.95. Lamo-Lem Labs, POB 2382, La Jolla CA 92038.

MINCE Version 2.4, word processor for the CP/M system. Eight-inch floppy disk, \$125. Mark of the Unicorn, POB 423, Arlington MA 02174.

Telelink I, terminal program for the Atari 400 and 800. Program cartridge, \$19.95. Atari Inc, POB 427, Sunnyvale CA 94086. ■

This is a list of software packages that have been received by BYTE Publications during the past month. The list is correct to the best of our knowledge, but it is not meant to be a full description of the product or the forms in which the product is available. In particular, some packages may be sold for several machines or in both cassette and floppy-disk format; the product listed here is the version received by BYTE Publications.

This is an all-inclusive list that makes no comment on the quality or usefulness of the software listed. We regret that we cannot review every software package we receive. Instead, this list is meant to be a monthly acknowledgment of these packages and the companies that sent them. Companies sending software packages must include the suggested list price of the packages and (where appropriate) the alternate forms in which they are available.



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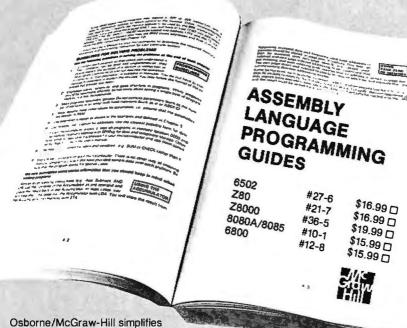


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Books Received

AIM-65, Laboratory Manual and Study Guide, Leo J Scanlon. Somerset NJ: John Wiley & Sons Inc, 1981; 21.5 by 28 cm, 179 pages; softcover, ISBN 0-471-06488-2, \$7.95.

APL-Stat, James B Ramsey and Gerald L Musgrave. Belmont CA: Lifetime Learning Publications, 1981; 21.5 by 28 cm, 356 pages; softcover, ISBN 0-534-97985-8, \$14.95. Solutions manual for above \$3.95.

Apple Machine Language, Don Inman and Kurt Inman. Reston VA: Reston Publishing Company Inc, 1981; 16 by 24 cm, 296 pages; hardcover, ISBN 0-8359-0231-5, \$9.95.

The Calculator Afloat, Captain Henry H Shufeldt, USNR (retired) and Kenneth E Newcomer. Annapolis MD: Naval Institute Press, 1980; 16 by 23.5 cm, 225 pages; hardcover, ISBN 0-87021-116-1, \$16.95.

Computers in Society, Donald H Sanders. New York: McGraw-Hill Book Company, 1981; 19.5 by 24 cm, 622 pages; hardcover, ISBN 0-07-054672-X, \$16.95.

Disassembled Handbook for TRS-80, Volume III, Robert M Richardson. Chautauqua NY: Richcraft Engineering Ltd, 1981; 24 by 28 cm, 239 pages; softcover, ISBN-none, \$18.

Electric Machines and Transformers, Leonard R Anderson, Reston VA: Reston Publishing Company Inc, 1981; 18.5 by 24 cm, 305 pages; hardcover, ISBN 0-8359-1615-4, \$18.95.

Experimentation with Microprocessor Applications, Thomas W Davis. Reston VA: Reston Publishing Company Inc, 1981; 17.5 by 23.5 cm, 237 pages; softcover, ISBN 0-8359-1812-2, \$9.95.

Fifty BASIC Exercises, J P Lamoitier. Berkeley CA: Sybex, 1981; 18 by 23 cm, 253 pages; softcover, ISBN 0-89588-056-3, \$12.95.

FORTRAN IV, Second Edition, J Friedmann, P



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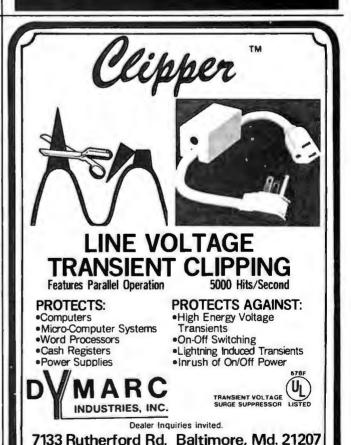
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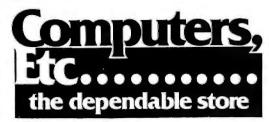
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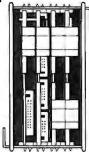
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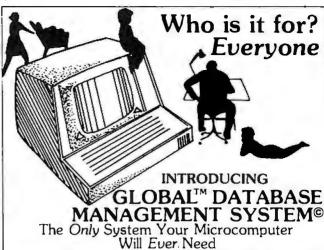
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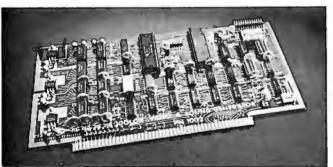
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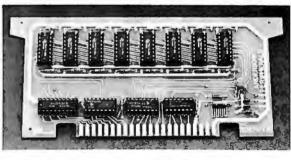
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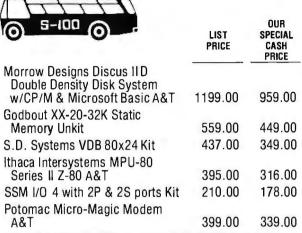
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A File Catalog System for UCSD Pascal

Edward Heyman 300 Center Hill Rd Centerville DE 19807

It doesn't take long to accumulate a large number of disks with assorted software, particularly if you insist on a reasonable amount of backup. Finding a program you worked on two months ago can be a problem without some type of file organization. Ward Christenson provided the CP/M world with that organization in his UCAT disk catalog system. I'd be lost without it.

As my collection of UCSD Pascal files grew I needed a system similar to UCAT to cope with the problem. Hence, I created CATALOG (see listing 1). Written in Pascal, it does all the things that UCAT does as fast or faster than UCAT (even though UCAT is written in assembly language). A new directory can be merged into a 600-entry catalog in about 30 seconds. A search for a file in a 600-entry catalog takes less than a second. A 600-entry catalog uses about thirty-six blocks, as does the backup file. The program code file and pointer file use another twenty blocks for a total of ninety-two blocks.

What CATALOG Does

CATALOG maintains a file of records in which each record is similar to a UCSD Pascal directory entry. The record contains the name of the volume, the file name, the type of file, the date the file was last changed, and the length of the file. CATALOG gets the records directly from a volume directory during UPDATE. Once the CATALOG file is filled with records you can locate a file with the SEARCH command.

Being lazy, I like to have my machine do as much of my work as possible, so I've added a few bells and whistles to the essential features.

Using CATALOG

For the CATALOG program to work, the files MASTCAT.DATA and CAT.POINT.DATA must be on Drive five. If they are not, the program asks if you want to create them. The first time the program is run you must respond with a "Y" to the prompts for file creation before you can proceed.

Thereafter, executing CATALOG will bring forth the command line:

CATALOG→S)earch D)isplay B)ackup U)pdate R)emove Q)uit.

The S Command

Entering "S" will put the program in the Search mode with the prompt:

ENTER THE NAME OF FILE TO BE FOUND-

Uppercase must be used for the file name. Wild-card searches can be made by replacing the wild-card section with "=". For example, the following entries may be made to find CATALOG.TEXT:

CATALOG.TEXT CAT = = LOG.TEXT

The directory of an entire volume can be obtained by typing the name of the volume followed by ":".

Entering file name FREE.SPACE will display a list of all the cataloged volumes, the available space, and the most recent date of catalog update of each volume.

The output of the Search command can be directed to the printer by typing "<" before the name of the file to be searched.

The D Command

Entering "D" in response to the main prompt line will display the entire catalog in alphabetical order.

The B Command

Entering "B" in response to the main prompt line will display all files that exist on only one volume (all files that do not have a backup). The routine checks only for the same file name; therefore, files with the same name but different dates are considered to be backed up.

The U Command

A response of "U" to the main prompt line will activate

the update routine, which will produce the prompt:

ENTER UNIT NUMBER CONTAINING UPDATE **VOLUME**-

If UNIT 5 is selected, the catalog file will be updated with the contents of the volume containing the catalog files (with the exception of MASTCAT.DATA). For all other volumes UNIT 4 should be used.

The update procedure will first rename the main catalog file (MASTCAT.DATA) to BACKCAT.DATA and then read the directory for the volume on the selected unit and create a file name FREE SPACE with the unused space on the volume. It will then sort the files by alphabetical order and merge the volume list with the catalog file (MASTCAT.DATA) and at the same time create the pointer file (CAT.POINT.DATA.).

While merging, any file names added will be displayed on the console terminal and any files that were previously on the volume but were removed will be removed from the master file and displayed as having been deleted. After completion, the number of entries in both the main and backup files will be displayed.

The beauty of Pascal is its selfdocumenting features—the program should not be difficult to follow.

The R Command

Entering an "R" in response to the main prompt will invoke the prompt line:

ENTER NAME OF VOLUME TO BE REMOVED-

Entering a volume name and a carriage return will cause all entries in the main catalog file for the selected volume to be removed from the file and to be listed on the terminal.

The Q Command

To leave CATALOG enter "Q". UNIT 4 will be checked to see if it contains the booted system volume; if not, a prompt to insert the original system volume will be displayed on the terminal before the program is exited (to prevent a system crash).

How the Program Works

The beauty of Pascal is its self-documenting features—the program should not be difficult to follow.

Since most systems will not have sufficient memory to hold a copy of both the old (BACKCAT.DATA) and the new catalog (MASTCAT.DATA) at one time, the files are read in and written out in sections. OCAT and NCAT are arrays that hold the records read from the old file and the records to be written to the new file, respectively. The size of these arrays is determined by the constant MAXREC. MAXREC should be adjusted to suit your memory size. NREC and OREC are variables associated with the number of records read or records written during the current read or write. DREC is associated with the

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number of records in the directory. OTOTREC and NTOTREC are the total records read or written to or from a file.

In order to speed the action of the SEARCH command a pointer file is created during UPDATE. The index to the pointers are the characters "A" to "Z". The array holding the pointers is called DEXRAY and is stored on disk in the file CAT.POINT.DATA. The pointer list is created by calls to the procedure SETDEX. It is written to file by procedure WRITEDEX and read into array DEXRAY by procedure READDEX.

Procedure BACKUP checks to see if the file name of a record is unequal to its predecessor and successor. If it is, it is not backed up. Since the array is not large enough to hold all of the catalog file, provisions must be made to compare the last entry in one array with the first entry in the next array. The Boolean variables PASS and UNBACK are used for this purpose.

To simplify the logic of procedure MERGE, several IF statements as well as the CASE statement have been used. The problem may be stated as follows:

• If the current directory record file name is less than the current old catalog record file name, insert the directory

record in the new catalog and increment the new file pointer (NREC) and the directory pointer (D).

• If the current directory record file name is equal to the current old catalog record file name, check the volume names. If the current directory record volume name is less than the old catalog record volume name, insert the directory record and increment the new catalog (NREC) and the directory (D) pointers. If the current directory record volume name is equal to the old catalog record volume file name, insert the directory record and increment NREC, OREC, and D. If the directory record volume name is greater than the old file record name, insert the old catalog record into the new catalog and increment the new catalog and old catalog pointers.

• If the current directory record file name is greater than the old catalog record file name, insert the old catalog record in the new catalog and increment the new catalog pointer and the old catalog pointer. If the directory record volume name is equal to the old file record volume name, do not enter the record in the new catalog, and

simply increment the old catalog pointer.

I hope that you will find CATALOG useful in keeping track of your files and programs.

Listing 1: A disk catalog system for UCSD Pascal. This program maintains a file of records in which each record is similar to a UCSD Pascal directory. Each record contains the name of the volume, the file name, the type of file, the date the file was last changed, and the length of the file.

```
<fs+>-(L CONSOLE:>-(L PRINTER:>-(L CAT.PRN.TEXT)-
```

```
PROGRAM CATALOG;
```

```
{* written by edward heyman
                               *}
{* 300 center hill road
                               *>
{* centerville delaware 19807 *}
```

CONST

```
BLANKS = '
                            1;
MAXREC=200;
MAXREC_1=201;
NFILENAME= '#5: MASTCAT. DATA';
OFILENAME='#5:BACKCAT.DATA';
PFILENAME='#5:CAT.POINT.DATA';
CLEARSCREEN=12;
```

TYPE

```
DATE_RECORD = PACKED RECORD
                    MONTH: 0.,12;
                    DAY: 0..31;
                    YEAR: 0..100
          END;
DIR_{-}SIZE = 0..77
```

```
VOL...ID = STRING[7];
FILE_ID = STRING[15];
FILE_TYPE = (UNTYPED, XDISK, CODE, TEXT,
             INFO, DATA, GRAF, FOTO, SECUREDIR);
DIR_RECORD = RECORD
              FIRST_BLOCK: INTEGER;
              LAST_BLOCK: INTEGER;
```

Listing 1 continued on page 411

CASE DIR_FILE_KIND:FILE_TYPE OF SECUREDIR.UNTYPED:

(DIR_VOL_NAME:VOL_ID; ZERO_BLOCK; NUM_OF_FILES;

TOTAL_BLOCKS: INTEGER; LAST_BOOT: DATE_RECORD);

XDISK, CODE, TEXT, INFO, DATA, GRAF, FOTO:

(DIR_FILE_NAME:FILE_ID; LASTBYTE:1..512;

DIR_FILE_DATE:DATE_RECORD)

ENDS

CATALOG_RECORD=PACKED RECORD

VOL_NAME: VOL_ID; FILE_NAME: FILE_ID; FILE_KIND: FILE_TYPE; FILE_DATE: DATE_RECORD; FILE_SIZE: 0..988;

END

DIRECTORY = ARRAY[DIR_SIZE] OF DIR_RECORD; CATARRAY = ARRAY [0..MAXREC] OF CATALOG_RECORD; FILEN = STRING[20]; RECNUM = 0..MAXREC_1; INDEX = 'A'..'Z'; INDEXARRAY = ARRAY [INDEX] OF INTEGER;

VAR

NREC, OREC, OLREC, DLREC; RECNUM;
NTOTREC, OTOTREC: 0...2047;
REMOV, NFILEEND, OFILEEND, DONE: BOOLEAN;
CH: CHAR;
DEX: INDEX;
DEXRAY: INDEXARRAY;
F: FILE OF CHAR; {used to switch from console to printer}
VOL, TEST, SYSTEM VOLUME: VOL_ID;
CATFILE, OCATFILE, NCATFILE: FILE OF CATALOG_RECORD;
NCAT, OCAT: CATARRAY;
Listing 1 continued on page 412

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411



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```
FUNCTION LOOKUP(FN:FILEN):BOOLEAN;FORWARD;
SEGMENT PROCEDURE INITIALIZE;
  VAR
        I:RECNUM;
        CAT: CATARRAY;
        DEXFILE: FILE OF INDEXARRAY;
  BEGIN
    IF (NOT LOOKUP(NFILENAME))
      THEN BEGIN
             WRITELN('THERE IS NO FILE NAMED ', NFILENAME, 'ON THIS DISK');
             WRITELN('DO YOU WANT TO CREATE A ',NFILENAME,' {Y/N}');
             REPEAT
               READ (CH)
             UNTIL (CH IN ['Y','g','N','n']);
             IF ((CH<>'Y') AND (CH<>'g')) THEN EXIT(CATALOG);
             writeln('FILLING ARRAYCOJ');
             WITH CATEOU DO
               BEGIN
                                     1 $
                 VOL...NAME:='
                 FILE ... NAME: = '
                                              1 ;
                 FILE KIND: =UNTYPED;
                 FILE_DATE.MONTH:=0;
                 FILE_DATE.DAY:=0;
                 FILE_DATE.YEAR:=0;
                 FILE_SIZE:=0;
               END;
             FOR I:=1 TO MAXREC DO CATCID:=CATCOD;
             writeln('ARRAY IS FILLED');
             REWRITE(CATFILE, NFILENAME);
               FOR I:= 0 TO MAXREC DO
                 BEGIN
                   CATFILE := CAT[];
                   PUT(CATFILE);
                 END; (for I)
             CLOSE(CATFILE, LOCK)
           END(if)
     ELSE WRITELN('THE FILE ',NFILENAME,' ALREADY EXITS ON THIS VOLUME ');
```



```
IF NOT LOOKUP(PFILENAME)
    THEN BEGIN
            WRITELN('THERE IS NO FILE NAMED ', PFILENAME, ' ON THIS DISK');
            WRITELN('DO YOU WANT TO CREATE A ',PFILENAME,' {Y/N}');
            REFEAT
               READ (CH)
             UNTIL (CH IN ['Y', 'y', 'N', 'n']);
             IF ((CH<>'Y') AND (CH<>'g')) THEN EXIT(CATALOG);
             FOR DEX:='A' TO 'Z' DO DEXRAY[DEX]:=Q;
             REWRITE (DEXFILE, PFILENAME);
             DEXFILE := DEXRAY;
             PUT(DEXFILE);
             CLOSE (DEXFILE, LOCK);
             WRITELN(PFILENAME, WRITTEN TO DISK')
             END(if)
      ELSE WRITELN('FILE ', PFILENAME, ' EXISTS');
END; {init}
FUNCTION LOOKUP;
{returns TRUE if filename present FALSE if not}
    VAR
          IOR: 0..15;
    BEGIN
      {$I-}
      RESET(CATFILE, FN);
      IOR:=IORESULT;
      CLOSE(CATFILE);
      {$1+}
      IF (IOR=0)
          THEN LOOKUP:=TRUE
          ELSE BEGIN
                 LOOKUP:=FALSE;
                 IF(IOR<>10) THEN WRITELN('IORESULT FOR ',FN,' IS ',IOR);
```

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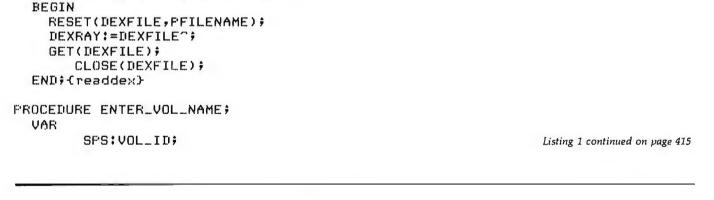
END; {lookus}

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Listing 1 continued on page 414

36 Deville Drive, Selden, NY 11784 516-561-1891

```
Listing 1 continued:
PROCEDURE WAIT;
  BEGIN
    GOTOXY(10,24);
    WRITE('PRESS SPACE BAR TO CONTINUE');
    READ(CH)
  END; {wait}
PROCEDURE MEM(PN:STRING);
  BEGIN
      writelm('MEMORY AVAILABLE AT PROCEDURE ',PN,' = ',MEMAVAIL);
  END;
PROCEDURE GET_SYS_VOL(VAR VOL:VOL_ID);
{sets name of volume in drive 4}
  UAR
      T.J: INTEGER;
      SPS:STRING[16];
      AVOL: VOL. ID;
      DIR: DIRECTORY;
  BEGIN
    UNITREAD(4,DIREO],2048,2);
    VOL:=DIRCOJ.DIR_VOL_NAME;
    SPS:=COPY(BLANKS,1,7-LENGTH(VOL));
    AVOL:=CONCAT(VOL,SPS);
  END; {set_sys_vol}
PROCEDURE READDEX;
freads the file of pointers to the first occurrence of each letter in the alpha
  VAR
      DEXFILE : FILE OF INDEXARRAY;
```



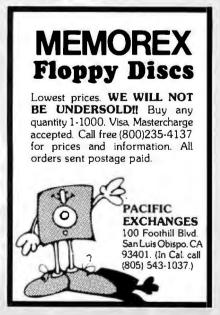


```
Listing 1 continued:
```

```
BEGIN
              / ;
    VOL. := '
    REPEAT
      WRITE(CHR(CLEARSCREEN));
      WRITE ('ENTER NAME OF VOLUME TO BE REMOVED --> ');
      READLN(VOL.);
    UNTIL (LENGTH(VOL)<=8);
    IF (POS(':',VOL)<>0) THEN DELETE(VOL,POS(':',VOL),1);
    SFS:=COFY(BLANKS,1,7-LENGTH(VOL));
    VOL:=CONCAT(VOL,SPS);
    WRITELN(VOL, (1())
    DREC:=0
  END; {enter_vol_name}
PROCEDURE PRINT_DATE (REC:DATE_RECORD);
{prints date to console or printer}
BEGIN
 WITH REC DO
   BEGIN
      WRITE(P,DAY:3,/-/);
        CASE MONTH OF
          1: WRITE(P,'Jan');
          2: WRITE(P, 'Feb');
          3: WRITE(P, 'Mar');
          4: WRITE(P, 'Apr');
             WRITE(F, 'May');
          6: WRITE(P,'Jun');
          7: WRITE(P, Jul1);
          8: WRITE(P, 'Aug');
          9: WRITE(P, 'Sep');
          10: WRITE(P, 'Oct');
          11: WRITE(P,'Nov');
          12: WRITE(F,'Dec');
        END; {case}
        WRITE(P,'-',YEAR:2,' ':3);
     END; {with}
   END; {print_date}
```







Circle 10 on inquiry card.

PROCEDURE PRINT_KIND(FILE_KIND:FILE_TYPE);
{prints file type to console or printer}

```
CASE FILE_KIND OF
         XDISK: WRITE(P,'Bad block');
                 WRITE(P,'Code file');
         CODE:
         TEXT:
                 WRITE(P,'Text file');
         INFO:
                 WRITE(P, 'Info file');
         DATA:
                 WRITE(P,'Data file');
                 WRITE(Fy'Graf file');
        GRAF:
        FOTO:
                 WRITE(P, 'Foto file');
      END; { case }
  END: (print_kind)
PROCEDURE PRINT_RECORD(CAT1:CATALOG_RECORD);
{prints record to console or printer}
    BEGIN
       WITH CAT1 DO
          BEGIN
            WRITE(P,FILE_NAME, /
                                      /:18-LENGTH(FILE_NAME));
            WRITE(P, VOL_NAME, / ':8-LENGTH(VOL_NAME));
            WRITE(P,FILE_SIZE:4);
            PRINT_DATE(FILE_DATE);
            PRINT_KIND(FILE_KIND);
            WRITELN(P);
         ENDICWITHE
     END; {print_record}
PROCEDURE READ_NEW_CAT;
{reads NREC records or to eof from NCATFILE}
  VAR
          I:RECNUM;
  BEGIN
     I:=I;NREC:=O;
     GET(NCATFILE);
     WHILE (NOT EOF(NCATFILE)) DO
          BEGIN
            NCATEIl: = NCATFILE ?;
                                                   ())
            IF - ((NCATCI]. VOL_NAME='
               THEN BEGIN
                        NREC:=I-1;
                        NTOTREC:=NTOTREC+NREC;
                        NFILEEND: =TRUE;
                        EXIT(READ_NEW_CAT);
                                                                           Listing 1 continued on page 417
                     END$ (if)
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                   FOR
                                    FOR
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Listing 1 continued:

```
IF (I=NLREC)
              THEN BEGIN
                    NREC:=I;
                    NTOTREC:=NTOTREC+I;
                    EXIT(READ_NEW_CAT);
                   END; (if)
          I:=I+19
          GET(NCATFILE);
        END; (while)
      NREC:=I-1;
      NTOTREC:=NTOTREC+NREC;
      NFILEEND:=TRUE;
  END; {nreadcat}
PROCEDURE READ_OLD_CAT;
{reads OREC records or to eof from OCATFILE}
VAR
      I:RECNUM;
BEGIN
  I:=1; OREC:=0;
  GET (OCATFILE);
  WHILE (NOT EOF(OCATFILE)) DO
      BEGIN
        OCATCIJ:=OCATFILE~;
        IF ((OCATEI], VOL_NAME='
                                         ())
          THEN BEGIN
                  OREC:=I-1;
                  ()TOTREC:=OTOTREC+OREC;
                  OFILEEND:=TRUE;
                  EXIT(READ_OLD_CAT);
                END; (if)
        IF (I=OLREC)
          THEN BEGIN
                  OREC:=I;
                  OTOTREC: =OTOTREC+I;
                  EXIT(READ_OLD_CAT);
                END; (if)
        I:=I+1;
        GET(OCATFILE);
      END; {while}
    OREC:=I-1;
    OTOTREC:=OTOTREC+OREC;
    OFILEEND:=TRUE;
END; {readcat}
```

Listing 1 continued on page 418

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```
PROCEDURE WRITECAT;
{writes NREC records to NCATFILE}
  VAR
         I:RECNUM;
  BEGIN
    IF (NTOTREC=0) THEN WITH NCATEO] do
                        BEGIN
                           VOL ... NAME := '
                                               1 ;
                                                         / ;
                           FILE ... NAME: = '
                           FILE_KIND:=UNTYPED;
                           FILE ... DATE . MONTH: = 0;
                          FILE_DATE.DAY:=0;
                          FILE ... DATE . YEAR: = 0;
                          FILE_SIZE:=O;
                          NCATFILE":=NCATEOJ;
                          PUT(NCATFILE);
                        END;
    FOR I:=1 TO NREC DO
      BEGIN
         NCATFILE := NCATEIJ;
         FUT(NCATFILE);
         WRITE((',');
      ENT 9
    WRITELNS
    NTOTREC:=NTOTREC+NREC;
    NREC: #09
    IF DONE THEN CLOSE(NCATFILE, LOCK);
  END; {writecat}
PROCEDURE DISPLAY;
{writes the entire MASTCAT.DAT file to the console}
   VAR
          I:RECNUM;
   BEGIN
    REWRITE(P, 'CONSOLE:');
    IF ( LOOKUP(NFILENAME))
      THEN BEGIN
              NREC:=0;
              RESET(NCATFILE, NFILENAME);
              REPEAT
                READ_NEW_CAT;
                FOR I:=1 TO NREC DO PRINT_RECORD(NCAT[]]);
              UNTIL NFILEEND;
```

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```
CLOSE(NCATFILE);
               WAIT;
             END-(then)
             ELSE WRITELN(NFILENAME, ' NOT PRESENT');
      WRITELN('MASTCAT CONTAINS ',NTOTREC,' RECORDS');
      CLOSE(P);
      WAIT
  END: {display }
PROCEDURE BACKUP;
(compares file names and reports files without backup)
  VAR
          PASS, UNBACK : BOOLEAN;
          N:RECNUM;
  BEGIN
    PASS:=FALSE; UNBACK:=FALSE;
    REWRITE(P, 'CONSOLE:');
    IF ( LOOKUP(NFILENAME))
        THEN BEGIN
                WRITE(CHR(CLEARSCREEN));
                WRITELN('THE FOLLOWING FILES ARE NOT BACKED UP');
                RESET(NCATFILE, NFILENAME);
                REPEAT
                  IF (PASS AND UNBACK)
                      THEN IF (NCATEO].FILE_NAME<>NCATE1].FILE_NAME)
                                THEN PRINT_RECORD(NCATEO1);
                  READ ... NEW ... CAT ;
                  FOR N:=1 TO NREC-1 DO
                     IF ((NCATEN).FILE_NAME <> NCATEN-1).FILE_NAME) AND
                             (NCATEND.FILE_NAME <> NCATEN+10.FILE_NAME))
                                      THEN PRINT ... RECORD (NCATEND);
                  PASS:=TRUE;
                  IF (NCATENREC].FILE_NAME <> NCATENREC-1].FILE_NAME)
                             THEN UNBACK:=TRUE;
                  NCATEOJ:=NCATENRECJ;
                  IF (NFILEEND AND UNBACK) THEN PRINT_RECORD(NCATENRECJ);
                UNTIL NFILEEND;
                CLOSE(NCATFILE);
             END(if)
        ELSE WRITELN(NFILENAME, ' NOT PRESENT');
     CLOSE(P);
     WAIT;
  END; (backup)
```

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```
PROCEDURE UPDATE;
VAR
        DCAT : ARRAY [DIR_SIZE] OF CATALOG_RECORD;
        RN: RECNUM;
 PROCEDURE RENAME:
                         {changes name of MASTCAT, DATA to BACKCAT, DATA}
    VAR
          I:INTEGER;
          SPS:STRING[16];
          VOL , AVOL: VOL_ID;
          DIR:DIRECTORY;
   BEGIN
      UNITREAD(5,DIRCO],2048,2);
     VOL:=DIREOJ.DIR_VOL_NAME;
     SPS:=COPY(BLANKS, 1, 7-LENGTH(VOL));
     AVOL:=CONCAT(VOL,SPS);
     FOR I:=1 TO DIREOJ.NUM_OF_FILES DO
          WITH DIRCID DO
            IF (DIR_FILE_NAME='MASTCAT.DATA')
              THEN DIR_FILE_NAME:='BACKCAT.DATA';
     UNITWRITE(5,DIRCOJ,2048,2);
   END; {rename}
 PROCEDURE WRITEDEX;
 {writes a file of pointers to the first occurrence of each letter in the alpha
   VAR
       DEXFILE : FILE OF INDEXARRAY;
   BEGIN
     REWRITE (DEXFILE, PFILENAME);
     DEXFILE":=DEXRAY;
     PUT(DEXFILE);
     CLOSE (DEXFILE, LOCK);
   END; {writedex}
 PROCEDURE SORT:
 {sorts the directory file in alphabetical order}
   VAR
          I:RECNUM;
          BUF: CATALOG_RECORD;
                                    {holds record during exchange}
          FLAG: BOOLEAN;
                                    (FALSE if an exchange made during pass)
```

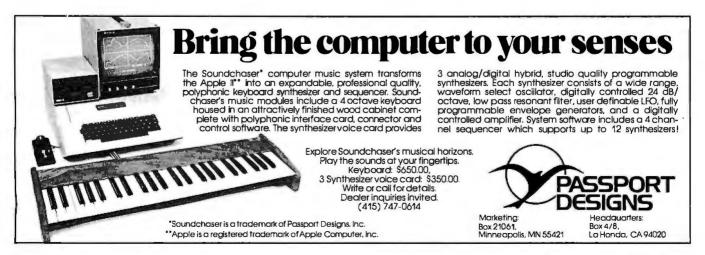
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```
Listing 1 continued:
   BEGIN
     WRITELN('SORTING ',DREC,' RECORDS');
     REPEAT
        FLAG: =TRUE;
        FOR I:=DREC DOWNTO 2 DO
           IF (DCATCI].FILE_NAME < DCATCI-1].FILE_NAME) THEN
                           {exchange routine}
                BUF: = DCATCID;
                DCATCID:=DCATCI-1D;
                DCATCI-13: =BUF;
                FLAG: = FALSE;
              END (Cif)
       WRITE((,');
       UNTIL FLAG;
      WRITELNS
      WRITELN('DONE SORTING');
    END# (sort)
PROCEDURE GETDIR;
₹reads directory of update volume and puts it in DCAT}
  VAR
      DIRX:DIRECTORY;
      UNITHUM, I: INTEGER;
      CHBUF : char;
      VOL: VOL ... ID;
      SPS:STRINGC161;
      BLOCKS_USED: 0..988;
  BEGIN
                          {assumes duplicate directories}
  BLOCKS_USED:=10;
```



ENI);

Listing 1 continued on page 422

```
VOL:=DIRX[0].DIR_VOL_NAME;
SPS:=COFY(BLANKS,1,7-LENGTH(VOL)); {put VOL in consistent format}
VOL.:=CONCAT(VOL,SFS);
FOR I:=1 TO DIRX[0].NUM_OF_FILES DO
                                              {move directors to DCAT}
  BEGIN
    WITH DIRXCID DO
      BEGIN
        IF LENGTH(DIR_FILE_NAME)>0
          THEN
            BEGIN
              DREC:=DREC+1;
              WITH DCATEDRECT DO
                BEGIN
                  VOL_NAME: = VOL;
                  FILE_NAME:=DIR_FILE_NAME;
                  SPS:=COPY(BLANKS,1,15-LENGTH(FILE_NAME));
                  FILE_NAME: = CONCAT(FILE_NAME, SPS);
                  FILE_KIND:=DIR_FILE_KIND;
                  FILE_DATE:=DIR_FILE_DATE;
                  FILE_SIZE:=LAST_BLOCK-FIRST_BLOCK;
                  BLOCKS_USED:=BLOCKS_USED+FILE_SIZE;
                END; {with}
            END; {if length}
      END@ {with dirx}
  END@ (for)
```

Coreate entry with name FREE.SPACE containing the unused space on the volume. DREC:=DREC+1; WITH DOATEDRECT DO Listing 1 continued on page 423

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```
Listing 1 continued:
        BEGIN
          VOL_NAME:=VOL;
          FILE_NAME:='FREE.SPACE'$
          SPS:=COPY(BLANKS,1,15-LENGTH(FILE_NAME));
          FILE_NAME:=CONCAT(FILE_NAME:SPS);
          FILE_KIND:=INFO;
          FILE_DATE:=DIRXCOl.LAST_BOOT;
          FILE_SIZE:=DIRX[0].TOTAL_BLOCKS-BLOCKS_USED;
        END; (with)
  END; {setdir}
  PROCEDURE SETDEX:
  (if first occurance of file name with DEX as first letter then
                               put record number in DEXRAY and increment DEX}
   BEGIN
     IF NCATCNREC].FILE_NAMEC1] >= DEX
     (have we reached or exceeded the next index?)
         THEN BEGIN
                 IF NCATENREC3,FILE_NAMEC13 > DEX
                    THEN REPEAT
                                         {fills dexray to the next valid index}
                           DEXRAYEDEX3:=0;
                           IF DEX='Z' THEN EXIT(SETDEX);
                           DEX: SUCC(DEX);
                         UNTIL (NCATENREC).FILE_NAMEC1] = DEX);
                DEXRAYEDEX3:=NTOTREC+NREC;
                 IF DEX='Z' THEN EXIT(SETDEX);
                 DEX:=SUCC(DEX);
              ENDSCIFE
    END: (setdex)
  PROCEDURE MERGE:
  Knerses DCAT with OCAT to form NCAT>
    VAR
          XyYyZ:1..33;
          CONTINUE: BOOLEANS
          OO,O,D:RECNUM;
    BEGIN
                              {set first match char for index at 'A'}
      DEX:='A';
      O:=OREC:
      OREC:=1;
      1) 1 = 1 $
                               {REMOV is true if volume to be deleted}
      IF (NOT REMOV) THEN VOL:=DCAT[1], VOL_NAME;
                               {DREC+1 is 1 more than the number of files in DCAT}
       WHILE (D < DREC+1) DO
        BEGIN
          WITH DCATEDO DO
            BEGIN-Cwith>
               IF (FILE_NAME < OCATCORECJ.FILE_NAME)</pre>
                   THEN X:=10
                   ELSE IF (FILE_NAME = OCATCORECJ.FILE_NAME)
                           THEN X:=20
                           ELSE X:=30;
               IF (VOL_NAME < OCATEORECJ. VOL_NAME)
                      THEN Y:=1
                      ELSE IF (VOL_NAME = OCATEOREC]. VOL_NAME)
                               THEN Y:=2
                               ELSE Y:=3;
                  Z:=X+Y;
                  IF ((OREC=0) or (OREC>0)) THEN Z:=11;
                                                              Listing 1 continued on page 424
```

```
CASE Z OF
                 11,12,13,21 : BEGIN
                                            {add record to NCAT from DCAT}
                                  NREC:=NREC+1;
                                  NCATENRECJ:=DCATEDJ;
                                  D:=D+1;
                                                               {increment D}
                                  WRITE ( 'ADD
                                                ',NCATENREC],FILE_NAME:18);
                                  WRITELN(NCATENREC], VOL_NAME:10)
                                END;
                 22
                              : BEGIN
                                                 {add record to NCAT from DCAT}
                                  NREC: MREC+1;
                                  NCATENRECJ:=DCATEDJ;
                                  OREC: =OREC+1;
                                                              {increment OREC}
                                  D := D + 1
                                                              {increment ID}
                                END;
                 23,31,33
                              # BEGIN
                                                 {add record to NCAT from OCAT}
                                  NREC:=NREC+1;
                                  NCATENRECJ:=OCATEORECJ;
                                  OREC: =OREC+1;
                                                              {increment OREC}
                                END
                 32
                              : BEGIN
                                                      {do not add record to NCAT}
                                  WRITE('DELETE ',OCATCORECJ.FILE_NAME:18);
                                  WRITELN(OCATEORECJ, VOL_NAME:10);
                                  OREC:=OREC+1;
                                                              {increment OREC}
                                END;
             END; {case of Z}
         SETDEX;
                          {check poniter index}
       END; {with}
   IF (NREC=NLREC) THEN WRITECAT;
                                        {NLREC is the max array size}
   IF ((OREC>OLREC) AND (NOT OFILEEND)) {if you are out of OCAT get some more}
         THEN BEGIN
                READ_OLD_CAT;
                 O:=OREC;
                OREC:=1;
              END; (if)
  END; {while}
                        {DCAT is empty}
                        {set whats left of OCAT}
REPEAT
  CONTINUE:=FALSE;
  IF (OREC<=0)
        THEN FOR OO:=OREC TO O DO
                 IF (OCATEOO).VOL_NAME <> VOL)
                      THEN BEGIN
                              NREC:=NREC+1;
                              NCATENRECJ:=OCATEOOJ;
                              IF (NREC=NLREC) THEN WRITECAT;
                              SETDEX;
                            END{then}
                     ELSE BEGIN
                             WRITE('DELETE ',OCATEOO].FILE_NAME:18);
                             WRITELN(OCATEOOJ, VOL_NAME:10)
                           END; {else}
  IF (NOT OFILEEND) THEN BEGIN
                                         {if you are out of OCAT set some more}
                            READ_OLD_CAT;
                            O:=OREC;
                            OREC:=1;
                            CONTINUE:=TRUE;
                          END; (if)
UNTIL (NOT CONTINUE);
IF (DEX <'Z')
  THEN FOR CH:=DEX TO 'Z' DO DEXRAYCCHJ:=DEXRAYCFRED(DEX)]; Listing 1 continued on page 425
```

```
DONE: = TRUE;
 WRITECAT;
 WRITEDEX;
END; {match}
BEGIN-Curdate>
  REWRITE(P, 'CONSOLE:');
  IF LOOKUP (OFILENAME)
      THEN BEGIN
              RESET(OCATFILE, OFILENAME);
              CLOSE(OCATFILE, PURGE);
                                                   {remove old BACKCAT}
            END; (if)
  RENAME #
                                                   {MASTCAT --> BACKCAT}
  IF (NOT REMOV)
            THEN BEGIN
                    GETDIR;
                    SORT;
                    FOR RN:=1 TO DREC DO PRINT_RECORD(DCATERNI);
                  END; {if}
  IF LOOKUP(OFILENAME)
        THEN BEGIN
                RESET(OCATFILE, OFILENAME);
                READ_OLD_CAT;
             END(if)
        ELSE OREC:=0;
  REWRITE(NCATFILE, NFILENAME);
  NREC: = 0;
  MERGE #
  CLOSE (OCATFILE);
  CLOSE(P) #
  WRITELN('BACKCAT CONTAINS
                               ',OTOTREC,' RECORDS');
  WRITELN('MASTCAT CONTAINS
                               ',NTOTREC,' RECORDS');
  CLOSE(NCATFILE, LOCK);
  WAIT
END; {update}
PROCEDURE SEARCH;
    VAR
        STOP, FOUND : BOOLEAN;
        TAR1, TAR2: CHAR;
        START: INTEGER;
        WILDCARD: 0..16;
        CAT: CATALOG_RECORD;
        TARGET, SPS: STRING;
   PROCEDURE LONGSEARCH;
   √search used when alphabetical pointer cannot be used }
VAR
            N:RECNUM;
BEGIN
  DELETE(TARGET, 1, 1);
                                      {remove wildcard char}
  writeln(TARGET);
  REPEAT
    READ_NEW_CAT;
    FOR N:=1 TO NREC DO IF POS(TARGET, NCATEN], FILE_NAME) <> 0
                                THEN PRINT RECORD (NCATEND);
  UNTIL (NFILEEND);
  CLOSE(NCATFILE);
  WAIT;
                                                                 Listing 1 continued on page 426
```

```
CLOSE(P);
       EXIT(SEARCH)
     END; {longsearch}
    PROCEDURE SEARCH_FOR_VOLUME;
     VAR
                 BLKS, SPS:STRING[7];
                 N: RECNUM;
     BEGIN
       BLKS:='
                      1 ;
       DELETE(TARGET, POS(':', TARGET), 1);
       SPS:=COPY(BLKS,1,7-LENGTH(TARGET));
       TARGET:=CONCAT(TARGET,SFS);
       writelm(TARGET);
       REPEAT
         READ_NEW_CAT;
         FOR N:=1 TO NREC DO
           IF (NCATEN].VOL_NAME=TARGET) THEN PRINT_RECORD(NCATEN]);
       UNTIL (NFILEEND);
       CLOSE(NCATFILE);
       WAIT
       CLOSE(P);
       EXIT(SEARCH)
     END@ (vsearch)
BEGIN-(search)
 STOP:=FALSE;FOUND:=FALSE;
  REPEAT
    WRITE('ENTER NAME OF FILE TO BE FOUND--> ');
    READLN(TARGET);
    IF(LENGTH(TARGET)>16) THEN WRITELN('NAME TOO LONG ');
 UNTIL (LENGTH(TARGET) <= 16);
  IF (POS('<',TARGET)=1)</pre>
                             {'<' sends output to printer}</pre>
            THEN BEGIN
                    DELETE(TARGET,1,1);
                    REWRITE(P, 'PRINTER:');
                 END(if)
            ELSE REWRITE(P, 'CONSOLE:');
 RESET(NCATFILE, NFILENAME);
 IF (POS(':',TARGET)<>0) THEN SEARCH_FOR_VOLUME;
  WILDCARD:=POS('=', TARGET);
  IF (WILDCARD = 1) THEN LONGSEARCH;
  IF (WILDCARD > 1) THEN TARGET:=COPY(TARGET,1,WILDCARD-1);
  TAR1:=TARGET[1];
                                   {TAR1 used to set pointer from DEXRAY}
  IF (WILDCARD <> 2)
                                   {TAR2 used to end search}
       THEN TAR2:=TARGET[2]
       ELSE TAR2:='z';
IF (TAR1 < 'A')
    THEN START:=0
    ELSE IF (TAR1 > 'Z')
          THEN START:=DEXRAY['Z']
          ELSE START: =DEXRAYCTAR1];
SEEK(NCATFILE, START);
GET (NCATFILE);
REPEAT
  CAT:=NCATFILE()
  IF ((WILDCARD = 0) AND (POS(TARGET,CAT,FILE_NAME) = 1))
                                            THEN BEGIN
                                              PRINT_RECORD(CAT);
                                              FOUND: =TRUE;
                                                                Listing 1 continued on page 427
                                            END;
```

```
IF ((WILDCARD > 1) AND (POS(TARGET, CAT, FILE_NAME) >= 1))
                                              THEN BEGIN
                                                PRINT_RECORD(CAT);
                                                FOUND:=TRUE;
                                             ENTI;
    IF ((CAT,FILE_NAME(1) > TAR1 ) OR (CAT,FILE_NAME(2) > TAR2))
                  THEN STOP:=TRUE;
    GET(NCATFILE);
  UNTIL (STOP OR EOF(NCATFILE));
  IF (NOT FOUND) THEN WRITELN('FILE ', TARGET,' NOT FOUND');
  CLOSE(NCATFILE);
 CLOSE(F);
  WAIT
END # (SEARCH)
BEGIN {main}
  IF ((NOT LOOKUP(NFILENAME)) OR (NOT LOOKUP(PFILENAME))) THEN INITIALIZE;
  GET_SYS_VOL(SYSTEMVOLUME);
                                {record system volume name for rebooting}
  DLREC:=MAXREC;OLREC:=MAXREC;NLREC:=MAXREC;
  READDEX;
                        {load the pointer array}
  REPEAT
    REMOV:=FALSE;NFILEEND:=FALSE;OFILEEND:=FALSE;DONE:=FALSE;
    NREC:=0;OREC:=0;DREC:=0;
    NTOTREC:=0;OTOTREC:=0;
    VOL.:='
    REPEAT
      WRITE(CHR(CLEARSCREEN));
      MEM('MAIN');
      WRITE('CATALOG --> S)earch D)isplay B)ackup U)pdate R)emove Q)uit');
      READ(KEYBOARD, CH);
      WRITELN;
    UNTIL (CH IN ['R', 'r', 'B', 'b', 'U', 'u', 'S', 'S', 'I', 'd', 'Q', 'Q', 'G']);
    CASE CH OF
          'U','u' # UPDATE;
          'S','s' # SEARCH;
          'D','d' | DISPLAY;
          'R'''' : BEGIN
                       REMOV: =TRUE;
                       ENTER_VOL_NAME;
                      UPDATE
                    END9 (case of R)
          'B','b' : BACKUP;
          'Q','a' : REPEAT
                       GET_SYS_VOL(TEST);
                       IF (TEST=SYSTEMVOLUME)
                             THEN EXIT(CATALOG)
                             ELSE WRITELN('INSERT SYSTEM DISK AND PRESS RETURN');
                       READLN(CH)
                    UNTIL CH#/P/; .
         END; {case}
  UNTIL (CH IN ['Q','a']);
END.
```

BYTE's Bits

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Programming Quickies

Printf for the C Function Library

Christopher Kern, 201 I St SW, Apt V-839 Washington DC 20024

One of the most-used functions in the standard library for the C programming environment is printf, the formatting print function. Printf accepts character, string, and numeric values as arguments and sends them to the standard output (normally the user's console) according to a specified format. It is used both as the main way to provide a program's output to the console and as a way of testing variable values during debugging. Its control-format string may specify that numerical values be represented in hexadecimal, octal, or decimal notation, that right or left justification be employed, and that arguments be printed in a given field width or restricted to a limited precision.

Although present versions of the BDS C compiler for the 8080 CP/M operating system have the standard printf function, earlier versions had a more primitive version of printf. If you have a version that *cannot* print numerical data in octal, does not permit precision to be specified to limit the length of a string, and only left justifies, the program shown in listing 1 will add all the standard features and a few new ones.

Except for the features that apply only to floating-point and long numerical data, this program conforms to the specifications for printf in Kernighan and Ritchie's *The C Programming Language* (Prentice-Hall, 1978). It is simple to adapt printf to other languages, so long as they permit functions, procedures, and subroutines with a variable number of arguments.

Functions compiled with the BDS C compiler find their arguments along an array of vectors stored at location BASE + 0x3f7, where BASE is the base address of the CP/M operating system for the particular machine being used (and "0x3f7" is C's idiosyncratic notation for hexadecimal 3F7). Up to twenty-four arguments are allowed. Because printf doesn't know in advance how many arguments will be needed as interpretation of the control format proceeds, and because the same function-argument vector will be used by subordinate functions called by printf, all the arguments are collected at the outset and stored in local argument array, "localarg[]." This is the one feature of the function that is specific to the BDS compiler. Note that because the control format is passed to printf as a formal parameter, the processing of the remaining arguments begins at FARGV + 2.

Listing 2 shows a sample run and a demonstration program that exercises printf by printing a series of integers in various notations and by printing a string in various

Text continued on page 434

Listing 1: This is a program for adding a full-featured printf function to some early versions of C compilers. These earlier versions did not allow the printing of numerical data in octal, and did not permit precision to be specified to limit the length of a string; they allowed only left justification. Two new functions which are called by printf have been added: "Nbase" converts a binary integer into a digit string in the requested radix; "Nspoct" does the same for split octal.

```
0×4200
                                 /* CP/M base address */
#define BASE
                                 /* BDS C compiler argument vector */
                         0 \times 3 f 7
#define FARGV
grintf(control)
char *control;
        char cy *es, rjustify, s[17], zerofill;
        int *args, k, localarg[23], proism, slem, width;
        /* copy arguments from function argument vector */
        for (k = 0) area = BASE + FARGV + 2; k < 23; ++k, ++area)
                localars[k] = *arss;
        arss = localars;
        while (c = *control++)
                /* check for conversion specification */
                if (c == '%') {
                         /* check for various options */
                         if ((c = *control) == '-') {
                                 rjustify = 0;
                                 c = *control++;
                         3.
                         else
                                 rjustify = 1;
                         if (c == '0')
                                 zerofill = 1;
                         else
                                 zerofill = 0;
                         width = 0;
                         while (isdigit(c = tolower(*control++)))
                                 width = 10*width + c - '0';
                         if (c == '.') {
                                 ercisn = 0;
                                 while (isdisit(c = tolower(*control++)))
                                          Preisn = 10*Preisn + c - '0';
                         3
                         else
                                 prcisn = 32767;
                         /* Process conversion characters */
                         switch (c) {
                         case 'b':
                                 PS = nbase(*args++, 2, s);
                                 breaki
                         case 'o':
                                 Ps = nbase(*args++, 8, s);
```

Listing 1 continued:

```
case 'd':
                         if (*arss < 0) {
                                 ps = nbase(-*args++, 10, s);
                                 *-- PS = '-';
                         3
                        else
                                 ps = nbase(*arss++, 10, s);
                        breaki
                case 'u':
                         ps = nbase(*args++, 10, s);
                         breaki
                case 'x':
                         ps = nbase(*args++, 16, s);
                         breaki
                case 'a':
                         ps = nspoct(*args++, s);
                         breaki
                case 's':
                         Ps = *args++;
                         breaki
                case 'c':
                         c = *arss++;
                default:
                         *(PS = S) = Cf
                         s[1] = '\0';
                }
                k = strlen(ps);
                slen = k > prcisn ? prcisn : k;
                if (rjustify)
                         while (width-- > slen)
                                 if (zerofill)
                                          putchar('0');
                                 else
                                          putchar(' ');
                for (k = 1; *ps && k <= prcisn; ++k)
                         putchar(*ps++);
                 if (!rjustify)
                         while (width-- > slen)
                                  putchar(' ');
        }
        else
                putchar(c);
nbase(n, base, s)
unsigned ny base?
char *s;
-{
        int di
        *(s += 16) = ' \setminus 0';
        if (n == 0)
```

breakf

Programming Quickies _

*PS = '\0';
return s;

```
Listing 1 continued:
                  *--s = '0';
         else
                  while (n > 0) {
                           *--s = (d = n\%base) + (d < 10 ? '0' : 55);
                           n /= base;
                  "}-
         return si
3
nspoct(ny s)
unsigned of
char stl;
1
         int d \neq d = 16384 \neq
         char *ps; ps = s;
         while (d > 0) \in
                  *ps++ = n/d + '0';
                  n %= di
                  if (d == 256) {
                           d = 649
                            *Fs++ = '.'$
                  3.
                  else
                           d /= 8;
```

Listing 2: Listing and sample run of a demonstration program which exercises the printf function.

```
A>TYPE PRINTX.C
main()
1
        unsigned if
        char *string; string = "hello, world";
        for (i = 1; i \leq= 16384; i *= 2) {
                rrintf("dec: %5d oct: %60 seloct: %a ", i, i, i);
                printf("hex: %4x bin: %016b\n", i, i);
        }
        printf("\n");
        printf(":%10s:\n", string);
        printf(":%-10s:\n", string);
        printf(":%20s:\n", string);
        Printf(":%-20s:\n", string);
        printf(":%20.10s:\n", string);
                                                            Listing 2 continued on page 434
```

Programming Quickies_

```
Listing 2 continued:
Printf(":%-20.10s:\n", string);
Printf(":%.10s:\n", string);
```

A>PRINTX

٦.

```
dec:
          1
             oct:
                        1
                           sploct:
                                    000.001
                                             hex:
                                                      1
                                                         bin: 000000000000001
          2
dec:
             oct:
                        2
                           sploct:
                                    000.002
                                                      2
                                             hex:
                                                         bin: 0000000000000000
dec:
          4
             oct:
                        4
                           sploct:
                                    000.004
                                             hex:
                                                      4
                                                         bin: 000000000000100
dec:
          8
             oct:
                       10
                           sploct:
                                    000.010
                                             hex:
                                                      8
                                                         bin: 000000000001000
dec:
        16
             oct.:
                       20
                           sploct:
                                    000,020
                                             hex:
                                                     10
                                                         bin: 000000000010000
dec:
        32
             oct:
                       40
                           sploct:
                                   000.040
                                             hex:
                                                     20
                                                         bin: 000000000100000
dec:
        64
                     100
                           sploct:
                                    000.100
                                                     40
             oct:
                                             hex:
                                                         bin: 000000001000000
dec:
       128
             oct:
                     200
                           seloct:
                                    000,200
                                             hex:
                                                     80
                                                         bin: 000000010000000
                                    001.000
dec:
       256
             oct:
                     400
                           seloct:
                                             hex:
                                                    100
                                                         bin: 000000010000000
                           sploct:
       512
                    1000
                                    002,000
                                             hex:
                                                    200
                                                         bin: 000000100000000
dec:
             oct:
                           select: 004.000
                                             hex:
                                                    400
                                                         bin: 000001000000000
dec:
      1024
             oct:
                    2000
                                    010,000
                                             hex:
                                                    800
                                                         bin: 000010000000000
      2048
                    4000
                           sploct:
dec:
             oct:
                                    020,000
                                                   1000
                                                         bin: 000100000000000
      4096
             oct:
                   10000
                           sploct:
                                             hex:
dec:
                                                         bin: 001000000000000
                   20000
                           sploct:
                                   040,000
                                             hex:
                                                   2000
      8192
             oct:
dec:
                   40000
                           sploct: 100.000
                                             hex:
                                                   4000
                                                         bin: 010000000000000
dec:
     16384
             oct:
```

```
:hello, world:
;hello, world:
; hello, world
; hello, world
; hello, wor
;hello, wor
;hello, wor;
```

0>

Text continued from page 430:

combinations of justification, field width, and precision (the ":" serves to delimit the field). Calls to printf take the form:

```
printf(control, argument 1, argument 2, ...)
```

where "control" is a format string composed of text interspersed with conversion specifications—one for each argument.

Each conversion specification begins with the "%" character and ends with a conversion character indicating the format to be used in printing the corresponding argument (character, string, or number). The standard conversion characters "d" (decimal notation), "u" (unsigned decimal), "o" (octal), "x" (hexadecimal), "c" (character), and "s" (string), are supported. I have added two others not specified in Kernighan and Ritchie's book: "b" (binary notation), which is especially useful for debugging programs that use bitwise logical operators, and "q" (split octal), because the front panel of my Heath H-8 computer has a split-octal display.

A number of options may be specified between the "%" character, which introduces the conversion specification, and the conversion character. A minus sign (—) indicates that left justification (instead of the default

right justification) is requested. A digit string indicates the field width; a number that fails to fill the width will be padded on the left or right, as necessary. If the field width is specified with a leading zero, a right-justified number will be padded with zeros instead of blanks, so an 8-bit binary number can be printed as 00100101 instead of 100101. A period followed by another digit string indicates the precision, the maximum field width in which an argument is to be printed; this is primarily useful for truncating strings that exceed the permissible line length.

This version of printf uses four other standard C library functions: "tolower(character)," which converts its argument to lowercase if it isn't lowercase already; "isdigit(character)," which returns true (not zero) if its argument is a digit and false (zero) otherwise; "putchar (character)," which outputs a character to the console; and "strlen(pointer to string)," which returns the length of the string its argument points to.

Two other functions, called by printf and independently useful additions to the standard library, are also included (see listing 1). "Nbase(number, base, pointer to array in which to store result)" converts a binary integer to a digit string of the requested number base. "Nspoct (number, string pointer)" does the same (with leading zeros, and a "." separating the 2 bytes) for the special case of split octal.■

Numerical Methods in Data Analysis

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In engineering research and design work, it is often necessary to determine analytically from a given set of n pairs of discrete data a function which best represents the dependence of one parameter (X) upon the other (Y). Moreover, other characteristics of the obtained function represent this dependence, such as information about its stationary (maximum or minimum) point and its roots, that is, values of X which make Y equal to zero.

Calling on our mathematical background, we know that most continuous functions with defined derivatives may be expressed in a form of a polynomial:

$$Y = a_0 + a_1X + a_2X^2 + a_3X^3 + \dots + a_mX^m$$

where m is the degree of the polynomial and a_0, a_1, \ldots, a_m are the coefficients.

For a given set of n pairs of data, there is usually a polynomial of degree m with corresponding coefficients a_0, a_1, \ldots, a_m which will approximately describe the general continuous relationship between the two parameters X and Y. The error incurred in obtaining this polynomial will usually be minimal when m is sufficiently large and useful values of Xs and Ys are in the neighborhood of the range $[(X_1, Y_1), (X_n, Y_n)]$ where $X_1 < X_2 < \ldots < X_n$.

By definition, the stationary point of a function is the point at which the dependent parameter Y attains a local maximum or minimum value. This stationary value of the variable X may be obtained by solving the equation Y' = 0, or:

$$a_1 + 2a_2X + 3a_3X^2 + \dots + ma_mX^{m-1} = 0$$

The determination of function Y = f(X) may be done by curve fitting, which requires solving a large set of

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Mr Nguyen has devoted much of his time to the application of computer programming in solving engineering problems. He is presently a senior engineer with the Pipe Hanger Division of ITT Grinnell Corporation. simultaneous linear equations. The Gauss-Jordan elimination method may be utilized to solve these simultaneous equations. Once the function f(X) is obtained, the values of quantity X for which f(X) equals zero may be calculated by the Newton-Raphson method, which is one of the various numerical methods for obtaining the roots of a continuous differentiable function.

Because many calculations will be performed repetitively, these tasks will be conveniently handled by a digital computer utilizing its ability for high-speed calculations. A scientific high-level language, such as FORTRAN IV, is a suitable language for the development of a computer program for use in this application.

This article will briefly review the principle of curve fitting, the Gauss-Jordan elimination technique, and the Newton-Raphson method. Included is a computer program written in FORTRAN IV with corresponding flowchart and explanations. Examples of practical engineering problems in different fields are also presented.

Curve Fitting: Method of Least Squares

In fitting a curve through the points representing $(X_1, Y_1), \ldots, (X_n, Y_n)$, we employ a mathematical principle that yields a *best-fit curve*: the method of least squares. This method utilizes the laws of probability in obtaining the most probable values for a given set of observations of independent and dependent parameters. According to this method, the coefficients a_0, a_1, \ldots, a_m of a polynomial of degree m may be determined from the following m+1 simultaneous equations:

$$c_{11}a_{0} + c_{12}a_{1} + c_{13}a_{2} + \dots + c_{1\{m+1\}}a_{m} = b_{1}$$

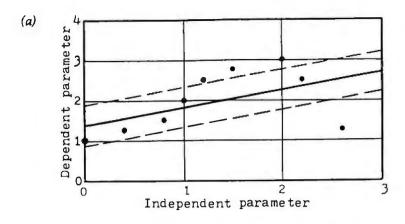
$$c_{21}a_{0} + c_{22}a_{1} + c_{23}a_{2} + \dots + c_{2[m+1]}a_{m} = b_{2}$$

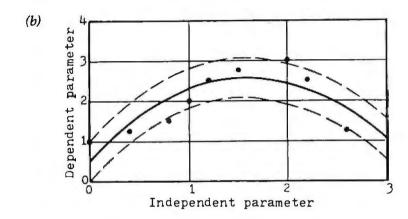
$$\vdots \qquad \vdots \qquad \vdots$$

$$c_{[m+1]}a_{0} + c_{[m+1]2}a_{1} + \dots + c_{[m+1][m+1]}a_{m} = b_{[m+1]}$$

where:

$$b_i = \sum_{i=1}^{n} x^{i-1} y$$
$$c_{ij} = \sum_{i=1}^{n} x^{i+j-2}$$





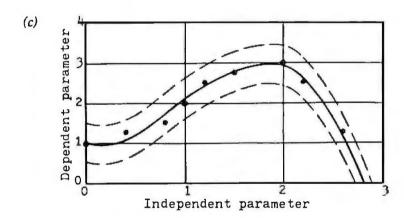


Figure 1: A representation of the least-squares curve-fitting method. In (a) we see the first-degree curve, which is not acceptable because the uncertainty envelope does not contain all the data points. The figure in (b) shows the second-degree curve, which is not acceptable for the same reason as (a). The third-degree curve is illustrated in figure (c). Here we can observe that the uncertainty envelope does contain all the data points and is, therefore, the desired degree of the least-squares polynomial.

and the summations Σ are performed from 1 to n, the number of pairs of data.

Most engineering data is taken with an uncertainty margin. This margin may be expressed as an absolute deviation or as a relative deviation, such as 50 ± 0.5 inches and 50 inches $\pm 1\%$, respectively. Therefore, when the uncertainty envelope has the most probable least-squares curve as its center line, it also has to cover all the given data points. This condition is illustrated in figure 1.

We usually start with a least-squares equation of relatively low degree and then check to see if all data points fall inside the uncertainty envelope before proceeding to the next higher degree least-squares equation. The process will continue until the uncertainty requirements are satisfied.

Gauss-Jordan Elimination Method

After all the summations of the set of simultaneous equations in equation (1) are calculated, our next step is to solve the set of simultaneous equations for a_0, a_1, \ldots, a_m . Although there are numerous techniques to handle this task, the method presented here is the Gauss-Jordan elimination method. The reason for using this method in-

	Variable D	Definitions	
FORTR AN		N	DO loop index for loop which calculates
FORTRAN Variable	Definition	NCODE	DO loop index for loop which calculates root of $f(X) = 0$
A (M)	a _m , the mth coefficient of a least-squares	NCODE	Code used before subroutine NEWRAP is called to indicate whether the calculation will be for $X_{Y=0}$ or X_{STA} (1: for $X_{Y=0}$,
C (I, J)	polynomial cu, element at ith row and jth column of) IFIAITON	2: for X_{STA})
	the augmented matrix of the set of $m+1$ simultaneous equations to be solved for a_0, \ldots, a_m	NEWTON	Code used in subroutine NEWRAP hav- ing the same function as NCODE; its value is transmitted from main program
ERR	$ f(X_n)/f'(X_n) $, absolute value of the nth incurring error in the determination of X	NITERA	Before the iteration process: maximum allowable number of iterations,
	for which $f(X) = 0$ by Newton-Raphson method		transmitted from main program; after the iteration process: actual number of
ERROR	ε, general term for allowable error (at the beginning of the iteration process) or resulting error (at the end of the process)		iterations used to obtain the required ac- curacy ε (this new value will be returned to main program)
	in the determination of $X_{Y=0}$ and X_{STA} ,	NMINUS	N-1
ERT_	used in subroutine NEWRAP $\epsilon_{r=0}$, allowable error in the determina-	NPAIRS NPLUS	Number of pairs of data N+1
The Control	tion of $X_{Y=0}$	NRERUN	Code to direct the calculation flow to the
EROOT	Error in the determination of $X_{r=0}$ (before calling subroutine NEWRAP: allowable error; after: resulting error)		beginning of the program (NRERUN=1) or only to the portion computing $X_{r=0}$ and X_{STA} (NRERUN=0)
ESTN	ϵ_{STA} , allowable error in the determination of X_{STA}	NROOT	Similar to NITERA, except that it is in main program and is used primarily for
ESTATN	Error in the determination of X _{STA} (before calling subroutine NEWRAP: allowable error; after: resulting error)	NSTATN	calculating $X_{r=0}$ Similar to NITERA, except that it is in main program and is used primarily for
I	DO loop index	SUM	calculating X_{STA} Σs , summations representing b_i or c_{ij}
ICHANG	String input specifying the name of the particular variable of which the value is to be changed	UNCERT	Uncertainty margin, may be entered as absolute or relative value
ICONTI	String input (YES or NO) to continue or to stop the process of changing values of	UNMARG	Uncertainty margin, calculated from the given UNCERT and IUNCER, and is
	some variables	X(N)	converted into an absolute value Xs, data entered as independent
IROOT	Code indicating whether the calculation of $X_{r=0}$ is needed or not (0: No, 1: Yes)		parameters
ISTATN	Code indicating whether the calculation	XO ,	Before the iteration process: initial approximation of X_n transmitted from
IUNCER	of X _{STA} is needed or not (0: No, 1: Yes) Code indicating whether the uncertainty		main program; after the iteration pro- cess: obtained value of X, which satisfies
	is entered as absolute or relative value (0: absolute, 1: relative)	, ×	the required accuracy (this new value
J	DO loop index	XS(N)	will be returned to main program) X_n , nth value of iterated X in Newton-
K KPLUS	DO loop index K+1		Raphson formula
LROOT	ROOT, string variable for printout purpose	XRT1	Similar to X0, except that it is in main program and is used primarily for
LSTAT	STATN, string variable for printout purpose	XSTN1	calculating X _{Y=0} Similar to X0, except that it is in main program and is used primarily for
M	DO loop index m, degree of the least-squares polyno-		calculating X _{STA}
MDEG	mial to be fitted through the given set of data, used as the first trial	Y(N)	Ys, data entered as dependent parameters
MDEGRE	Incrementing m, starting from MDEG to a maximum of 10	YDEN	f'(X,), denominator value in Newton- Raphson formula
MMINUS	M-1	YNUM	f(X _n), numerator value in Newton- Raphson formula
MPLUS1 MPLUS2	MDEGRE+1 MDEGRE+2	YOFX	Y(X), value of Y corresponding to a given value of X

Listing 1: FORTRAN listing of the program CURFIT that solves the least-squares polynomial for the entered pairs of data X(n) and Y(n). Some language features used here differ from standard FORTRAN.

```
01110*****
01120 PRINT 10.MDEGRE.HPLUS1.MDEGRE
01130 D0 500 H=1.MFLUS1
01140 HMINUS=H-1
01150 PRINT 20.HMINUS.A(H)
01160 500 CONTINUE
 00100 PROGRAM CURFIT (INPUT, DUTPUT)
00110 DIMENSION X(100),Y(100),A(11),C(11,12)
00120 COMMON/BLOCK/A,MPLUS1,MPLUS2
  00130*****
00140***** DATA STATEMENTS
 00140**** DATA STATEMENTS
00150*****
00160 DATA NPAIRS, MDEG, IUNCER, UNCERT, IROOT, XRT1, ERT, ISTATN, XSTN1, ESTN/
00170+10,1:0,-1:1,-1,-,001,1:0...001
00180+1, X/
00190+-2,-1.5,-1.,0.,1:,2.,2.5,3.,4.,5.
00200+/-Y/
00190-25.1,-6.9,3.1;5.,-6.9,-21,,-25,1;-7.,45.
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    01170****
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     01180**** CALCULATION OF VALUES OF XF: OUT OR XSTA
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               0180**** CALCULATION OF VALUES OF XF:00T OR XSTA
01900****
01200 IF (**RUEGRE.EG.1) IRODT=0
01210 SSO IF (**RUDT.NE.1) GD TO 620
01210 SSO IF (**RUDT.NE.1) GD TO 620
01230 600 NCODE = 1
01240 CALL NEWRAP (**KT1.ERDOT.NRODT.NCODE)
01250 IF (**RUDT.T.20) GO TO 610
01260 PRINT 30.NRODT.LRODT.XT1.FRODT.LRODT
01270 READ.XRT1.ERT
01290 IF (**XRT1.EG.0..AND.ERODT.EG.0.) GO TO 626
01300 GD TO 600
01310 610 PRINT 40.NRODT.RROT.XRT1
01320 620 IF (**ISTATN.NE.1) GO TO 700
01310 610 PRINT 40.NRODT.RROT.XRT1
01320 620 IF (**ISTATN.NE.1) GO TO 700
01330 630 NCODE = 2
01340 CALL NEWRAP (**XSTN1.ESTATN.NSTATN.CUBE)
01350 IF (**NSTATN.LT.20) GO TO 640
01360 PRINT 30.NSTATN.LSTAT.XSTN1.ESTATN.LSTAT
01370 READ.XSTN1.SSTN
01380 ESTATN=ESTN
01380 ESTATN=ESTN
01380 ESTATN=ESTN
01390 IF (**XSTN1.E0.0..AND.FSTATN.EU.0.) RU 10 700
01410 640 FRINT 40.NSTATN.LSTAT.XSSN1
00230****
00240***** FORMAT STATEMENTS
00240***** FORMAT (//2X,12HTHE DESIRED ,12,47H-TH DEGREE LEAST-SQUARES EQUATION HA
00260 10 FORMAT (//2X,12HTHE DESIRED ,12,47H-TH DEGREE LEAST-SQUARES EQUATION HA
00270+S A FORM OF ,/5X,14HY(X) = SUM OF ,12,19H-TERHS OF A(1)*X**I,5X,12HI = 0,1
00260+..., 12,//20X,1HI.5X,4MA(1),/19X,3H---,2X,8H------,/)
00290 20 FORMAT (19X,12,3X,F8,3)
00300 30 FORMAT (//2X,6MAFTER ,12,35H ITERATIONS, THE OBTAINED VALUE OF ,A6,3H II
00310+S ,F8,3,7H GIVING,/12HAN BERRON OF ,78,5,2X,33HIF YOU WANT TO TEX YEW VALUE
00320+S DF, ,A6,1H AND ERROR:,/45HENTER THEM IN THAT ORDER: IF NOT: ENTER 0.,0.)
00330 40 FORMAT (/2X,6MAFTER ,12,35H ITERATIONS, THE OBTAINED VALUE OF ,A6,4H IS
00340+ ,F8,3)
00350 50 FORMAT (//2X,*ZD YOU WANT TO CHANGE ANY VARIABLES AHONG HDEG. UNCERT, E
00360+RT, ESIN:*,*/11,**XRT1, XSTM1, IUNCER, IROUT, ISTATH ? (YES OR N)*)
00370 40 FORMAT (/2X,*ENTER THE VARIABLE TO BE CHANGED (HIT RETURN), AND THEN IT
00380+S NEW VALUE*)
  00230****
    0038045 NEW VALUE*)
00390 70 FORHAT (/2X,*ANY HORE VARIABLES TO BE CHANGED 7*)
00400 80 FORHAT (//2X,*ALTHOUGH A *,12,*-TH BEGREE LEAST SQUARES CURVE HAS BEEN
00410+FITTED THROUGH THEA,/1X,*GIVEN SET OF IATA, THE SPECIFIED UNCERTAINTY MARG
00420+IN IS NOT YET SATISFIED*)
00430 90 FORHAT (2X,*THE CORRESPONDING VALUE OF Y(XSTATH) IS *,F8.3)
00430 90 FORHAT (Y,2X,*THE SPECIFIED DEGREE OF THE LEAST SQUARES EQUATION IS =>
00450+ THE NUMBER*,/1X,*OF PAIRS OF DATA. REENTER HDEG (< NFAIRS )*)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              01400 GD TO 630
01410 640 FRINT 40,NSTAIN,LSTAI,XSFN1
01420 YOFX=0.
01430 DO 650 H=1,HF-LUSI
01440 YOFX=YOFX+C(H,MPLUS2)*XSTNI**(H-1):
01450 650 CONTINUE
01440 PRINT 90,70FX
01470*****
01470*****
01490*****
01490*****
01500 700 PRINT 50
01510 READ, ICONTI.
01520 IF (ICONTI.EG,2HNO) GD TO 800
01530 NERUN=0
01540 710 PRINT 60
01540 710 PRINT 60
01540 710 PRINT 60
01550 READ, ICHANG
01560 IF (ICHANG.EG,4HMDEG) READ,HDEG
01550 READ, ICHANG
01550 FREAD, ICHANG
01550 IF (ICHANG.EG,4HMDEG) READ,HDEGT
01590 IF (ICHANG.EG,4HMDEG) READ,HDEGT
01590 IF (ICHANG.EG,4HMDEG) READ,HDEGT
01590 IF (ICHANG.EG,4HMDEG) READ,XSTN1
01600 IF (ICHANG.EG,4HKRTI) READ,XSTN1
01610 IF (ICHANG.EG,5HXSTNI) READ,XSTN1
01620 IF (ICHANG.EG,5HXSTNI) READ,XSTN1
01630 IF (ICHANG.EG,5HKSTNI) READ,XSTN1
01630 IF (ICHANG.EG,5HKSTNI) READ,XSTN1
01640 IF (ICHANG.EG,5HKSTNI) READ,XSTN1
01650 NRODT=NTATN=20
01460 FRODT=ERT
     00460*****
00470***** DEFINITIONS OF SOME VARIABLES
  00470#### DEFINITIONS OF SOME VARIABLES
00480#### DEFINITIONS OF SOME VARIABLES
00480#### DEFINITIONS OF SOME VARIABLES
00500 LSTAT=6HXSTATN
00500 LSTAT=6HXSTATN
00510 110 HDEGRE=HDEG
00520 112 ERODT=ERT
00530 ESTATN=ESTN
00540 115 IF (HDEGRE.LT.NPAIRS) GO TO 120
00550 PRINT 100
00550 HT-USI=HDEGRE+1
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    01640 IF (ICHANG.E0.6HISTATN) READ-JSIAIN
01650 NROUI-SMATATN=00
01660 EROUI-SET
01670 ESTATN=ESTN
01670 ESTATN=ESTN
01680 IF (ICHANG.E0.4HMDEG.OR.ICHANG.E0.6HUNCERT.OR.IDHANG.EQ.IUNCER) NRERUN=1
01690 PRINT 70
01700 READ-ICONTI
01710 IF (ICUNTI.E0.3HYES) GO TO 710
01720 IF (NRERUN.EO.1) GO TO 110
01730 GO TO 550
01740 BOO STOP
01750 END
01760*****
        00630****
       00640 DO 210 I=1, HPLUS1
00650 DO 200 J=1, HPLUS2
     00650 DD 200 J=1/HPLUS2

00606 SUH=0.

00670 DO 220 N=1/NPAIRS

00680 IF (J.HE./HPLUS2) SUH=SUH+X(N)**(I+J-2)

00690 IF (J.E0./HPLUS2) SUH=SUH+Y(N)*X(N)**(I-1)

00700 220 CONTINUE

00710 C(1,J)=SUH

00730 200 CDHINUE
  UNJOURNAL ON THE CONTINUE

00740*****

00750***** DETERMINATION OF COEF. A0...,AM OF THE M-TH

00760***** POLYMOHIAL BY GAUSS-JORDAN ELIMINATION ME

00770***** POLYMOHIAL BY GAUSS-JORDAN ELIMINATION ME

00770 KPLUS=K+1

00800 DO 330 K=1,MFLUS1

00810 C(K,J)=C(K,J)/C(K,K)

00820 300 CONTINUE

00830 DO 320 I=1,MFLUS1

00840 IF (I.ED.K) GO TO 320

00850 DO 310 JeKPLUS+MPLUS2

00860 C(I.J)=E(I,J)=E(I,K)*E(K,J)

00870 310 CONTINUE

00890 320 CONTINUE

00890 320 CONTINUE

00890 320 CONTINUE

00990 330 CONTINUE

00970 00762***

00970 VOFX=0.

00950 DO 400 H=1,MFAIRS

00940 YOFX=0.

00950 DO 400 H=1,MFLUS1

00960 4(H)=C(H,HPLUS1)

00970 YOFX=OFX+A(H)*X(N)**(H-1)

00980 400 CONTINUE

00970 IF (IUNCER.NE.1) UNMARG = UNCERT

01000 IF (IUNCER.NE.1) UNMARG = ABS(UNCERT*YOFX)

01101 IF (ABS(Y(N)-YOFX).E.UMARG) GO TO 410

01020 MEGRE=HDEGRE+1

01030 IF (MEGRE.LI.MFAIRS.ANI:MI-EGRE-LE.10) GO TO 112

01040 MEGRE=HDEGRE-1
       007302****
007505***** DETERMINATION OF COEF. A0,...,AM OF THE M-TH DEGREE LEAST-SQUARES
00760***** POLYMOMIAL BY GAUSS-JORDAN ELIMINATION METHOD
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     01770***** CALCULATION OF ROOT OF F(X)=0., AT THE WEIGHEBRHOOD OF X=X0.
01780***** BY NEWTON-RAPHSON METHOD
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 01700*****
01800 SUBROUTINE NEURAP (X0,ERROR,NITERA,NEWTO
01810 DIMENSION XS(21),A(11)
01820 COMMON/BLOCK/A:HPLUS1;MFLUS2
01840 IF (X0,ER,0.) XS(1)=X0=.0001
01850 DIO 950 N=1,NITERA
01860 NPLUS=N+1
01870 NMINUS=N-1
01880 910 YNUM=YPEN=0,
01890 DIO 930 I=1.HPLUS1
01970 IF (NEWTON.ED.2) GD TO 920
01910 YNUM=YNUM+4(1)*XS(N)**(I-1)
01970 IF (NEWTON.ED.2) GD TO 920
01910 YNUM=YNUM+(I-1)*XS(N)**(I-2)
01930 GD TO 930
01940 920 YNUM=YNUM+(I-1)**(I)*XS(N)**(I-2)
01950 YDEN=YPEN+(I-1)**(I)*XS(N)**(I-2)
01950 YDEN=YPEN+(I-1)**(I)*XS(N)**(I-3)
01940 920 YNUM=YNUM+(I-1)**(I)*XS(N)**(I-3)
01950 GD TO 930
01940 920 YNUM=YNUM+(I-1)**(I-2)**(I)*XS(N)**(I-3)
01950 GD TO 940
01960 XS(N)=(XS(N)+XS(MNIMUS))/2,
01970 GD TO 940
01980 XS(N)=(XS(N)+XS(MNIMUS))/2,
01970 GD TO 940
01990 GD TO 940
02000 940 ERR=ABS(YNUM/YDEN)
02001 IF (FERR.LE.ERROR) GD TO 960
02020 XS(NPLUS)=XS(N)-YNUM/YDEN
02030 950 CONITINUE
02040 X0=XS(MPLUS)
02050 GD TO 970
02050 GD TO 970
02050 960 X0=XS(N)-YNUM/YDEN
02070 970 ERROR=ERR
02070 RETURN
02100 END
READY.
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       01800 SUBROUTINE NEWRAP (XO, ERROR, NITERA, NEWTON)
          01050 PRINT 80, HIEGRE
01060 GD TD 700
01070 410 EQNTINUE
01080*****
          01090***** PRINT-OUT OF COEF. AO,..., AM OF THE OBTAINED M-TH DEGREE 01100***** LEAST SQUARES EQUATION
```

stead of Cramer's rule is that it proves to be a simpler and a less time-consuming procedure, especially when the system to be solved has more than three simultaneous linear equations.

This method is a combination of the Gaussian forward and backward eliminations. The forward elimination consists of the following steps:

• Elimination of ao from the second and succeeding equations by dividing the first equation by c_{11} ; multiplying the modified equation respectively by c_{21} , c_{31} , . . . , $c_{\{m+1\}1}$;

and then subtracting the obtained equations respectively from the second, third, ..., (m+1)th equations. The resulting set of equations is of the form:

- Elimination of a₁ from the third and succeeding equations by dividing the second equation in the set of equations in (2) by c'_{22} ; multiplying the modified equation respectively by $c'_{32}, c'_{42}, \ldots, c'_{[m+1]2}$; and then subtracting the obtained equations respectively from the third, fourth, . . . , (m+1)th equations.
- The elimination process continues until the system is of the form:

The backward substitution process may now be used to find the values for all a_i in the reverse order. The value of a_m is calculated from the last equation in equation set (3) and is substituted in the next-to-last equation to solve for a_{m-1} , etc.

In the Gauss-Jordan elimination method, the last procedure (backward substitution process) is replaced by the elimination of a_i , starting from the second step, not only from the (i+2)th and succeeding equations, as previously mentioned, but also from all preceding equations, (from the first to the ith equation). Thus, at the end of the process, the final set of equations is of the form:

$$\begin{array}{l}
 a_0 &= b_1' \\
 a_1 &= b_2'' \\
 \vdots & \vdots \\
 a_m &= b_{m+1}^{m+1} \\
 \end{array} \tag{4}$$

As we notice, the values of a_0 , a_1 , . . , a_m are obtained

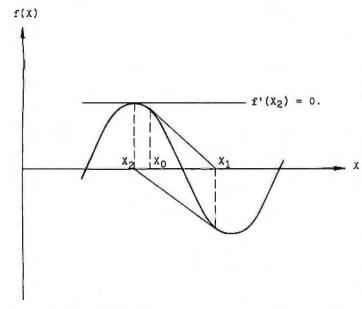


Figure 2: An example of a function f(X) that is not monotonically increasing or decreasing. This is clearly an undesirable situation for application of the Newton-Raphson method as the successive approximations diverge rather than converge on the desired root of the equation.

directly from equation set (4) as $b_1', b_2'', \ldots, b_{m+1}^{m+1}$.

One remark about this method is that the values c_{11} , c'22, ... must be different from zero to make all divisions meaningful. If this is not the case for some equations, these equations may be rearranged with others which have nonzero values of c.

Newton-Raphson Method

So far, utilizing the preceding techniques, we are able to determine for a given set of n pairs of data, a best-fit curve which is represented by the polynomial:

$$Y = a_0 + a_1 X + a_2 X^2 + ... + a_m X^m$$

The roots of Y(X) = 0 and the X-coordinates of the stationary points (referred to as X_{sta}) are determined by the following equations:

$$Y = a_0 + a_1X + a_2X^2 + \dots + a_mX^m = 0$$

 $Y' = a_1 + 2a_2X + 3a_3X^2 + \dots + ma_mX^{m-1} = 0$

As long as Y(X) has first and second defined derivatives and the equations Y(X) = 0 and Y'(X) = 0 are solvable, the values of $X_{Y=0}$ and X_{STA} may be calculated by using the well-known Newton-Raphson method.

This is an iteration process in which successive approximations are made in accordance with the formula

$$X_{n+1} = X_n - \frac{f(X_n)}{f'(X_n)}$$
 $n = 1, 2, ...$

For rapid convergence, the initial approximation X_0 should be in the neighborhood of the desired root of the equation f(X) and such that $f'(X) \neq 0$. This value of X_0 may be obtained with the aid of a rough sketch or tabulation of f(X) versus X.

The iteration process continues with converging X_{n+1} until the required accuracy ϵ is obtained, that is

$$|X_{n+1} - X_n| \le \epsilon \text{ or } |f(X_n)/f'(X_n)| \le \epsilon$$

When f(X) is not a monotonically increasing or decreasing function, or when there is a point of inflection in the interval $[X_1, X_2]$, the Newton-Raphson method may cause difficulties. In this case, X_{n+1} may tend to diverge or $f'(X_n)$ may happen to be very small or equal to zero, as illustrated in figure 2. A new value of X, should be reassigned to avoid additional unnecessary iterations or to make $f'(X_n) \neq 0$. This may be accomplished by taking the average of that particular X_n and the previous value X_{n-1} (that is, $(X_n)_{new} = (X_n + X_{n-1})/2$).

Application of this method to our problem yields:

$$\begin{aligned} (X_{Y=0})_{n+1} &= (X_{Y=0})_n - \frac{Y[(X_{Y=0})_n]}{Y'[(X_{Y=0})_n]} , \quad \frac{Y[(X_{Y=0})_n]}{Y'[(X_{Y=0})_n]} \leq \varepsilon_{Y=0} \\ (X_{STA})_{n+1} &= (X_{STA})_n - \frac{Y'[(X_{STA})_n]}{Y''[(X_{STA})_n]} , \quad \frac{Y'[(X_{STA})_n]}{Y''[(X_{STA})_n]} \leq \varepsilon_{STA} \end{aligned}$$

Computer Program

The program is written in an interactive manner for use with a timesharing system. To provide flexibility and ease of execution, some of the variables of the program

Listing 2: Sample execution of the program CURFIT.

```
00170+10,1,0,,1,1,-1,,001,1,0,,,001
00190+-2,,-1,5,-1,,0,,1,,2,,2,5,3,,4,,5,
00210+-25,1,-6,9,3,1,5,,-6,9,-21,,-25,,-25,1,-7,,45,
PRODRAM CURFIT
  THE DESIRED 3-TH DEGREE LEAST-SQUARES EQUATION HAS A FORM OF Y(X) = SUM OF 4-TERMS OF A(I)*X**xI I = 0,1,..., 3
                                  5.070
-7.010
                                  -7,028
                                   2.005
  AFTER 4 ITERATIONS, THE OBTAINED VALUE OF XROOT IS
                                                                          -1,195
  AFTER 4 ITERATIONS, THE OBTAINED VALUE OF XSTAIN IS
THE CORRESPONDING VALUE OF Y(XSTAIN) IS 6.646
 DO YOU WANT TO CHANGE ANY VARIABLES AMONG MIREG, UNCERT, ERT, ESTN, XRT1, XSTN1, IUNCER, IROOT, ISTATN ? (YES OR NO)
  ENTER THE VARIABLE TO BE CHANGED (HIT RETURN), AND THEN ITS NEW VALUE
   ANY MORE VARIABLES TO BE CHANGED ?
   ENTER THE VARIABLE TO BE CHANGED (HIT RETURN), AND THEN ITS NEW VALUE
 XSTN1
2.5
   ANY MORE VARIABLES TO BE CHANGED ?
  AFTER 4 ITERATIONS: THE OBTAINED VALUE OF XROOT IS
                                                                             .504
   OFTER 3 ITERATIONS, THE OBTAINED VALUE OF XSTATM IS THE CORRESPONDING VALUE OF Y(XSTATM) IS -25.629
                                                                            2,759
 IIO YOU WANT TO CHANGE ANY VARIABLES AMONG MUEG, UNCERT, ERT, ESTN, XRT1, XSTN1, IUNCER, IROOT, ISTATN ? (YES OR NO)
  ENTER THE VARIABLE TO BE CHANGED (HIT RETURN), AND THEN ITS NEW VALUE
  ANY MORE VARIABLES TO BE CHANGED ?
                                                                            4.174
  AFTER 3 ITERATIONS, THE ODTAINED VALUE OF XROOT IS
           1 LIERATIONS. THE OBTAINED VALUE OF XSTATN IS
                                                                            2.759
   THE CORRESPONDING VALUE OF Y(XSTAIN) IS
                                                       -25,629
 DO YOU WANT TO CHANGE ANY VARIABLES AMONG HDEG, LUNCERT, ERT, ESIN, XRT1, XSTN1, LUNCER, IROOT, ISTATN ? (YES OR NO)
```

Listing 3: Application of the program CURFIT to a chemical engineering problem.

```
00170+6,2,1,,005,0,0,,0,,0,,0,,0,
00170+5,,10,,20,,30,,40,,45,
00210+18,24,18,56,19,03,19,42,19,74,19,89
RUN
```

PROGRAM CURFIT

THE DESIRED 2-TH DEGREE LEAST-SQUARES EQUATION HAS A FORM OF $\gamma(x) = \text{SUM OF}$ 3-TERMS OF A(1)*x**1 $I = 0,1,\ldots,2$

DO YOU WANT TO CHANGE ANY VARIABLES AMONG MIEG, UNCERT, ERT, ESTN, XRTI, XSTNI, 1UMCER, IROOT, ISTATN ? (YES OR NO)

ENTER THE VARIABLE TO BE CHANGED (HIT RETURN), AND THEM ITS NEW VALUE ? UNCERT ? .002

ANY MORE VARIABLES TO BE CHANGED ?

THE DESIRED 2-TH DEGREE LEAST-SQUARES EQUATION HAS A FORM OF Y(X) = SUM OF 3-TERMS OF A(I)*X**I I = 0+1,..., 2

DO YOU WANT TO CHANGE ANY VARIABLES AMONG MDEG, UNCERT, ERT, ESTN, XRT1, XSTN1, IUNCER, IROOT, ISTATN ? (YES OR NO)

ENTER THE VARIABLE TO BE CHANGED (HIT RETURN), AND THEN ITS NEW VALUE ? UNCERT ? .001

ANY MORE VARIABLES TO BE CHANGED ?

THE DESIRED 3-TH DEGREE LEAST-SQUARES EQUATION HAS A FORM OF Y(X) = SUM OF - 4-TERMS OF A(D *X**) I = 0.1,..., 3

1	A(I)			
0	17.894			
1	.076			
2	001			
3	.000			

DO YOU WANT TO CHANGE ANY VARIABLES AHONG MDEG, UNCERT = ERT, ESTN, XRT1, XSTN1, JUNCER, IROOT, ISTATN ? (YES OR NO)
? NO

may be modified directly at the terminal in response to those questions printed by the program (see listing 2).

General Features

NO SIDE

The program allows the user to:

- Enter up to 100 pairs of data.
- •Enter the uncertainty margin as an absolute or relative value.
- Specify the magnitudes of the accuracy margins $\epsilon_{r=0}$ and ϵ_{STA} required in the calculation of $X_{r=0}$ and X_{STA} .
- Determine the least-squares polynomial and the values of $X_{Y=0}$ and X_{STA} .
- Initialize the iteration for finding the least-squares polynomial with any degree which, in the user's opinion, may be the desired one. This option eliminates unnecessary calculations resulting from the choice of the first degree as the initial trial.
- Modify information or values of variables after the completion of the first run. These variables include the lowest desired degree of the least-squares polynomial m, the uncertainty margin, the initially guessed values of $X_{y=0}$ and of the abscissa of the stationary point X_{STA} (this

is helpful when the least-squares function in question has more than one value of $X_{Y=0}$ or X_{STA} in the range under consideration), and desired accuracy margins $\epsilon_{Y=0}$ and ϵ_{STA} . (This option may be repeated as many times as the user wishes.)

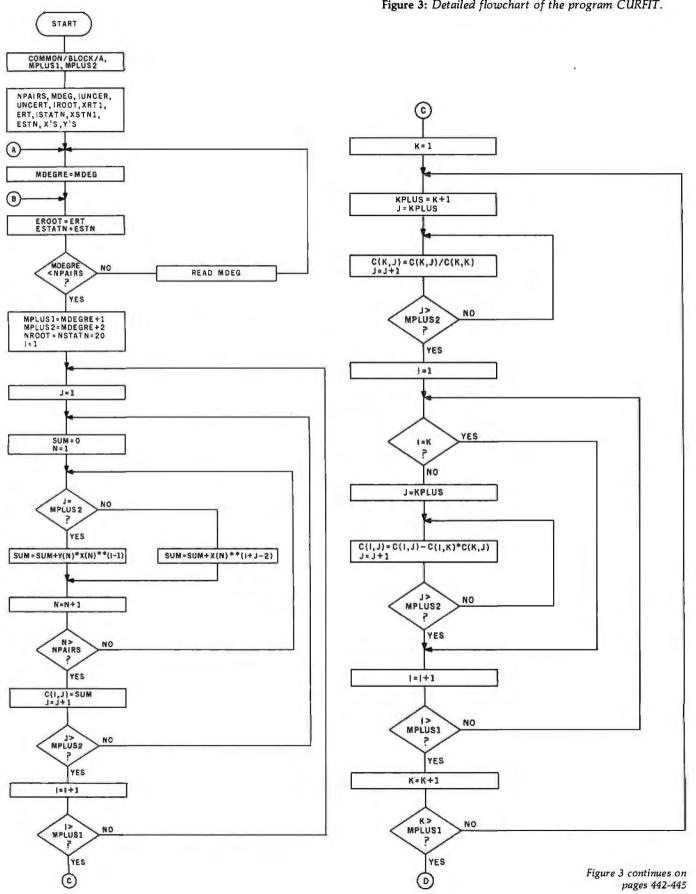
•Monitor when the Newton-Raphson iteration process does not converge or does not give the required values of $X_{Y=0}$ or X_{STA} the desired accuracy so that a new value of $\epsilon_{Y=0}$ or ϵ_{STA} may be entered.

Flowchart and Program Listing

A detailed flowchart and the complete program listing are given in figure 3 and listing 1 respectively. The structure of the flowchart is relatively straightforward and should be reviewed along with those definitions or explanations given in the variable-definition text box on page 437.

•Input: the input data is arranged in three groups of DATA statements in the program listing. The first group contains the values for NPAIRS, MDEG, IUNCER, UNCERT, IROOT, XRT1, ERT, ISTATN, XSTN1, and

Figure 3: Detailed flowchart of the program CURFIT.



ESTN. The second group contains the *n* values for the independent points X_n , or X (NPAIRS). The third group contains the n values for the dependent points Y_n , or Y(NPAIRS). These statements are modified to accommodate different data.

 Output: the results consist of the degree of the soughtfor least-squares polynomial and a set of calculated values, which are printed in two columns, representing the *i*th subscript and corresponding a_i in the representation $\Upsilon(X) = \sum_{i=0}^{m} a_i \times X^i$.

Sample Run

Assuming that the following set of 10 pairs of data is given:

i	1	2	3	4	5	6	7	8	9	10
X(i)	-2.0	-1.5	-1.0	0.0	1.0	2.0	2.5	3.0	4.0	5.0
Y(i)	-25.1	-6.9	3.1	5.0	-6.9	-21.0	-25.0	-25.1	-7.0	45.0

MDEGRE + I

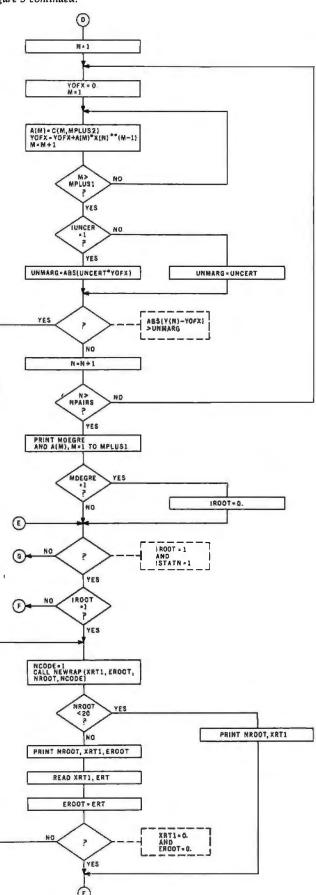
NO

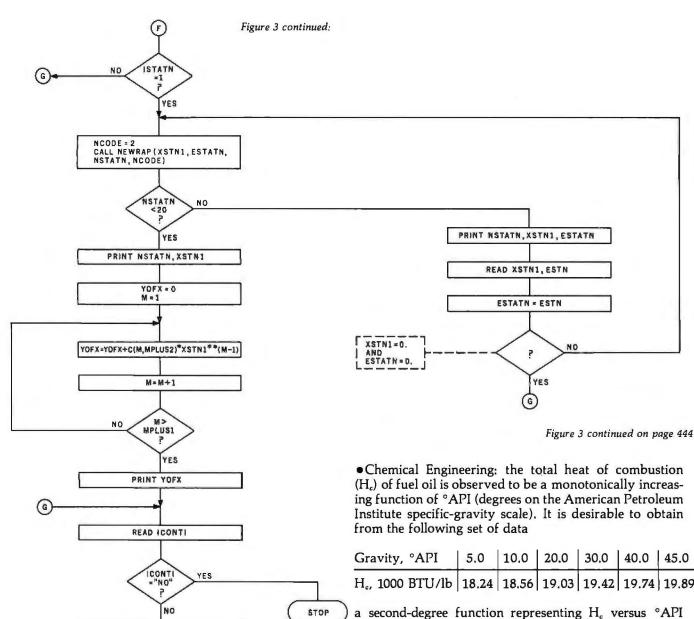
PRINT MOEGRE-1

(1)

MDEGRE < NPAIRS AND MDEGRE≤10

Figure 3 continued:





We are going to use the program CURFIT to determine the continuous relationship between quantities X and Y as well as all values of $X_{r=0}$ and X_{STA} . A quick look at the foregoing tabulation reveals that, in the specified range of X_s (-2.0 to 5.0), there are:

NRERUN . O.

 \oplus

• three distinct values of $X_{Y=0}$ between [X(2), X(3)], [X(4),X(5)], and [X(9),X(10)] due to the change in signs of corresponding pairs of Y(i)s

 two stationary points of which the maximum one is in the neighborhood of pair number 4 and the minimum near pair number 8.

Listing 2 illustrates some possible inputs and outputs for this particular example.

Application to Some Engineering Problems

The applications of the program CURFIT to engineering problems are innumerable. Here are a few simple examples of these applications:

• Chemical Engineering: the total heat of combustion (H_c) of fuel oil is observed to be a monotonically increasing function of °API (degrees on the American Petroleum Institute specific-gravity scale). It is desirable to obtain

Gravity, °API	5.0	10.0	20.0	30.0	40.0	45.0
H _c , 1000 BTU/lb	18.24	18.56	19.03	19.42	19.74	19.89

a second-degree function representing H_c versus °API with an uncertainty of less than 0.5% (UNCERT=0.005) for the given range of degrees API (5 to 45).

As illustrated in listing 3, the required function may be obtained with an uncertainty (to third decimal place) of 0.2% as follows:

$$H_c$$
=17.960+.062(°API) - negligible term (°API)², ± 0.2%

To obtain an uncertainty of 0.1%, a third-degree function will be required, as shown in the last portion of the listing.

· Civil Engineering: in an experiment determining the compressive stress-strain diagram of a concrete mix of cement, sand, and gravel (mix proportion by volume is 1, 2, and 4, respectively), the following data is observed (a kip is a 1000-pound load):

unit strain ϵ (10 ⁻³ inch/inch)	0.1	0.2	0.3	0.5	0.6	0.8	1.0
unit stress σ (kips /inch²)	0.44	0.82	1.21	1.78	2.08	2.54	2.83

Listing 4: Application of the program CURFIT to a civil engineering problem.

Figure 3 continued:

00170+7,270,.02,010..0,.00.,0. 00190+.1,.2,.3,.5,.6..8,1, 00210+.44,.82,1.21,1.78,2.08,2.54,2,83 RUN

PROGRAM CURFIT

THE DESIRED 6-TH DEGREE LEAST-SQUARES EQUATION HAS A FORM OF Y(X) = SUM OF 7-TERMS OF A(I)*X**I I = 0,1,..., 6

DO YOU WANT TO CHANGE ANY VARIABLES AMONG MDEG, UNCERT, ERT, ESTN, XRT1, XRTN, IUNCER, IROOT, ISTATN ? (YES OR NO) ? NO STOP

Listing 5: Application of the program CURFIT to an electrical engineering problem.

00170+5,1,1,.001,0,0,,0,,0,0,.0, 00190+50,,55,,60,,70,,75, 00210+239,2,244,1,247,,254,9,258,8 RUN

PROGRAM CURFIT

THE DESIRED 1-TH DEGREE LEAST-SQUARES EQUATION HAS A FORM OF Y(X) = SUM OF 2-TERMS OF A(I)*X***I $I = 0,1,\ldots,1$

DO YOU WANT TO CHANGE ANY VARIABLES AMONG MDEG, UNCERT, ERT, ESTN, XRII, XSTN1, IUNCER, IROOT, ISTATN ? (YES OR NO) ? YES

ENTER THE VARIABLE TO BE CHANGED (HIT RETURN), AND THEN ITS NEW VALUE ? UNDERT 7.0005

ANY MORE VARIABLES TO BE CHANGED ? NO

THE DESIRED 1-TH DEGREE LEAST-SQUARES EQUATION HAS A FORM OF Y(X) \approx SUM OF 2-TERMS OF A(I)*X***I I \approx 0,1,..., 1

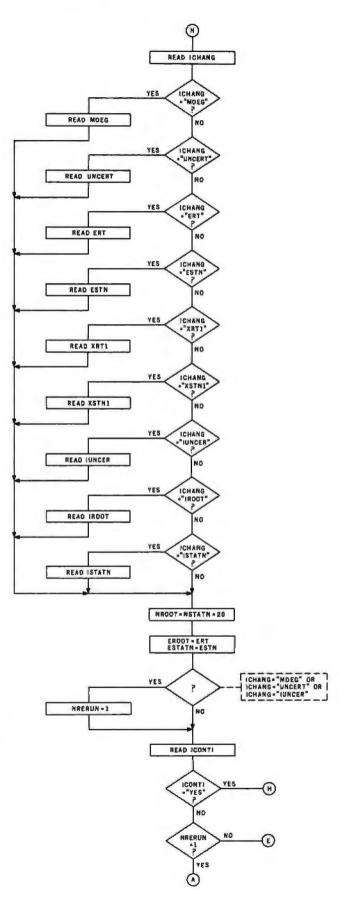
DO YOU WANT TO CHANGE ANY VARIABLES AMONG MDEG, UNCERT, ERT, ESTN, XKT1, XSTN1, IUMCER, IROOT, ISTATN ? (YES OR NO)

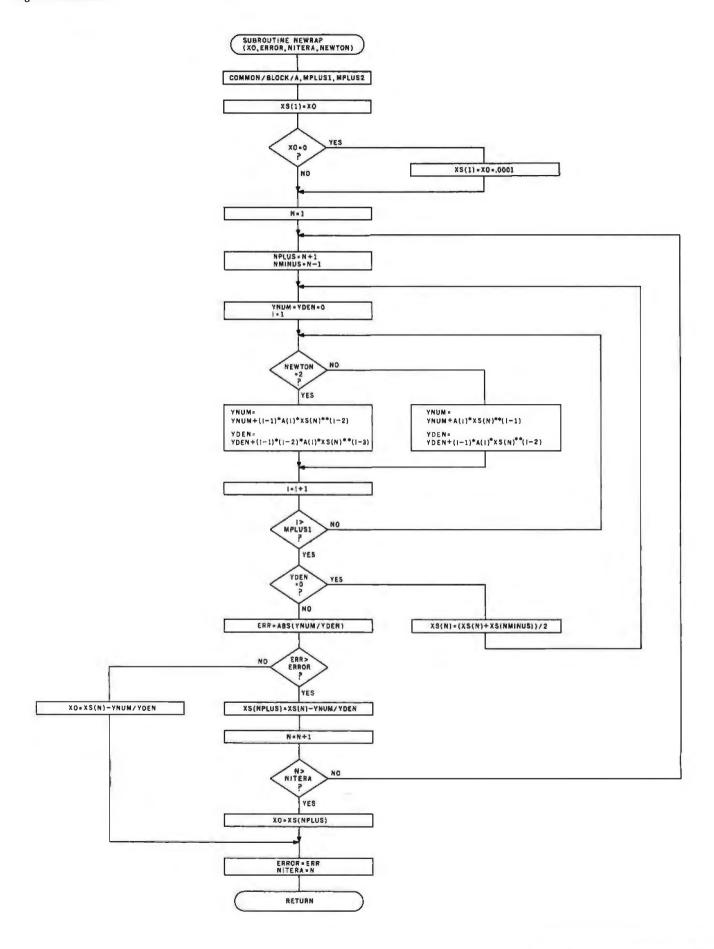
ENTER THE VARIABLE TO BE CHANGED (HIT RETURN), AND THEN ITS NEW VALUE ? UNCERT ? .0001

ANY MORE VARIABLES TO BE CHANGED ? NO

THE DESIRED 3-TH DEGREE LEAST-SQUARES EQUATION HAS A FORM OF Y(X) = SUM OF 4-TERMS OF A(I)*X**I I = 0*1,..., 3

DO YOU WANT TO CHANGE ANY VARIABLES AMONG MDEG, UNCERT, ERT, ESTN, XRT1, XSTN1, IUNCER* IROOT, ISTATN ? (YES OR NO)
7 NO
STOP





Deflection (inches)	10.8	21.6	27.0	37.8	48.6	64.8	81.0	86.4	97.2	108.0
Load (pounds)	74.0	117.0	132.0	145.0	150.0	152.0	168.0	183.0	226.0	300.0

Table 1: Data collected when determining the load/deflection characteristics of a bevel spring, supported and loaded at the edges. The program execution in listing 6 will generate the best-fit curve for all points.

For a required absolute uncertainty of ± 0.02 kips/inch2, from listing 4 we know that a sixth-degree polynomial representing σ versus ϵ is obtained as follows:

$$\sigma = 0.641 - 8.762\epsilon + 95.608\epsilon^2 - 333.314\epsilon^3 + 573.012\epsilon^4 - 477.63\epsilon^5 + 153.274\epsilon^6$$

Listing 6: Application of the program CURFIT to a mechanical engineering problem.

```
00170+10,2,1,.03,0,0.,0,,0,0.
00190+10.6,21.6,27.,37.8,48.6,64.8,81.,86.4,97.2,108.
00210+74.,117.,132.,145.,150.,152.,168.,183.,226.,300.
```

FROCKAM CURFIT

THE DESIRED 3-TH DEGREE LEAST-SQUARES EQUATION HAS A FORM OF = SUM OF 4-TERMS OF A(I)*X**I I = 0,1,... 3

1	A(I)
0	1.164
1	8.241
2	153
3	.001

DO YOU WANT TO CHANGE ANY VARIABLES AMONG MDEG, UNCERT, ERT, ESTN, XRT1, XGTN1, IUNCER, IROOT, ISTATN ? (YES OR NO)

ENTER THE VARIABLE TO DE CHANGED (HIT RETURN), AND THEN ITS NEW VALUE ? UNCE

ANY MORE VARIABLES TO BE CHANGED ?

THE DESIRED 3-TH DEGREE LEAST-SQUARES EQUATION HAS A FORM OF Y(X) = SUM OF A-TERMS OF A(I)*X**I I = 0,1,..., 3A(I)

0 1,164 8.261

TO YOU WANT TO CHANGE ANY VARIABLES AMONG MDEG, UNCERT, ERT, ESTN, XRT1, XSTN1, IUNCER, IROOT, ISTATN ? (YES OR ND)

ENTER THE VARIABLE TO BE CHANGED (HIT RETURN). AND THEN ITS NEW VALUE ? UNCERT

? .005

ANY MORE WARIABLES TO BE CHANGED ? NO

THE DESIRED 8-1H DEGREE LEAST-SQUARES EQUATION HAS A FORM OF Y(X) = SUM OF 9-TERMS OF A(1)*X**** $I = 0,1,\ldots,8$

100 HBH WANT TO CHANGE ANY VARIABLES ABONG MUCG, UNDERT, ERT, ESTN, $4841-31\,\text{TM}_{\odot}$ INCOR. IROUT, ISTATE ? (YES OR NO.) t do stop

 Electrical Engineering: in an electrical testing laboratory, a technician obtains the following set of data for the determination of resistance Ro at 0°C and temperature coefficient of resistance α of a conductor.

T, °C	50.0	55.0	60.0	70.0	75.0	
R_r , ohms	239.2	243.1	247.0	254.9	258.8	

Listing 5 gives the following results:

$$R_T = R_o(1 + \alpha T) = 199.937 + 0.785T$$
, $\pm 0.05\%$ or $R_o = 199.937$ ohms $\alpha = 0.785/199.937 = 0.00393 (°C)^{-1}$

This value of α indicates that the conductor is made of platinum.

 Mechanical Engineering: the data observed in the determination of the load/deflection characteristics of a bevel spring, supported and loaded at its edges, is illustrated in table 1.

As shown in listing 6, for an uncertainty of 1%, a third-degree polynomial is determined as follows, where D is the deflection:

Load=
$$1.164+8.261(D)-0.153(D)^2+0.001(D)^3$$

An eighth-degree polynomial will be required for an uncertainty of 0.5%.■

Glossary

Gauss-Jordan elimination: This mathematical algorithm is a means of solving a system of simultaneous equations. It proves to be most effective when the system to be solved has more than three simultaneous linear equations. The procedure itself involves the simplification of a matrix formed from the coefficients of the system of simultaneous equations. This method is also referred to as the Gaussian reduction method.

Newton-Raphson method: A mathematical technique which employs an iteration process in which successive approximations are made to determine the roots of a polynomial equation. These successive approximations are calculated from the following formula:

$$X_{n+1} = X_n - \underbrace{f(X_n)}_{f'(X_n)}$$

Cramer's Rule: An approach to solving a system of simultaneous equations involving the use of determinants. This method is most desirable when dealing with a small system of equations.

Event Queue

May 1981

May-lune

Data-Processing Courses, the Hartford Graduate Center. Hartford CT. For information on these courses, contact the Hartford Graduate Center. Attn: Don Florek. 275 Windsor St, Hartford CT 06120, (203) 549-3600, ext 252.

May-lune

Workshops from the National Institute for Management Research, various cities throughout the US. Wordprocessing implementation and supervision and automated office implementation workshops are to be held. The weekend courses are \$395 and \$495, with discounts available for attendance at two or three workshops. Contact Department C-Wordprocessingfeb2, NIMR Seminars, POB 3727, Santa Monica CA 90403. (213) 450-0500.

May-July

Courses from Integrated Computer Systems Inc, various cites throughout the US. Courses on computer network design and protocols, multiple micro- and minicomputer systems, and fiber-optics communications systems are to be held. The fees for these 3- to 4-day courses range from \$695 to \$795. Contact Integrated Computer Systems Inc, 3304 Pico Blvd, POB 5339, Santa

Monica CA 90405. (213) 450-2060.

May-July

Courses from Zilog, various cities throughout the US. An introduction to microprocessors: the Z80, Z8, and Z8000 family of components; PLZ/ SYS programming; development systems; and other topics concerning Zilog products are covered in these courses. Fees range from \$150 to \$595. For a schedule of times and places, contact Zilog, 10340 Bubb Rd, Cupertino CA 95014, (408) 446-4666, ext 5586,

May 1-2

The Third Annual Computers in Education Conference, Seattle Pacific University, Seattle WA. This conference will feature panel discussions, workshops, and exhibits. Special emphasis will be placed on the use of microcomputers in elementary and high schools. Contact Jerry Johnson, Seattle Pacific University, Seattle WA 98119.

May 4-7

National Computer Conference, McCormick Pl, Chicago IL. Approximately 90,000 people are expected to attend this year's National Computer Conference (NCC). The use of robots and artificial intelligence will be among the program sessions at the Personal Computing Festival during the NCC. This will be the first time that personal-computing exhibits

In order to gain optimal coverage of your organization's computer conferences, seminars, workshops, courses, etc, notice should reach our office at least three months in advance of the date of the event. Entries should be sent to: Event Queue, BYTE Publications, 70 Main St, Peterborough NH 03458. Each month we publish the current contents of the gueue for the month of the cover date and the two following calendar months. Thus a given event may appear as many as three times in this section if it is sent to us far enough in advance.

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have joined the rest of the conference in the main exhibit area. Over thirty technical sessions will be held. All major companies will be represented, Contact the American Federation of Information Processing Societies Inc. POB 9658, 1815 N Lynn St, Arlington VA 22209, (703) 558-3617.

May 5-8 INTELCOM 81/Paris, Paris, France, INTELCOM (International Telecommunications and Computer Conference and Exhibition) 81/Paris is part of a program to promote an international dialog on vital subjects in the telecommunications field. This conference attempts to guide the evolution of the computer and its technology by combining the efforts of private companies, government, and equipment users.

For information about attending, presenting a paper, or exhibiting at INTELCOM 81/Paris, contact the Conference Affairs Group, Horizon House, 610 Washington St, Dedham MA 02026, (800) 225-9977; in Massachusetts (617) 326-8220.

May 7-8

The Eighth Annual Computer Show, Valley Plaza Mid-

land, Midland MI. This show is being sponsored by the Saginaw Valley Chapter of the Data Processing Management Association. It will feature data processing software and hardware, computer peripherals and equipment, forms, supplies, graphics equipment, and educational services. Contact Don Seidel, DPMA, Saginaw Valley Chapter, University Center MI 48710, (517) 790-4220.

May 11-13

Custom Integrated Circuits Conference, CICC'81, Americana Hotel, Rochester NY. The CICC aims to bring together designers, producers, and users of custom integrated circuits to discuss recent developments and future directions in the field. Papers will be read on applications. algorithm-implementing integrated circuits, fabrication techniques, interfaces and interconnects, computer-aided design, and testing and qualification. Contact Dr Rajinder Khosla, General Chairman, Research Laboratories, B-81, Eastman Kodak Company. Rochester NY 14650, (716) 722-2525.

May 11-13

Fourth Annual Rosen Research Personal-Computer Forum, Playboy Resort, Lake Geneva WI. This forum features guest speakers from all the major personal-computer hardware and software companies. The Rosen Forum is one of the most prestigious and important seminars in the industry. The registration fee for this 3-day session is \$295. For further details, contact Rosen Research Inc., 200 Park Ave, New York NY 10166, (212) 586-3530.

May 11-13

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Colony Square Hotel, Atlanta GA. Papers will be read on semiconductor-processing technology, optoelectronic devices, manufacturing technology, materials, hybrid microcircuits, discrete components, interconnections, reliability, and connectors. Contact T G Grau, Bell Laboratories, Whippany Rd, Rm 3B-312, Whippany NJ 07981; or Electronic Industries Association, 2001 Eye St NW, Washington DC 20006.

May 14-16

The Tenth ASIS Mid-Year Meeting, Fort Lewis College, Durango CO. The American Society for Information Science's (ASIS's) theme for this year's meeting is "Using Information." Among the topics to be addressed are user studies, decision making, organizational change, government, education, management, access to information, and designing information systems for use. For information, contact ASIS, 1010 16th St NW, Washington DC 20036, (202) 659-3644.

May 16

Introduction to Pascal. Princeton NJ. The Princeton, New Jersey, chapter of the ACM (Association for Computing Machinery) is sponsoring this seminar. Contact Ronald Orcutt, EDUCOM, POB 364, Princeton NJ 08540; or Bill Hafstad, (201) 457-4055.

May 17-20

Expo '81, Loew's Anatole Hotel, Dallas TX. Expo '81 is a combination of exhibits and technical sessions. The exhibits cover everything from graphics systems to industrial computer-control systems. The technical sessions range from tool design, design engineering, and robotics to numerical control. For more information, contact Numerical Control Society, 519 Zenith Dr, Glenview IL 60025, (312) 297-5010.

May 20-22

Joint Conference on Easier and More Productive Use of Computing Systems, University of Michigan, Ann Arbor MI. This conference intends to combine the insights of the social sciences, humanities, computer science, and human-factors engineering.

Contact Gregory A Marks, 4258 Institute for Social Research, University of Michigan, Ann Arbor MI 48106, (313) 763-3482.

May 20-22

Videotex '81, Royal York Hotel. Toronto. Ontario. Canada, Videotext information systems allow users to call up information, make reservations, pay bills, exchange electronic mail, read

an electronic newspaper, shop, and play video games. This conference will review videotext developments in Europe, Japan, and North and South America. Demonstrations of videotext systems will be given. Seminars on standards, legal aspects, and economic issues will be featured. Contact Videotex '81, 316 Lonsdale Rd. Suite 3. Toronto, Ontario, M4V 1X4, Canada, (416) 598-1981.

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May 21-23

Annual Conference of the Educational Computing Organization of Ontario, Sheraton Centre and the Ontario Institute for Studies in Education, Toronto, Ontario, Canada. Exhibits on the use of computers in schools and discussions on how to locate suitable educational materials will be featured. Contact the Conference Office, OISE, 252 Bloor St W, Toronto, Ontario, M5S 1V6, Canada.

May 22-24

National TRS-80 Microcomputer Show, Statler Exposition Center, New York NY. Exhibits from over 100 manufacturers, distributors, and retailers of equipment for the TRS-80 Models I, II, and III, and Color and Pocket computers, will be featured. Seminars and talks will be held at the show. Contact Kengore Corporation, 3001

Rt 27, Franklin Park NJ 08823. (201) 297-6918.

May 26-29

Office Korea 81, Korea Exhibition Center, Seoul, South Korea. Exhibitors will come from the United States. Japan, the United Kingdom, and South Korea. Computers, copiers, facsimile systems, and office equipment and supplies will be presented. Further information may be obtained from Clapp & Poliak International, 7315

Wisconsin Ave. Washington DC 20014, (301) 657-3090.

May 30

Amateur Fair, Minnesota State Fairgrounds, St Paul MN. Exhibits, prizes, and booths are featured at this swapfest for computer hobbyists. Contact the Amateur Fair, POB 30054, St Paul MN 55175.

June 1981

lune 6-9

Atlanta Small Computer Show, Atlanta Hilton, Atlanta GA. Producers of small computers, peripherals, supplies, and services will be exhibiting at this show. Business owners, corporate and government executives, dataprocessing managers, doctors, lawyers, and other professionals are expected to attend. Obtain additional information from The Atlanta Small Computer Show, 4060 Janice Dr, Suite C-1, East Point GA 30344, (404) 767-9798.

June 9-11

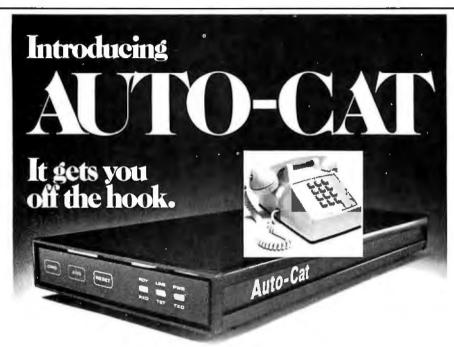
Understanding and Using Computer Graphics, Chicago IL. This seminar covers the latest in graphic-system technology, including hardware, software, and applications. Contact Bob Sanzo, Frost & Sullivan Inc, 106 Fulton St, New York NY 10038, (212) 233-1080.

June 14-18

The Second National Conference of the National Computer Graphics Association. Baltimore Convention Center, Baltimore MD. Computer-graphics demonstrations, exhibits, and workshops will be held. Contact the National Computer Graphics Association Inc, 2033 M Street NW, Suite 330, Washington DC 20036, (202) 466-5895.

June 16-18

NEPCON East '81, New York Coliseum, New York NY. This exposition is aimed at



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engineers, prototype developers, production specialists, and testing personnel. Technical programs will be presented. Contact Industrial & Scientific Conference Management Inc. 222 W Adams St, Chicago IL 60606, (312) 263-4866.

lune 17-19

National Educational Computing Conference. North Texas State University, Denton TX. This conference will provide a forum for individuals and institutions interested in educational computing. Computer literacy, computer education for teachers. and computers in education are some of the topics to be covered. Contact Dr Jim Poirot, NECC-81 General Chairman. Computer Sciences Department, North Texas State University, Denton TX 76203.

May 29-31

The Sixth Annual Computerfest, Franklin University, Columbus OH, Talks on robots and calculators will be featured. Microcomputers and small-business systems will be presented. This show is being sponsored by the Midwest Affiliation of Computer Clubs and Franklin University. Contact Computerfest '81, Paul Pittenger, 215 Delhi Ave, Apt J, Columbus OH 43202, (614) 224-6237.

June 23-25

Comdex/Spring, Madison Square Garden and the New York Statler Hotel, New York NY. Computer and computer-related manufacturers, systems houses, computer retailers, dealers, distributors, manufacturers' representatives, commercial OEMs (original equipment manufacturers), and other related businesses will be exhibiting. Contact The Interface Group, 160 Speen St, Framingham MA 01701, (800) 225-4620; in Massachusetts. (617) 879-4502.

lune 29-luly 1

The Nineteenth Annual Meeting of the Association for Computational Linguistics, Stanford University, Stanford CA. Syntax, parsing, and sentence generation, computational semantics, discourse analysis and speech acts, speech analysis and synthesis, machine and machineaided translation, and mathematical foundations of computational linguistics are some of the topics that will be

discussed. Contact Don Walker, Artificial Intelligence Center, SRI International, Menlo Park CA 94025, (415) 326-6200, ext 3071.

July 1981

July 29-31

The 1981 Microcomputer Show, Wembley Conference Centre, London, England. Seminars on microcomputer

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Technical Forum

Build a Noise-Based Random Number Generator

Terry Mayhugh, 11632 Midhurst Dr, Concord TN 37922

At some time, nearly every programmer finds it necessary to generate random numbers. If a card dealer is being simulated, or a Klingon scanner display is being created, the RND function available in most versions of BASIC may be adequate. However, the pseudorandom sequence generated by RND can bomb in critical applications where a truly random number sequence is needed. Truly random numbers are extremely difficult to generate, especially within a nonrandom machine such as a computer.

The best that can be accomplished purely by software is the generation of finite-length sequences that appear to be random. However, the actual members may be related to specific calculations recently completed by the computer. Such complications will contaminate the results of signal-recovery simulations or digital-filter problems. Even a computer card game may be biased by a previous bet. Ideally, the actual random number generation should be done outside the computer.

Figure 1 is a block diagram of a simple generator capable of producing *truly random* sequences of any length. A free-running oscillator, running asynchronous

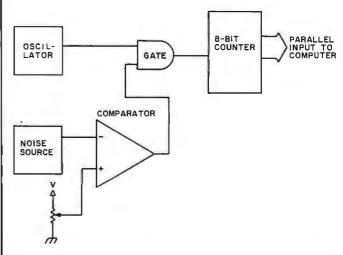


Figure 1: Block diagram of a generator that produces true random numbers. Through pulses created by the random-noise source, the free-running oscillator is gated to the 8-bit binary counter. Since the instantaneous amplitude of the voltage from the noise source is unpredictable, the width and arrival of the gate pulse generated by the comparator are also random. Therefore, the 8 bits available from the counter are truly random

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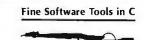
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to the microprocessor clock in the computer is gated to an 8-bit binary counter through pulses created by a randomnoise source. Since the instantaneous amplitude of the voltage from the noise source is not predictable, the width and the time of arrival of the gate pulse generated by the comparator are unpredictable. The sequence of numbers available from the counter is truly random (if you do not try to sample them at an excessively high rate). For the component values shown in figure 2, there should be no problem in any microprocessor application.

The numbers generated by this technique are uniformly distributed; any number in the set of all possible numbers (0 thru 255) has the same probability of occur-

ring. The mean or expected value of the distribution lies at the center of the set of all possible numbers.

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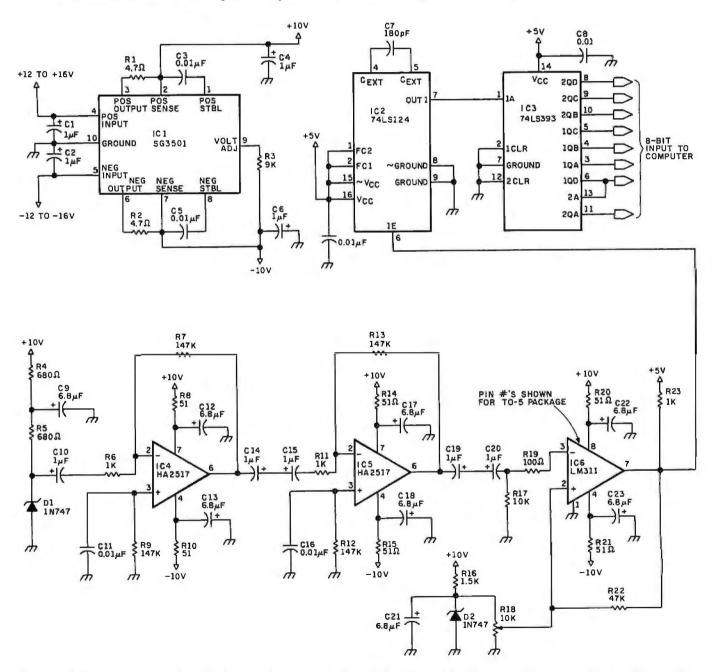


Figure 2: Schematic diagram of the random number generator described in this article. The noise of D1 is amplified by IC4 and IC5. The amplified noise from IC5 is compared with the DC wiper voltage of R18 at the comparator input of IC6. The level generated at the comparator input gates IC2 (running at about 3 MHz). The oscillator is clocked by IC3 (a cascaded 4-bit binary counter). The circuit should be shielded. Pin numbers shown for IC1 (Silicon General 3501) are those for a TO-5 package.

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Technical Forum

Parts List

IC1 Silicon General SG3501 dual regulator IC2 74LS124 oscillator IC3 74LS393 dual 4-bit counter IC4, IC5 Harris HA2517 op-amp IC6 LM311 comparator

R1, R2 4.7 ohm 1/4 W 5% CC (carbon composition) R4, R5 680 ohm 1/4 W 5% CC R8,R10,R14,R15,R20,R21 51 ohm 1/4 W 5% CC R17 10 k-ohm 1/4 W 5% CC R19 100 ohm 1/4 W 5% CC R22 47 k-ohm 1/4 W 5% CC R23 1 k-ohm 1/4 W 5% CC R16 1.5 k-ohm 1/4 W 5% CC R3 9.00 k-ohm 1/8 W 1% mF R6, R11 1.00 k-ohm 1/8 W 1% mF R7,R9,R12,R13 147 k-ohm 1/8 W mF R18 10 k-ohm miniature 10-turn potentiometer

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Table 1: Parts list for the circuit shown in figure 2.

A great deal of power-supply decoupling and isolation is used in the analog section of the generator. This is necessary to avoid picking up the 60 Hz power signal or any other periodic power-supply noise that could destroy the randomness of this circuit.

The circuit should be constructed within a shielded enclosure to avoid RF (radio frequency) or other interference that could cause a periodic output from IC6. The ± 12 V supply in my SwTPC 6800/2 (actually ± 14 V) has an unacceptable amount of 60 Hz ripple for this application, so a dual IC regulator (IC1) regulates this voltage to a clean ± 10 V for the analog electronics.

Alignment of the generator is relatively simple if an oscilloscope is available. R18 is adjusted while viewing the waveform at pin 7 of IC2. This potentiometer should be adjusted until the waveform at pin 7 spends an equal amount of time in its high and low status. That is, the brightness of the scope trace should be adjusted for uniform brightness at its top and bottom edges. If no scope is available, set the potentiometer for 50 to 100 mV at the wiper.

The eight counter bits may be connected in any order to the eight lines of the parallel port of the computer. In my particular application the port is read with a loadaccumulator instruction when a number is needed. No strobe or handshaking is used.

A Gaussian, or normal, distribution can also be created using this uniform generator. Using what statisticians call the Central Limit Theorem, a normal distribution can be created by averaging several random numbers of any other type distribution. I have found that a convenient and sufficient number of samples in most cases is 64. Averaging multiples of 2 maintains maximum speed because the division in the averaging process can be done with simple accumulator shifts. Of course, speed is sacrificed with this method because only one normally distributed number is created for every 64 uniform numbers generated.



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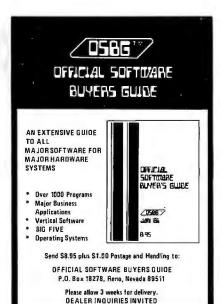
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Technical Forum

Fast Fourier Comes Back

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The program "Fast Fourier for the M6800," by Richard H Lord (February 1979 BYTE, page 108), contains an overflow bug that I discovered while testing a version of the program written for the 8080 processor. (See listing 1.) After the exact nature of the fault was ascertained, a theoretical explanation for it was easy to find. The problem concerns the maximum two's-complement value allowed before scaling commences. The 6800 program requires that any data point outside the range of -64 <data < 64 be scaled down before the next pass. Scaling divides all data values by 2. However, during passes 2 thru 8 it is quite possible for the results of arithmetic operations to exceed the 8-bit two's-complement-number range of $-128 \le data < 127$. The reason for this can be seen by referring to lines 205 and 215 in the original program. These lines yield:



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RM' = RM + RN*COS(X) + IN*SIN(X)

Letting RM=RN=IN=M, the maximum data value, then:

RM' = M*(1 + COS(X) + SIN(X)).

The maximum value of RM' is then M times the maximum value of 1 + COS(X) + SIN(X). This maximum value occurs at an angle of 45° , given by TAN(X) = 1.

Thus, the maximum value of RM' is $M(1 + \sqrt{2})$ or approximately (2.414)M. Letting RM' = 127 (the maximum positive 8-bit two's-complement-data value), then $M = INT(127/(1 + \sqrt{2})) = 52$.

Thus, the data should be scaled before *each* pass if any point exceeds the range $-52 \le \text{data} \le 52$. It makes little difference to the spectra whether the relational operators here are greater-than-or-equal or merely just greater-than. The 6800 program should be amended accordingly:

00268 CMP A # \$CC (-52) 00270 CMP A # \$34 (52)

The test program that uncovered the overflow error used program-generated square waves with a period of six data points (equivalent to 10.667 Hz using a sampling rate of 64 Hz). Every amplitude from 128 ± 127 down to 128 ± 1 was tested and the power spectra, as well as SCLFCT, were printed out (requiring approximately three hours at 110 bits per second).

Each transform in the 8080 program takes 3.6 seconds to compute with a 2 MHz processor clock. The power calculation is fast, because a lookup table is used.

When FFTs (fast Fourier transforms) are computed on a minicomputer that has sophisticated error-trapping hardware, the usual practice is not to perform any prescaling, but instead to allow arithmetic overflow to occur, do a software interrupt to a scaling routine, and return. This way, fewer scalings of all the data are required, yielding results with the maximum possible numerical precision. The 6800 can detect two's-complement overflows and can efficiently perform (two's complement) arithmetic shifts to scale the data, but it does not have an automatic overflow trap. The advantage of slightly better numerical results would be outweighed by the time required to call an overflow-checking subroutine after most arithmetic operations. The 8080 is even worse off: it has neither a two's-complement-overflow indicator nor a single-instruction equivalent of the 6800's ASR.

Text continued on page 460

Listing 1: The 8080 version of the fast Fourier transform program originally written for the 6800 processor by Richard Lord. In this version, Mr Roxburgh has corrected an overflow problem that he discovered and diagnosed in the original version.

0000	ODIO ;; PAST FOURIFR TRANSFORM.	
0 0 0 0	0020 ;; = = = = = = = = = = = = = = = = = =	805E 2C 1190 INR L SNEW PAIR.
	0040 JJRORD VERSION PY:- ALASTAIN NORPUFGH- 0050 JJDATF:- 4 UCT 1979.	8 05C C2 50 80 1200 JNZ PAI 8 05F 12 0 1;
0.000	0060 ;; 256 POINT IN-PLACE COMPLEX FOURIER TRANSFORM.	805F 1220 ;; COMPUTATION OF FFT. PASS 2 THRU N.
0 0 0 0	0070 ##INPUT DATA UNSIGNED WITH ZERO = 80H- 0080 ##COMPLEX OUTPUT SIGNED (2°S COMP-)-	8 05F 3F 40 1240 FPASS:MVI A,64 ;SET UP PARAMETERS
0 0 0 0	0090 ##RFAL CUTPUT (POWER OR AMPLITUDE) UNSIGNED-	8 061 32 08 83 1250 STA CELNUM ; FOR NO. OF CELLS, 8 064 32 0F 83 1260 STA DELTA ; ANGLE,
n 0 0 0 0 0 n 0	0110 \$ 0120 SCADE FOU 9FOOK 32'S COMP. SQU. TAPLE.	8 067 3F D2 1270 MVI A,2 ; & FOR 8 069 32 0C 83 1280 STA PAIRNM; PAIRS/CELL.
0 n n o	0130 STADE FOU 9FOOK 32'S COMP. SINE TABLE:	8 06C 32 0D 83 1290 STA CELDIS ; SPAN PETWEEN PAIRS.
0000	0140 ; 0150	8072 3A 00 83 1310 LDA CELNUM :GET NO. OF CELLS &
8300 8302	0160 RLPTI DS 2	8 075 32 0A 83 1320 STA CELCT ; PUT INTO CELL CTR- 8 078 21 00 85 1330 LXI H;REAL
8 3 0 4	DIBO IMPTI DS 2 ; IMAG. PTR 1.	8 07P 22 00 83 1340 SHLD RLPT1 8 07E 22 02 83 1350 SHLD RLPT2
8 306 8 308	0200 SINPT DS 2 SINF TAPLE PTR-	8 NS 1 21 00 86 1360 LXI H, IMAG
830A 830A	0210 CFLCT DS 1 CFLL CTR- 0220 CFLNUM DS 1 CFLLS-	8087 22 06 83 1380 SHLD IMPT2
830C 830D	0230 PAIRNM DS 1	8 08A 21 00 9F 1390 NCELL:LX1 H, STADR 8 08D 22 08 83 1400 SHLD SINPT
830F 830F	0250 DELTA DS 1 JANGLE INCREMENT. 0260 SCLECT DS 3 JMULTIPLY OUTPUT AMPLITUDE	8 090 3A 0C 83 1410 LDA PAIRNM ; GFT PAIRS/CELL CTR+ 8 093 47 1420 MOV B,A
F 310	0270 ; PY PISCLECT.	8 09/1 21 0D 83 1430 NC1: LXI H, CFLDIS
8310 8311	0280 SINE DS 1 0290 COSINE DS 1	8 09A 86 1450 ADD M ; ADD PAIR OFFSET.
8312 8313	0300 TRFAL DS 1 0310 TIMAG DS 1	8 09 B 32 02 B3 1460 STA RLPT2 ; SET UP ROTH 8 09 F 32 06 B3 1470 STA IMPT2 ; SND PTRS-
R314 R314	0320 i 0330 DRG 8400H	8 0 A 1 C 5 14 M PUSH F SAVE PAIR CTR B 0 A 2 A 0 B 8 3 1490 LHLD SINPT
R 400	0350 INPD DS 256 ; INPUT DATA BUFF.	8 DAS 7F. 1500 MOV A.M GFT SINE OF ANGLE
8500 8600	0360 REAL DS 256 ;"REAL" BUFF- 0370 IMAG DS 256 ;"IMAG" BUFF-	8 0A9 3F 40 1520 MVI A.SA ;ADD COSINE OFFSFT
8700 8700	のできる。 0384 ORG 877FH	8 DAP 85 1530 ADD L ; MIDDULT 856. 8 DAC 6F 1540 MID L.A
877F 8800	0385 PARUF DS 100 ; POWER/AMPLITUDE SPECTRUM.	8 DAD 7F 1550 MOU AAM GGFT COSINF OF ANGLE 8 DAF 32 11 83 1560 STA COSINF & SAUF.
RAND	0390 ORG 8000H	ROPI 2A 02 83 ISTO LHLD RLPT2 GET REAL PTR 2-
8000 8000	0400 ## 0410 ##TEST FFT PROGRAM-	R MPS C5 1590 PUSH B SAVE RN.
8000 CD D5 81	04/20); 0//30 CALL WAVE GFT TEST SIGNAL.	ROPS 3A 11 R3 1600 LDA COSINE ; GET COSINE ; ROPS CD 7P R1 1610 CALL MPY ; A = RN*COS(Z) ;
BOOM CD OC BO	0460 CALL FFT ;FORM COMPLEX SPECTRUM-	ROPE 32 12 83 1620 STA TREAL ISAVE PRODUCT.
8 006 CD AF 81 8 009	0470 CALL POWER CONVERT COMPLEX SPECTRUM 0480 ; TO 188 PT POWER SPECTRUM.	8 0CH 3A 10 83 1640 LDA SINF
A 009 A 009	0490 ;CALL MAGNI ;CONVERT COMPLEX SPECTRUM 0500 ; TO 128 PT AMPLITUDE SPECTRUM	8 003 CD 7P 81 1650 CALL MPY ;A = RN·SIN(Z). 8 006 32 13 83 1660 STA TIMAG ;SAVE PRODUCT.
8 009 C3 F4 AF 8 00C	0540 JMP OAFEAR FRETURN TO BMCOS- 0550 B	8009 2 0 6 93 1670 LHLD IMPT2 8000 4F 1680 MOV C.W (GFT IN-
8 000	0560 11	R DCD C5 1690 PUSH P ISAVE IN-
8 0 0 C 8 0 0 C	057 0 ;;INITIALISF DATA ARFAS. 058 0 ;;	# ROCF 3A 10 83 1700 LDA SINF # RODI CD 7P 81 1710 CALL MPY 3A = IN+SIN(Z)-
800C 3F 00 800F 32 0F 83	0590 FFT: MVI A.0 0600 STA SCLECT	8 0D4 24 12 83 1720 LXI HATRFAL 8 0D7 86 1730 ADD X
8011 8011	0610 : 0620 :CLEAR IMAG. ARRAY.	8 009 C1 1750 POP P 3 GFT 1N+SIN+
8011 21 00 BE	0630 CLFAR:LXI H, IMAG	8 0DA 3A 11 83 1760 LDA COSINE
8014 36 00 8016 8C	0640 CLR1: MVI M,0 0650 INR L	8 DDD CD 7P 81 1770 CALL MPY ;A = IN*COS(Z). 8 DEC 21 13 83 1780 LXI H.TIMAG
8017 (12 14 80 8016	0660 JNZ CLR1	80E3 96 1790 SUP M 80E4 77 1800 MOV M.A ;TI = IN*COS - BN*SIN.
801A 801A	0680 ; MOVE INPUT DATA INTO REAL ARRAY- 0690 ; DE=SDURCE, HL=DFST-	8 0F5
BOTA 11 00 84	0700 MOVE: LXI D, INPD	8 0F8 7F 1830 MOV A#M #GET EM*
801D 21 00 85 8020 1A	0710 LXI HARFAL 0720 MOVI: LDAX D	8 0F9 AF 1840 MOV C.A JSAVE RM. 8 0FA 3A 12 83 1850 LDA TREAL
8021 D6 40 8023 77	0730 SUI 80H ; CONVERT TO 2'S COMPLEMENT- 0740 MOV M.A	8 NFD 91 1860 ADD C 8 NFE 77 1870 MOV MAA FRM' = RM+TR+
8 024 1C 8 025 2C	0750 INF F. 0760 INF I.	8 NEF 79 1890 MNV A.C 19FT RX. 8 NEO 21 12 53 1890 LXI Y.TREAL
8 026 C2 20 80	0770 JNZ MINU1	8 0F3 96 1900 SUR M
8 050	0780 ;; PRF-TRANSPORM PIT SWAP-	80F7 77 1920 MOV M.A ;RN' = EM-TE-
8029 8029 11 00 85	0800 EL DEFAL	8 OF 8
8 020 21 00 85 8 02F 06 08	0820 LXI H.REAL	ROFF 7F 1950 MOU A.M JGFT IM BOFC //F 1960 MOU C.A JSAVF IM-
8 031 7D	0840 MOV A.L ;LOW-ORDER PITS OF ELPTI-	BOFD 3A 13 B3 1970 LDA TIMAG
6032 1F 8033 4F	0850 PRVI: RAR ;MOVE LS BIT OF RLPTI 0860 MOV C.A ; INTO CY & SHIFT	8101 77 1990 MOV MAA ; [M" = IX+TI.
8 0 3 4 7 P 8 0 3 5 1 7	0870 MOV A.F. 0880 FAL I IN REVERSE ORDER PACK	8108 79 2000 MOV A.C. JGET IM. 8103 21 13 83 2010 LXI H.TIMAG
8 036 5F 8 037 79	0890 MOV F.A ; INTO REPTS.	8106 96 2020 SUP M 8107 28 06 83 2030 LHLD IMPT2
8 0 3 8 0 5	0910 DCR P	810A 77 2040 MOV MAA JIN' = IM-TI.
8 039 C2 32 B0 8 03C 7D	0920 JNZ RRV1 0930 MNV A.L	810P 21 08 83 2060 LXI H-SINPT
8 03D PP 8 03F DA 46 80	0940 CMP F ; CR:PARF VALS, 8 IF 0950 JC SWP1 ; SAMF, DNN'T SWAP.	810F 3A 0F 93
8 041 4E	0960 SWAP: MOV C.M JGET VAL 1 INTO C-	8112 77 2090 MOV M.A 8113 21 00 83 2100 LXI H.RLPT1
8 042 1A 8 043 77	0970 LPAX D GET VAL P INTO A. 0980 MOV M.A STORE IN SWAPPED ORDER.	8116 34 2110 INP M
8044 79 8045 12	0990 MNV A,C 1000 STAX D	8117 21 04 83 2120 LXI H,IMPT1 8114 34 2130 INR M
8 046 2C 8 047 C2 2F 80	1010 SWP1: INR 1.	811B C1 2140 POP P 811C 05 2150 DCB B ; DECREMENT PAIR CTR-
8 04A	1030);	811D C2 94 80 2160 JNZ NC1 8120 2170 ;;;
8 04A 8 04A	1040 SIFFT FIRST PASS- 1050 BI	8120 21 00 83 2180 LXI H,RLPT1 ;GET PTRS &
8 04A CD 4B 81 8 04D 21 00 85	1060 PASSI: CALL SCALF ; SCALF IF DATA TVFR-RANGE.	8123 30 0D 83 2190 LDA CELDIS 8126 86 2200 ADD M # ADD CELL OFFSET.
9 050 7F 8 051 AF	1080 PA1: MOV A.M ; GFT RM.	8127 77 8210 MOV M.A 8128 32 04 83 8220 STA IMPT1
8 052 RC	1100 INR L	8 12F 21 0A 83 2230 LXI H, CFLCT
8 853 76 8 854 2D	1110 MOV B.M ;GFT RN TDO- 1120 DCR L	812F C2 8A 80 2250 JNZ NCFLL
8 055 80 8 056 77	1130 ADD B ;RM' = RM+RN. 1140 MOV M.A ;STORF NEW RM'.	8132 2250 \$1 8132 2270 f1 CHANGE PAPAMETERS FOR NEXT PASS.
8 057 79 8 058 90	1150 MOV A.C PRETRIEVE RM.	8132 21 0F 83 2290 NP1: LXI H, CFLNUM
8059 RC	1170 INR L	Listing 1 continued on page
9 05A 77	1180 MOV M.A ;STORE RN'.	Listing I committed on page

```
Listing 1 continued:
                                                                                                                                                           MDV
ANA
RAR
                                                                                                                                                                                  A,M
                                                                                                                                                                                                                      CLEAR CY & SHIFT
                                                                                                                                                                                 Α
                                                                                                     2310
                                                                                                                                                                                                                      ; RIGHT TO HALVE NO. CFLLS.
                                                                                                     2330
                                                                                                                                                                                 A
M. A
                                                                                                                                                           DRA
                                                                                                                                                         MOV
RZ
I NX
MOV
ADD
                                                                                                   2346
                                                                                                                                                                                                                     ; DUT OF CELLS -> ***FINISH***.; PAIRNM.
                                                                                                   P350
P360
P370
P360
P390
     B 13A
   813F C4
813F C7
813C 7E
813F 77
                                                                                                                                                                               H
A,M
A
M,A
                                                                                                                                                                                                                    STWICE AS MANY PAIRS.
                                                                                                                                                          MOV
   8 1 3 F

8 1 4 0

8 1 4 1

8 1 4 2

8 1 4 3
                                                                                                                                                                                H ...
                                                                                                                                                                                                                    CFI DIS.
                                                                                                   2000
                                                                                                                                                                                                                     STUICE AS FAR APART.
                                                                                                                                                                                                                    IDELTO-
                                                                                                                                                          INX
                                                                                                                                                                                A-M
    8144
                                                                                                   2451
                                                                                                                                                                                                                    CLEAR CY & SHIFT : RIGHT TO HALVE THE ANGLE.
    R 145
                                                                                                     2460
                                                                                                                                                         ANA
                                                                                                  2470
2470
2480
2490
2500 ;;
                                                                                                                                                        RAR
MAY
JMP
                             15
   8147 77
8148 C3 6F 80
814P
814F
                                                                                                  2510 : SCALE DVFR-RANGE DATA.
    RIME
                                                                                                                       JSCALF RFAL & IMAG IF -SP > ANY DATA >= 5P.
IND RFGISTERS PRESERVED.
SCALF:LXI P.RFAL JSFT UP TAPLF PTR-
LXI D-1
LXI H-51P-1 JND. DF PTS - 1.
   8 14|1
8 14|P
8 14|P
8 14|P 01 01 | 95
8 14|F 11 | FF | FF
8 151 | 21 | FF | 01
9 154 | 04
                                                                                                  2530
2540
2550
2560
                                                                                                                       257 D
258 D
                                                                                                                                                                                                                1 JAID. OF PTS - 1.
JGFT DATA.
JUMP PTI.
JTEST LOWER LIMIT.
JSKIP TO NEXT PT.
JTEST UPPER LIMIT.
JSCALF ALL IF OUT OF RANGE.
JTEST NEXT PT.
8 158 0A

8 155 03

8 156 FF CC

8 158 D2 60 81

8 150 D2 65 81
                                                                                                259 II
26 I II
26 I II
26 2 II
26 3 II
                                                                                                                                                        INX
CPI
JNC
CPI
                                                                                                                                                                              SCLA
SCLA
D
                                                                                                 26/in SCL3: DAD
8160 19
8161 DA 50 81
8164 CD
8165 21 DF 83
8168 30
8169 01 00 85
8160 21 FF 01
8170 FF 80
8170 FF 80
8173 1F
8174 02
8175 03
 8160 19
                                                                                                 2650
                                                                                                                                                                               SCLS
                                                                                                                                                                          H.SCLFOT

# JPUMP SCALE FACTUR COUNT-
PARFAL JSET UP TAPLE PTD-
H.S12-1 IND. OF PTS - 1-
P JGET DATA;

# J TEST SIGN &
# J FAST SIGN &
# J TAPLE PTD-
# JDIVIDE BY 2-

K P JETURN DATA TO TAPLE-
# JPUMP PTR-
                                                                                                                                                                                                                  DONE TESTING.
                                                                                                 2690
                                                                                                                                                        1.X1
                                                                                                 2700
2710 SCL6:
                                                                                                                                                     LDAX P
                                                                                                                                                     CPI SI
CMC
RAR
STAX P
INX P
                                                                                                 2710
2720
2730
2740
2750
  8174 02
8175 03
8176 19
8177 DA 6F 81
817A C9
817F
                                                                                                  2760
                                                                                                                                                                                                                  INEXT PT.
                                                                                                  277 n
                                                                                                                                                                               SCL6
                                                                                                2780 JC SCL6
2790 RET JOONE SCA
2800 JJ
2810 JJSIGNED MULTIPLY PROUTINE.
2820 JJ
                                                                                                                                                                                                                 DONE SCALING.
   8178
   8 17P
8 17P
8 17P
8 17P
                                                                                                2830 ; EXTERNAL REG. USAGE:- A <- (C*A)/128.

9440 : INTERNAL REG. USAGE:-

2850 ; HL = PRODUCT (MISHY,LSBY).
  8 17 P
8 17 P
                                                                                                2860 ; DF = XULTIPLICAND-
2870 ; PC = MULTIPLIER-
2880 ;NO EFGISTERS PRESERVED-
2890 MPY: MOV F.A ; PUT A
                                                                                                                                                                                                                 PUT ARGI INTO MULTIPLICAND.
JARGE ALREADY IN MULTIPLIFR.
817C AF
817C AF
817C AF
817D 47
817F 57
817F 67
818B 17F 67
8182 FF 00
8184 FP 8A 81
8187 7A
8188 2F
8187 7A
8188 2F
8189 57
  8178 SF
                                                                                                 2900
                                                                                                 2910
                                                                                                                                                                              P,A
D,A
H,A
L,A
A,F
                                                                                                                                                                                                                  JOLEAN MERRY'S.
                                                                                                2930
                                                                                                                                                                                                                  CLEAR PRODUCT.
                                                                                                 2950
                                                                                                                                                                                                                 GET LSHY OF MULTIPLICAND.
                                                                                                2960
                                                                                                 297 0
                                                                                                                                                                               MPYI
                                                                                                                                                                                                                INEGATIVE MULTIPLICAND?
                                                                                                                                                                               A.D
                                                                                                                                                                                                                 FEXTEND SIGN TO MSPY.
  8 189 57
8 18A 79
8 18P FE 00
                                                                                                                                                                              D. A
A. C
                                                                                                 3010
3020 MPY1:
                                                                                                                                                                                                                 GET LSPY OF MULTIPLIFE.
                                                                                                                                                     MOV
                                                                                                3030
3040
3050
3060
RIAP FE 00
RIABD F2 93 81
RIABD F2 93 81
RIABD F2 F6
RIABD F7
RIAB
                                                                                                                                                        CPI
                                                                                                                                                        JP
MUC
CMA
MOV
                                                                                                                                                                              MPYR
                                                                                                                                                   CMA SEXTEND NEG TO MSPY.

MOU P.A

MUI A.15 SET ITERATION CTR
PUSH PSW : & SAVE.

SARITH. SHIFT MULTIPLIFR RIGHT (PC).

MOU A.P

CPI AGH

CMC
                                                                                                                                                                                                                  INEGATIVE MULTIPLIER?
                                                                                                 3070
                                                                                                                        MPY2:
                                                                                                 3090
                                                                                                                        MPY3:
                                                                                                3100
3110
3120
                                                                                                                                                                                                                  MAKE CY = MSBIT-
                                                                                                3130
   8 19 A
8 19 P
                           1 F
                                                                                                 3140
                                                                                                                                                        RAR
                                                                                                                                                                             A,C
                                                                                                                                                        MOV
   819C 79
819D 1F
819F 4F
                                                                                                 3160
                                                                                                                                                      MOU A.C
RAR ;LSPIT->CY.
MOU C.A
;TFST MULTIPLIER LSPIT & IF SET,
; ADD MULTIPLICAND TO PARTIAL PRODUCT-
JNC MPY4
DAN D
DAN D
   8 19F
8 19F D2 A3 B1

8 197 D2 A3 B1

8 102 19

8 103 FP

8 104 29

8 105 FP

8 106 FI

8 107 SD

8 108 C2

9 5 B1

8 108 C2

9 5 B1

8 108 C2

8 108 C2

8 108 C2

8 108 C3

8 108 C3

8 108 C4

8 108 C4

8 108 C5

8 108 C5

8 108 C5

8 108 C6

                                                                                                3200
                                                                                                  3210
                                                                                                                                                     DAD D
JARITH. SHIFT MULTIPLICAND LEFT (DF).
XCHG SWAP MULTIPLIER & PROD.
DAD H SHIFT LEFT.
XCHG FESTORE REGS.
JCHECK LOOP CTR.
POP PSW
DCR A JDECREMENT COUNT.
JNZ MPY3
                                                                                                3230
3240
3250
3260
                                                                                                 3270
                                                                                                  3280
                                                                                                   3300
                                                                                                                                                      JNZ MPY3
jDIVIDE 16-RIT PRODUCT RY 128 SO THAT
; SINF & COSINF AMPLITUDE = UNITY-
DAD H ; SHIFT-IN MSDIT OF
MOV AH ; LSPYTE & RETURN IN A-
                                                                                                 3370
3330
3340
3350
                                                                                               C=REAL
                           12
20
10
02
21
                                                                                                                                                                                                                    # A= (REAL 12)/64.
                                                                                                                                                        STAX D
INR L
INR F
JNZ PI
LXI H
                                                                                                                                                                                                                  STORE.
                                                                                                  3460
                                                                                                                                                                                PWR1
H.IMAG : RESET PTRS.
                                           P6 91
00 86
7F 87
                                                                                                     3480
3490
   8 1 PE
8 1C 1
8 1C 4
                                                                                                                                                                                D. PARUF
```

```
8105 0A
8106 FB
8107 86
                                      3520
3530
3540
                                                                                 JA=(IMAG12)/6/1:
                                                            ADD
                                                                                  ##= (EEAL 12 + IMAG12) /64.
 8 1CB
8 1CP
8 1CD
8 1CF
          D2
3F
77
                                       3550
                                                            JNC
                                                                     A. OFFH : A SATURATES AT OFFH.
                                       3560
                                                           MVI
                                                           MOV
XCHG
INR
INR
                                       357 (
                C# 81
                                                                     PWRP
                                     3620 RFT
3630 P;FILL INPD WITH 10.666 HZ, SQUARF WAVF-
3650 H3
3660 WAVF: LXI H:INPD
3660 WAVF: LXI H:INPD
3660 WAVFP:CALL LAG
3660 WAVFP:CALL LAG
3660 CALL HI
3660 CALL HI
3660 CALL HI
                                       3620
 RIDS
 8 1 05
 81D5
81D5
81D8
81D8
81DA
81DD
81F0
          21 00 80
0F 2P
CD EA 91
CD F5 81
                                      3710
3720
3720
 81F1
          CS DA 81
                                                                     WAVES
 81E4 C9
81E5
81E5
81E5
                                                           ECU
ECU
                                                                     117
                                      3750 MID
                                      3750 MID
3760 HI:
3770 LO:
3780 LO:
3790
3800 VAVE3
3810
3820
 8 1F:5
                                                                     A.MID+APC
          3F F5
C3 FC 81
3F OD
06 03
77
23
05
C2 FF 81
                                                                     A-MID-APC
P-3
M-A
                                                                     H
WAUFS
                                       3830
3840
 8 1F5
                                      3850
 APC
BITRE
               0075
802F
8032
                                  37/10
                                  0830
0850
                                              1020
 PRV1
CELCT
                                              0920
               830A
                                  1211
                                              1320
                                                         2230
 CELDI
               8300
                                  02/10
CFLNU
CLEAR
CLP1
COSIN
                                  0270
0220
0630
0640
0250
                                              0660
DELTA
FFT
FPASS
                                              1260
                                                        2070
              800C
                                  0460
                                              0590
                                  1200
                                              3760
0630
1370
HI
               RIFS
                                  3600
                                 0370
0180
0190
 I MAG
                                                                     2120
              8306
                                              1380
                                                                                2030
 INPD
                                  0350
                                              0701
                                                        3660
MID
                                 3680
                                              3780
                                 3750
0720
0700
1610
2980
                                                        378
MOVE
MOVE
MOVE
              8020
                                                         1710
                                                                   1770
                                             1650
3020
MPY1
              8193
8195
                                 3040
                                             3080
MPYA
NCI
NCFLL
                                 309 N
32 1 N
1 430
1 39 N
                                 2290
NPASS
                                 1300
                                             2490
                                             1200
3420
1280
              8050
                                 0385
0230
1060
PAPUF
              977F
POVIFE
                                  [1470
                                             3400
PUP I
              BIP6
                                 3/13 0
                                             3480
                                 3510
PURS
                                             3610
RFAL
RLPT1
                                 0360
0160
0170
                                                                    0320
                                                        0810
                                                                                                      2550 2690 3410
                                                                                            1330
SCALE
                                 1060
                                             1300
SCL2
              8154
                                 2590
                                             2650
                                 2610
2630
2710
0260
0260
S CL3
                                             2640
SCL4
SCL6
SCLFC
SINF
                                             2780
0600
1510
                                                        2670
1640
1490
SINPT
                                                                    2060
5 CADE
             9F:00
                                 0120
                                             3400
STADE
                                             1390
SWAP
SWP1
TIMAG
                                 0960
0950
0310
                                             1660
TREAL
                                                                    1850
              8312
                                 0300
                                                        1720
WAVE
                                 0430
                                             3661
WAVEP
WAVES
```

Text continued from page 458:

I intend to write a subroutine to compute amplitude spectra following the method pointed out by Bob Leedom. (See "Approximation Makes a Magnitude of Difference," June 1979 BYTE, page 188.) This routine does not appear in listing 1, except as a comment.

Pass 1 of the FFT requires a trivial amount of computer arithmetic. Pass 2 is fairly trivial too, since sine and cosine have only the values -1, 0, and 1. Therefore, a simple way to increase the speed of the program would be to largely duplicate the coding of passes 2 thru N (inserting constants instead of variables and using a new sine/ cosine table $\{0,1,0,-1,0\}$, etc). A special multiply subroutine could be used for this: a subroutine that can multiply only by 0, 1, or -1, but do it very quickly. This could shave up to one second off the transform time.

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I CO IMAG.

Listing 2: Object-code listing in hexadecimal format of the assembly-language program given in listing 1. The IBC at the end of this listing is a checksum of the whole code.

```
8000 CD D5 81 CD 0C 80 CD AF 81 C3 E4 AF 3F 00 32 OF
8010 83 21 00 86 36 00 2C C2 14 80 11 00 84 21 00 85
                 1C 2C C2 20 80 11 00 85 21 00 85 06
8020 1A D6 80 77
                           79
                                           7D BB DA 46
           1F 4F
                  7B
                     17
                        5F
                              05 02 32 80
8 030
        7 D
8 04 0 80 4E 1A 77 79 12 2C C2 2F 80 CD 4B 81 21 00 85
8 05 0 7 F 4 F
           2C 46
                  2D 80
                        77
                           79
                              90 SC
                                     77
                                        2C C2 50 80
                                                     3E
8 N 6 0 4 0 3 2 0 B 8 3 3 2 0 F 8 3 3 F 0 2 3 2 0 C 8 3 3 2 0 D 8 3 C D
           3A OP
                  83
                     32
                        0A
                           83
                               21
                                 00 85 22
                                           00 83 22
                                                     02
8070 4B 81
                               06 83 21 00 9F 22
           00 86 22
                     04 83 22
                                                  08 83
8 08 0 83 21
           83 47 21 0D 83 3A 00
                                  83 86
                                        32
                                            02 83
                                                  32
                                                     06
8 09 0 3A 0C
           2A 08 83 7E 32
                           10 83 3E 40 85 6F 7E
8 0A0 83 C5
8 0B0 83 2A 02 83 4E C5
                        3A 11 83 CD 7B 81 32
                                               12
                                                  83 CI
8 0 C 0 3 A 10 8 3 C D 7 B 8 1 3 2 1 3 8 3 2 A 0 6 8 3 4 E C 5
8 0D0 83 CD 78 81 21 12 83 86 77 C1
                                     3A 11 83 CD
                                        12 83 81 77
                  77
                     2A 00 83 7E 4F
                                     3A
           83 96
8 0E0 21
        13
                     02 83 77 2A 04 83 7F 4F
                                               3A 13 83
80F0 21 12 83 96 2A
                                        21 08 83
                                                  3A 0F
8100 81 77
           79
               21
                  13
                     83 96 2A
                               06 83
                                     77
                                                  94 80
8 11 0 83 86 77
               21
                  00 83 34 21 04 83 34 C1
                                            05 C2
               3A
                  0 D
                     83 86
                           77
                               32
                                  04
                                     83
                                        21
                                            0A 83
                                                  35 C2
8120 21 00
           83
               0B 83 7F A7 1F B7 77
                                     CB 23 7F 87
                                                  77
                                                     23
8 130 8A 80 21
                  7 F. A7
                            77
                               C3
                                     80
                                         01
                                            0.0
                                               85
                                                  11
                                                     FF
8140 7F. 87
           77
               23
                        1F
                                  6F
8 150 FF 21 FF
               01 0A 03 FE
                           CC
                               D2 60
                                     81
                                        FE
                                            34 D2 65
                                                     81
                                                     0 A
R160 19 DA 54 81
                  C9
                     21
                        OF
                            83
                               34
                                  01
                                     00
                                         85
                                            21
                                               FF
                                                  01
               1F
                  02 03 19 DA
                               6F 81 C9 5F AF
                                               47
                                                     67
           3F
8170 FE
        80
                                                  93
                                                     81
8180 6F 7E FE 00 F2
                     8A 81
                           7 A
                               2F
                                  57
                                     79
                                         FE.
                                            0.0
                  0F
                     F5 78 FE 80
                                  3F 1F 47 79
8190 78 2F
           47
               3F
81A0 A3 81 19 EB 29 EB F1
                            3D C2 95 81 29
                                            7C
                     87 4E 0A 12 2C
                                        C2 B6
                                               81 21 00
                  7 F
                                     1 C
8180 21 00 85 11
81C0 86 11 7F 87 4F 0A ER 86 D2 CD 81 3F FF
                                               77
                                                  EB
     1C C2 C4 81 C9 21 00 84 OF 2B CD FA 81 CD E5 81
81E0 0D C2 DA 81 C9 3E F5 C3 EC 81 3E 0B 06 03 77 23
81F0 05 C2 EE 81 C9 /BC
```

Listing 3: Listing in hexadecimal format of the two'scomplement square table and sine table used by the FFT program.

```
9F00 00 00 00 00 00 00 01 01 01 01 02 02 02 03 03 04
9F10 04 05 05
              06 06 07 08 08 09 0A 0B 0B 0C 0D 0E 0F
9E20 10 11 12
                        17 18 19 1A
              13 14
                     15
                                     1 C
                                        1D 1F
                                               20
                                                  21
9E30 24 26 27 29 2A 2C 2E
                           2F 31 33 35 36 38 3A
                 48 4A
                                        58 5A
9F40 40 42 44 46
                        4D 4F
                               51
                                  53
                                     56
                                               5D
                                                  5F
9E50 64 67 69 6C 6E 71 74 76 79 7C 7F 81 84 87 8A 8D
9£60 90 93
           96
              99
                 9 C
                     9F
                        A3 A6
                              A9
                                  AC
                                     B0 B3
9E70 C4 C8 CF CF D2 D6 DA DD E1 E5 E9 EC F0 F4 F8 FC
                           E5 E1 DD DA
9E80
     FF
        FC
           F8
              F4
                 FO
                     EC
                        F.9
                                           D2 CF
                                                  CE
9 E 9 0
     C4
        C1 BD BA B6 B3 B0 AC A9 A6 A3
                                                  69 67
9 F.A0
     90 8D
              87
                 84 81
                        7 F
                           7 C
                              79
                                  76
                                     74 71 6E 6C
           8.4
                 5A 58 56 53 51 4F
                                              46
9 EB0 64 62 5F
                                     4D 4A 48
                                                  44 42
              5D
                           33 31 2F 2F 2C 2A 29 27
                                                     26
9 ECO 40 3E
           3C
              3A
                 38
                     36
                        35
              20
                        1 C
                           1A 19 18
                                     17
                                           14
                                              13 12
                                                     11
9 ED0 24 23
           21
                 1 F.
                     1 D
                                        15
9 EE 0 10
        OF
           OF UD OC
                     0B 0P 0A 09
                                  08 08 07
                                           06 06
                                                  0.5
                                                     0.5
9 EF 0 04
        04 03 03 02
                     05 05 01 01 01
                                     0.1
                                        0.0
                                           0.0
                                              0.0
                                                  0.0
                                                     nn
9F00 00 03 06 09 0C 10 13 16
                              19 1C 1F
                                        22
                                           25 28
                                                  29
                                                     SE
9F10
                     3F
                        41 44 47
                                 49
                                     4C 4E
                                           51
                                               53
                                                  55
                                                     58
     31
        33
           36
              39
                  3C
                 62 64 66 68 6A 6B
9F20 5A
        5C
           5F. 60
                                     6D 6F 70
                                              71
                                                  73
                                                     74
                                        7F.
                                                  7F
9F30
     75
        76
           78
                 7 A
                     7 A
                        7 F
                           7 C
                              7 D
                                  7 D
                                     7E
9 F4 0
     7F 7F
           7F 7F
                 7 E.
                     7E
                        7F. 7D 7D 7C
                                     7B
                                        7A 7A 79
                 70 6F
                        6D 6P 6A 68 66
                                                  5 F.
9F50 75 74
           73 71
9F60 5A 58 55 53 51 4F 4C 49 47 44 41 3F
                                           3C
                     22
                        1 F
                           1 C
                                  16 13
                                        1.0
                                           0 C
                                                  06
           2FI 28
                 25
                              19
9 F 7 0 31 2F
                                     E1 DE DB D8
9F80 00 FD FA F7 F4 F0 ED
                           EA E7 E4
                                                  AR
                                                     AR
                 C4 C1 BF
                           BC P9 B7
                                     B4 B2
                                           AF
                                               AD
9F90 CF CD CA C7
                                     93 91
                 9E 9C
                        9A 98 96 95
                                            90 BF
                                                  8D
9FA0 A6 A4 A2 A0
                                                  81 81
9FB0 8F 8A 88 87 86 86 85 84 83 83 82 82 82
                                               81
9FC0 81 81 81 81 82 82 82 83 83 84 85 86 86 87
                                                  88 8A
9FD0 8B 8C 8D 8F 90 91 93 95 96 98 9A 9C 9E AD A2 A4
9FF0 A6 A8 AB AD AF B2 P4 R7 H9 BC RF C1 C4 C7 CA CD
9 FFO CF D2 D5 D8 DP DE E1 E4 E7 EA ED F0 F4 F7 FA FD
```

The 8080 program in listing 1 has been dumped out in hexadecimal format with checksum and appears in listing 2. The sine and square tables appear in listing 3. The equations used to define the tables are:

Two's-complement square table:

- Table entries are unsigned 0 thru 255
- Table index I = 0 thru 127 (two's complement 0

Table (I) = INT (((I $\uparrow 2)/64$) +0.5)

• Table index 129 thru 255 (two's complement -127 thru -1)

Table (I) = INT ((((256 - I) \uparrow 2)/64) + 0.5)

• Table index 128 ('two's-complement – 128) Table (128) = 255 (not exact value of 256)

Two's-complement sine table:

- Table index I runs from 0 thru 255
- Table (I) = INT(0.5 + 127*SIN((I)*2*PI/256)) where PI = 3.1416

An optimization of the 6800 FFT would be to replace lines 285 thru 287 inclusive by the single instruction ASR A. This has been incorporated into the 8080 program, but it makes a negligible difference because there is no single 8080 instruction equivalent of the 6800 ASR A instruction (arithmetic shift right, A accumulator). The test power spectrum produced by the 8080 FFT program is printed out in listing 4.

Listing 4: Test power spectrum produced by the 8080 FFT program in listing 1. The waveform is a square wave with a period of six data points. The first byte is 0 frequency.

```
878F 00
     00 00 00 00 00 00 00 00 00 00 00
                            00 00 00 00
                                0.1
879F 00 00 00 00 00 00 01 01 02 03 10 3C 03 01
87AF
     00 00
         0.0
           0.0
             0.0
               0.0
                 0.0
                   00 00 00
                         0.0
                            n n
                              0.0
                                  0.0
   0.0
                                n n
87PF 00 00 00 00 00 00
               00 00 00 00 00 00 00 00 00 00
               00
                 0.0
                   00
                     0.0
                        00 00 00
87CF
     00 00
         0.0
           0.0
             0.0
                              00 00 00
87FF 13 /72
```

Richard Lord Replies

Mr Roxburgh is indeed correct about the possibility of overflow with my scaling routine. I tried slowly increasing the amplitude of a square-wave input and discovered that for amplitude pairs of \pm hexadecimal 1B, 1F; 33, 3F; and 6A, 6E the algorithm produces overflow artifacts. This did not show up in initial testing because integral binary amplitudes (10, 20, 40) were used. The scaling routine immediately fixes these values before overflow has a chance to occur. For sampled audio, this overflow has undoubtedly introduced errors. Insertion of new limits, as Mr Roxburgh proposed, fixed the overflow problem so that the FFT yields correct results at all amplitudes. My thanks to Mr Roxburgh for pointing this out. I hope that this has not created too many difficulties for anyone who has been using the FFT previous to this discovery.

Many letters have come to me in response to this article and the response has been very gratifying. Most of the letters have been requests for the 6502 verison which I never got around to writing. (At this time I'd be more inclined to try a 6809 version.) Quite a few readers suggested great improvements to the "sum of absolute values" method, and one letter pointed out that the SIN table is actually a -1*SIN yielding inverted imaginary terms. All these improvements are greatly appreciated.



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SYSTEMS

Handy Pocket Computer Uses BASIC

Sharp Corporation will announce the introduction of its PC-1211 Pocket Computer into the American market at this month's National Computer Conference (NCC). Measuring only 17.5 by 7 by 1.5 cm (6% by 2% by 1%, inches), the battery-powered PC-1211 contains BASIC in ROM (read-only memory). A 24-character LCD (liquid-crystal display) can be used to show program lines, prompt the user for input from the keyboard, or display results. The unit's typewriter-like keyboard includes a calculator-type keypad. The PC-1211's memory can hold up to 1424 program steps and 26 data variables, or program memory can be used for data leight steps are equivalent to one variable). Information in memory is retained even when the power is off due to a memory safeguard circuit.

The PC-1211 uses a reservable key system, making it possible to assign a key for a frequently used function or command. Reserved keys provide one-key recall during both manual calculation and programming. In addition, a definable key system fixes 18 programs for each key, allowing the user to recall and run each program at the touch of the proper key. Transparent templates that fit over the keyboard portion of the unit are included to allow labeling



of reserved and defined kevs.

The BASIC interpreter has the more common BASIC commands and functions, as well as DEBUG. PRINT USING, BEEP, ASN Jarcsine), ACN (arccosine), EXP (ex), and more. Editing functions allow left and right cursor shifting, insertions and deletions, and scrolling up or down. Subroutines and FOR...NEXT loops can be stacked to four levels, and 15 levels of parentheses can be maintained. An 80-character input buffer and multiple statements per line allow easy program entry. A ten-digit mantissa and two-digit exponent are used in all calculations. Four mercury batteries provide approximately 300 hours of operation, thanks to the automatic poweroff feature. An applications manual containing 134 programs in ten application areas such as math, statistics, civil engineering,

and electrical is included. Each program is accompanied by a description of how it works and a complete list of variable assignments. A beginner's BASIC book is also included in the package.

Also being introduced at NCC are two peripherals for the PC-1211. The CE-121 Cassette Interface allows programs, key assignments, and data to be saved or loaded to or from a cassettetape recorder. For hard-copy output, Sharp has the CE-122 Printer/ Cassette Interface. In addition to the cassette-interface functions. the CE-122 features a 16-character dot-matrix printer capable of printing one line per second. The unit is powered by a rechargeable nickel-cadmium battery and includes a battery indicator that flashes when the battery becomes low.

The PC-1211 will have a suggested retail price of \$249. The CE-121 and the CE-122 will have suggested retail prices of \$49 and \$149, respectively.

The PC-1211 has been previously sold by Radio Shack as the TRS-80 Pocket Computer.

For more information on the PC-1211 Pocket Computer, the CE-121 Cassette Interface, or the CE-122 Printer/Cassette Interface, contact Sharp Electronics Corporation, 10 Keystone PI, Paramus NJ 07652, (201) 265-5600.

Circle 500 on inquiry card.

Master Controller Board

The Master Controller Board is a Z80-based single-board computer that can be customized for each application. Customization is accomplished by inserting various ROMs (read-only memories), programmable memories, and control integrated circuits as needed. All the I/O (input/output)

circuits are mapped into both memory and I/O address space. The board provides three ROM/EPROM (erasable programmable ROM) sockets for up to 12 K bytes of mixed ROM/EPROM. Also included are 2 K bytes of programmable memory, provision for up to 72 lines of parallel I/O, a keyboard controller, and an integrated circuit that provides

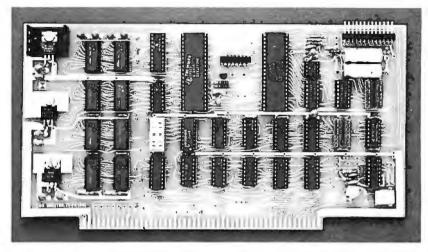
two serial I/O ports. Two counter/ timers and an arithmetic circuit can be added. The Master Controller Board costs \$49.95 for a bare board, \$99.95 for the minimum kit, and \$199.95 assembled. Other options are available. Contact R W Electronics, 3165 N Clybourn, Chicago IL 60618, (312) 248-2480.

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PERIPHERALS

Video and Audio on One Board

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generator uses the Texas Instruments TMS9918A Video Display Processor and the General Instrument AY-3-8910 Programmable Sound Generator, and is compatible with Z80, 8085, and 8080 microprocessors on S-100 bus systems. Documentation in-

cludes programming examples and test routines. It is available for \$475 assembled and tested or \$375 in kit form. Contact Electronic Design Associates, POB 94055, Houston TX 77018, (713) 999-2255.

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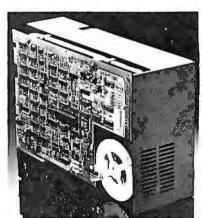
nique. The Q2000 family features a 4.34-megabit-per-second transfer rate, an average latency of 10 ms, and access times of 15 ms track-to-track, 100 ms maximum, and 50 to 60 ms average. Maximum recording density is 6600 bits per inch, and track density is 345 tracks per inch. Rotational

speed is 3000 rpm (revolutions per minute). Soft-sectoring is offered.

In OEM (original equipment manufacturer) quantities of 500 per year, pricing is \$1200 for the 10-megabyte Q2010, \$1500 for the 20-megabyte Q2020, and \$1800 for the 30-megabyte Q2030. For more information, contact Quantum Corporation, 2150 Bering Dr, San Jose CA 95131, (408) 262-1100.

Circle 503 on inquiry card.





PUBLICATIONS



Inside BASIC Games

Inside BASIC Games, by Richard Mateosian, uses games as a framework for teaching BASIC programming. Eight games, ranging from simple arithmetic to complex matching games, are described and analyzed so that readers can learn how to design their own programs, as well as play the game. The games are written for most microcomputers. Inside BASIC Games is a Sybex publication, and it costs \$13.95. Contact Sybex Inc. 2344 6th St. Berkeley CA 94710, (415) 848-8233.

Circle 504 on inquiry card.

Microcomputer Software Catalog

Creative Discount Software has released its Winter-Spring Software Catalog for the TRS-80, TI-99/4, and the Apple II and the Apple II Plus microcomputers. The catalog features professional, educational, and business software at discounts of up to 30%. Medical and dental office-management systems are also available. For your free copy, request catalog number 47B, from Creative Discount Software, 256 S Robertson, Suite 2156, Beverly Hills CA 90211, (800) 824-7888; in Alaska and Hawaii, 18001 824-7919; in California, (800) 852-7777. Ask for operator 831.

Circle 505 on inquiry card.

Solutions from Serendipity

Serendipity Software Solutions features commercial-application software packages designed for Z80 and 8080/8085-based microcomputers operating under CP/M. Among the products featured in the catalog are general-ledger accounting, commercial accounts receivable and payable, payroll, inventory control for retailers and manufacturers, and professional client billing. There is a \$1 handling charge for the catalog. Contact Serendipity Systems Inc, 225 Rd. Ithaca 14850, (607) 277-4889.

Circle 506 on inquiry card.

Supercap Series Catalog

NEC Electron's Supercap catalog includes specifications, dimensions, applications, discharge characteristics, and lists of features for high-capacitance Supercap memory-backup devices. The Supercaps supply capacitances of up to 1 F [yes, one farad...RSS].

They feature a slow rate of discharge and can provide very low currents for approximately one week. The catalog is free from the Product Marketing Manager for Capacitors, NEC Electron Inc. 252 Humboldt Ct, Sunnyvale CA 94086, [408] 745-6520.

Circle 507 on inquiry card.

Optoelectronics and Fiber-Optics Manual



A 286-page optoelectronics and fiber-optics data manual has been published by Motorola Semiconductor Products Inc. The manual provides device data sheets, selector quides, cross-references, and applications information. The manual includes gallium-arsenide infrared emitters, silicon detectors, opto-coupler/isolators, the family of opto-triac drivers, and Motorola's SCR (siliconcontrolled rectifier) couplers.

The manual's fiber-optic section is intended principally to address fiber-optic communications systems in the computer, industrial controls, medical electronics, consumer, and automotive applications.

The data book, Mototola Optoelectronics Device Data, costs \$3.25. It is available from Motorola Semiconductor Products Inc. POB 20912, Phoenix AZ 85036, (602) 244-4306.

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SOFTWARE

Two New Products from Commodore

Ozz-The Information Wizard lets users design data-management and retrieval systems. Ozz was created for the Commodore CBM 8032 microcomputer. The program allows users to set up formats, store information, perform calculations and global searches, design forms and documents, analyze information, and access files.

Wordcraft 80 is a word-processing program designed for the 8032 system. Wordcraft 80 offers variable page layouts of up to 117 characters by 98 lines; screen display of finished-format documents; tabs, indentations, decimal tabs, columns; automatic centering and right-margin justification; automatic pagination, headers, and trailers; deletion and insertion of text; transfer of text from one page to another; merging of form letters with name/address files; handling of single sheets or continuous-form paper; sub- and superscripts; and automatic underlining and emboldening of text.

For more information on both products, contact Commodore Business Machines Inc. 950 Rittenhouse Rd, Norristown PA 19403, 12151 666-7950.

Circle 509 on inquiry card.

Atari Graphic Editor

Plot & Draw is a cassette-based graphics-generation and editing package that creates graphics in three colors plus a background. Video drawings can be created and saved on cassette. It requires an Atari computer with 8 K bytes of programmble memory and a joystick. The price is \$18 from Mosaic Electronics, POB 748, Oregon City OR 97045.

Circle 510 on inquiry card.

The Voice

The Voice gives the Apple II or the Apple II Plus the power of speech. The Voice's built-in vocabulary allows expression of many combinations of phrases, or the user can enter his own vocabulary and make the 48 K-byte Apple say anything. Floppy disks store up to 80 words or phrases that can later be sorted for quick reference. The Voice allows any BASIC program to speak by using PRINT statements. The price is \$39.95, from Muse Software, 330 N Charles St. Baltimore MD 21201, (301) 659-7212.

Circle 511 on inquiry card.

FORTH-79 for the Apple

MicroMotion's FORTH-79 conforms to the International FORTH-79 standard. It is suited for data acquisition, process control, animation, and video games.

FORTH-79 comes with a screen editor and macroassembler, and vocabularies for strings, double-precision integers, lowresolution graphics, and modem communications. The operating system allows for multiple disk drives and is 13- or 16-sector disk compatible. It runs on a 48 K-byte Apple II or Apple II Plus. FORTH-79 can be obtained for \$89.95 from MicroMotion 12077 Wilshire Blvd, Suite 506, Los Angeles CA 90025, [213] 821-4340.

Circle 512 on inquiry card.

TFORTH

TFORTH is a fig- (FORTH Interest Group) standard version of FORTH, extended for the TRS-80. It contains an operating system, assembler, text editor, floating-point mathematics package, I/O (input/output) package, graphics links into TRS-80 BASIC, and a phoneme assembler to support voice synthesizers. TFORTH is supplied on 5-inch floppy disks for \$130. Con-

tact Advanced Technology Cor-

poration, 1617 Euclid Ave, Knox-

ville TN 37921, (615) 525-1632.

Circle 515 on inquiry card.

A Stellar Trek

This high-resolution color version of the Star Trek game runs on the Apple II. Three different Klingon opponents and the Romulan Star Empire are pitted against the user. Users have many command prerogatives, including movement throughout the galaxy, use of starship weaponry, maintenance of energy reserves, repair of damage, and more. A Stellar Trek requires 48 K bytes of memory and Applesoft BASIC in ROM (read-only memory). The price is \$24.95 on floppy disk. Contact Rainbow Computing, 9719 Reseda Blvd, Northridge CA 91324, [213] 349-5560.

Circle 513 on inquiry card.

Combine Hard Disks and the TRS-80

HDOS-2 is a hard-disk operating system designed to be used with TRSDOS 1.2 on the TRS-80 Model II. The advantage of this software is that it allows a Corvus hard-disk drive to be interfaced with existing software with only minor changes to the programs. HDOS-2 requires 1 K bytes of memory and allows use of multiple drives. The system costs \$ 125. Contact Computer Program Associates, 15076 Beltway Dr, Dallas TX 75234, (214) 233-2039.

Circle 514 on inquiry card.

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SOFTWARE

Link the TRS-80 with Other Systems

The Super-Host program allows any type of system to communicate with the TRS-80 Model I microcomputer. The program will configure itself to run under TRSDOS, NEWDOS 2.1, or NEWDOS-80. It keeps track of the date and time, even after reboot or system resets. One function of the program protects the user's own and any foreign system from unwanted control codes. Another feature allows users to customize transmissions to conform to other systems' standards and block out characters that might affect those systems.

Super-Host is a menu-driven program, so users can set up all system parameters. Other features are its lowercase driver, uppercase lock for incoming data, and independent uppercase lock on outgoing data. It has user-programmable nulls and line feed. TRS-80 computers with a printer can be programmed to maintain a printed record of callers who have accessed the system.

Super-Host is available for \$29.95 from Programs Unlimited, POB 265, Jericho NY 11753, [516] 997-8668.

Circle 516 on inquiry card.

FORTH for Atari

This FORTH system for the Atari 400 and 800 computers requires a minimum of 16 K bytes of programmable memory. The disk-based system has a screen editor and the capability to review and modify disk contents. Included with the program package is dictionary documentation and a customization guide. The system costs \$50. For further information, contact Pink Noise Studios, 1411 Center St, Oakland CA 94607, [415] 465-1212.

Circle 517 on inquiry card.

Softstuff Software from Heath



Heath's utility and applications programs in the Softstuff line include the General Ledger II on a floppy disk for use with the HDOS operating system or Heath's version of the CP/M operating system. The price for the program is \$124.95. The Small Business In-

ventory program for HDOS systems is \$69.95. The CBASIC language, a disk-based, noninteractive language with pseudocode compiler and run-time interpreter for CP/M systems is priced at \$110. The BDS C compiler includes a linking loader, a library containing file I/O (input/output) and floating-point functions, and a library manager. The C compiler runs on CP/M systems and is priced at \$119.95.

The Softstuff product line also offers the Microsoft MACRO-80 package, a full-screen editor, a sort program, and a network system. For more information on Softstuff programs, contact Heath Company, Department 350-670, Benton Harbor MI 49022, (616) 982-3210.

Circle 518 on inquiry card.

Software for Law Offices

Law-1 is a time-management and billing system for the legal professional. It features system and program security, client/matter and attorney reporting, accounts-receivable ledgers, ageing analysis, pre-billing worksheets, invoicing, and automatic file backup, and it performs other-than-standard inquiries.

Law-1 is written in CBASIC for CP/M-based systems. It comprises 38 applications packages. The system is parameter driven and can support floppy- and hard-disk configurations. Different terminals are supported. A demonstration package is available for \$75, and the single-user package price is \$800. For further information, contact Microcon Inc, POB 805, Amherst NH 03031, [603] 673-0230.

Circle 519 on inquiry card.

Learn Trigonometry on the Compucolor II

Using a circular functions approach to trigonometry, these teaching programs provide experiences with radian measure, sine function development, graphing the sums of functions, drill with identities, and polar graphs. All programs encourage the user to explore functions under computer guidance, to recognize identities, and to notice patterns. Program listings are included, so users can create additional variations and drills. This disk for the Compucolor or Intecolor computers requires a 64- by 32-character screen with 127 by 127 color blocks in low- and high-resolution graphics. It is available for \$29.95 from Metra Instruments Inc, 2056 Bering Dr, San Jose CA 95131, (408) 297-8530.

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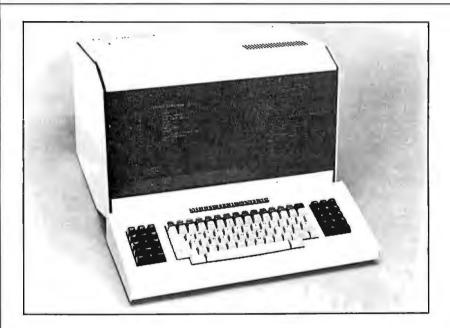
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SYSTEMS



MT500 System

The MT500 microcomputer provides data- and word-processing capabilities for business and scientific applications. The MT500 features a video display, a Z80A microprocessor, the CP/M operating system, 64 K bytes of programmable memory, two 500 K-byte 5-inch floppy-disk drives, and a keyboard. Printers and modems can be attached. The MT500 has a suggested price of less than \$6000. For details, contact Maatra Corporation, 1835 W Shryer Ave, Roseville MN 55113, (612) 631-3555.

Circle 521 on inquiry card.

Memory-Mapped S-100 Video Board

The VB3 is a memory-mapped board with a video-display system for S-100 computers. The display can be programmed for up to forty-eight 80-character lines featuring upper- and lowercase letters with true descenders. The VB3 features user-programmable fonts, low intensity, reverse and inverted video, and added print functions such as underscore, strike-through, thin line, or dot graphics. While the VB3 is memory mapped, it occupies memoryaddress space only when activated.

Software for the VB3 includes a CP/M-compatible driver routine and a terminal-simulator routine. Software controller timing, top and bottom margins, horizontal position, one level of gray, blinking and blank-out character and cursor features are offered. The VB3 video board costs \$654.

For further information, contact SSM Microcomputer Products Inc, 2190 Paragon Dr, San Jose CA 95131, (408) 946-7400. Circle 522 on inquiry card.

HP-83 from Hewlett-Packard

The HP-83 microcomputer is designed for business and technical professionals. The HP-83 is identical to Hewlett-Packard's HP-85 except that it does not have a built-in tape-cartridge drive and thermal printer. The HP-83 has a high-resolution video display, keyboard, enhanced BASIC, and graphics capabilities. Floppy-disk drives and printers can be interfaced to the unit. A data-base system, graphics software, a communications program, and a graphics digitizing tablet are some of the software and peripheral packages devel-



oped for the machine. The HP-83 has a list price of \$2250. For more information, contact Inquiries Manager, Hewlett-Packard Company, 1507 Page Mill Rd, Palo Alto CA 94304, (415) 857-1501.

Circle 523 on inquiry card.

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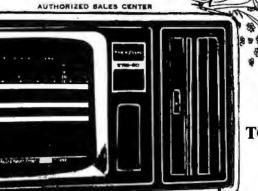






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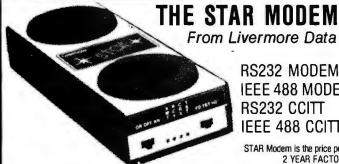
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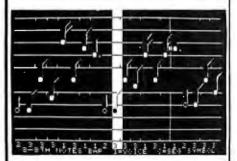
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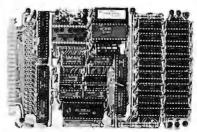
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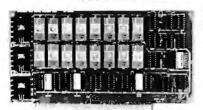
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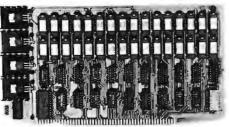
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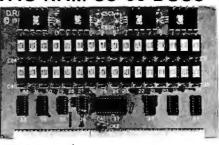
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FOR 2MHZ **ADD \$10**



FOR SWTPC 6800 BUSS!

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- A'l Parts and Sockets included
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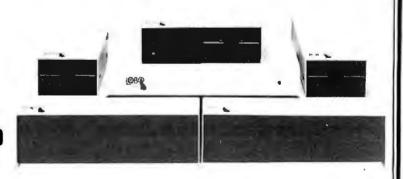


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Compare features before you decide to buy any other computer. There is no other computer on ome computer. I mere is no other computer on the market today that has all the desirable bene-fits of the Super Elf for so little money. The Super Elf is a small single board computer that does many big things. It is an excellent computer for training and for learning programming with its machine language and yet it is easily expanded with additional memory, Full Basic, ASCII Keyboards, video character generation, etc.

Before you buy another small computer, see if it includes the following features: ROM monitor; State and Mode displays; Single step; Optional address displays; Power Supply; Audio Amplifier and Speaker; Fully socketed for all IC's; Real cost of in warranty repairs: Full documentation.

The Super Elf includes a ROM monitor for program loading, editing and execution with SINGLE STEP for program debugging which is not included in others at the same price. With SINGLE STEP you can see the microprocessor chip operating with the unique Quest address and data bus displays before, during and after executing in-structions. Also, CPU mode and instruction cycle are decoded and displayed on 8 LED indicators.

An RCA 1861 video graphics chip allows you to connect to your own TV with an inexpensive video modulator to do graphics and games. There is a speaker system included for writing your own music or using many music programs already written. The speaker amplifier may also be used to drive relays for control purposes.

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This is truly an astounding value! This board has been designed to allow you to decide how you want it optioned. The Super Expansion Board comes with 4K of low power RAM fully addressable anywhere in 64K with built-in memory protect and a greenth laterage. Provisions have tect and a cassette Interface. Provisions have been made for all other options on the same board and it fits neatly into the hardwood cabinet alongside the Super Elf. The board includes slots for up to 6K of EPROM (2708, 2758, 2716 or TI 2716) and is fully socketed. EPROM can be used for the monitor and TinyBasic or other purposes. A IK Super ROM Monitor \$19.95 is available as an on board option in 2708 EPROM which has been preprogrammed with a program loader/ editor and error checking multi file cassette read/write software, (relocatable cassette file) another exclusive from Quest. It includes register save and readout, block move capability and

Quest Super Basic V5.0

Quest Super Basic V5.0

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Super Monitor VI.I Source Listing

plus foad, reset, run, wait, input, memory protect, monitor select and single step. Large, on board displays provide output and optional high and low address. There is a 44 pin standard connector slot for PC cards and a 50 pin connector slot for the Quest Super Expansion Board. Power supply and sockets for all IC's are included in the price plus a detailed 127 pg. instruc-tion manual which now includes over 40 pgs. of software info. including a series of lessons to help get you started and a music program and graphics target game. Many schools and univer-sities are using the Super Elf as a course of study. OEM's use it for training and R&D.

Remember, other computers only offer Super Elf features at additional cost or not at all. Compare before you buy. Super Ell Kit \$106.95, High address option \$8.95, Low address option \$9.95. Custom Cabinet with drilled and labelled plexiglass front panel \$24.95. All metal Expansion Cabinet, painted and silk screened, with room for 5 S-100 boards and power supply \$57,00. NICad Battery Memory Saver Kit \$6.95. All kits and options also completely assembled

Questdata, a software publication for 1802 computer users is available by subscription for \$12.00 per 12 issues. Single issues \$1.50. issues 1-12 bound \$16.50.

Tiny Basic Cassette \$10.00, on ROM \$38.00, original Elf kit board \$14.95. 1802 software; Moews Video Graphics \$3.50. Games and Music \$3.00, Chip 8 Interpreter \$5.50.

points can be used with the register save feature to isolate program bugs quickly, then follow with single step. If you have the **Super Expansion Board** and **Super Monitor** the monitor is up and running at the push of a button.

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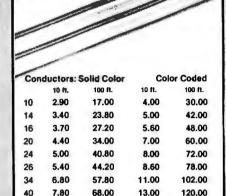
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3.5"	1.37	4.15	7.37	7.5"	2.08	7.07	13.09	100	4"	100	6"	500	3 1/2"	500	51
4.0"	1.42	4.44	7.94	8.0"	2.14	7.38	13.73				- 617	500	4"	500	6'
4.5"	1.48	4.74	8.54	8.5"	2.18	7.69	14.36								_
5.0"	1.54	5.04	9.13	9.0"	2.24	8.11	15.01	Kit	No. 2	\$2	4.95	Kit N	lo. 4	\$59.	.95
5.5"	1.58	5.38	9.72	9.5"	2.30	8.32	15.65								
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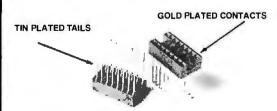
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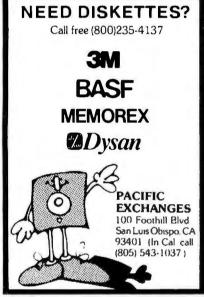
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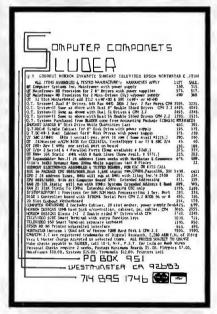
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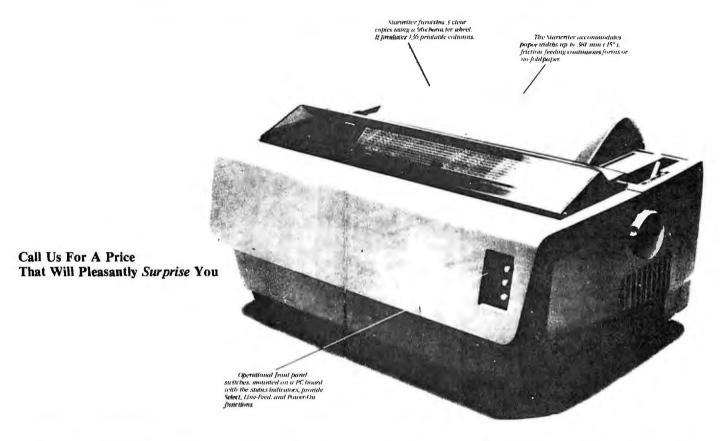
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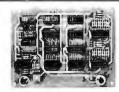
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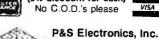
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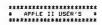


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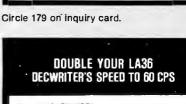
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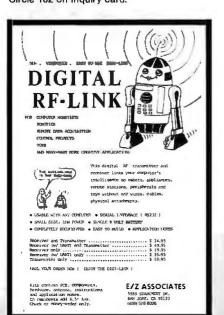
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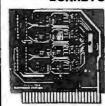
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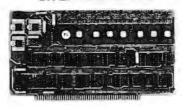
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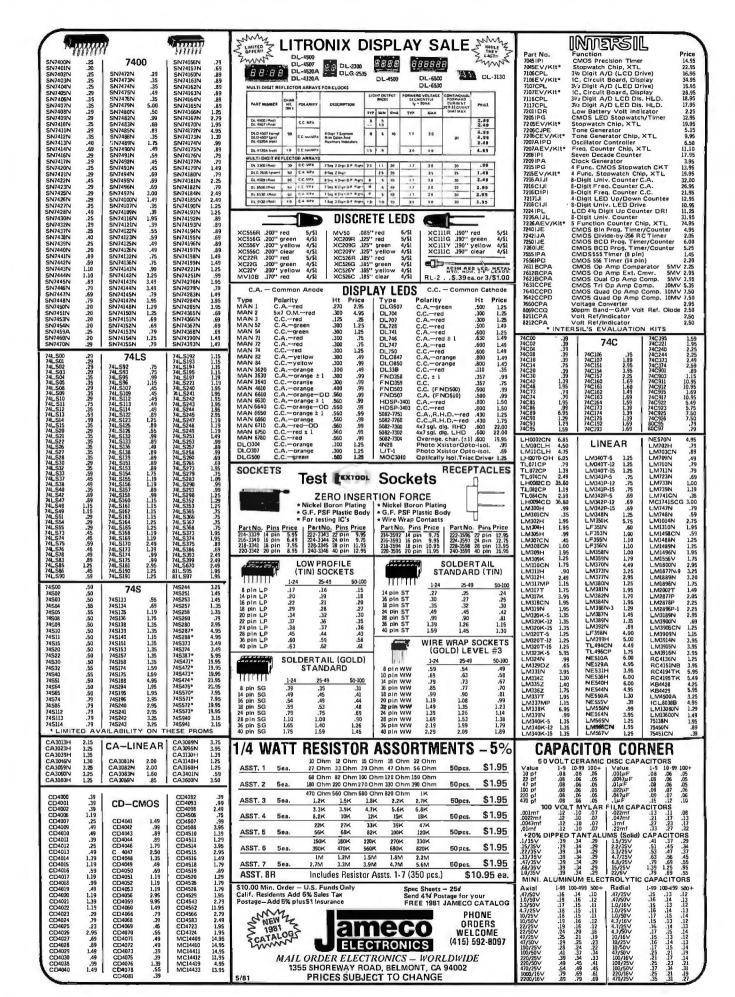
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General Description: The JE215 is a Dual Power Supply with independent adjustable positive and negative output voltages. A separate adjustment for each of the supplies provides the user unlimited applications for IC current voltage requirements. The supply can also be used as a general all-purpose variable power FEATURES: supply.

- FEATURES:

 Adjustable regulated power supplies, pos. and neg. 1.2VDC to 15VDC.

 Power Output (each supply):

 5VDC @ 500mA, 10VDC @ 750mA, 12VDC @ 500mA, 10VDC @ 750mA, 10VDC @ 750mA, 10VDC @ 775mA, 10V

JE215 Adj. Dual Power Supply Kit (asshown) . . S24.95 (Picture not shown but similar in construction to above) JE200 Reg. Power Supply Kit (5VDC, 1 amp) . . \$14.95 JE206 Adapter Brd. (to JE200) ±5,±9 &±12V. \$12.95 JE210 Var. Pwr. Sply. Kit, 5-15VDC, to 1.5amp. \$19.95

National Semiconductor Clock Modules MICROPROCESSOR COMPONENTS

	A/8080A SUPPORT DEVICES -			CQUISITION (CONTINUED)-	_
INS8080A	CPU	4,45	ADC0609CCN	8-Bit A/O Converter (8-Ch. Multi.)	5.
OP8212	8-Bit Input/Dutout	3.25	ADC@17CCN	8-Bit A/D Converter (16-Ch. Multi.)	10.
DP8214	Priority Interrupt Control	5.95	DAC1000LCN	10-Bit D/A Conv. Micro. Comp. (0.05%	
OP8216	Bi-Directional Bus Driver	3.49	DACIMBLEN	10-BitO/AConv. Micro. Comp. (0.20%	
OP8224	Clock Generator/Driver	3.95	DACI020LCN	10-Bit D/A Converter (0.05% Lin.)	8.
DP8226	fine Defree	1.49	DACIGZILON	10-Bit D/A Converter (0.20% Lin.)	5.
DP8228	System Controller/Bus Driver	4.95	DACIZZLON	12-BItO/A Converter (0.20% Lin.)	9.
OP8238	System Controller	5.95	CD4051N	8-Channel Multiplexer	1.
INS8243	I/O Expander for 48 Series	9,95	AY-5-1013	30K BAUD UART	5
INS8250	Asynchronous Comm, Element	16.95		RAM'S	_
DP8251	Prog. Comm. I/O (USART)	1.95	1700	256x1 Static	b.
DP8253	Prog. Interval Timer	14.95	1103	1024×1 Dynamic	
OP8255	Prog. Peripheral I/O (PPI)	9.95	2101 (5101)	256x4 Static	1.
DP8257	Prog. DMA Control	19,95	2102	Min State	î.
OP8259	Prog. Interrupt Control	14.95	23 L.02	100m) Static	1.
OP8275	Prog. CRT Controller	49.95	2111 (8111)	256rd Static.	3.
DP8279	Prog. Keyboard/Osplay Interface	19.95	2112	256±4 STATIC MOS	4
DP8300	Octal Bus Receiver	6.95	2114	1024x4 Static 450ns	3.
OP8303	System Timing Element	6.95	2114)	1024x4 Static450ns Low Power	4.
DP8304	8-Bit Bi-Directional Receiver	3.95	2114-3	1024×4 Static 300ns	4.
DPSIDE	8-Bit Bi-Oirectional Receiver	3.95	213413	1024x4 Statte 300ns Low Power	5.
EDFF8300	8-Bit BI-Directional Receiver	3,95	2117	16,384x1Oynamic550ns(housemarket	1) 4.
50	00/6800 SUPPORT DEVICES -		4116 (UPD416)	15K Dynamic #0ns	3.
MC6800	00/6800 SDPPORT DEVICES -	14.59	4164 MM2142.1	64K Dynamic 250ns	49
WC6803CP	MPU with Clock and RAM	M.99 ML95	MM2147.J	4096x1 Fast 70ns	19.
MC6802CP	128x8 Static RAM	4.95		26rd Status	3.
MC6821	Perioheral Inter, AdaPt (MC6820)	2,00	MM5261	1024x1 Dynamic Fully Decoded	
MC6821 MC6828	Priority Interrupt Controller	7,49 (4.95	MM5262 MM5280/2107	2Kxl Dynamic	
MC6828 MC6830L8	1024±1-Bit ROM (MC68A30-8)	14.95		40%x) Dynamic	4
MC6830L8	Asymchronous Comm. Adapter	4.95	MM5290J-2(4)16) MM5298J-3A	16K Dynamic 150ns 8K Oyn, 200ns (tower/a of MM52903)	5.
MC6852	Synchronous Serial Data Adapter	6.95	MM5298J-3A UPEJ414/MK4027	8K Dyn. 200ns (tower-in of MM52903) 4K Dynamic 16-pin	4
MC6852 MC6860	0-600bos Digital MODEM	6.99	TMS4044-45NL		14
MC6862	2000b0s Modulator	12.95	TMS4044-45NL TMS4045	AK Static	14
MC6880A	Quad 3-State Bus, Trans, (MC8T26)	12.75	+ MP 4042	MNK4 Static	14
	MICROPROCESSOR CHIPS-		IRRA	PROMS/EPROMS	5
260 (780 C)	CPU (MK380N) (2MHz)	13.95	2706	BK EPROM	5
Z80 A (780-1)		15.95	TMS2716	16K EPROM (5V, +5V, +12V)	19.
COPIECE	CPU	19.95	2716intci(25161T)		10.
2650	MPU	16.95	2732intel(2532)TI		19.
	C CPU-4-Bit Slice(Com.Temp.Grade		275211101(2532)11	8K EPROM (450ns) (Single +5V)	19.
MC56502	MPU w/Clock t&KBytes Memory)		5203	2048PROM	H
INS8035N-6	MPU- II- BIL (6M HZ)	16.95	82523(745388)	32×8P ROM (Open Collector)	4
INS1039N-6	EPU-Sqt. Chie 1-Bit((2) bytes RAM)		\$25115	40% Bipplar PROM	Ph.
INS8040N-6	CPU (256 Bytes RAM)	24.95	825123(745288)	Exil Tri-State Bippiar PROM	4
IN58070N	CPU-64 Bytes RAM	Z4.95	825185	EXCENSION	29
INS8073N	CPU w/Basic Micro Interpreter	21.95			d

MPU-H-Bh SHIFT REGISTERS— Dual 25-Bit Dynamic Dual 35-Bit Dynamic Dual 105-Bit Static Dual 106-Bit Static Dual 106-Bit Static Dual 106-Bit Static Dual 106-Bit Dynamic 107-Bit Dynamic/Accumulator Octal 106-Bit Octal 106-Bit Octal 106-Bit

CPU-16-Bit

JAIPC File (Goste)

AFION-ICN Universit Active Filter 25%
AFI23-ICJ TOUCH TOUC

POM'S— Cherecter Denerator (Upper Case) Character Generator (Lower Case) Character Generator 2018-Bit Read Only Memory Striking) -NMOS READ ONLY MEMORIES

DP 178/9x7 ASC 11 Shifted w/Greek

DP 128/9x7 Ma h Symbol & Pictures

DP 128/9x7 Ma h Symbol & Pictures

DP 128/9x7 Afaba, Equivol Char Gar. MC###305 ##C###105 ##C###1105 13.50 13.50 13.50

OPROCESSOR MANUALS M-Z80 M-CDP1802 e4-2850 User Manual 5.00
SPECI AL FUNCTION
Dual MGS Clock Driver (MAZ)
Floody Onc Coart older
Communication Chie
Microprocessor Compatible Clock
Microprocessor Compat

MMSAL74N CDP402N CCIPADMAN COPION

N 12-Ses, VAC Fluor, Driver (26-pin 184);
TELEPHONE (KEYENGARO CHIPS —
Dun Button Tetephone Dalet
Resettory Duller
CMDS Clock Generator
CMDS Clock Generator
Keyende Finder (18-keye)
Port Button Butte Ghaler
Port Systak-Keyende Keyender Encoder
Systak-Keyender Keyender Encoder AV-5-3100 AV-5-9000 AV-5-9300 AV-5-23M HI30M6-5 1402827 74C903 MM53150N

Blank Desk-Top Electronic Enclosures



- Top/bottom panels 080 thk alum. Alodine type 1200 finish (gold tint color) for best paint adhesion after modification.
- Vented top and bottom panels for cooling efficiency.

Rigid construction provides unlimited applications.

CONSTRUCTION:

The "DTE" Blank Desk Top Electronic Enclosures are designed to blend and complement today's modern computer equipment end can be used in both industrial and home. The end pieces are precision molded with an internal slot (all eround) to accept both top and bottom panels. The panels are then festened to X" thick tabs inside the end pieces to provide maximum rigidity to the enclosure. For east of equipment servicing, the rear bottom panel slides back on slotted tracks while the rest of the enclosure remains intact. Different panel widths may be used while maintaining a common profile outline. The molded end pieces can also be painted to match any panel color scheme.

DESIGNERS' SERIES



T	Jr ""		Englosura Model No.	Panel Width	PRICE
	-	1	DTE-8	8.00"	\$29.95
		N ₃ T	DTE-11	10,65"	\$32.95
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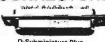
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AC1700	117V/60Hz	9 VAC 1.7 amp	\$6.95
DV 9200	117V/60Hz	9 VDC 200mA	\$3.25
DC 900	120V/60Hz	9 VOC SDOMA	\$3.95

CONNECTORS



DB25P	D-Subminiature Plug \$2,95
D825S	D-Subminiature Socket , , . \$3.50
DB51226	Cover for DB25P/S \$1.75
22/44SE	P.C. Edge (22/44 Pin) \$2.95
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UG89/U	BNC Jack
UG175/U	UHF Adapter
SO239	UHF Panel Recp. ,
PL258	UHF Adapter \$1.60
PL259	UHF Plug
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UG1094/U	BNC Bulkhead Recg \$1,29

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Expand your 4K TRS-80 System to 16K.

(it comes complete with:

#8 ea, MM5290 (UPD416/4116) 16K Dyn. Rams (*NS)

Documentation for Conversion Kit comes

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JE610 ASCII **Encoded Keyboard Kit**



The JES10 ASCII Keyboard Kit can be interfeced into most any computer system. The kit comes complete with the system of the syst

JE610/DTE-AK (as pictured above) . . . \$124.95 JE610 Kit & Components (no case). . . . \$ 79.95 K62 62-Key Keyboard (Keyboard only) ...\$ 34.95

DTE-AK (case only - 344"Hx11"Wx844"D)\$ 49.95

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The JESOO Encoder Keyboard Kit provides two separate hoxadecimal digits produced from sequential key entries a sillow direct programming for 8-bit microprocessor or 8-bit memory circuits. Three additional keys are provided for user apparations with one having a bitable output swallable. The outputs are instehed and monitored with 9 LEO readouts. Also included is a key entry strobs. Features: Full 8-bit fatched output for microprocessor operation. Debounce circuit provided for all 518 bits of 18 bits o

JE600/DTE-HK (as pictured above) \$99.95 JE600 Kit PC Board & Computs, (no case) . . \$59.95 K 19 19-Key Kayboard (Keyboard only) \$14,95

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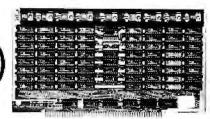
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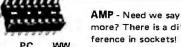
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REGULATOR

\$5.50



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PINS	PC	ww	ference in so These aren't
8	.10	.26	est prices vo
14	.13	.29	find. But, if
16	.16	.32	been"burned
18	.18	.34	by bad conn
20	.22	.38	in your com
24	.32	.48	few pennies
28	.34	.50	best is worth
40	.45	.61	
	RES	ISTOI	RS .02 ea!

75

100

150

220

330

470

680

1K

2.2K

1.0

4.7

6.8

10

15

22

27

33

47

more? There is a difference in sockets! These aren't the lowest prices you can find. But, if you've been"burned" before by bad connections in your computer, a few pennies for the best is worth it!

22K

24K

27K

33K

39K

47K

68K

100K

150K

220K

330K

470K

680K

1.5M

2.2M

4.7M

10M

1M

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7805 +5V 1A 7905 -5V 1A 7812 +12V 1A -12V 1A 7912

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TI or Better



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20K WIRE WRAP WIRE

(100 PACK) %W

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10K

12K

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Color - R, Bu, G, Y, Bk, W

50 ft. \$1.65 - 100 ft. \$3.00 - 500 ft. \$9.50

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2,5-3,25	4.0-3.75	6.0 - 4.75
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6 Amps 125 VAC 7 Amps 30 VDC \$1.25 ea.

DPDT STANDARD TOGGLE

ST21 (ON-NONE-ON)

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ST24 (ON-OFF-MOM ON)

ST25 (ON-NONE-MOM-ON) ST26 (ON-ON-ON)

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	PARI#	LINS	PRICE
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NEWGWWWW	14DP	14	.55
	16DP	16	.58
DEFIDITE	24DP	24	.95
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Socket and Dip Plug priced based on gold not exceeding \$700 per ounce.

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26	3.00	26	3.80
34	3.85	34	4.65
40	4,50	40	5.50
50	5.50	50	5.90

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4MHZ Beastie with extra instructions!

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DMA - \$18.75

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74LS01	.33	74LS109	.59	74LS240	2.95
74LS02	.33	74LS112	.59	74LS241	2.49
74LS03	.33	74LS113	.59	74LS242	1,95
74LS04	.59	74LS114	.49	74LS243	1,95
74LS05	.39	74LS122	.59	74LS244	2.95
74LS06	.39	74LS123	1,19	74LS245	8.95
74LS07	.39	74LS124	1.49	74LS247	1.19
74LS08	.59	74LS125	.89	74LS248	1.19
74LS09	.39	74LS125	.89	74LS249	1.69
74LS10	.29	74LS132	.79	74LS251	1.79
74LS11	.39	74LS133	1.19	74LS253	.95
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74LS13	.69	74LS138	.99	74LS258	1.95
74LS14	1.25	74LS139	.99	74LS259	2.95
74LS15	.49	74LS145	1.25	74LS260	.75
74LS20	1.95	74LS148	1.49	74LS266	1,15
74LS21	3.7	74LS151	.79	74LS273	1.75
74LS22	.29	74LS154	2.49	74LS275	4.39
74LS26	.39	74LS155	1.49	74LS279	.79
74LS27	.49	74LS156	1.49	74LS283	1.49
74LS28	.39	74LS157	1.49	74LS289	5.75
74LS30	.49	74LS158	1,49	74LS290	1,29
74LS32	.95	74LS160	.75	74LS293	1,95
74LS33	1.95	74LS161	1.99	74LS295	1.95
74LS37	.75	74LS162	1.25	74LS298	1.29
74LS38	.39	74LS163	1.25	74LS324	1.75
74LS40	.25	74LS164	2,15	74LS352	1.65
74LS42	1.39	74LS165	1.49	74LS353	1.65
74LS47	.79	74LS166	2.49	74LS365	.95
74LS48	.79	74LS168	2,95	74LS366	.79
74LS35	.25	74LS169	1.95	74LS367	.99
74LS55	.25	74LS109	1,95	74LS368	.99
74LS55	.70	74LS173	1.25	74LS373	2.95
74LS73	.79	74LS174	1.49	74LS374	3.95
74LS74	.59	74LS175	1.49	74LS377	1.95
74LS75	.79	74LS181	2.15	74LS378	1.95
74LS76	.79	74LS189	6,95	74LS379	1,95
74LS78	.49	74LS190	.99	74LS386	.59
74LS83	.95	74LS191	1.95	74LS390	1.95
74LS85	1.49	74LS192	1.95	74LS393	1.95
74LS86	.95	74LS193	1.95	74LS395	1.95
74LS90	.75	74LS194	1.49	74LS490	4.95
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+5V @ 9A	-5V @ .8A	+24V @ 7A	US-384	89.00
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SH	IUGART -	SIEMANS -	CDC 8"	
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Also have 920C, SOROC. HAZELTINE, etc. What we don't have is room on this page, Call Toll Free 800 number for prices.



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\$499.00

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5%"	SOFT	\$2.65 ea.
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\$189.00 MADE IN



Complete S-100 12 Slot Computer, Ample system power with regulated power for drives, Excellent for Subsystem or Hobby use. 4 hours to build, (6 conn. incl., less fans)

DUAL DRIVE SUBSYSTEM \$995.00

\$195.00 w/no Drives

If this looks like a Lobo Drive System, don't be fooled, Just because it

2 SHUGART 801R looks like one, works like POWER SUPPLY one, smells like one, and tastes like one (?) doesn't mean it has to cost like one!

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5" \$550.00 - 8" \$980.00 Attractive, convenient and compact Two Drive Mass Storage includes Power Supply, Drives, Cabinets and Cables. Double Sided, Double Track available tool





Z-80 CPU (KIT)

first time this The world popular CPU offered in Kit. 2 serial, 3 parallel, CTC, EProm Z-80 at 4 mhz. Software



\$212.00

buad rate, etc. (less Prom & cable)

EXPANDABLE RAM *SPECIAL*SPECIAL*SPECIAL*

This is the best all around 64K board you can buy, If after you see it, you don't agree return for full refund. Bank Select by extended address lines or I.O. 40H.



\$389.00 A& T

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Double Density 8" and 5" Disk Controller disigned for S-100 IEEE standards, Uses Western Digital 1795, 1691 2143 Chip Set.



FANS \$14.95

These are brand new, in the box fans, Not noisey bearing pullouts. Never again at these low prices!



4-5/8"

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Expansion 16K Dynamic RAMs for Apple, TRS-80 S-100 systems. T.I., Mostek Intel, Call for manufacturer. \$2.95 200 NS

DIP-80 \$399.00

Don't be mislead by this LOW price. This is a rug-ged 100% Duty Cycle 7 by 7 Dot Matrix Printer. Brand new, factory warr.



• RS-232 ADD \$65.00 • TRACTOR FEED ADD \$70.00

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One of the world's two most popular STATIC RAMs, Factory prime

200 NS

tested units, Sold in lots of 8 only. FUJITSU, HITACHI, etc.

> TMS-4044 MM-5257 **INTEL 2147**

\$4.25 250 NS

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buy Gold, buy these, the price won't last!

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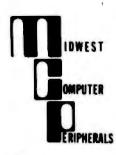
8" 851R \$585.00

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SIEMANS DRIVE 120-8

\$375.00 Very Special Price on

these BRAND NEW current production units Add \$10.00 for Extended 1 Year Warrantee!



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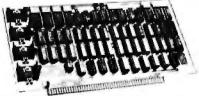
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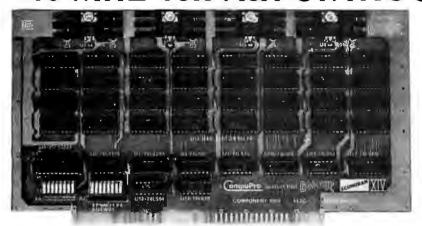
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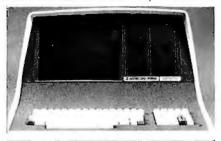


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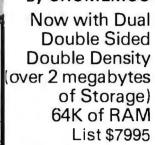
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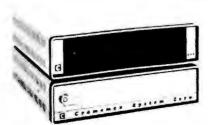
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FOR SALE: Digital Group TVC-F 512-character video programmable memory/1.1 Kbaud audio-cassette board with all documentation. Assembled and in perfect working order. Also. listings for Z80 operating system, and documentation and listing for EDFORM1 text editor. All for \$95 including UPS shipping, or make offer. Martin Tobias, POB 3766, Nashua NH 03061, (603) 889-0901.

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February BOMB Falls on Tank

Steve Ciarcia captured first place in the voting with "A Computer-Controlled Tank" (page 44), a description of his effort at wireless remote control. He will receive the \$100 prize.

James C Anderson took second place with "An Extremely Low-Cost Computer Voice Response System" (page 36), the lead article in our issue theme of "The Computer and Voice Synthesis." He wins the \$50 second-place prize.

Third place was shared by Mark Zimmermann, who wrote "A Beginner's Guide to Spectral Analysis, Part 1" (page 68), and Roger Mikel, who contributed "A/D and D/A Conversion—An Inexpensive Approach" (page 312).

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