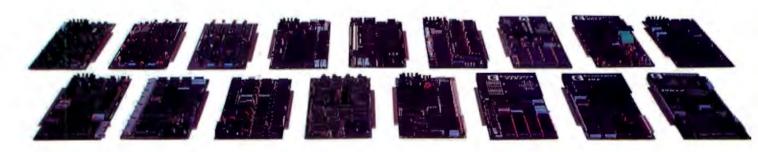
1 FEBRUARY 1982 Vol. 7, No. 2 \$2.95 In USA/\$3.50 In Canada A McGraw-Hill Publication ms journal WINTER COMPUTING

A new small computer that won't limit you tomorrow



New Cromemco System One shown with our high-capability terminal and printer.



Expandability

Here's a low-priced computer that won't run out of memory capacity or expandability halfway through your project.

Typically, computer usage tends to grow, requiring more capability, more memory, more storage. Without a lot of capability and expandability, your computer can be obsolete from the start.

The new System One is a real building-block machine. It has capability and expandability by the carload.

Look at these features:

- Z80-A processor
- 64K of RAM
- 780K of disk storage
- CRT and printer interfaces
- Eight S-100 card slots, allowing expansion with
 - color graphics
 - additional memory
 - additional interfaces for telecommunications, data acquisition, etc.
- Small size

GENEROUS DISK STORAGE

The 780K of disk storage in the System One Model CS-1 is much greater than what is typically available in small computers. But here, too, you have a choice since a second version, Model CS-1H, has a 5" Winchester drive that gives you 5 megabytes of disk storage.

MULTI-USER, MULTI-TASKING CAPABILITY

Believe it or not, this new computer even offers multi-user capability when used with our advanced CROMIX* operating system option. Not only does this outstanding O/S support multiple users on this computer but does so with powerful features like multi-

ple directories, file protection and record level lock. CROMIX lets you run multiple jobs as well.

In addition to our highly-acclaimed CROMIX, there is our CDOS*. This is an enhanced CP/M[†] type system designed for single-user applications. CP/M and a wealth of CP/M-compatible software are also available for the new System One through third-party vendors.

COLOR GRAPHICS/WORD PROCESSING

This small computer even gives you the option of outstanding high-resolution color graphics with our Model SDI interface and two-port RAM cards.

Then there's our tremendously wide range of Cromemco software including packages for word processing, business, and much more, all usable with the new System One.

ANTI-OBSOLESCENCE/LOW-PRICED

As you can see, the new One offers you a lot of performance. It's obviously designed with antiobsolescence in mind.

What's more, it's priced at only \$3,995. That's considerably less than many machines with much less capability. And it's not that much more than many machines that have little or nothing in the way of expandability.

Physically, the One is small -7" high. And it's allmetal in construction. It's only 141/6" wide, ideal for desk top use. A rack mount option is also available.

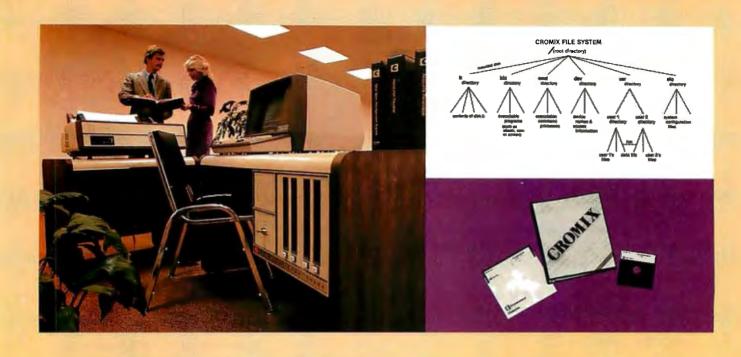
CONTACT YOUR REP NOW

Get all the details on this important building-block computer. Get in touch with your Cromemco rep now. He'll show you how the new System One can grow with your task.



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CROMIX*— Cromemco's outstanding UNIX†—like operating system

CROMIX is just the kind of major development you've come to expect from Cromemco. After all, we're already well-known for the most respected software in the microcomputer field.

And now we've come up with the industry's first UNIX-lookalike for microcomputers. It's a tried and proven operating system. It's available on both 5" and 8" diskettes for Cromemco systems with 128K or more of memory.

Here are just some of the features you get in this powerful Cromemco system:

- Multi-user and multi-tasking capability
- Hierarchical directories
- Completely compatible file, device, and interprocess I/O
- Extensive subsystem support

FILE SYSTEM

One of the important features of our CROMIX is its file system comprised of hierarchical directories. It's a tree structure of three types of files: data files,

*CROMIX is a trademark of Cromemco, Inc.
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directories, and device files. File, device, and interprocess I/O are compatible among these file types (input and output may be redirected interchangeably from and to any source or destination).

The tree structure allows different directories to be maintained for different users or functions with no chance of conflict.

PROTECTED FILES

Because of the hierarchical structure of the file system, CROMIX maintains separate ownership of every file and directory. All files can thus be protected from access by other users of the system. In fact, each file is protected by four separate access privileges in each of the three user categories.

TREMENDOUS ADDRESS SPACE, FAST ACCESS

The flexible file system and generalized disk structure of CROMIX give a disk address space in excess of one gigabyte per volume — file size is limited only by available disk capacity.

Speed of access to disk files has also been optimized. Average access speeds far surpass any yet implemented on microcomputers.

'C' COMPILER AVAILABLE, TOO

Cromemco offers a wide range of languages that operate under CROMIX. These include a high-level command process language and extensive subsystem support such as COBOL, FORTRAN IV, RATFOR, LISP, and 32K and 16K BASICS.

There is even our highly-acclaimed 'C' compiler which allows a programmer fingertip access to CROMIX system calls.

THE STANDARD O-S FOR THE FUTURE

The power and breadth of its features make CROMIX the standard for the next generation of microcomputer operating systems.

And yet it is available for a surprisingly low \$595.

The thing to do is to get all this capability working for you now. Get in touch with your Cromemco rep today.



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In This Issue

It's time again to start worrying about your annual accounting to Uncle Sam. April 15 is only two months away. And it's probably time you sat down to crunch out those numbers. As Robert Tinney's cover suggests, staying warm by your computer is an attractive alternative to braving the cold winter winds. To help ease the pain, we review two software packages designed specifically for computing taxes. If you have access to UCSD Pascal, Edward Heyman's federal income tax program can help you avoid overpayments and lost interest. In "Tax Tips for Computer Owners" Melvyn Feuerman and Melvyn Moller discuss tax breaks for computer owners.

This month we begin another new series: The Input/Output Primer by Steve Leibson. The six-part tutorial will take you through computer interfacing from simple serial and parallel ports to IEEE-STD-488. The Atari Tutorial continues with a look at Atari BASIC. William Barden details an easy way to provide voice synthesis for the Color Computer. And Steve Ciarcia shows you how to build a computerized weather station that will talk to you.

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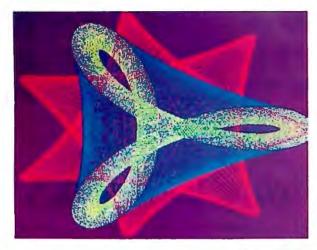
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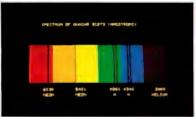
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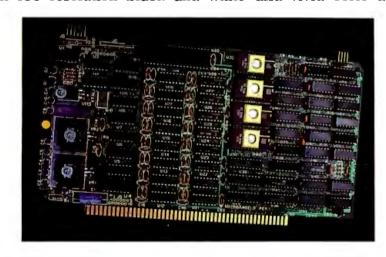
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Editorial

Report from COMDEX

by Chris Morgan, Editor in Chief

Software is growing up—fast. And hardware isn't far behind.

That was the double-barreled message from the COMDEX show, an exhibition designed to pair up small-systems vendors with their independent sales organizations. Held in Las Vegas last November, COMDEX has become a major event in the personal computing world. A record 631 exhibitors displayed their wares. With a nonstop flurry of press conferences and receptions, the atmosphere was more reminiscent of the NCC than of a small-systems show. What follows are some of the highlights.



Photo 1: The Fortune 32:16 microcomputer with Motorola 68000 processor.



Photo 2: Microsoft's new Multiplan, a Visicalc-like spreadsheet program.

The Fortune 32:16 Computer

A big hit was the Fortune 32:16 desktop microcomputer. Within the unit's elegant exterior are a Motorola 68000 processor, 32-bit data and address registers, a 24-bit memory address bus, and a 16-bit data bus. The basic model, which sells for \$5000, features 128K bytes of memory; a 720K-byte (formatted) 51/4-inch floppy-disk drive; keyboard; and a 12-inch, 80-column black-and-white video display. A 51/4-inch Winchester disk drive with optional 5, 10, or 20 megabytes of storage is also available. The machine supports BASIC, COBOL, FORTRAN, Pascal, and C. and I found the Fortune's menudriven business software packages to be promising. (Fortune Systems Corporation was launched with \$8.5 million of venture capital, which the company claims is the largest amount of money ever raised to start a microcomputer company.) The Fortune

32:16 computer will be sold in Computerland stores and other outlets. We plan to review it in detail soon.

The "Visiclones" Are Coming

In our business, imitation is the sincerest form of survival. Personal Software's Visicalc has the nearest thing to software sex appeal and the sales figures to prove it. Consequently, a plethora of Visicalc-like electronic spreadsheets is upon us. First it was Supercalc from Sorcim; now the second generation has arrived. It's too early to tell how good they are, but we'll be reviewing them soon. At the forefront is Microsoft's Multiplan, a financial spreadsheet program that sports such interesting features as text windows à la Smalltalk. Win-

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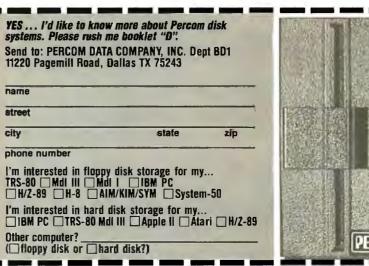
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Editorial-

dows can be "closed" or "opened" so you can see the effect of what you're doing in an area off the screen. Available commands are displayed at the bottom of the screen. A lot of attention has been given to the documentation. Incidentally, Microsoft has announced a series of executive program aids called the "Manager Series." It will include Time Manager (currently available) and Project Manager and Personnel Manager (now being completed).



Photo 3: Commodore's new, under-\$100 modem for the VIC-20 color computer. The VIC-12 plugs directly into the VIC-20 and features a modular jack.

I was given a demonstration of Time Manager. It's definitely a useful tool.

From Target Software Inc. of Atlanta comes a series of business-planning programs, including Plannercalc and Masterplanner. Plannercalc is a financial-planning tool that has a couple of interesting features: the program lets you enter procedures in English using conventional mathematical logic, and it can be integrated with the Masterplanner program. The latter has a more extended spreadsheet and "gridsheet" program.

Context Management Systems Inc. of Torrance, California, has announced its MBA program for the IBM Personal Computer. It's a combination database, electronic spreadsheet, word-processing, graphics, and communications package. It's also available in a version for the Apple III.

NEC Home Electronics USA announced "Report Generator," a CP/M-based program being marketed with NEC's PC-8000 series microcomputer system. It is designed to generate income statements, balance sheets, sales forecasts, and other business reports.

Other Software Developments

Intel has signed agreements with both Microsoft and Digital Research to distribute both companies' operating systems for a wide variety of Intel microcomputer systems and boards. This is a continuation of an interesting phenomenon that began when IBM announced it was go-



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- · Edition II of Nevada COBOL is based on ANSI-74 Standards
- With 48K RAM, you can compile and execute up to 4000 state-
- COPY statement for library handling.
- CALL...USING...CANCEL
- PERFORM...THRU...TIMES...UNTIL...paragraph or section names.
 IF...NEXT SENTENCE...ELSE...NEXT SENTENCE AND/OR <=>
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- INSPECT...TALLYING...REPLACING.



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Photo 4: Techmar's new expansion chassis for the IBM personal computer shown directly beneath the IBM main chassis.

ing to make available both Microsoft's DOS operating system and CP/M-86 for the IBM Personal Computer. With corporate giants like Intel giving Microsoft and Digital Research a boost, it appears that both families of operating systems will coexist for quite some time.

Systems Group of Orange, California, demonstrated some of the practical advantages of the CP/M system on its System 2800 microcomputer line. Its CP/M errorrecovery routines are more sophisticated than others we have seen. We plan to analyze this system in greater detail later this year. CP/M users should also check out Epic Software's Supervyz, an application software control program for CP/M. Supervyz does a nice job of cleaning up some of CP/M's rough edges.

Hardware News

First Metamorphics announced one; now Caltech Computer Services in San Diego is offering an 8088 plugin card for the Apple II. Called Macrosystem-88, it contains an 8088 microprocessor, 64K bytes of RAM (expandable to 128K bytes) and 4K bytes of PROM all on a single board, and its power supply is contained in a case designed to sit on top of the Apple. A DMA (direct-memory access) control card enables the communication between the Macrosystem-88 and the Apple. This card may be installed in any slot (except 0) within the Apple. The Macrosystem-88 can run CP/M-86 as well as UCSD

Editorial continued on page 14

San Francisco, CA 94121

S-100 Fast-Aid.

Including 3 new boards for system design relief.

The MB64.

An economical, highperformance 64K static RAM memory.

Just what the doctor ordered. A new 64K static RAM configured as two 32K blocks that's fast (in excess of 6MHz), reliable and economical. The MB64 supports IEEE 696/S-100 24-bit extended addressing for up to 16MB of RAM. Bank switching permits compatibility with popular multi-user computer systems (such as CROMIX*). Up to 8K can be replaced with 2716 EPROMs. The MB64 offers low power consumption (typically less than 600 milliamps). And a provision for optional battery backup.

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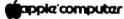
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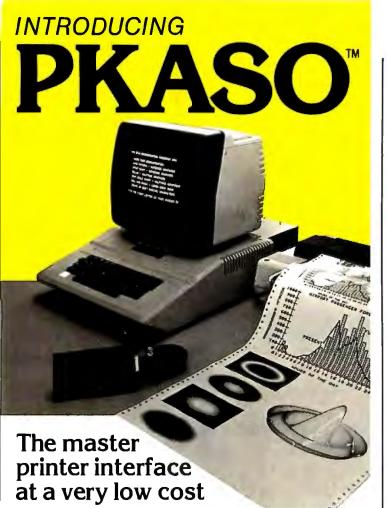




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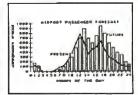
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Editorial.



Photo 5: Epson's HX-20 prototype computer. This new briefcase-sized computer, which looks like the Sony Typecorder, will be formally introduced this summer.

Pascal-77 and BASIC. To switch between Apple DOS and CP/M-86, you simply boot up with the appropriate disk. The price of the system is \$995.

Speaking of 16-bit capability, Techmar exhibited an impressive array of IBM plug-in boards and an expansion chassis for the IBM Personal Computer. Included in this new product line are a speech masterboard with a built-in standard vocabulary of 143 words; a Winchester disk and controller; a video digitizer board to convert images from any standard video camera for use with the computer; a board that allows up to four IBM computers to share the same printer; a stepper motor controller; and a series of memory-expansion boards.

Digital Equipment Corporation unveiled its new Letterprinter 100. This machine offers near-letter-quality printing for less than \$3000.

Epson displayed an intriguing prototype of the Epson HX-20 personal computer. Looking a lot like the Sony Typecorder, the HX-20 has the advantage of a four-line liquid-crystal display. The HX-20 and the Typecorder signal the beginning of a new trend to what I call "briefcase" computers: battery-operated machines that combine portability with powerful computer features. It's the sort of design that will appeal to people on the move.

Also on display at the Epson suite was a newly designed 51/4-inch floppy-disk drive that stands 1 inch high. It will be formally announced later this year, along with the HX-20. Epson is definitely a company to watch in the personal computing field.

For further information on some of the new products I have described in this editorial, see this month's New Products section.

Postscript

This past November, I was honored to give the keynote address at the Symposium on Small Computers in the Arts held in Philadelphia. It was sponsored by the

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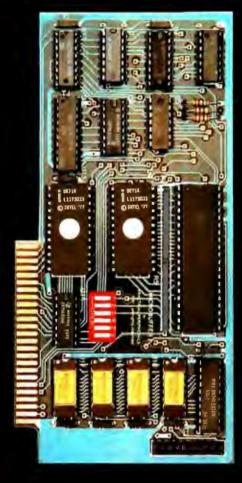
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IEEE Computer Society and the IEEE Philadelphia section and organized by the Personal Computer Arts Group of Philadelphia. Dick Moberg's organizing committee brought together artists, musicians, and computer scientists from around the country to discuss microcomputer music and art. I urge all BYTE readers interested in the use of small computers in the arts to contact the Personal Computer Arts Group. Write to: Personal Computer Arts Group, POB 1954, Philadelphia, PA 19105.

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Letters

Canon Dealer Organization

Sol Libes has been misinformed as to Canon policy regarding marketing of the CX-1 computer. Canon markets all system products through a dealer organization and is dedicated to supporting its dealers in marketing all Canon software products, including the seven accounting packages (order entry, accounts receivable, accounts payable, inventory control, general ledger, job costs, payroll) which were mentioned in his November column (BYTELINES, November 1981 BYTE, page 302).

Irwin Danowitz National Software Manager Systems Division Canon U.S.A., Inc. One Canon Plaza Lake Success, NY 11042

An Untapped Work Force

Perhaps BYTE readers can help handicapped persons overcome some frustrating barriers. Most handicaps result in a mobility problem that effectively leaves the person house-bound (or, if lucky, carbound). Many handicapped persons are in minimum-income situations that barely allow them to meet the expenses of survival. It is ironic that handicapped individuals may be highly trained, but without the ability to relocate or commute to a workplace daily, they cannot increase their income.

The personal computer could go a long way to solving this problem. For example, a house-bound worker with a computer and a modem could use off-the-shelf software to perform functions from accounting and data processing to engineering analysis and even managerial assistance. A printer with a Braille printhead would allow a blind person to communicate via electronic mail, to use databases, and to perform electronic-banking services being considered by many banks. The problem seems to be finding a "conduit" to companies willing to take on such employees.

I have approached about five hundred companies nationwide (IBM, ITT, GTE, and Boeing, among them). Their personnel departments treat me as a disabled person seeking employment at their plant location. Their management and dataprocessing systems, it seems, cannot accommodate an off-site employee who works at home in a service-type capacity. (Even more frustration is felt when a handicapped person tries to use employment agencies-this usually involves long delays, and only about a third of the agencies even bother to acknowledge receipt of your resume.)

Perhaps BYTE readers could help the handicapped (who represent an untapped work force of 10 million) on a level that could be mutually beneficial.

Kenneth Willoughby Box 317 Fairacres, NM 88033

Faster Algorithms

From time to time I'm sure most readers have run across benchmarking articles comparing various pieces of hardware or software and found these articles followed up by letters to the editor critical of a particular algorithm which was used incidental to the test. In general, it seems, such criticisms are unfair, bearing little relation to the purpose for which the original article was written.

I introduce my comments this way for fear that I might otherwise be accused of a similar unfairness. I am speaking of the article "BASIC, Pascal, or Tiny-c? A Simple Benchmarking Comparison" by Phil Hughes (October 1981 BYTE, page 372) in which he uses a card-shuffling program to benchmark three languages with regard to speed of execution. In this he does a fine job. My only reason for commenting about his choice of algorithms is that this seems to be a routine that many readers will have some use for and be inclined to copy directly into some application program. For such readers I would like to offer an alternative program, which runs considerably faster.

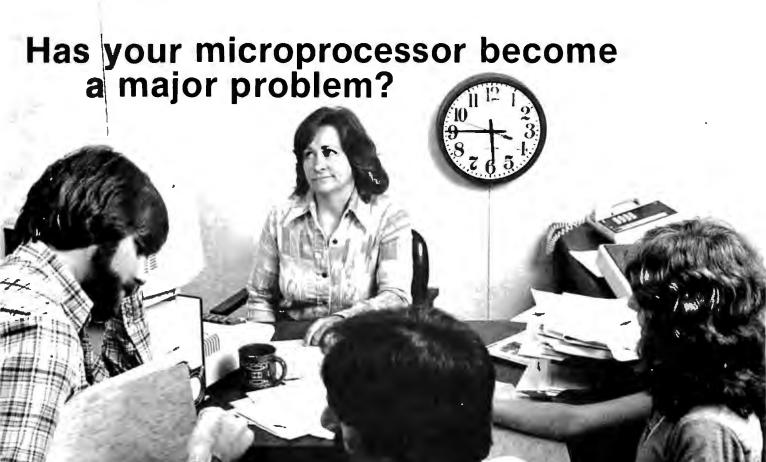
First, however, let me make some observations about the routine used by Mr. Hughes and some of the characteristics leading to its slowness. The strategy used in this program (a modified version of which appears as listing 1 below) is to generate a random number and check to see if this number has been generated earlier in the sequence. If not, it is added; if so, the duplicate is ignored and another random number is generated and tested. This is continued until 52 distinct random numbers have been created. For the first several passes this causes no problem since the chance of duplication is small and only a few elements need to be tested. After 10 or 20 random numbers have been generated, however, the chance of duplication increases significantly, and the time needed to search for duplicates also increases. By the time the last 10 or 15 numbers are to be generated, the combined effect of duplication and search length has slowed this algorithm considerablv.

Listing 1

```
100 DEFINT A-Z
110 DIMC(51)
120 RANDOM
130 A\$ = TIME\$
140 J = 0
150 T = RND(52)
160 IF J = 0 THEN 200
170 FOR I = 0 TO J - 1
180
        IF C(I) = T THEN 150
190 NEXT I
200 C(J) = T
210 J = J + 1
220 IF J < 52 THEN 150
230 FOR I = 0 TO 51
        PRINT C(I):
240
250 NEXT I
260 B$ = TIME$
270 PRINT
280 PRINTA$.B$
```

The program shown in listing 2 is a variation of one I have used several times both for card-shuffling routines and for programs to generate nonduplicated random numbers for programming bond retirement. The strategy here is to start with a sorted sequence and literally shuffle it. This is done by generating a random number between 1 and the total number of objects to be shuffled. Then comes the key step in this algorithm: the object in the position given by that random number is exchanged with the object in the last position.

Next, the maximum number of objects is decremented by 1 and the process is re-



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peated until this maximum equals 1.

Stepping through an example may be useful. Suppose we wish to shuffle 10 elements. We start out by arranging them in order as:

1 2 3 4 5 6 7 8 9 10

Next we generate a random number between 1 and 10, say 6. Now we exchange the objects in position 6 (the number generated) and 10 (the top of the range for the random-number generation). This leaves:

1 2 3 4 5 10 7 8 9 6

For the next step we generate a random number between 1 and 9 (10 -1). Suppose this time we get 4. Then we exchange the objects in positions four and nine and decrement the maximum element count to 8. We now have

1 2 3 9 5 10 7 8 4 6

The entire set will be sorted after 10 random numbers have been generated. (By the way, this does bring up one criticism of the algorithm used by Mr. Hughes for benchmarking. Because of the nature of his algorithm it is likely that every time the program is run a different number of random numbers will have to be generated due to the chance occurrence of duplication. While this should work out to a predictable average, the possibility of variation makes its usefulness as a benchmark somewhat doubtful.)

I ran both versions of the shuffling program which appear here on my TRS-80 Model I. As mentioned above, the timing on listing 1 was quite variable, ranging from 40 to 66 seconds. For listing 2 the time was consistent at 3.5 to 4 seconds. (And no, I didn't compile the second version. I did subsequently compress it, deleting spaces and packing the entire program on a single line and got average speeds of about 2.25 seconds.)

Listing 2

```
100 DEFINT A-Z
110 RANDOM
120 N = 52
130 DIM A(N)
140 A$ = TIME$
150 FOR I = 1 TO N
160
          A(I) = I
170 NEXT I
180 FOR I = N TO 2 STEP - 1
190
          R = RND(I)
200
          T = A(I)
          A(I) = A(R)
210
220
          A(R) = T
230 NEXT I
```

250

```
240 FOR I = 1 TO N
          PRINT A(I);
260 NEXT I
270 PRINT
280 B$ = TIME$
290 PRINTA$,B$
```

Finally, I'm not sure of the origin of this second algorithm. I don't remember inventing it, but then I don't recall reading or hearing about it elsewhere. I do know that it has been very useful to me. I hope BYTE readers will find it equally valuable.

David R. Borger 16835 Westmoreland Detroit, MI 48219

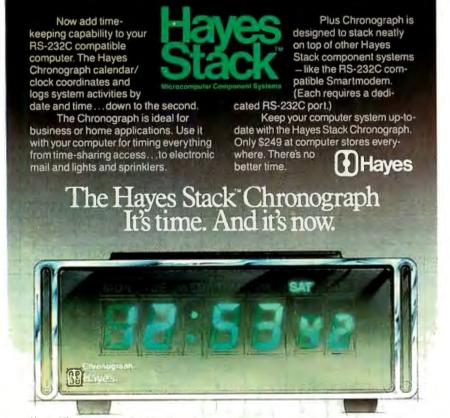
Mr. Hughes's article comparing BASIC, Pascal, and Tiny-c for writing a cardshuffling program is useful for comparing the ease of programming in those languages. Some caution must be exercised in using the timing results, however. The algorithm he uses is very sensitive to the order of the random numbers. The algorithm is as follows:

- A. Get a number from 1 to 52 from the random-number generator. If the number has already been used, repeat this step.
- B. Put this number in the array (deck) at the next location. If we have 52 numbers, we are done. Otherwise go back to step A.

As we get toward the end of the deck, there are fewer acceptable numbers. One number generator may require many more calls than another. To get a "good" sequence of random numbers, the range of the random-number generator should be much larger than the range required by the program. In order to compare Mr. Hughes's algorithm in the three languages, we should assure ourselves that the number of calls to the random-number generator is at least on the same order.

It's possible to generate a random list of numbers n long with only n calls to the random-number generator. The idea is to generate *n* random numbers and then sort them. The random numbers are distributed across the range of the number generator, not the range of the program. If the random-number generator is good, this means that any number generated will not be repeated until all other numbers in the range of the number generator have been generated.

Here is one possible algorithm for get-



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ting a shuffled deck of cards. Use two arrays, KEY and CARD:

- A. Initialize CARD by letting CARD(I) I for elements in CARD.
- B. Put a random number in each element of KEY.
- C. Find the smallest element of KEY that has not been used. This is the next card. Save it in array CARD, Repeat this step until all the elements of KEY have been used.

A BASIC program that performs this algorithm follows. Note that the sort used is a bubble sort and is not as efficient as some others.

```
10 DIM C(51), K(51)
20 GOSUB 1000
30 FOR I = 0 TO 51
40 PRINT C(I);
50 IF INT ((l+1)/10) = (l+1)/
     10 THEN PRINT
60 NEXT I
70 PRINT
80 PRINT "ALL DONE!"
90 END
1000 FOR I = 0 TO 51
1010 \text{ K(I)} = \text{RND}(0)
1020 C(l) = l
1030 NEXT I
1040 FOR I = 0 TO 50
1050 S = 1
1060 FOR J = I + 1 TO 51
1070 IF K(J) © K(S) THEN S = J
1080 NEXT J
1090 K(S) = K(I)
1100 T = C(I)
1110 C(I) = C(S)
1120 C(K) = T
1130 NEXT I
1140 RETURN
```

I hope this will be of some use to those who shuffle cards. The inside loop is performed approximately 1352 times, so if you require fewer calls than this to your random-number generator to get 52 numbers, Mr. Hughes's algorithm may be better.

Emmet R. Beeker III 1123 Maple Dr. Mountain Home, ID 83647

Single-Drive Success Story

The review 'The Radio Shack FOR-TRAN Package" by Tim Daneliuk (October 1981 BYTE, page 385) is a good overview of an excellent software package. However, I must take exception to the statement "In single drive systems, the relocatable object file must always be on the

disk containing the linker and FORTRAN library." This is not true. In fact, the source, relocatable, listing, and object codes may reside on a disk separate from both supplied FORTRAN disks.

First I'll name the three disks that I'll be using and then I'll lead you through the steps necessary to compile and link a FORTRAN source program using one disk drive. It did take some time to figure this out because Radio Shack forgot to document the procedure. The disk containing the editor and the FORTRAN compiler will be called FOR/EDIT, the disk containing the linker and the FOR-TRAN library will be called FOR/LINK, and the disk containing the source, relocatable, and object codes will be called PROGRAM.

- 1. Insert the FOR/EDIT disk and boot the system. Load and execute the editor by entering EDIT.
- 2. After the editor has loaded and you receive the prompt, remove the FOR/EDIT disk and insert the PRO-GRAM disk that contains, or will contain, the source program.
- 3. Create or change the source code, as necessary. When finished, write the source code to the PROGRAM disk.
- 4. Remove the PROGRAM disk and insert the FOR/EDIT disk, Load and execute the FORTRAN compiler by entering F80.
- 5. After the compiler has loaded and you receive the prompt, remove the FOR/EDIT disk and insert the PRO-GRAM disk that contains the program to be compiled, and where the relocatable code is to reside.
- 6. Enter TEMP. TEMP = TEMP. or whatever program name you are working with. This will compile the source code and write out the relocatable code along with a print file.
- 7. Remove the PROGRAM disk and insert the FOR/LINK disk, Load and execute the linker by entering L80.
- 8. After the linker has loaded and you receive the prompt, remove the FOR/LINK disk and insert the PRO-GRAM disk that contains the relocatable code to be linked.
- 9. Enter TEMP, or whatever program name you are working with. This will load the relocatable code and display all the undefined globals.
- 10.Remove the PROGRAM disk and insert the FOR/LINK disk. Enter FORLIB/ REL-S to search the FORTRAN Library to resolve all undefined

- globals. If you need to search other files to satisfy undefined globals, enter FILENAME-S.
- 11. Remove the FOR/LINK disk and insert the PROGRAM disk that will contain the executable object code.
- 12.Enter TEMP-N to name the output obiect code. Then enter -E to write out the object file and exit the linker.
- 13. You are now ready to execute the command (object) file TEMP/CMD.

Note that no data was written to the two FORTRAN disks. In fact, I keep writeprotect tabs on these disks just to avoid disasters. This procedure seems to be a lot of work, but those of us with single-drive systems are used to the inconvenience. If we couldn't hack it, we'd have two disks!

Spencer R. Lepley 1655 Capital Circle SE, Lot #12 Tallahassee, FL 32301

Tim Daneliuk replies:

Mr. Lepley seems to be absolutely correct! I entered a short FORTRAN program and linked it as he suggested: it works just fine. As he points out, the documentation does not discuss singledrive use in any real depth. Personally, I think a book is needed that would document these kinds of procedures as well as the many advanced features of both the Radio Shack/Microsoft FORTRAN and the M-80 Macro Assembler. How about it Radio Shack?

One other point has come to my attention since I first did the FORTRAN review: as of this writing, the package has not been implemented on the TRS-80 Model III. However. Model III systems that use the LDOS disk operating system can use not only FORTRAN, but M-80 Macro Assembler, BASCOM compiler, RS COBOL compiler, and RS BASIC compiler. This is accomplished by "patching" the Model I versions of these languages. Complete instructions for these procedures are found in the latest issue of the LDOS Quarterly (Vol. 1, No. 2).

More on VOS

Since Sol Libes's mention of the Software Tools Virtual Operating System in BYTELINES (October 1981 BYTE, page 306) our research group at the Lawrence Berkeley Laboratory has been inundated with requests for information. Although

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Letters_

we are certainly pleased with the interest, the Users Group is better able to deal with these requests than we are. Inquiries should be addressed to:

> Software Tools Users Group 1259 El Camino Real, Box 242 Menlo Park, CA 94025

The 1600-member group issues newsletters, distributes a software catalog, provides an information referral service, produces a distribution tape, and holds biannual meetings. I am sure the Users Group would welcome the inclusion of microcomputer enthusiasts.

And, to answer the question most asked by BYTE readers who contacted us: Yes, the software tools have been brought up on a CP/M system. This implementation includes all the tools distributed through the Users Group, plus many of the extensions specified in the CACM article describing the VOS project ("A Virtual Operating System," Dennis Hall, Deborah Scherrer, and Joe Sventek, Communications of the ACM, September 1980, pp. 495-502). For more complete CP/M information, BYTE readers should contact:

Unicorn Systems 30261 Palomares Rd. Castro Valley, CA 94546

We welcome the enthusiasm and interest shown by BYTE and its readers and hope the above information will answer most of their questions.

Deborah K. Scherrer Computer Scientist Lawrence Berkeley Laboratory University of California Berkeley, CA 94720

"BYTE" Fights Mice

The staff at the Poricy Park Nature Center was delighted with the article, "Bridging the 10-Percent Gap," by Paul Brady (October 1981 BYTE, page 264) which described our computer system.

On the day we received the magazine, we were given a black cat to help keep the mice from the bird seed we sell. We have appropriately named the cat "BYTE."

Patricia Contreras, Director Poricy Park Nature Center POB 36 Middletown, NJ 07748

Ultra-Low-Cost Protocol

Ken Clements and Dave Daugherty's article, "Ultra-Low-Cost Network for Personal Computers" (October 1981 BYTE, page 50), presents an excellent idea. Personal computing does need a low-rent Ethernet, especially for group applications, such as schools. However, the protocol described is both more complex and less reliable than necessary. A few minor changes would fix this.

In the RECEIVER layer, if a message has a bad checksum, just throw it away—there's no need to tell the protocol layer because it doesn't do anything with bad messages. In the PROTOCOL layer, pick one protocol and stick to it. A good simple one is as follows:

- Every message has a message number.
 This includes ACK (acknowledge) utility messages.
- 2. Message numbers are either 0 or 1.
- 3. The sender starts by sending a message with a number of 0. The original sender then awaits a corresponding acknowledgment from the original receiver. Upon receiving an "ACK 0" message (with a correct checksum) the original message is considered acknowledged and the sender can send the next message, with message number 1. The sender expects an "ACK 1" reply to its number 1 message. This cycle repeats indefinitely.
- 4. All the receiver has to do is send a matching ACK whenever a message addressed to it is received, i.e., ACK 0 is sent in reply to a message number of 0, and ACK 1 in reply to a message number of 1. However, the receiver throws away (after ACKing them) messages with the same number as the last good message received, because such messages are duplicates.
- 5. When the sender fails to get a proper ACK in a reasonable time, the last message should be re-sent. After some number of unsuccessful attempts, the sender should give up and report the receiver down.

This protocol provides a guarantee that messages are not lost or duplicated, unlike the ACK/ACK-ACK protocol, provided that a bad message doesn't get past the checksum error-detection mechanism. A longer checksum (say 16 bits) will reduce the odds of this substantially—from 1 in 256 to 1 in 65,536. In a contention-type local network, there will be errors when



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messages collide, so this is not a minor consideration.

As a last point, it is very useful to provide a high-level time-out interval, say of about 30 seconds, so that if nothing happens during that length of time, everything gives up trying to communicate and goes back to the initial state. Otherwise, if for some reason things get stuck, it may be necessary to reset all the computers connected to the network to get them all back in synchronism on message numbers. If all the systems in your classroom full of microcomputers need to be reset whenever any one gets fouled up, this trick is a big help.

With these fixes, the Ultra-Low-Cost Network should fly. There are more elaborate schemes, but this is the simplest one that doesn't get intermittent errors.

John Nagle 340 Ventura, Apt. 11 Palo Alto, CA 94306

Software Considerations

I would like to comment on "Bridging the 10-Percent Gap" by Paul Brady (October 1981 BYTE, page 264). Mr. Brady points out that a wide range of reasonably priced hardware for small-business reguirements is available. This is true and should encourage progressive small-business owners to move into the computer age. However, Mr. Brady demonstrated the classic "small-business mistake" in this statement: "We barely managed the funds required for the hardware. We simply cannot spend hundreds or thousands more on software."

Prospective computer owners need to realize that good software is a labor-intensive product and must be included in the budgeting for a computer system. Mr. Brady was lucky that his organization had people willing to donate their time to design, code, test, and document customized software. Not all small businesses have this advantage.

My advice to a small-business owner who needs a computer but lacks the time and inclination to become a computer expert is to hire a local computer professional or small firm to put together the best hardware and software combination for his application. I will be glad to mail free copies of my article, "The Small-Business Owner's Guide to Hiring a Computer

Expert," to anyone who sends me an address and 40¢ in stamps.

Diane P. Kerkhoff Kerkhoff Computers 6309 Ambassador Dr. Orlando, FL 32808

Altos Gamesmen

While Thomas Wadlow's "The Xerox Alto Computer" (see September 1981 BYTE, page 58) was most interesting, I'm sorry he didn't mention that Xerox also donated four Altos to the Computer Science Department at the University of Rochester in 1974. In fact, two of the games pictured in the article were written by graduate students there.

Trek is the work of Eugene Ball, who also wrote Death Star (in which you pilot your Alto down a trench in the Death Star and fire a torpedo at its only vulnerable spot to save the Federation). Pinball was written by Clint Parker, You can jiggle the "table" by holding down the space bar. Overly energetic application of the space bar results in a "tilt." Clint's version of Space Invaders remains one of the most popular Alto games. It keeps track of the op ten scores on the net. No still photograph can convey the fine graphic letails of these programs.

Incidentally, the four original Altos at Jniversity of Rochester are named John, Paul, George, and Ringo (my own suggesion was Groucho, Harpo, Chico, and Zeppo).

Michel Denber Kernx 300 Phillips Rd. Nebster, NY 14580

Exploring Zork's Origins

While praising so highly the efforts to fight software piracy undertaken by the vendors of "Zork, The Great Underground Empire," Bob Liddil in his review (February 1981 BYTE, page 262) perhaps forgot to mention that the release of Zork seems to be an act of software piracy itself. From the description given, I infer that Zork is just an implementation of the well-known PDP-11 game Dungeon, distributed by Digital Equipment Corp.'s user group, DECUS. All the situations, descriptions, treasures, reactions, etc. are nearly identical to those found in Dungeon: the white house with the sack



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of peppers on the kitchen table, the forest where players are reincarnated, the jewel-encrusted egg in a nest on a tree, and more. The colorful description of situations has especially set Dungeon apart from preceding adventure games. Even the name Zork is taken from a situation in Dungeon. Yet in Zork's advertising you will not find a tiny nod to any of the numerous authors outside Personal Software Inc. who have done 99 percent of the work.

Greetings from a fanatic BYTE reader.

Hans Strasburger Dipl. Math. Dipl. Psych. Tal 58/IV D-8000 Munich 2 West Germany

Response to Hans Strasburger:

A call to Personal Software Inc. revealed that Zork will no longer be distributed by that company. Zork is now being sold by Infocom of Cambridge, Massachusetts. Joel Berez, president of Infocom, gave us a short history of Zork.

According to Mr. Berez, Zork was originally developed around 1977 and run on a Digital Equipment Corporation PDP-10 using a language called MDL. Sometime later a version was developed for the PDP-11 using FORTRAN, and this is the version being distributed by DECUS. This version was written by someone who had access to the original Zork source code. The microcomputer version formerly sold by Personal Software and now by Infocom was written by the authors of the original Zork: Marc Blank, Dave Lebling, Bruce Daniels, and Tim Anderson. The first micro-Zork, Zork I, was a subset of the original version. Zork II includes more of the original Zork situations than Zork I plus some additional enhancements. A future Zork III will contain the remaining original Zork material plus even more enhancements. Thus, the combination of Zork I, Zork II, and Zork III would give the user all the original PDP-10 version plus many enhancements. For more information on Zork, see "Zork and the Future of Computerized Fantasy Simulations." December 1980 BYTE, page 172.

Old Clothes Issue New Clarion Call

I enjoyed BYTE's reprint of Charles Anthony Richard Hoare's Turing lecture of 1980. (See "The Emperor's Old Clothes," in the September 1981 BYTE, page 414.) One of the points he made about the programming language Ada deserves some extension. He said, "...do not allow this language in its present state to be used in applications where reliability is critical.... The next rocket to go astray as a result of a programming-language error may not be an exploratory space rocket on a harmless trip to Venus. It may be a nuclear warhead exploding over one of our cities."

Some BYTE readers may not know that a hardware error nearly caused us to launch a nuclear attack against the Soviet Union on June 6, 1980. The North American Air Defense Command (NORAD) command center in Colorado Springs detected an illusory Soviet nuclear attack on us, and our bombers were taxiing to take off, our nuclear-missile submarines alerted, and our land-missile launch keys inserted into their sockets, ready to go in retaliation. The error was detected with little time to spare. It was traced to a \$0.46 integrated circuit. This was not an isolated incident. A similar alert was signaled only three days earlier. (See The Progressive magazine, August 1980, pages 29-30.)

As we automate more and more of the decisions involved in launching our arsenal of 10,000 strategic nuclear weapons, most of which are far more powerful than the bombs used in Hiroshima and Nagasaki in 1945, we leave ourselves more and more vulnerable to computer errors. Professor Hoare's warning comes at a critical time.

To prevent accidental nuclear war, "debugging" our software and hardware plays a part. But, most important, we as computer professionals and human beings must speak out in favor of nuclear-weapons limitations. Specifically, we can endorse the "Call to Halt the Nuclear Arms Race," a statement that says that "the U.S. and the U.S.S.R. should adopt a mutual freeze on the testing, production, and deployment of nuclear weapons and of missiles and new aircraft designed primarily to deliver nuclear weapons. This is an essential, verifiable first step toward lessening the risk of nuclear war and reducing the nuclear arsenals." The "Call" is available in bulk for \$0.05 per copy, plus postage, from:

American Friends Service Committee 1501 Cherry St. Philadelphia, PA 19102 Single copies and more information can be obtained from:

Nuclear-Weapon Freeze 251 Harvard St. Brookline, MA 02146

Many other organizations around the country are also working to support a weapons freeze. Would you believe, High-Technology Professionals for Peace, in Cambridge, Massachusetts? (See Computer magazine, September 1981, page 95.)

I hope that we can see the day when Professor Hoare's caution will be unnecessary.

Steven Pacenka 812 Hanshaw Rd. Ithaca, NY 14850 ■

A Note on Our Database Issue

BYTE readers have shown a great deal of interest in the articles on database management systems, the theme of the November 1981 BYTE—particularly the article "A Survey of DataBase Management Systems for Microcomputers" by Kathryn S. Barley and James R. Driscoll. While we are pleased that our readers liked the articles in that issue, we are concerned about some of the questions we have been asked, such as "What's wrong with this database? It wasn't listed in your November issue."

Readers must keep in mind that we are not the definitive source for microcomputer information; we cannot review every product on the market. We operate in a world of time constraints and deadlines. We present as many reviews of as many products as time and personnel resources allow. Barley and Driscoll noted that their survey of 18 databases was not comprehensive and that "a potential buyer . . . can determine which database features he or she considers most important and then seek a system that offers those features."

Database management is one of the fastest-growing fields in the microcomputer industry. We will try to keep you informed about as many products as we can. Please remember that the absence of a product review in BYTE does not imply that we have a negative opinion of it. Look for additional database reviews in future issues of BYTE.



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Software Review

The Flexibility of VisiPlot

Robert E. Ramsdell **POB 59** Rockport, MA 01966

One of the most important communication functions your microcomputer can perform is to create, display, and print charts and graphs. For several months I have been using the methods described here to develop presentations for my clients. The graphics format dramatically increases my ability to communicate complex financial information and analyses to the client. In addition, charts and graphs tend to hold an audience's interest during a presentation.

Some of the many uses for this type of graphic communication include stock-market charting, budget analyses, and forecast and projection display. You can do all of this with VisiPlot, the latest and most powerful plotting and graph-generating program available for Apple computers.

About the Program

VisiPlot is a series of programs that allow entry and editing of data, design of a graphic screen presentation, and printing of the screen's contents to a graphics printer. All features are menu selected using the arrow keys, space bar, and return key. The data program allows full entry and editing of the information to be graphed, with as many as 645 points in 16 series. In addition, data can be automatically transferred to the program from a Data Interchange Format file created by another program, such as VisiCalc or DB Master. A comprehensive storage management program allows extensive file manipulation. Completed graphs (which I refer to as slides) can be saved to the disk and/or printed on any graphics printer.

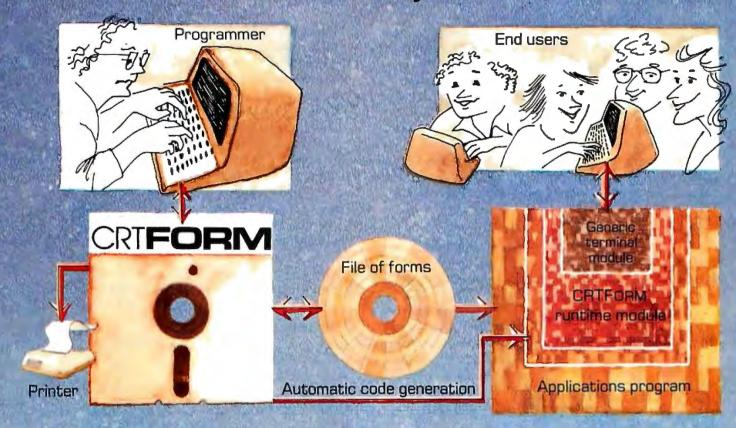
The plotting program is extremely comprehensive and permits line, bar, half-bar, area, pie, high-low, and scatter graphs. Display-value ranges for the two axes are automatically determined by the program, but these default values can be overridden. After the basic graph is on the screen, VisiPlot's flexibility becomes evident.

A vast number of titling, formatting, and color options are available. The five fixed-title options have a choice of

About the Author

Robert E. Ramsdell, CPA, is a microcomputer consultant who lives and works in Rockport, Massachusetts. His company, Pansophics Ltd., publishes business- and financial-modeling applications software for use with VisiCalc and SuperCalc programs.

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5766 BALCONÉS SUITÉ 202 AUSTIN, TEXAS 78731 PHONE 512/451-0221 normal or boldface type, though the movable-title option is by far the most powerful. A title can be created, moved, and placed anywhere on the screen in normal or reverse (black-on-white) print. This feature allows you to label individual points on the graph.

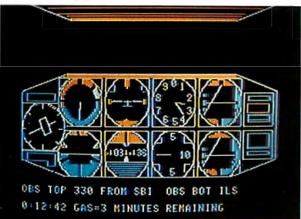
Among the formatting options is the ability to simultaneously compare two graphs (except the pie graph) on the screen, either side-by-side or one over the other. Bars in the bar graph appear as solid, shaded, or in outline. One graph can be overlaid on another, and horizontal and vertical grids facilitate reading the graph.

The user is offered a choice of black, white, violet, blue, orange, and green for use as background or in the bars, areas, and pie segments of the graphs. Printer drivers for most graphics printers are included on the disk and operate automatically from within the program.

Specific Examples

I have prepared several examples of graphs. Figure 1 shows the dramatic effect on profitability and customer returns resulting from an improved inspection program; figure 2 shows the distribution of a company's sales dollar; figure 3 compares sales and net operating income for a 10-year period; figure 4 compares the average inventory with the cost of sales for a company during seven years; figure 5 shows the performance of "My Mutual Fund" in comparison with the NYSE Index; figure 6 is a scatter graph of some mathematical functions.

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PROGRAMMERS SOFTWARE 2110 N.2nd St. Cabot Arkansas 72023 (501) 843-2988 In each example, you can see that the information is much more interesting and understandable when presented graphically. On a color monitor, the impact is even more dramatic.

Documentation

The documentation for VisiPlot is thorough, inclusive, and contains tutorial and reference sections. Because of the many possible uses, the program takes several hours to learn, but the tutorial is easy to follow and the user interface is very well designed. The disk contains sample data files that the user can examine, edit, and graph.

The reference section contains examples and full explanations of every command. A pocket reference card with less detailed information is also included.

Program Constraints

Because of the program's sophistication and the many options it offers, much work is required at the keyboard to create a slide. Another major constraint is that the program cannot reload and adapt a slide already created and stored. It takes about 15 minutes to create a slide, and you must start from scratch each time you want to make

At a Glance

Name

VisiPlot

Type

High-resolution color-graphing and plotting program for data-series display

Author

Mitch Kapor for Micro Finance Systems Inc.

Distributor

Personal Software Inc. 1330 Bordeaux Dr. Sunnyvale, CA 94086 (408) 745-7841

Price

\$199.50

Format

51/4-inch floppy disk

Language

Applesoft Basic and 6502 machine language

Computers

Apple II Plus and Apple III computers, minimum 48 K bytes of programmable memory

Documentation

Loose-leaf binder with 140-page tutorial and reference manual; reference card

Enhancements

Data Interchange Format files for communication with other programs (VisiCalc, DB Master, etc); also available with time-series analyses (VisiTrend/VisiPlot)

Audlence

Businessmen, accountants, stockbrokers—anyone who can use graphic presentations

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a change. Because it is impossible to print a slide later in the program, any printing must be done before you begin to create another slide.

The disk cannot be copied or backed up, but a backup copy of the disk can be obtained from the distributor for an additional \$35.

Conclusions

VisiPlot is a well-designed software package that will prove useful to all those who want to use screen or

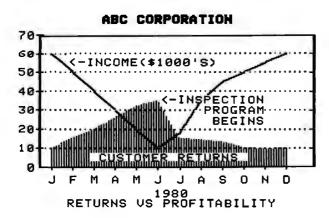


Figure 1: A line and area graph created using VisiPlot.

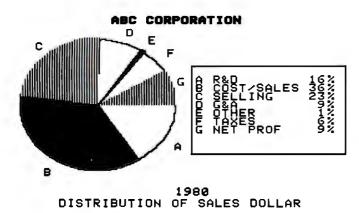


Figure 2: A pie chart, used to illustrate relative quantities.

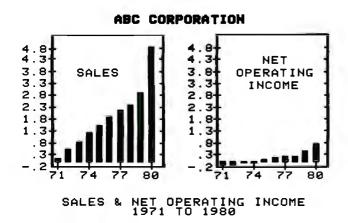


Figure 3: A bar chart or bar graph.

printed graphics in their communications processes. The user interface is well planned, with all options selected from menus, and the data-entry and editing procedures are well conceived and implemented.

The ability to interchange data with other programs makes VisiPlot an integral part of any business systems package, while the combination of VisiPlot and a time-series analysis program (VisiTrend) is the most powerful forecasting and analysis software presently available.

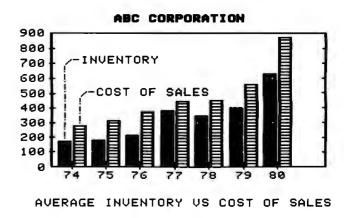


Figure 4: This chart combines bar and half-bar representations.

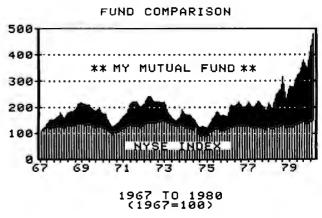


Figure 5: An area graph that plots investment activity over time. (The graph is real—the profits are imaginary.)

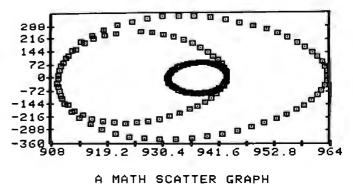


Figure 6: A scatter graph of some mathematical functions.



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Ciarcia's Circuit Cellar

Build a Computerized Weather Station

Steve Ciarcia POB 582 Glastonbury, CT 06033

One of the few redeeming features of the weather here in New England is the abundance of wind. It may change directions five times a day, but there always seems to be a breeze.

For some time I have been thinking of installing a windmill at my house to provide supplemental electrical power. Maps and charts of my locale suggest that it might be feasible, but considering the complexities of the interactions of climate and terrain in

Connecticut, I thought it might be worthwhile to gather more on-site weather data before pouring concrete.

The practical problem of collecting the data inspired this article. I started out by adapting a commercially available anemometer (wind-speed gauge) and wind vane for computer attachment. To simplify getting the data to the computer inside the house, I decided to convert the parallel output from the rooftop transmitter/sensor unit into serial format. Instead of stringing 200 feet of 12-lead cable from the rooftop unit to the computer, I could run a single two-conductor twisted-pair cable.

After this unpretentious start, I got a little carried away thinking how I could do away with even this one cable. But first let me describe the system as I initially built it, starting with the wind sensors.

Weather Instrumentation

Devices capable of sensing and measuring wind speed and direction can be built from several different basic designs, but probably the most cost-effective wind-speed and direction sensors are the familiar cup anemometer and wind vane, shown in photo 1. The cup anemometer captures the moving air in cup-shaped air scoops that are attached via spokes to a shaft. The assembly spins at a rate proportional to the wind's velocity.

A wind vane looks and works like an arrow with a big tail. As the wind blows, the tail fin acts like a sail, causing the vane to align itself with the direction of the wind.

I briefly considered trying to design a homebrew cup anemometer and wind vane, but several factors argued against this.

In my application, survivability



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and accuracy are important. To determine the economic feasibility of a windmill, measurements must be taken, for several months, from a location exposed to the full fury of the weather. An anemometer constructed from paper cups and a small permanent-magnet motor/generator would have been a kluge at best. It might have been capable of measuring wind speed for a little while, but it would not have survived exposure to the elements for very long. Also, I needed to have reliable accuracy to determine the potential power output of a windmill, which is a function of wind speed.

It is not easy to construct a reliable cup anemometer and wind vane. For weather instruments to work, they must survive the weather they are to monitor.

I prefer to concentrate on the applications of electronic technology rather than on techniques of fabrication or artistic excellence. Instead of attempting homebrew sensor designs, I decided to use the wind sensors from a commercially available weathermonitor kit, the Heathkit ID-1890 Digital Wind Computer, sold by the Heath Company, Benton Harbor, Michigan. This is a microprocessorbased unit that displays wind velocity and the date and time of peak gusts. The unassembled parts of the anemometer are shown in photo 2.

If you wish to duplicate my project, you can order the complete kit from Heath and use the appropriate parts. It is unlikely that the required parts will be available separately. (At the time of this writing, the ID-1890 Digital Wind Computer kit is on sale at \$164.95, reduced from the regular price of \$194.95.)

The required parts from the ID-1890 kit are listed in the text box on page 48. The ones unique to the kit are marked with an asterisk, while the rest are fairly common hardware or electronic parts.

The same wind vane and anemometer are used in the more complex ID-4001 Digital Weather Computer kit, which displays wind velocity, temperatures, barometric pressure, and the current date and time and stores weather data for future recall. The ID-4001 sells for \$399.95. (In addition, the ID-4001 contains an output port designed to feed data into a Heath H-8 computer system for logging of weather conditions; it is likely that other computers could be connected through this interface as well.)

If you want to build an anemometer, you might try a different

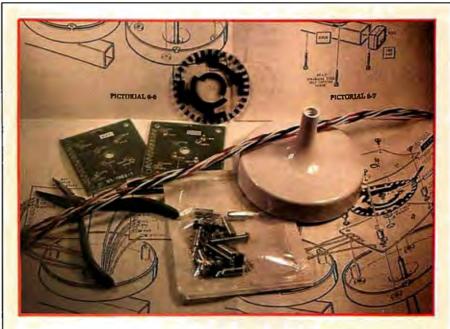


Photo 2: The anemometer and wind vane were constructed from parts used in the Heathkit ID-1890 Digital Wind Computer, shown here.

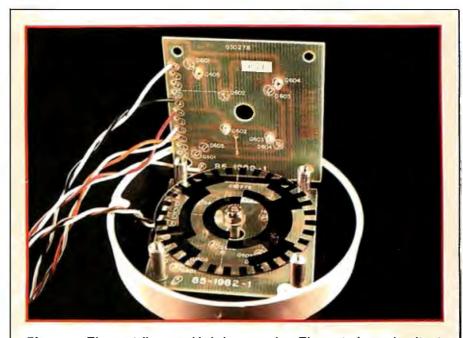


Photo 3: The partially assembled data encoder. The optical encoder disc is mounted on a shaft between the phototransistors and the LEDs. The opaque areas of the disc block the light path between appropriate phototransistor/LED pairs, producing a unique Gray-coded output value.

measuring technique, such as the sonic anemometer described in BYTE several years ago by Neil Dvorak (see reference 5, listed on page 68). His design used four ultrasonic transducers to measure wind speed, direction, and the temperature of the air. But due to the tight tolerances of the analog circuitry involved, I recommend the cup-anemometer approach.

Adapting the Wind Sensors

The output from the Heathkit cup anemometer and wind vane consists of encoded electrical impulses, which must be specially interpreted by the computer to derive information about wind conditions. Each of these wind-sensor units is not much more than a weatherproof mechanical housing for pairs of phototransistors and LEDs (light-emitting diodes) separated by an optical encoding disc.

As shown in figure 1, the anemometer and wind vane each have six basic components: the air-catching apparatus (the wind cup or vane), the top housing, two printed-circuit (PC) boards, the plastic optical encoder disc, and the bottom housing. The wind cup (or vane) and encoder disc are connected by a shaft supported by

ball bearings. As the cup and shaft turn, the shaft rotates the encoder disc between the phototransistors, which are mounted on the top PC board, and the infrared LEDs, which are mounted on the bottom PC board.

As the encoder disc turns, the opaque portions of its surface interrupt the light path between the LEDs and the phototransistors. A schematic diagram of the configuration is shown in figure 2.

There are five separate concentric bands on the encoder disc, as shown in figure 3. An identical disc is used in both the wind vane and the anemometer, but the two units use different portions. In the anemometer, the outside ring of the disc is positioned between a single LED/phototransistor pair. For each revolution of the cup shaft, 32 electrical pulses are generated as the 32 opaque disc areas pass the LED. The wind speed can be measured by simply determining the frequency of these pulses.

The wind vane uses four LED/ phototransistor pairs to read the four inner tracks of the encoder disc. These four outputs form a 4-bit Graycode value (interpreted in table 1), which defines the angular position to a resolution of 1 part in 16. Gray code is a modified binary code in which sequential numbers are represented by expressions that differ in only one bit position. This technique is preferable in slowly revolving encoders because "bit chatter" (oscillation between a 0 and 1 logic level at the point of transition) is less conspicuous than in simple binary or binary-coded-decimal (BCD) encoders. In such encoders, all four bits can change in certain positions (from 0111 to 1000, for example) with only a small change in angular position. Bit chatter can lead to ambiguous indications of direction.

A fairly simple circuit (shown in figure 4 on page 43) provides a 20-mA (milliamp) current to the LEDs and conditions the output from the phototransistors. The outputs of the 74LS04 inverter are TTL- (transistor-transistor logic) compatible and can be connected to any computer's pa-

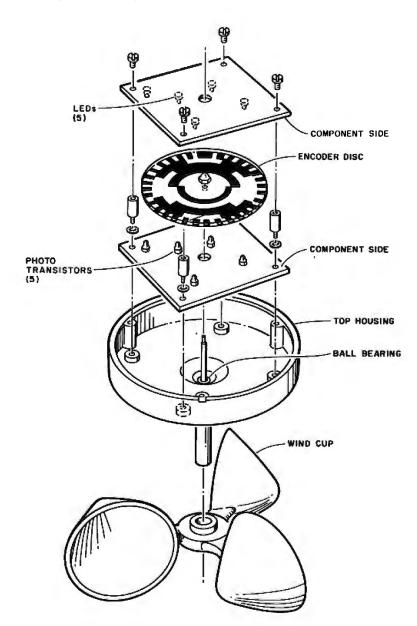


Figure 1: Exploded mechanical diagram of the inverted Heathkit anemometer unit, showing the five LED and phototransistor positions on the two PC boards. The wind vane uses four LED/phototransistor sets, while the anemometer actually uses only one set.

rallel input port should you care to use the wind sensors as they are presently configured. Four LEDs connected to the vane output light up to aid calibration.

Calibrating the Wind Vane

Calibration of the vane for installation is simple and requires only a compass. Observe the state of the indicator LEDs with power applied to the vane. Rotate the housing and the vane until the indicators show all zeros. This setting of the vane should be oriented toward true north when the vane is installed. Be sure that the vane housing is secured so it won't rotate.

(In Connecticut there is a 14-degree difference between magnetic and true north, and the vane must be oriented 14 degrees from magnetic north to compensate. This sort of adjustment must be made in most of North America.)

Calibrating the Anemometer

Calibrating the anemometer is another story. The instructions that come with the kit make no mention of how many pulses are produced per second as a function of wind speed. The conversion of pulses to conventional units of speed (miles per hour [mph], kilometers per hour [kph], or knots) is handled by a microprocessor in the Digital Wind Computer, and this information is unnecessary for most users.

For me, however, it was essential. The only way to determine it was by empirically measuring the pulse rate in a known wind velocity. This can be accomplished by moving air across the anemometer, as in a wind tunnel, or moving the anemometer itself in still air. The indications should be the same.

As you can see in photo 6 on page 46, I moved the anemometer in still air by hanging the anemometer out the side window of my car while driving down a side street near my house (I got some strange looks). As I drove, I measured the output frequency of the encoding mechanism.

Because it was inconvenient to use my frequency counter in the car while

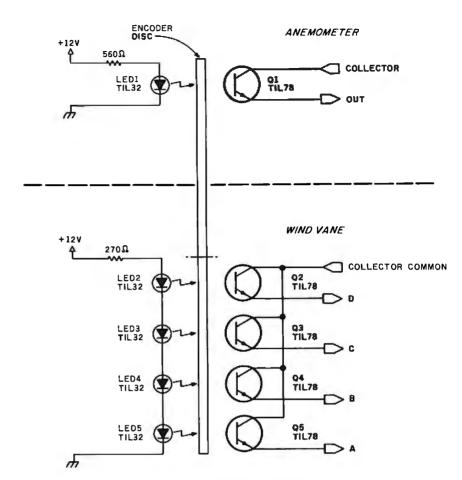


Figure 2: Schematic diagram of the simple position-encoding circuitry inside the Heathkit wind-sensor units. The TIL32 LEDs and the TIL89 phototransistors operate in the infrared region.

driving, I used a battery-operated audio-cassette tape recorder. Connecting it using the circuit of figure 5. which is a portable version of the conditioning circuit previously discussed, I simply recorded the tone produced as the cups spun. The frequency rose and fell as the relative wind velocity increased and decreased. After returning home, I played back the recording into the frequency counter.

I tried various speeds between 15 and 60 mph, and the results were fairly consistent. (I was unable to drive slower than 15 mph without creating a traffic jam.)

The results of my calibration runs are shown in figure 6 on page 46. The output of this anemometer appears to be 11.6 pulses per second per mile per hour. A frequency of 600 Hz (hertz) corresponds to 50 mph. The curve is guite linear between 20 and 60 mph. but I suspect that readings below 10 mph might exhibit nonlinearities.

Decoding the reading of the anemometer with a computer can be accomplished most easily in software. The anemometer's pulse output can be measured by a machine-language subroutine that simulates a frequency



Figure 3: The optical encoding disc uses a Gray code to eliminate ambiguity in angular position of the wind vane, while in the anemometer only the outermost ring is used as a sort of tachometer.

counter; the algorithm for this will appear later in this article. The result is simply divided by 12 (close enough) to convert to miles per hour.

Adding a Digital Thermometer

With my scheme for measuring wind velocity well under way, I decided that I could easily upgrade the system to keep track of other weather conditions as well. While wind parameters were essential to my feasibility study, monitoring temperature provided an extra dimension to the data-gathering effort.

Most temperature indicators are analog in nature and require an A/D (analog-to-digital) converter to be read by a computer. This is not only an added complication, but it consumes more parallel-port resources to accommodate the A/D converter. A conversion resolution of 0.4 percent in parallel conversion requires 8 bits and generally occupies an entire 8-bit input port. Similarly, 0.002-percent converters use 16 bits.

Fortunately, parallel conversion is not a necessity in this application and others like it, which require modest accuracy but where input lines are at a premium. Here an analog-input-todigital-frequency converter is more

applicable. In my weather-monitoring system, I already had a digital frequency input from the anemometer. It was advantageous, therefore, to treat the temperature as a second frequency input and use the same software to measure it.

Figure 7 on page 48 is the schematic diagram of a temperature-to-frequency converter suitable for this application. IC1 is an LM134 analog current source/temperature sensor with an operating range of -55 to

To add excitement to the project, I decided to make my weather station talk.

+125°C (degrees Celsius). (You could substitute an LM334 to function within a temperature range of 0 to +70°C.) With a 230-ohm value set on the calibrating potentiometer (the R_{ser} value), the voltage from it will increase 10 millivolts per degree Celsius (mV/°C) from some nominal output. Through IC2, the rate is amplified to 100 mV/°C and the offset adjusted to a convenient value. IC3 is a type-2207 voltage-controlled oscillator that acts as a voltage-to-frequency converter. As configured, a 0- to 10-V input will result in a 0- to 10-kHz output. This output frequency is then measured by the computer.

Calibration is best established by immersing the temperature sensor (IC1) in ice water at 0°C and then in a liquid at a known elevated temperature. The calibration curve will be linear, but its slope is dependent on the particular components used to build the sensor. It's probably best to have a frequency of 2 kHz represent 20°C and 5 kHz represent 50°C. Conversion from Celsius to the Fahrenheit scale should be done by the host computer.

Serial Link to the Roof

Most wind sensors are located remotely from the recording devices. In the Heathkit units, a 150-foot 8-conductor cable is available for this connection. I don't like stringing any more wire than I have to, and I prefer to communicate digested rather than raw data.

The easiest way to condition the weather-sensor outputs and reduce the wiring is to attach a computer directly to the wind and temperature sensors. Any computer could be

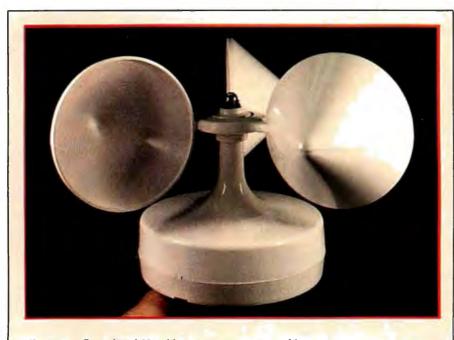


Photo 4: Completed Heathkit anemometer assembly.

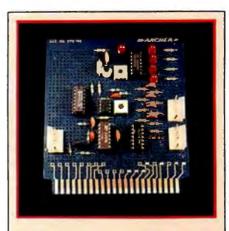


Photo 5: Prototype of the windsensor signal-conditioning circuit board, which combines the inputconditioning and calibrating-display circuitry of figure 4 with the digitalthermometer circuitry of figure 7. The two 4-pin connectors on the right side connect to the wind vane, and the connector on the left goes to the anemometer.

Number	Туре	+ 5 V	GND
IC1 IC2	74LS04	14	7
IC2	7406	14	7

used, of course, but I decided that this was a natural application for the Z8-BASIC Microcomputer (which I described in the July and August 1981 issues of BYTE) used as a device controller and data concentrator. because it contains the necessary I/O (input/output) ports and can be programmed directly in BASIC.

I connected the Z8-BASIC Microcomputer/controller to the sensor units, ran my twisted-pair cable, and set up the computer/controller to use its RS-232C serial port to transmit the results to another computer inside the house for recording or for display on a video terminal.

A message sent down the serial link for recording need only consist of a header and the reduced data. A program running on the display computer could format the data as a compass diagram on the screen, or the Z8-BASIC Microcomputer could perform the formatting, given a more sophisticated program. In either case, the Z8-BASIC Microcomputer/controller board has the latent capability to reduce, record, and format the wind and temperature data as desired.

A Synthesized Weatherman

Having come so far in devising a versatile weather-monitoring system, how could I stop without giving it the ultimate in capability? Using serial communication for recording data was satisfactory, but dull. To add futuristic excitement to the project, I decided to make my weather station talk.

Exploiting as-yet-unused system resources, I connected a parallel-port Sweet Talker voice synthesizer (the subject of my September 1981 article) to port 2 on the computer/controller. I stored a simple phonetic vocabulary consisting of words like "wind," "velocity," and "temperature" in a table in the Z8-BASIC Microcomputer's memory and wrote a program to

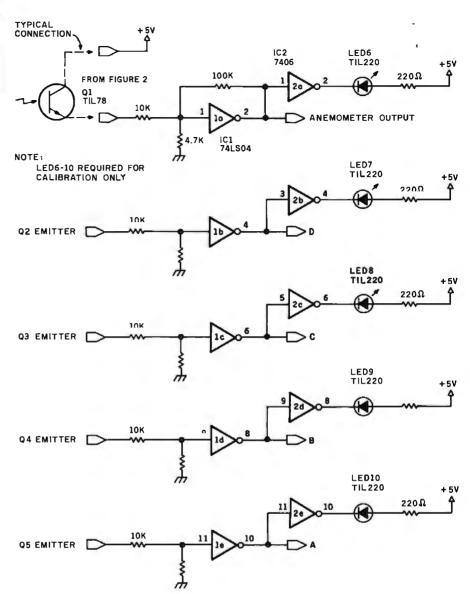


Figure 4: Schematic diagram of the signal conditioner that accepts output from the phototransistors in the wind sensors and sends it to the controlling computer system. LED6 through LED10 are required only for calibration of the vane.

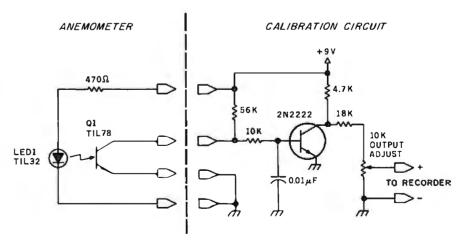


Figure 5: A simple circuit that allowed me to calibrate the anemometer from my moving car by holding it out the window. The anemometer's output was fed through this circuit into a small, battery-operated cassette tape recorder, and the tape was later played back into a frequency counter.

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MP/M & PL/I-80 are trademarks of Digital Research. CBASIC is a trademark of Compiler Systems, Inc. PASCAL /MT+ is a trademark of MT Micro Systems. read the sensors and send appropriate word phonemes out the port to the Sweet Talker. (A list of appropriate words is contained in table 2.) Continuing along this line of thought to its logical conclusion, I connected the audio output of the Sweet Talker to the input of a low-power radio transmitter.

In the final configuration, the computer/controller board digests the weather-instrument data, the Sweet Talker converts it to English, and the transmitter transmits it to my radio.

For up-to-the-minute weather data, I merely tune my radio to 98 MHz and listen to my own synthesized weatherman announcing, "Wind heading: north northwest at twenty miles per hour."

System Configuration

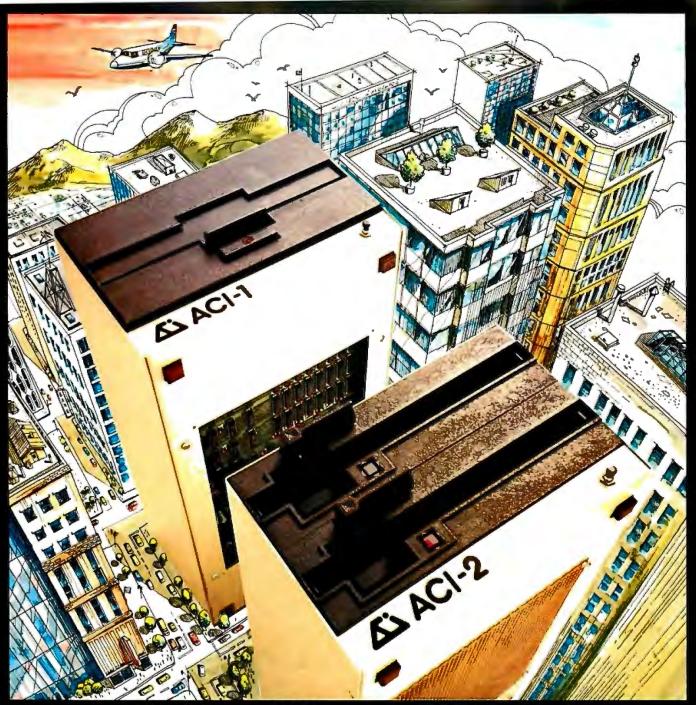
Figure 8 on page 54 shows an outline of the connections in the completed system between the wind instrumentation, the temperature sensor, and the computer/controller board. The circuit boards are shown

Compass Position	Gray Code D C B A
N	0 0 0 0
N N W	0 0 0 1
N W	0 0 1 1
W N W	0 0 1 0
W	0 1 1 0
W S W	0 1 1 1
S W	0 1 0 1
S S W	0 1 0 0
S	1 1 0 0
S S E	1 1 0 1
S E	1 1 1 1
E S E	1 1 1 0
E	1 0 1 0
E N E	1 0 1 1
N E	1 0 0 1
N N E	1 0 0 0

Table 1: Interpretation of the optical Gray code produced by the LED/photo-transistor detectors inside the Heathkit wind-vane sensor unit.

anemometer average Celsius computer direction east Fahrenheit frequency hour kilometers maximum miles minimum north peak per south	AE, N, AH1, M, AW1, AW2, M, I3, T, ER AE1, EH3, V, R, I1, D, J S, EH1, L, S, I1, UH2, S K, UH1, M, P, Y1, IU, U1, T, ER D, I1, R, EH1, K, T, SH, UH3, N E1, AY, S, T F, EH1, R, I2, N, H, UH3, AH2, Y, T F, R, E1, K, W, EH3, N, DT, S, Y AH1, UH3, W, ER K, I1, I3, L, AW1, M, I1, T, ER, Z M, AE1, EH3, K, PAO, S, EH3, M, UH2, M M, AH1, EH3, I3, UH3, L, Z M, I2, N, I2, M, UH3, M N, O2, O2, R, TH P, E1, AY, K P, ER S, AH1, UH3, U1, TH
•	· · · · ·
•	
temperature	T, EH1, EH3, M, P, ER, UH1, T, CH, ER
velocity	V, UH1, L, AW1, S, I1, T, E1, Y
west	W, EH1, EH3, S, T
wind	W, I1, I3, N, D, D

Table 2: A list of words useful in describing weather conditions, with their Votrax phonemes. These phonemes can be transmitted to the Sweet Talker voice synthesizer by the controlling software running on the Z8-BASIC Microcomputer, in accordance with the prevailing weather.



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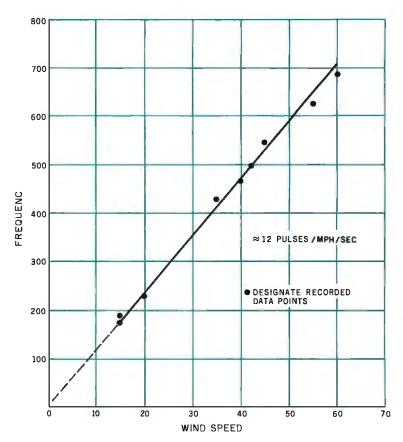


Figure 6: Graph of anemometer-output voltage as a function of relative wind speed.



Photo 6: The anemometer was calibrated by moving it relative to still air; holding it out the window of a moving automobile worked quite well. Driving at a known speed, I used the circuit of figure 5 to record its pulses; the characteristic curve is shown in figure 6.

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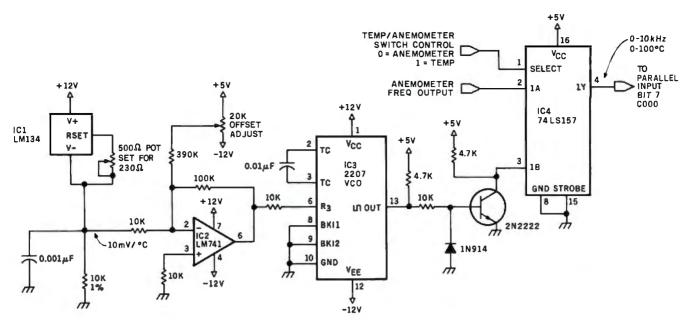


Figure 7: Schematic diagram of a digital thermometer that varies its output frequency as a function of ambient temperature. The output can be read by the same frequency-counter software that interprets the wind-speed data from the anemometer.

Component Sources

The following parts list is taken from the Heathkit ID-1890 Digital Wind Computer assembly manual. This list comprises the components necessary to build the wind-vane and cup-anemometer assemblies. Parts unique to the project are marked with an asterisk.

Part Number	Quantity	Description
250-235	8	6-32- by 1/4-inch stainless-steel screw
250-1168	6	#4 by 1-inch stainless-steel screw
254-25	8	#6 lockwasher
253-713	1	#6 rubber washer
252-80	1	6-32 cap nut
255-735	8*	short spacer
250-328	1	8-32 by 3/8-inch stainless-steel screw
250-43	2	8-32 by 1/4-inch setscrew
252-27	2	6-32 locking nut
253-1	2	#6 fiber flat washer
85-1982-1	4*	sensor printed-circuit board
412-635	5	TIL32 infrared light-emitting diode
417-919	5	TIL78 phototransistor
214-208-1	2*	top housing
214-209-1	2*	bottom housing
266-930	1*	wind vane
266-939	1*	wind cup
266-942	1*	wind vane cap
266-943	1*.	counterweight
266-1032	2*	optical encoder disc
453-282	2*	1/8- by 3-inch shaft
253-712	4*	C-ring
455-643	4*	bearing
142-711	1	boom parts
142-712	1	boom
595-2399	I*	ID-1890 assembly manual
		miscellaneous hookup wire

mounted on a connecting mother-board in photo 8 on page 64.

Figure 9 on page 56 is a flowchart of a minimal application routine that reduces and transmits the resulting data down the serial communication line. Figure 10 on page 60 is the flowchart of a frequency-counter subroutine written in Z8 machine language. This routine reads the inputs from the temperature sensor and anemometer and derives numeric values in hertz. The routine is stored in memory beginning at hexadecimal location 1500 (as presently assembled) and is invoked from the BASIC/Debug interpreter by the statement

$$A = USR(\%1500)$$

The value returned in the variable A is the frequency. Listing 1 on page 52 is the assembly-language listing.

If you wish to set up a radio weather station with a personal touch, as I did, you can use a low-power transmitter: either the AM (amplitude modulation) transmitter in figure 11a on page 62 or the FM (frequency modulation) unit in figure 11b on page 64.

Ideas for Improvement

I have thought about enhancing the

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Stop following the competition.

Circle 106 on inquiry card.



Listing 1: Assembly listing of the "Windy" routine in Z8 machine language. "Windy" is called by the BASIC statement A = USR(%1500). The frequency is read from bit 7 of the input port mapped into memory-address space at hexadecimal 1500, and the numeric value is returned to BASIC in the variable A. The routine "Windclk" is called in response to an interrupt that occurs every 0.01 seconds.

Address	Op Code	Dl	D2 Line	Label	Mnemonic	Comment
				* Windy-	Count anemometer p C000, bit 7 (pin K)	ulses coming in at hexadecimal
				* Inputs-	None. Called as a "U	ISR" routine from BASIC/Debug
				* Output- *	Count of number of p Result returned in red	oulses seen at location C000, bit 7 gisters R12 and R13
				· Uses- · Calls- · Notes-	R12 - R13 T1,T1 prescale R32 R33 R34 - 35 R36 - 38 LOC. 100F-1011 None, but tests flag s driven routine "Wind	lclk" is as follows:
				•	WX - Denotes work WPX - Denotes wo XX - Denotes hexac ** All notation is in ** unless otherwise	rk-register-pair address decimal data n hexadecimal radix ** e indicated **
1500 1501	8F E4	FD	32	Windy	DI LD R32. RFD	Don't bother me 'til I'm set up Save current work-register pointer

1500	8F			Windy	DI	Don't bother me 'til I'm set up
1501	E4	FD	32		LD R32, RFD	Save current work-register pointer
1504	E6	FD	30		LD RFD, 30	Point to my work registers
1507	E6	F3	03		LD RF3, 3	Set up T, Prescale for mod-n, 64 count
150 A	E6	F2	90		LD RF2, 90	Set up T, to give 0.01-second interrupt
150D	E6	FB	20		LD RFB, 20	Turn on IRQs I/R mask
1510	4C	C0			LD W4, C0	Registers 34 and 35 point
1512	5C	00			LD W5, 00	to the data-input address
1514	B0	12			CLR R12	Clear registers 12 and 13. We
1516	В0	13			CLR R13	will pass count in them.
1518	3C	00			LD W3,00	Clear number of I/R's accumulator
151 A	6C	10			LD W6,10	Set up registers 36 and 37 to
151C	7C	OF			LD W7,0F	store I/R vector for IRQ5
151E	8C	8D			LD W8,8D	lst byte to store is JP op code
1520	92	86			LDE WP6, W8	Move register 38 to address at registers 36 and
						37
1522	7E				INC W7	Step to next byte
1523	8C	15			LD W8, 15	2nd byte is high byte of address
1525	92	86			LDE WP6, W8	Store it.
1527	7 E				INC W7	Step to next byte
1528	8C	55			LD W8, 55	3rd byte is low byte of address
152A	92	86			LDE WP6, W8	Store this too
152C	46	Fl	0C		OR RF1, 0C	Initialization all done, start T1
152F	7C	00			LD W7, 0	Clear register 37 toperbised as flag
1531	9F			•	EI	Turn on I/Rs tcactory carler pops



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Circle 374 on inquiry card.

Address	Op Code	DI	D2	Line	Lαbel	Mnemonic	Comment
					*This is the	e main counting loop	
1532	76	37	80		Count	TM R37, 80	Test to see if we're done
1535	EB	17				JR NZ, Done	If bit on, we're through
1537	82	84				LDE W8, WP4	Load data at C000 into R38
1539	76	38	80			TM R38, 80	Is bit 7 at logic 1?
153C	6B	F4				JR Z, Count	If not, loop until it is
153E	76	37	80		Lowwait	TM R37, 80	Check to see if done just like before
1541	EB	0B				JR NZ, Done	If bit on, we're through
1543	82	84				LDE W8, WP4	Pick up data at C000 again
1545	76	38	80			TM R38, 80	Check bit 7 for transition to 0
1548	EB	F4				JR NZ, Lowwait	If not, wait for it
154A	A0	12				INCW R12	If yes, then high-to-low $= 1$ pulse
154C	8B	E4				JR Count	Do the whole mess over again
					*This is wh	nat we do when we're fi	nished
154E	56	Fl	F3		Done	AND RF1, F3	Shut down T1 counter
1551	E4	32	FD			LD RFD, R32	Restore work-register pointer for BASIC/Debug
1554	AF					RET	Go back to BASIC pgm/monitor
					•		
					 This is th 	e interrupt-driven rout	ine that counts clock cycles
1555	3E				Windclk	INC W3	Add 1 to number of cycles
1556	A6	33	64			CP R33, 64	have we done 100?
1559	1B	02				JR LT, More	No, do more
155B	60	37				COM R37	Turn all bits on in register 37
155D	BF				More	I RET	Issue Return-from-interrupt
					* That's al	l, folks!	
					•		

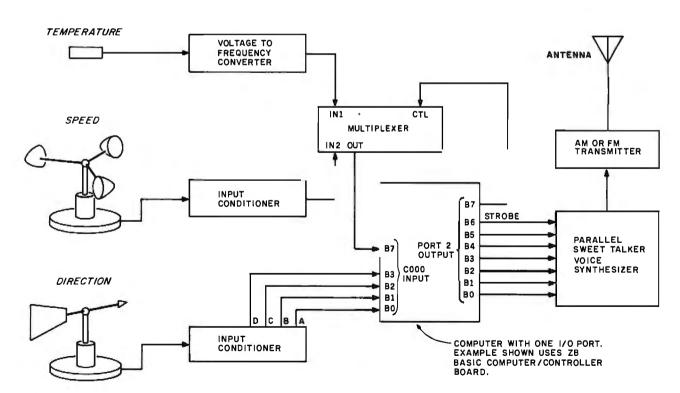


Figure 8: Block diagram of the complete computerized, voice-synthesized weather radio station. The weather data may be directed to a host computer system for logging if radio transmission is not desired, or the output of the Z8-BASIC Microcomputer/controller could be sent directly to a printer or video terminal.

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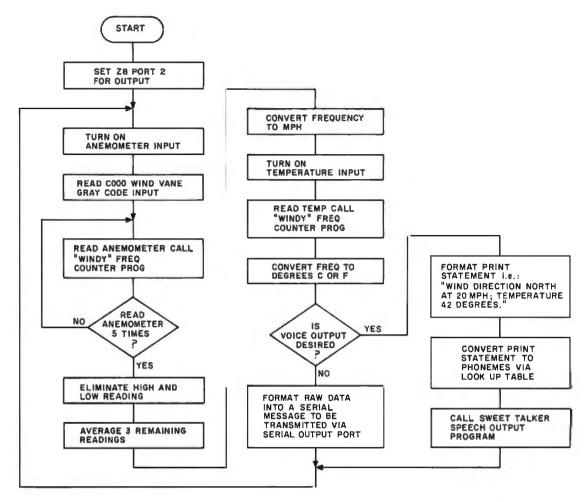


Figure 9: Flowchart of the program that directs the Z8-BASIC Microcomputer to collect raw data from the wind sensors, digest it, and provide output either to the serial communication line or the Sweet Talker voice synthesizer.



Photo 7: The wind vane must be oriented in accordance with true north, which may vary from the magnetic north shown on the compass. Point the vane to the north and rotate the housing until the Gray-code value shown in the calibration display reads all zeros.

system to measure barometric pressure in addition to the wind velocity and temperature. Conceivably, it could be accomplished with the hardware as presently configured plus one more sensor.

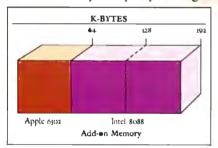
The method I thought might work was some sort of capacitance detector. The majority of modestly priced (\$100) barometers are spring-and-bellows pressure detectors. The bellows contracts and expands with the changes in atmospheric pressure. Given the extremely short linear motion and low masses involved, a measuring technique that doesn't require mechanical sensing seems best.

One idea is to use the bellows as one side of a two-plate capacitor. As the pressure changes, the bellows contracts, changing the spacing of the capacitor plates and therefore the capacitance. This capacitor is in turn used to set the frequency of an oscillator. As the capacitance



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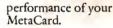


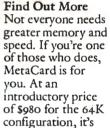
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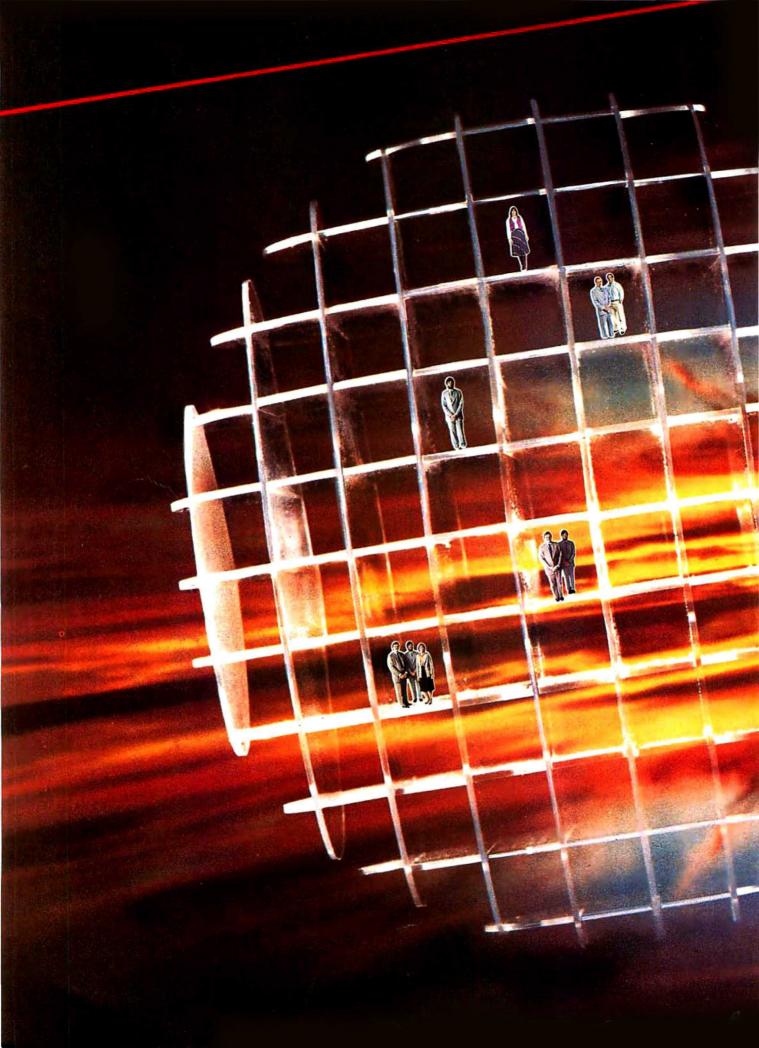
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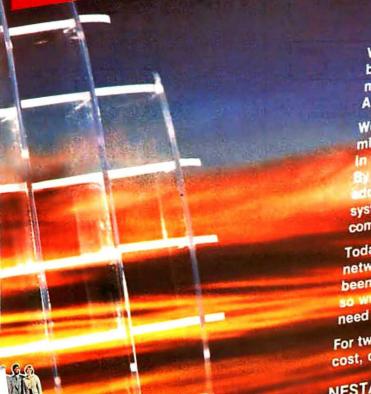
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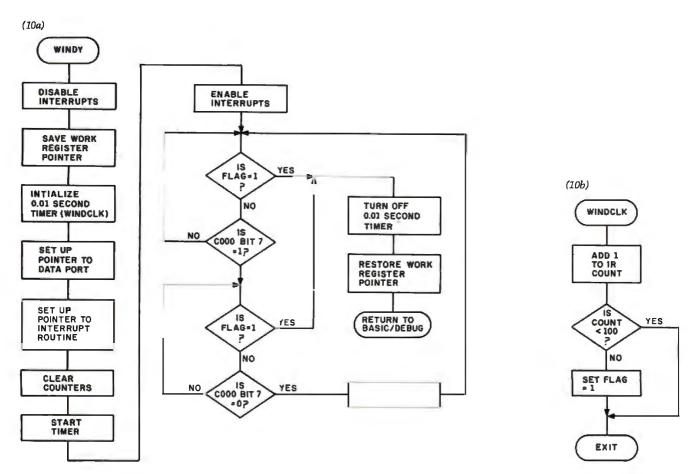
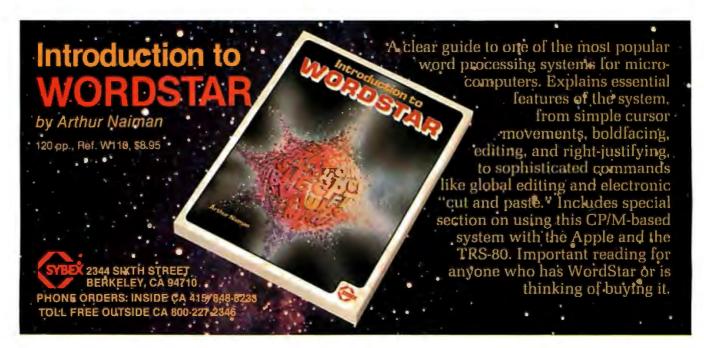


Figure 10: Flowcharts of the machine-language routine "Windy" (figure 10a) and "Windclk" (figure 10b). The assembly-mnemonic listing is given as listing 1 on page 52. "Windy" is called from the BASIC interpreter by the statement A = USR(%1500), while "Windclk" is called when the Z8 processor receives an interrupt from the real-time clock.

changes, it varies the frequency. This output frequency can then be read by the computer/controller in the same way as the anemometer and thermometer.

Concluding Thoughts

I doubt that many of you will go to the extremes that I did to eliminate a few wires, but even directly attaching weather sensors to your computer is a satisfying project. In the process of reading about the specifics of my "synthesized weatherman," you may have seen an application for one of the subsystems. Or with this informa-





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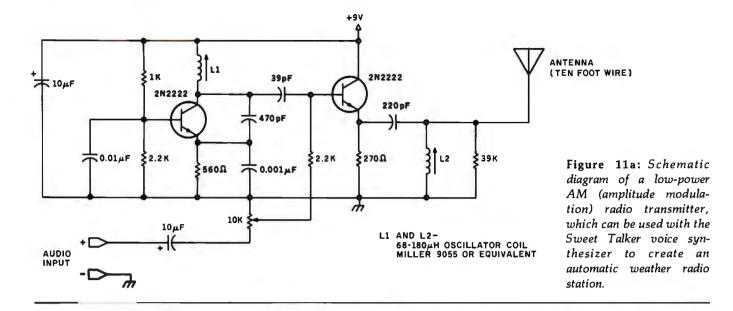
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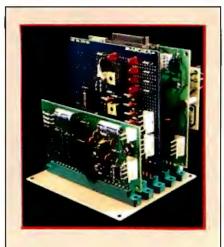


Photo 8: The complete talking, broadcasting weather station is made up of the Z8-BASIC Microcomputer/controller board, in back, the input-conditioning and temperature board, in the center, and the Sweet Talker voice-synthesizer board, in front. The Z8-BASIC Microcomputer is based on the Zilog Z8 microcomputer-on-a-chip, and the Sweet Talker employs the Votrax SC-01.

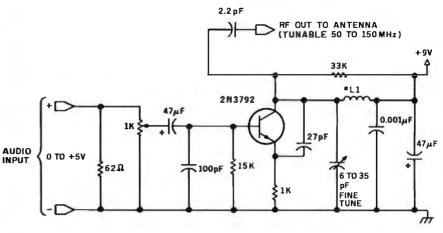


Figure 11b: Diagram of a low-power FM (frequency modulation) radio transmitter, for use with the Sweet Talker voice synthesizer.

tion you could easily configure your own custom weather station.

I think I'll listen to my voice-synthesized weatherman for a while before making modifications to the system. My only regret is that I won't be able to observe the expression on my neighbor's face the first time he tunes his radio across the dial. And I may never install a windmill after analyzing the accumulated data, but I

will have the most personal weather reports in Connecticut.

Next Month:

One of my ambitions is to put together a computer speech-recognition system. The first step is to analyze the audible components of spoken words. In March, my project will be a circuit that helps perform this analysis.

Continued on page 68



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A text file is compiled into a BRUNable program. RGL is a very efficient structured language, similar to 'C'. No additional hardware or software is needed. Also available on Apple CP/M disk.

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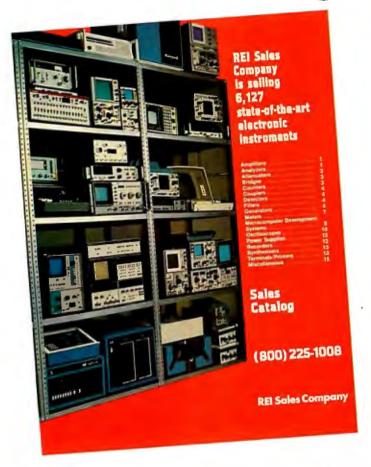
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Special thanks to Bill Curlew for his help in writing the software for the Z8 processor.

Editor's Note: Steve often refers to previous Circuit Cellar articles as reference material for each month's current article. Most of these past articles are available in reprint books from BYTE Books, 70 Main St., Peterborough, NH 03458. Ciarcia's Circuit Cellar, Volume I covers articles that appeared in BYTE from September 1977 through November 1978. Ciarcia's Circuit Cellar, Volume II contains articles from December 1978 through June 1980. Ciarcia's Circuit Cellar, Volume III contains the articles that were published from July 1980 through December 1981.

The Z8-BASIC Microcomputer and the Sweet Talker voice synthesizer are available from:

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A Homebrew Graphics Digitizer

Neal Atkins 5 Island Ave., Apt. 16-C Miami Beach, FL 33139

Enrique Castro-Cid 7136 Bonita Drive Miami Beach, FL 33141

For the past six years, coauthor Enrique Castro-Cid has been developing a new art form that combines art, computers, and mathematics. In particular, it uses branches of mathematics called conformal mapping and complex variables. Castro-Cid's technique is related to such topics as relativity and black holes in space. Images of giant objects the size of the earth are transformed to canvas size through a process that involves converting a drawing to coordinates and transforming the coordinates using mathematical functions to new points plotted and painted on canvas. Although the early work was done completely by hand, the use of computers for this process was a natural evolution.

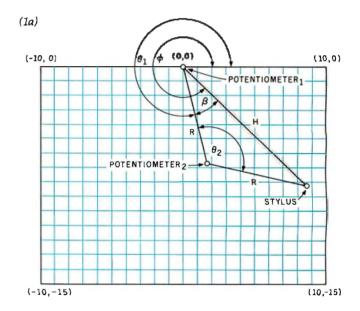
This article describes a device that, when used with a computer, converts a drawing to its Cartesian coordinates (see photo 1). This graphics tablet is inexpensive and easy to build using the most elementary tools, yet it provides a high degree of accuracy. It can be implemented on most microcomputers that have two A/D (analog to digital) input channels. It can also replace the paddles or joysticks found on some computers.

Child's Play

We considered several designs for this graphics tablet. The simplest scheme to implement mathematically is a Cartesian-coordinate device having two linear potentiometers, one for the *X* direction and one for the *Y* direction. This idea is similar to the way the child's toy Etch-A-Sketch works. The disadvantage of such a device is the user must turn two knobs. If the two potentiometers are somehow connected, the mechanical linkage becomes quite difficult to fabricate, requiring either a rack-and-pinion gear or a string drive. A second design is based on polar coordinates, where the angle and radius are measured. The device to measure the angle can be easily built using a potentiometer, but the varying radius is still difficult to measure.

However, the human anatomy provides a very workable solution to this problem. A person's shoulder and elbow are able to cover a wide area without actually changing the length of his arm. Using the human arm as a model, a two-section mechanical arm, having pivots

analogous to the shoulder and elbow joints (see figure 1) can be built. Such a design is easily fabricated using two fixed-length members and two potentiometers. The mathematics becomes more involved than in the other designs, but the use of a computer makes construction a simple task.



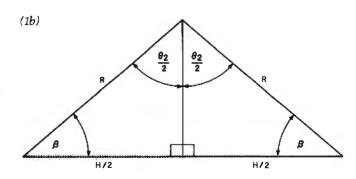


Figure 1: Trigonometric conception of the graphics digitizer. Figure 1a shows the physical arrangement of the potentiometers on the arms. Figure 1b is labeled with the variables used to represent measurements made by the device.

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trol and finally to after sales support and service.

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Geometry and Formulas

To find the coordinates X, Y of the stylus, given any voltages V_1 , V_2 provided from two potentiometers, the

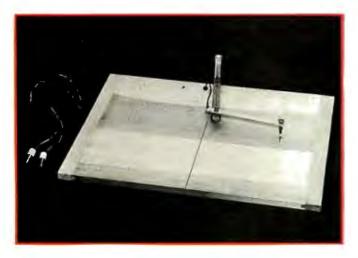
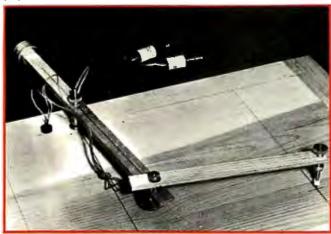


Photo 1: The homebrew graphics-tablet digitizer, built from a standard drafting table.

(2a)



(2b)

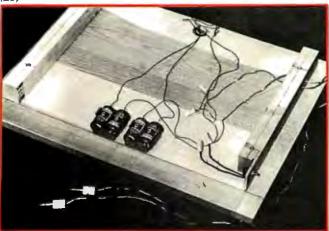


Photo 2: Construction details of the graphics tablet. Photo 2a shows the arrangement of the potentiometers on the table and the arms. Note the stylus holder borrowed from a commercial pantograph. Photo 2b shows how clearance was obtained for the batteries and the on/off switch.

voltages are converted to angles using the following equations:

$$\theta_1 = scale_1 \times V_1 + trans_1$$

 $\theta_2 = scale_2 \times V_2 + trans_2$

The isosceles triangle (see figure 1b) formed by the two equal, fixed-length arms R has a variable-length hypotenuse H. At its apex is the potentiometer that produces V_2 . This voltage is converted to angle θ_2 using the equation above. Trigonometry relates the base angles β , and the lengths H and R, as follows:

$$\beta = 90 - \theta_2/2$$
and
$$H/2R = \sin(\theta_2/2)$$

$$H = 2R \sin(\theta_2/2)$$
Thus
$$\theta_2 = 2 \arcsin(H/2R)$$

The angle ϕ of the radius H is the sum of angle θ and angle β :

$$\phi = \theta_1 + \beta$$

Using the equation for β above:

$$\phi = \theta_1 + 90 - \theta_2/2$$

This provides a solution, expressed in polar coordinates, involving a radius of length H and angle ϕ as its only variables. This is easily transformed to Cartesian coordinates:

$$X = H \cos(\phi)$$

and $Y = H \sin(\phi)$

The computational procedure is as follows: beginning with voltages V_1 and V_2 , the angles θ_1 and θ_2 are computed. Radius H is found from angle θ_2 and R. Angle ϕ is found using angles θ_1 and θ_2 . Finally, the coordinates X and Y are computed using H and ϕ .

Calibration

The device is calibrated by setting the stylus to two known test points (X_1, Y_1) , (X_2, Y_2) on the table and sampling the corresponding voltages V_{ij} , where i is the potentiometer and j is the test point number. Then for each of the two positions:

$$\phi_j = \arctan(Y_j/X_j)$$
 and $H_j = \sqrt{X^2 + Y^2}$

Using earlier equations (remember that θ_{2j} refers to potentiometer 2 and θ_{1j} refers to potentiometer 1):

$$\theta_{2j} = 2 \arcsin (H_j/2R)$$
 $\theta_{1j} = \phi_j - 90 + \theta_{2j}/2$
 $\theta_{i1} = scale_i \times V_{i1} + trans_i$
for potentiometer i test point 1
 $\theta_{i2} = scale_i \times V_{i2} + trans_i$
for potentiometer i test point 2

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CP/M is a registered trademark of Digital Research, Inc. ‡MmmOST™ (Multi-User, multi-task, multi-processor Operating System Technology) is a trademark of TeleVideo Systems, Inc. For each potentiometer i there are two equations and two unknowns: *scale* and *trans*. However, θ and V are known. Therefore, the next step is to solve for the calibration factors:

$$del = V_{i1} - V_{i2}$$

$$scale_1 = (\theta_{i1} - \theta_{i2})/del$$

$$trans_i = (V_{i1} \theta_{i2} - V_{i2} \theta_{i1})/del$$

The computational procedure is as follows: compute the angles θ for both potentiometers (*i*) at both positions (*j*). Then, compute the calibrating factors for potentiometer i = 1, and repeat for the second potentiometer.

Construction Details

The graphics tablet was constructed using materials readily available from most art or drafter's suppliers. The table is a standard 18-inch by 26-inch wooden drawing board, drilled and countersunk to accommodate potentiometer 1 (see photo 2a). Two 14-inch-long two-by-twos were screwed to the underside of the table, providing clearance for the batteries and the on/off switch (see photo 2b). The A/D converter accepts signals in the ± 2.56 -V range. Four D cells were selected as a power supply (see figure 2) because of their low cost and noise immunity. Also, due to the high resistance of the potentiometers and the A/D converter's high internal resistance, the battery drain is very low. The batteries provide ±3 V. If your A/D converter requires only a positive voltage, the two batteries on the negative side of ground can be eliminated. Batteries of other voltages can be substituted to meet other applications or completely omitted if you substitute the potentiometers for paddles or joysticks.

The graphics tablet operates by measuring angles; therefore, in order to achieve high degrees of accuracy, the potentiometers must have a very linear taper (response). At first we used inexpensive 10 percent tolerance potentiometers as shown in the photos. We found when a straight line was drawn, the digitized computer-graphics line had a slight waviness. However, a later model of the tablet was built using precision linear taper 0.5 percent potentiometers that greatly reduced this problem. They are mounted so that when the arms are at the middle of their range of motion, the shafts of the potentiometers are rotated approximately halfway. They must never be at their limit. Another condition affecting accuracy is mechanical rigidity; the arms must be free of play and torsion. The working arm length from potentiometer to potentiometer and from potentiometer to stylus is *exactly* 7 inches. This measurement is critical if the device is to be linear. Notice the longer arm is counterbalanced to prevent potentiometer 2 from dragging on the drawing surface. The counterweight consists of a number of metal washers mounted on a bolt. Some of the hardware, such as the knurled nuts and stylus holder, was borrowed from a pantograph (a device for enlarging drawings) that we purchased at the local art store.

Operation and Programming

The program in listing 1 was written in BASIC and can be easily modified for other systems. The main routine has two options: Calibration and Draw. During calibration, the computer asks the artist to place the stylus at position one, where X = -4 and Y = 0. The artist then enters the coordinates -4, 0, and the computer samples the voltages from both potentiometers. Then the process is repeated for position two, where X = 8 and Y = -8. We found the choice of test points not to be critical, but these two provide a good compromise for the physical placement of the stylus and the accuracy of the trigonometric functions. However, the measurement and perpendicularity of the points should be as exact as possible. The program now has all the information it requires to compute the calibrating factors scale and trans. Once the calibration procedure has been done, it does not have

Text continued on page 86

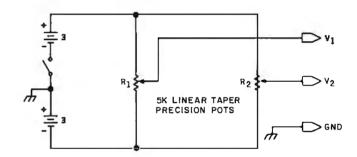


Figure 2: A schematic diagram of the digitizer showing the simplicity of the device. The analog voltages provided by the potentiometers are stored in a computer after they are put through an analog-to-digital converter.

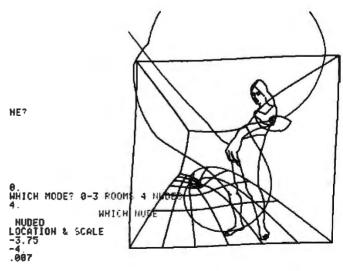


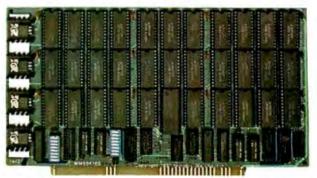
Figure 3: A representation of an original drawing after it has been digitized and transformed according to a mathematical equation of the artist's choosing.

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```
100 REM *****
110 REM * ETCH *
120 REM ******
130 REM
140 REM
150 DIM X(200), Y(200), THETA(2,2), VCAL(2,2), SCALE(2), TRANS(2)
160 REM INITIALIZE
170 R
           = 7.0
200 REM MAIN LOUP. READ USERS RESPONCE.
210 INPUT "CAL UR DRAW", ANSS
550
       IF ANSS = "CAL" THEN GOSUB 300
230
       IF ANSS = "DRAW" THEN GOSUB 600
240 GU 10 210
250 REM
260 REM
270 REM
280 REM
290 REM
300 REM *****
310 REM * CAL *
320 REM *****
330 FOR IPOSTN=1 to 2
340
       PRINT "SET THE ARMS TO POSITION", IPOSTN
350
       INPUT "X POSITION IS", XCAL
360
       INPUT "Y POSITION IS", YEAL
370 REM SAMPLE A/D CONVERTER AND GET VI, V2.
380
       GOSUB 1000
390
       VCAL(1, IPOSTN) = V1
400
       VCAL(2, IPOSTN) = V2
410
              = SOR( XCALT2 + YCALT2 )
       H
       PHI
420
              # ATN( YCAL / XCAL )
       IF XCAL < 0 AND YCAL >=0 THEN PHI = PI(1.0) + PHI
430
440
       IF XCAL < 0 AND YCAL < 0 THEN PHI = PI(1.0) + PHI
450
       IF XCAL > 0 AND YCAL < 0 THEN PHI = PI(2.0) + PHI
460
       THETA(2, IPOSTN) = 2.0 \times ASN(H/(2.0 \times P))
470
       THETA(1, IPOSTN) = PHI + ( THETA(2, IPOSTN) - PI(1.0) ) / 2.0
480 NEXT IPOSTN
490 FOR IPOT=1 TO 2
500
       DENOM = VCAL(IPOT,1) - VCAL(IPOT,2)
510
       SCALE(IPUT) = ( THETA(IPOT,1) - THETA(IPOT,2) ) / DENOM
       TRANS(IPOT) # ( V(IPOT,1) * THETA(IPOT,2)
520
                       -V(IPOT,2) * THETA(IPOT,1) ) / DENOM
530 NEXT IPOT
540 RETURN
550 REM
560 REM
570 REM
580 REM
590 REM
600. REM *****
610 REM * DRAW *
620 REM *****
630 REM INITIALIZE BUFFER INDEX
640 I
650 REM SAMPLE A/D CONVERTER. GET V1, V2.
```



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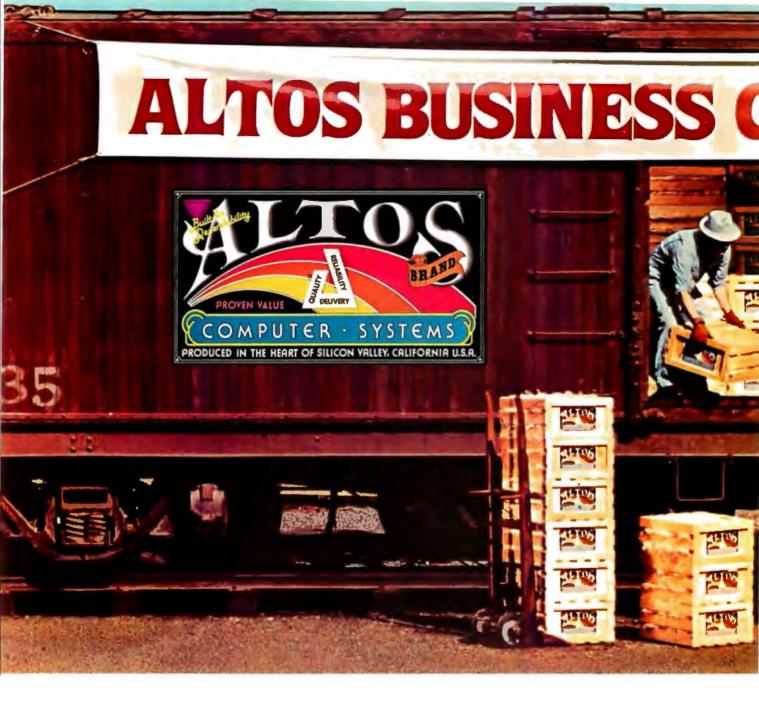
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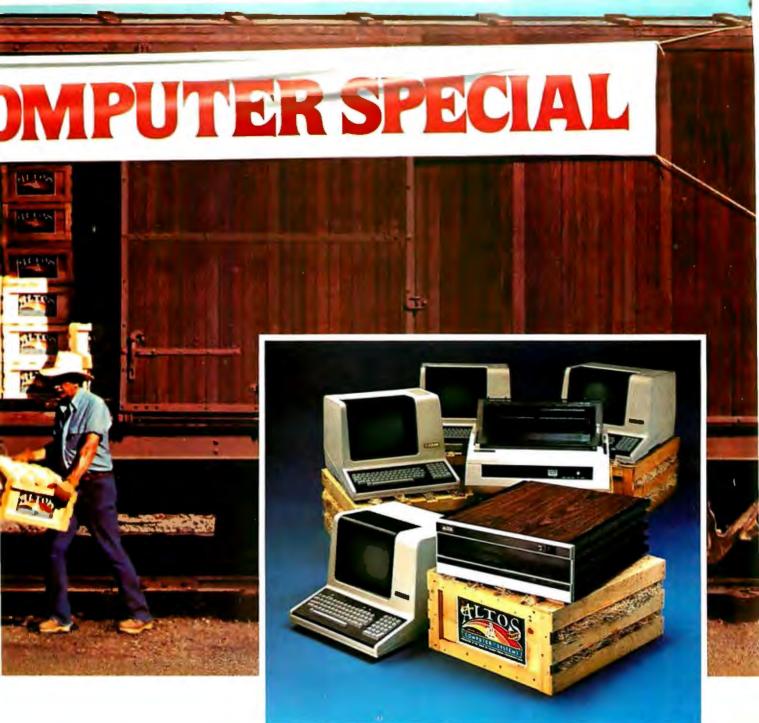




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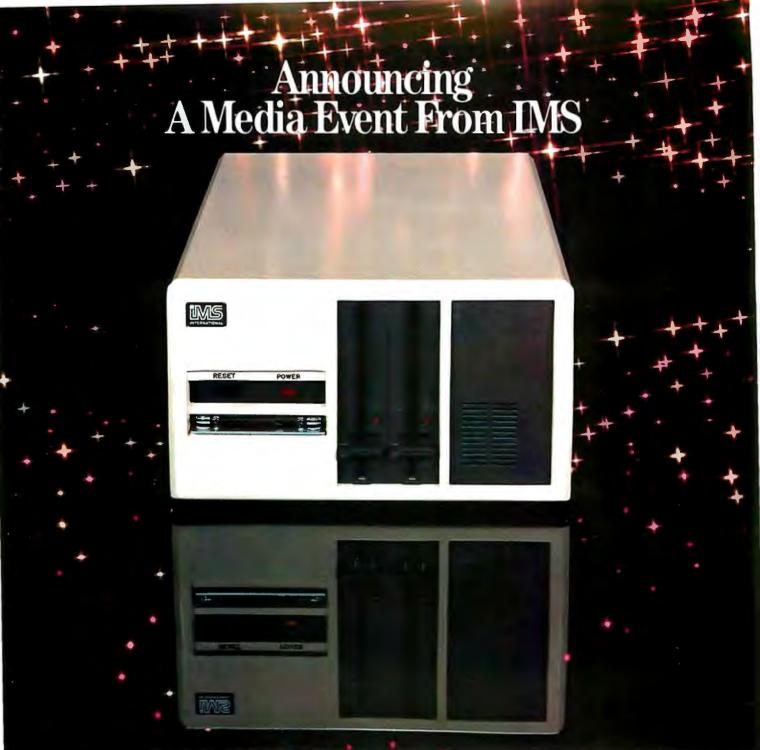


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```
660 GUSUB 1000
670 THETA1 = SCALE(1) \star V1 + TRANS(1)
680 THETA? = SCALE(2) \star V2 + TRANS(2)
690 PHI
          = ( PI(1.0) = THETA2 ) / 2.0 + THETA1
700 H
           = 2.0 + R + SIN( THETA2 / 2.0 )
710 I
           = I + 1
720 IF I>200 THEN DO
       PRINT "***** BUFFER FULL *****
730
740
       RETURN
750 DOEND
           * H * COS(PHI)
760 X(I)
770 Y(I)
           = H * SIN(PHI)
780 REM CHECK IF KEY HAS BEEN STRUCK. GO TO SUBROUTINE "DONE".
790 GOSUB 2000
800 IF DONE=0 GOTO 660
807 REM
810 RETURN
820 REM
830 REM
840 REM
850 REM
860 REM
1000 REM *****
1010 REM * A/D *
1020 REM *****
1030 REM THIS ROUTINE IS COMPUTER DEPENDENT AND MUST BE WRITTEN
1040 REM BY THE PROGRAMMER. EACH TIME IT IS CALLED IT SHOULD SAMPLE
1050 REM BOTH POTS, GIVING V1 AND V2. 2 TO 5 PAIRS PER SECOND IS AN
1060 REM APPROPRIATE SAMPLING RATE.
1070 REM *
1080 REM *
1090 REM *
1100 REM *
1110 REM *
1120 REM V1
1130 REM V2
1140 RETURN
1150 REM
1160 REM
1170 REM
1180 REM
1190 REM
2000 REM *****
2010 REM * DONE *
2020 REM ******
2030 REM THIS SUBROUTINE IS USED TO TERMINATE THE COLLECTION OF DATA.
2040 REM IT CHECKS IF THE USER HAS STRUCK A KEY WHICH INDICATES THE
2050 REM END OF COLLECTION.
2060 REM IF DONE = 0 THEN CONTINUE SAMPLING.
2070 REM IF DONE NOT = 0 THEN STOP SAMPLING.
2080 REM THIS ROUTINE MUST BE SUPPLIED BY THE PROGRAMMER.
2090 REM *
2100 REM *
2110 REM *
2120 REM *
2130 REM DONE
2140 RETURN
2150 STUP
```



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Photo 3: The finished acrylic-on-canvas work.

Text continued from page 78:

to be repeated unless the geometry or batteries are changed.

The Draw option collects and digitizes the voltages from the potentiometers as the artist draws a figure. A sampling rate of four points per second (a point consisting of two samples, V_1 and V_2) was found experimentally to be an appropriate rate for the A/D converter. The voltages are converted to the coordinates X, Y. The program continues in a loop, collecting data until one of two events occurs: the user strikes the return key (the program branches out of the loop through the subroutine DONE, which reads the key) or the buffer is full (the program branches out).

Remember that pivot 2, analogous to the human elbow, should not be extended beyond 180 degrees; to do so will cause erroneous results. However, this limitation will not cause any restriction in drawing.

The program in listing 1 is an example of how to program the graphics tablet; it is up to the programmer to decide how to use the coordinates. Most likely he will display them on the video terminal.

Results

Figure 3 shows a typical drawing produced using the graphics tablet. Enrique Castro-Cid drew the original figure by hand and then digitized the coordinates using the graphics tablet. Once the points were stored in the computer, the drawing was transformed using the mathematical function (Z+i/Z). The new coordinates were plotted on a Tektronix 4001 graphic terminal. The completed acrylic-on-canvas work is shown in photo 3.

We have found the graphics tablet has eliminated the bottleneck of digitizing our drawings. The system has developed into a good man-machine partnership, allowing each to do what it does best.



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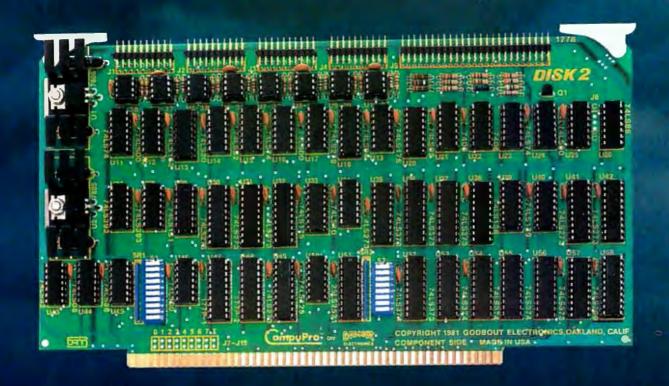
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The Atari Tutorial

Part 6: Atari BASIC

Lane Winner Atari Inc. 1265 Borregas Ave. POB 427 Sunnyvale, CA 94086

Atari BASIC is like other BASIC languages in that it is interpreted, which means that programs can be run when they are entered without intermediate stages of compilation and linking. The Atari BASIC interpreter resides in an 8K-byte ROM (readonly memory) cartridge in the left slot of the computer. It encompasses addresses A000 through BFFF hexadecimal. You must have at least 8K bytes of RAM (random-access read/write memory) to use Atari BASIC.

Strengths and Weaknesses

To use Atari BASIC effectively, you must know its strengths and weaknesses. With this information, programs can be written that make good use of its assets and features.

The strengths of Atari BASIC are:

•It supports the operating system graphics. Simple BASIC statements

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can be used to display graphics information on the screen.

- •It supports the hardware. BASIC statements such as SOUND, STICK, and PADDLE are simple interfaces to the hardware of the computer.
- •It has a simple interface to assembly-language routines through the USR function.
- The BASIC interpreter is in ROM. This prevents accidental modification of the interpreter by the user pro-
- It supports the Atari disk operating system (DOS). Specialized calls such as NOTE and POINT (in DOS 2.0S) allow the user to randomly access a disk through the disk operating
- It offers peripheral support. Any peripheral recognized by the operating system can be accessed from a BASIC program.

The weaknesses of Atari BASIC

• It gives no support of integers. All numbers are stored as 6-byte binarycoded-decimal (BCD) floating-point numbers.

- Mathematical operations are slow. Since all numbers are 6 bytes long, math operations become rather slow.
- It does not allow string arrays. Only one-dimensional strings can be created.

How Atari BASIC Works

The workings of the BASIC interpreter are summarized as follows:

- 1. BASIC gets a line of input from the user and converts it into a tokenized form.
- 2. It then puts this line into a token program.
- 3. This program is then available for execution.

The details of these operations are discussed in the following four sections:

- The Tokenizing Process
- The Token File Structure
- The Program Execution Process
- System Interaction

The Tokenizing Process

In simple terms, the tokenization of

a line of code in BASIC looks like this:

- 1. BASIC gets a line of input.
- 2. It then checks for legal syntax.
- 3. During syntax checking, the line is tokenized.
- 4. The tokenized line is moved into the token program.
- If the line is in immediate mode, it is executed.

To better understand the tokenizing process, some terms must first be defined:

Token—An 8-bit byte containing a value that corresponds to a BASIC keyword or element of syntax.

Statement—A complete "sentence" of tokens that causes BASIC to perform a meaningful task. When listed on the same line, statements are separated by colons.

Line—One or more statements preceded either by a line number in the range of 0 to 32,767, or an

immediate-mode line with no line number.

Command—The first executable token of a statement that tells BASIC to interpret the tokens that follow in a particular way.

Variable—A token that is an indirect pointer to its actual value; this is done so that the value can be changed without changing the token.

Constant—A 6-byte BCD value preceded by a special token. This value remains unchanged throughout program execution.

Operator—Any one of 46 tokens that in some way move or modify the values that follow them.

Function—A token that returns a value to the program when executed.

EOL—An end-of-line character that has the value 9B hexadecimal.

BCD—Binary-coded decimal. This refers to a number that uses the 6502 microprocessor's decimal mode.

BASIC begins the tokenizing process by getting a line of input. This input will be obtained from one of the handlers of the operating system. Normally, it is from the screen editor: however, with the ENTER command (which merges new program lines with an existing program), any device can be specified. The call BASIC issues is a GET RECORD command, and the data returned are ATASCII information terminated by an EOL. (ATASCII is a modified ASCII code used to represent characters and symbols within the Atari computers.) These data are stored by a part of the Atari operating system called the central I/O utility (CIO) into the BASIC input line buffer from locations 580 to 5FF hexadecimal.

After the record is returned, the syntax-checking and tokenizing processes begin. First, BASIC looks for a line number. If one is found, it is converted into a 2-byte integer. If no line number is present, the computer is assumed to be in immediate mode and the line number 8000 hexadecimal is assigned to it. These are the first two tokens of the tokenized line. This line is built in the token output buffer, which is 256 bytes long, and resides at the end of the reserved operating system RAM.

The next token is a dummy byte reserved for the byte count (or offset) from the start of this line to the start of the next line. Following this is another dummy byte for the count of the start of this line to the start of the next statement. These values are set when tokenization is complete for the line and the statement, respectively. The use of these values is discussed later in the program execution process section.

BASIC now looks for the command of the first statement of the input line. A check is made to determine if this is a valid command by scanning a list of legal commands in ROM. If a match is found, the next byte in the token line becomes the number of the entry in the ROM list that matched.

If at any time an error is found, a syntax error token is assigned to that byte and BASIC stops tokenizing,



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ATASCII format to the token output buffer, and prints the error line.

Assuming a good line, one of seven items can follow the command: a variable, a constant, an operator, a function, a double quote, another statement, or an EOL. BASIC tests to see if the next input character is numeric. If not, it compares that character and those following against the entries of the variable name table. If this is the first line of code entered in the program, no match will be found. The characters are then compared against the function and operator tables. If no match is found there. BASIC assumes that this is a new variable name. Since this is the first variable, it will be assigned the first entry in the variable name table. The characters are copied out of the input buffer and stored into the name table with the most significant bit (MSB) set to a logical 1 on the last byte of the name. Eight bytes are then reserved in the variable value table for this entry. (See the discussion of

copies the rest of the input buffer in the variable value table in the next section.)

> The token that ends up in the tokenized line is the variable number minus one with the MSB set. Thus. the token of the first variable entered would be hexadecimal 80, the second would be hexadecimal 81, and so on up to hexadecimal FF, for a total of 128 unique variable numbers.

> If a function is found, its entry number in the operator function table is assigned to the token. Functions require certain sequences of parameters; these are contained in syntax tables. If they are not matched, a syntax error will result.

> If an operator is found, a token is given its table entry number. Since operators can follow each other in a rather complex fashion (such as multiple parentheses), the syntax checking of them is a bit complicated.

> In the case of the double quotes. BASIC assumes that a character string is following, assigns a hexadecimal OF to the output token, and reserves a dummy byte for the string

length. The characters are moved from the input buffer into the output buffer until the second set of quotes is found. The string-length byte is then set to the character count.

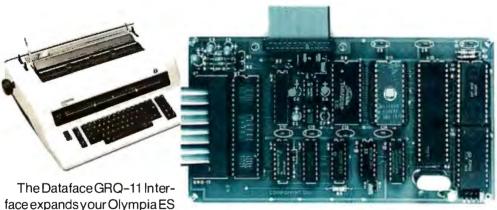
If the next characters in the input buffer are numeric, BASIC converts them into a 6-byte BCD constant. A hexadecimal OE token is put in the output buffer, followed by the 6-byte

When a colon is encountered, a hexadecimal 14 token is inserted in the output buffer, and the offset from the start of the line is stored in the dummy byte that was reserved for the count to the start of the next statement. At this point, another dummy byte is reserved and the process goes back to get a command.

When the EOL is found, a hexadecimal 16 token is stored and the offset from the start of the line is put in the dummy byte for the line offset. At this point, tokenization is complete and BASIC moves the token line into the token program. First, it searches the program for that line number. If

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the same number is found, the computer replaces the old line with the new one. If it is not found, the computer inserts the new line in the correct numerical sequence. In both cases, the data following the line are moved either up or down in memory to allow for an expanding and contracting program size.

BASIC now checks to see if the tokenized line is an immediate-mode line. If so, that line is executed according to the methods described in the interpretive process; if not, BASIC goes back to get another line of input.

If at any time during the tokenizing process the length of the token line exceeds 256 bytes, an Error 14 message (line too long) is sent to the screen and BASIC goes back to get the next line of input.

An example line of input and its token form are shown in figure 1. Table 1 shows the token values for Atari BASIC.

The Token File Structure

The token file contains two major segments: a group of zero-page pointers that point into the token file, and the actual token file itself. The zero-page pointers are 2-byte values that point to various sections of the token file. There are nine 2-byte pointers in locations 80 to 91 hexadecimal. The textbox on page 112 gives a list of the pointers and the sections of the token file they reference.

The Program Execution Process

Executing a line of code involves reading the tokens created during the

tokenization process. Each token has a particular meaning that causes BASIC to execute a specific series of operations. The method of doing this requires BASIC to get one token at a time from the token program and process it. Since the token is an index into a jump table of routines, a PRINT token points indirectly to a PRINT processing routine. When that processing is complete, BASIC returns to get the next token. The pointer used to fetch each token is called STMCUR and is at locations 8A and 8B hexadecimal.

The first line of code executed in a program is the immediate-mode line. This is usually a RUN or GOTO. In the case of the RUN, BASIC gets the first line of tokens from the statement table (tokenized program) and processes it. If all the code is in-line. BASIC merely executes consecutive lines.

If a GOTO is encountered, the line to go to must be found. The statement table contains a partially linked list of line numbers and statements. The lowest line number is first. followed by increasing line numbers up to the largest. If a line somewhere in the middle of the table is needed. the following process occurs.

The address of the first line is found in the STMTAB pointer at hexadecimal 88 and 89. This is stored in a temporary pointer. The first 2 bytes of the first line are its line number. This number is compared to the requested line number. If the first number is less, BASIC gets the next line by adding the third byte of the first line to the temporary pointer.

10 LET X=1: PRINT X

THE LINE: 1

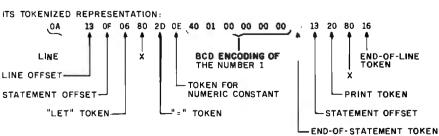


Figure 1: A line of Atari BASIC in tokenized form. The tokenized form of the line is the one stored in memory.

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(a) Commands				(b) Operators	Ue	(c) Functions	3	
Hexa- decimal	Decimal	Meaning	Hexa- decimal	Decimal	Meaning	Hexa- decimal	Decimal	Meaning
00 01 02 03 04 05 06 07 08 09 08 09 00 00 00 01 11 12 13 14 15 16 17 18 19 10 11 11 11 11 11 11 11 11 11 11 11 11	0 1 2 3 4 5 6 7 8 9 10 1 12 13 14 5 6 17 8 9 10 1 12 13 14 5 6 17 8 9 10 1 12 13 14 5 6 17 8 9 10 1 12 13 14 5 6 17 8 9 10 1 12 13 14 5 6 17 8 9 10 1 12 13 14 5 6 17 8 9 10 1 12 13 14 15 6 17 8 9 10 11 12 13 14 15 15 15 15 15 15 15 15 15 15 15 15 15	REM DATA INDUCT LIST ER LET FOR NEXT O O SUB TRAP BYE COM SAVE STATE STOR NEW STATE NO POR SAVE STATE STOR RESTOR RESTOR RESTOR RESTOR RESTOR RESTOR RESTOR RUN STOP POR SETCATE SOUNT CSAVE STATE STOP POR SETCATE SOUNT CSAVE STATE SOUNT CSAVE SOUNT STATE SOUNT STAT	0E 0F 10 11 12 13 14 15 16 17 18 19 14 11 15 16 17 18 19 11 11 11 11 11 11 11 11 11 11 11 11	14 15 16 17 18 19 21 22 32 42 56 27 28 29 30 31 32 33 34 40 41 42 43 44 45 55 55 55 55 56 57 58 59 60	[numeric constant] [string constant] [not used] [not used] \$: [statement end] [line end] GOTO GOSUB TO STEP THEN # <=	3D 3E 3F 401 423 444 456 47 489 44B 44 455 555 555 555 566 767 767 767 767 767 7	61 62 63 64 65 66 67 68 69 70 71 73 74 75 77 80 81 82 83 84	STR\$ CHR\$ USR ASC VAL ASC VAL ATOS FRA COS FRA COS FRA COS FRA COS FRA FRA COS FRA

Table 1: A table of token values for Atari BASIC. Table 1a shows the interpretation of a given value as a BASIC command token. Table 1b shows the interpretation of a value as a BASIC operator token. Table 1c shows the interpretation of a value as a BASIC function token. The interpretation of a token value varies with its position in the line.

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Prime 550	Mainframe	PRIMOS	BASIC V16.4 (I)	63
Digital Equipment PDP-10	Mainf rame	TOPS-10	BASIC (I)	65
IBM System 34	Mainframe	Release 05	BASIC (I)	129
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Seattle Computer System 2	Micro	MS-DOS	Microsoft BASIC (I)	310
Alpha Micro AM-100/T	Micro	AMOS 4.3a	Alpha BASIC (SC)	317
Digital Equipment PDP 11/45	Mini	n/a	BASIC (I)	330
Data General NOVA 3	Mini	Time Share	BASIC 5.32	517
Ohio Scientific C4-P	Micro	OS65D 3.2	Level 1 BASIC (I)	680
North Star Floating Point	Micro	NSDOS	NorthStar BASIC (I)	685
Radio Shack TRS-80 II	Micro	TRSDOS 1.2	BASIC (I)	792
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Cromemco System 3	Micro	CDOS	32K BASIC (Ì)	1074
Commodore Pet 2001	Micro	n/a	Microsoft BASIC (I)	1374
IBM 5100	Micro	n/a	BASIC (I)	1951
Vector MZ	Micro	n/a	Micropolis BASIC (I)	2251

^{*} C = Compiler; I = Interpreter. Times (except for Seattle Computer) taken from August 1981 issue of Interface Age.

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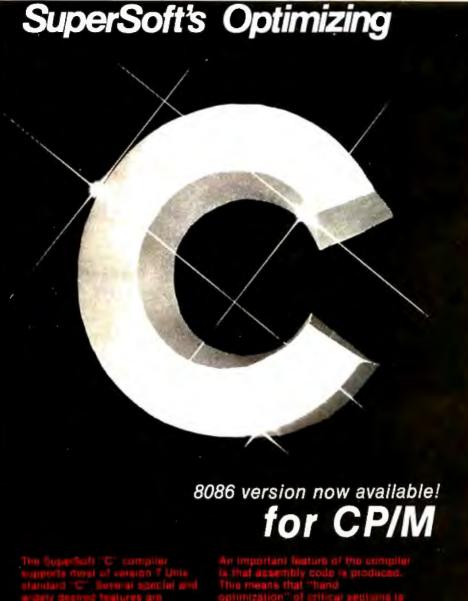


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The temporary pointer will be pointing to the second line. Again, the first 2 bytes of this new line are compared to the requested line. If they are less, the third byte is added to the pointer. If a line number does match, the contents of the temporary pointer are moved into STMCUR and BASIC fetches the next token from the new line. Should the requested line number not be found, an Error 12 (line not found) is generated.

The GOSUB involves more processing than the GOTO. The linefinding routine is the same, but before BASIC goes to that line, it sets up an entry in the run-time stack. It allocates 4 bytes at the end of the stack and stores a 0 in the first byte to indicate a GOSUB stack entry. It then stores the line number it was on when the call was made into the next 2 bytes of the stack. The final byte contains the offset in bytes from the start of that line to where the GOSUB token was found. BASIC then executes the line it looked up. When the RETURN is found, the entry on the stack is pulled off, and BASIC returns to the calling line.

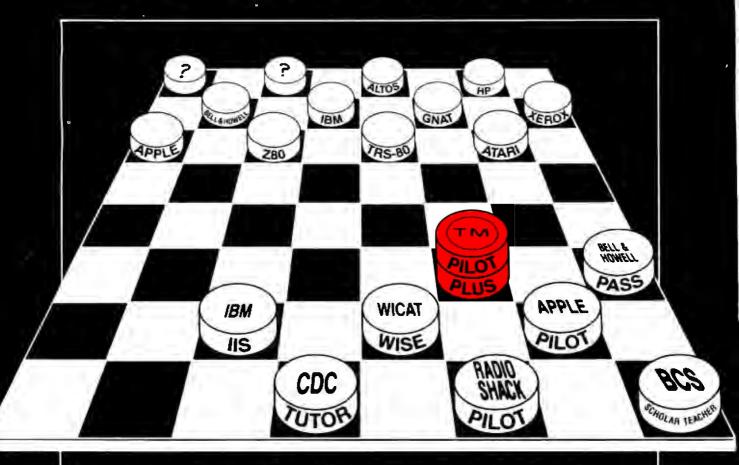
The FOR command causes BASIC to allocate 16 bytes on the run-time stack. The first 6 bytes are the limit the variable can reach in 6-byte BCD format. The second 6 bytes are the step, in the same format. Following these, BASIC stores the variable number (MSB set) of the counting variable. It then stores the present line number (2 bytes) and the offset into the line. The rest of the line is then executed.

When BASIC finds the NEXT command, it looks at the last entry on the stack. It makes sure that the variable referenced by the NEXT is the same as the one on the stack and checks if the counter has reached or exceeded the limit. If not, BASIC returns to the line with the FOR statement and continues execution. If the limit was reached, the FOR entry is pulled off the stack and execution continues from that point.

When an expression is evaluated, the operators are put onto an operator stack and then pulled off one at a time and evaluated. The

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BASIC Command Operating System IOCB Parameters

OPEN #1.12.0."E:" IOCB = 1

Command = 3 (OPEN) Aux1 = 12 (Input/Output)

Aux2 = 0

Buffer Address = ADR("E:")

GET #1.X IOCR = 1

Command = 7 (Get Characters)

Buffer Length = 0

Character returned in accumulator

PUT #1.X IOCB = 1

Command = 11 (Put Characters)

Buffer Length = 0

Character output through accumulator

INPUT #1.A\$

Command = 5 (Get Record)

Buffer Length = Length of A\$ (not over 256)

Buffer Address = Input Line Buffer

PRINT #1, A\$

BASIC uses a special put byte vector in the

IOCB to talk directly to the handler.

XIO 18,#6,12,0,"S:" IOCB = 6

Command = 18 (Special—Fill)

Aux1 = 12Aux2 = 0

Table 2: Examples of BASIC 1/O commands and the corresponding parameters that are passed to the operating system IOCBs (input/output control blocks).



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order in which the operators are put onto the stack can either be implied, in which case BASIC looks up the operator's precedence from a ROM table, or the order can be explicitly stated by the placement of parentheses.

Pressing the BREAK key at any time causes the operating system to set a flag to indicate this occurrence. BASIC checks this flag after each token is processed. If it finds it has been set, it stores the line number at which this occurred, prints a "STOPPED AT LINE XXXX" message, clears the BREAK flag, and waits for user input. At this point, the user could type CONT and program execution would continue at the next line.

System Interaction

BASIC communicates with the operating system primarily through the use of I/O calls to the central I/O utility. Table 2 gives a list of user BASIC calls and the corresponding operating system IOCBs. (IOCB stands for "input/output control block." An IOCB is a table of information used to control information flow between the computer and either a disk file or I/O device.)

When a BASIC token program is SAVEd or CSAVEd to a device, two blocks of information are written. The first block consists of seven of the nine zero-page pointers that BASIC uses to maintain the token file. These are LOMEM through STARP (see textbox). One change is made to these pointers when they are written out: the value of LOMEM is subtracted from each of the 2-byte pointers, and these new values are written to the device. Thus, the first 2 bytes written are 0.0.

The second block of information written consists of the following token file sections: the variable name table, the variable value table, the token program, and the immediatemode line.

When this program is LOADed or CLOADed into memory, BASIC looks at the operating system variable MEMLO and adds its value to each of the 2-byte zero-page pointers as they are read from the device. These

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pointers are placed back on page zero. The values of RUNSTK and MEMTOP are then set to the value in STARP. (See figure 2 for the locations of these and other pointers.)

Next, 256 bytes are reserved in memory above the value of MEMLO to allocate space for the token output buffer. Then, the token file information, consisting of the variable name table through the immediate-mode line, is read in. These data are placed in memory immediately following the token output buffer.

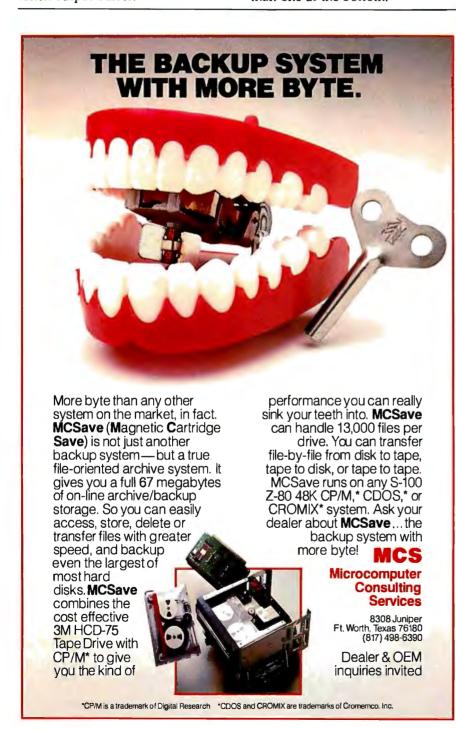
Improving Program Performance

Program performance can be improved in two ways. First, the execution time can be decreased (it will run faster); second, the amount of space required can be decreased, allowing it to use less RAM. To attain these two goals, the following lists can be used as guidelines. The methods of improvement in each list are primarily arranged in order of decreasing effectiveness. Therefore, the method at the top of a list will have more impact than one at the bottom.

The following methods will help speed up a BASIC program:

- •Recode—Because BASIC is not a structured language, the code written in it tends to be inefficient. After many revisions, it becomes even worse. Thus, the time spent to restructure the code is worthwhile.
- Check algorithm logic—Make sure that the code to execute a process is as efficient as possible.
- •Put frequently called subroutines and FOR/NEXT loops at the start of the program—Since BASIC starts at the beginning of a program to look for a line number, any line references near the end take longer to reach.
- •For frequently called operations within a loop, use in-line code rather than subroutines—The program speed can be improved here since BASIC spends time adding and removing entries from the run-time stack.
- Make the most frequently changing loop of a nested set the deepest—In this way, the run-time stack will be altered the fewest number of times
- •Simplify floating-point calculations within the loop—If a result is obtained by multiplying a constant by a counter, time can be saved by changing the operation to the addition of a constant
- Set up loops as multiple statements on one line—In this way, the BASIC interpreter will not have to get the next line to continue the loop.
- •Disable the screen display—If visual information is not important for a period of time, up to a 30-percent time savings can be made with a POKE 559,0. Save the previous value in location 559 so you can later restore the video output.
- •Use a coarser graphics mode or a short display list—If a full screen display is not necessary, up to a 25-percent time savings can be made by causing the computer to spend less time on video display.
- Use assembly code—Time savings can be made by encoding loops in assembly language and using the USR function.

The following methods will help save space in a BASIC program:



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- Recode—As mentioned previously. restructuring the program makes it more efficient. It also saves space.
- Remove remarks—Remarks are stored as ATASCII data and merely take up space in the running program.
- Replace a constant used three times or more with a variable-BASIC allocates 7 bytes for a constant, but only 1 for a variable reference. Therefore, 6 bytes can be saved each time a constant is replaced with a variable assigned to that constant's value.
- Initialize variables with a READ statement-A data statement is stored in ATASCII code, 1 byte per character, whereas an assignment statement requires 7 bytes for one constant.
- Try to convert numbers used only

- once and twice to arithmetic combinations of predefined variables—An example is to define Z1 to equal 1 and Z2 to equal 2; if the number 3 is required, replace it with the expression Z1 + Z2.
- Set frequently used line numbers (in GOSUB and GOTO) to predefined variables—If the line 100 is used in 50 different places, approximately 300 bytes can be saved by equating Z100 to 100 and referencing Z100.
- Keep the number of variables to a minimum—Each new variable entry requires 8 more bytes in the variable value table and a few bytes for its
- •Clean up the value and name tables—Because the variable value and name tables are normally saved with the BASIC program, variable entries continue to take up space even

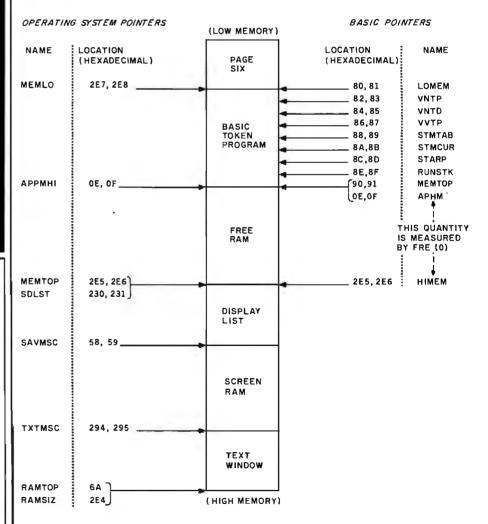


Figure 2: A list of pointers used by BASIC and the Atari operating system to keep track of memory usage. These pointers are described in greater detail in the operating system section of the Atari Personal Computer System Operating System User's Manual and Hardware Manual.

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after all references to them are removed from the program. To delete the entries, LIST the program to disk or cassette, type NEW, and ENTER the program. (Unlike SAVE or CSAVE, LIST stores the program as a file of characters and ENTER reads the program in as if it had been typed in from the keyboard.)

- •Keep variable names as short as possible—Each variable name is stored in the name table as ATASCII information. The shorter the names. the shorter the table.
- Replace text used repeatedly with strings—On screens with a lot of text, space can be saved by assigning a string to a commonly used set of characters.
- Initialize strings with assignment statements-An assignment of a string with data in quotes requires less space than a READ statement and a CHR\$ function.
- Concatenate lines into multiple statements—Three bytes can be saved each time two lines are converted into two statements on one line.
- Replace once-used subroutines with in-line code-The GOSUB and RE-TURN statements waste bytes if used only once.
- Replace integer numeric arrays with strings if the data values fall between 0 and 255 (or if the data can be scaled to that range)—Numeric array entries require 6 bytes each. However, each number can be reduced to one character by using the CHR\$ function: it can later be restored with the ASC function.
- Replace SETCOLOR statements with POKE commands—This saves 8 bytes per occurrence.
- Use cursor-control characters rather than POSITION statements-The POSITION statement requires 15 bytes for the x and y parameters, whereas the cursor-editing characters are 1 byte each.
- Delete lines of code via program control-See the next section on advanced programming techniques.
- Modify the string/array pointer to load predefined data—SAVE and CSAVE save the part of the token file from VNTP up to STARP. By changing the value in STARP to point to

the end of the data, string and array information can be saved.

• Small assembly-language routines can be stored in USR calls—An example would be:

X = USR(ADR("hhh | LV | d | "),16)

(The boxes represent inverse video characters.) Eight bytes are saved by not placing the string in a named string variable.

•Chain programs—An example would be an initialization routine that is run first, then loads and runs the main program.

Advanced Applications

An understanding of the fundamentals of Atari BASIC makes it possible to write some interesting applications. These can be strictly BASIC operations, or they can also involve features of the operating system. The following paragraphs give examples of three such techniques.

String initialization—The program in listing 1 sets all the bytes of a string of any length to the same value. BASIC copies the first byte of the Text continued on page 118

Listing 1: Quick string manipulation using the Atari BASIC substring function. This program will initialize every character of the string A\$ to the value "A".

- 10 REM STRING INITIALIZATION
- 20 DIM A\$(1000)
- 30 A\$(1)="A":A\$(1000)="A"
- 40 A(2) = A

Listing 2: Modification of an Atari BASIC program under program control. By using a special "forced read" mode, information on the screen can be automatically read into BASIC without user intervention. In this program, this ability is used to delete lines 70 through 90 while the program is being run.

- 10 REM DELETE LINE EXAMPLE
- 20 GRAPHICS 0:POSITION 2,4
- 30 ? 70:? 80:? 90:? "CONT"
- 40 POSITION 2,0
- 50 POKE 842,13:STOP
- 60 POKE 842,12
- 70 REM THESE LINES
- 80 REM WILL BE
- 90 REM DELETED

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Atari BASIC Zero-Page Pointers

Pointer

Location

Name Part of Token File Pointed To (hex)

LOMEM 80,81 Token output buffer—The buffer BASIC uses to tokenize one line of code. It is 256 bytes long and resides at the end

of the operating system's allocated RAM.

VNTP 82,83 Variable name table—A list of all the variable names that

> have been entered in the program. They are stored as ATASCII characters, each new name stored in the order it was entered. Three types of name entries exist:

> 1. Scalar variables-MSB (most significant bit) set on last character in name.

> 2. String variables—last character is a "\$" with the MSB

3. Array variables—last character is a "(" with the MSB

VNTD 84,85 Dummy end of the variable name table—BASIC uses this pointer to indicate the end of the name table. When there are less than 128 variables, this normally points to a dummy zero byte. When 128 variables are present, this points to the last byte of the last variable name.

VVTP 86,87 Variable value table—This table contains current information on each variable. For each variable in the name table. 8 bytes are reserved in the value table. The information for each variable type is:

Byte Number	1	2	3 4	5 6	7 8
Scalar	00	Var#	6-1	yte BCD consta	ınt
Array (explicitly dimensioned) (undimensioned)	41 40	Var#	Offset from STARP(8C,8D)	first DIM + 1	second DIM + 1
String (explicitly dimensioned) (undimensioned)	81 80	Var#	Offset from STARP(8C,8D)	Length	DIM

A scalar variable contains a numeric value. An example is X=1. The scalar is X and its value is 1, stored in 6-byte BCD format. An array is composed of numeric elements stored in the string/array area and has one entry in the value table. A string, composed of character elements in the string/array area, also has one entry in the table.

The first byte of each value entry indicates the type of variable: 00 for a scalar, 40 for an array, and 80 for a string. If the array or string has been dimensioned, the least significant bit (LSB) is set on the first byte.

The second byte contains the variable number. The first variable entry is number zero. If 128 variables were present, the last would be hexadecimal 7F.

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In the case of the scalar variable, the third through eighth bytes contain the 6-byte BCD number that has currently been assigned to it.

For arrays and strings, the third and fourth bytes contain an offset from the start of the string/array area (described below) to the beginning of the data.

The fifth and sixth bytes of an array contain its first dimension. The quantity is a 16-bit integer, and its value is 1 greater than the limit the user entered. The seventh and eighth bytes are the second dimension, also a value of 1 greater.

The fifth and sixth bytes of a string are a 16-bit integer that contains its current length. The seventh and eighth bytes are its dimension (up to 32,767 bytes in size).

STMTAB 88,89 Statement table—This block of data includes all the lines of code entered by the user and tokenized by BASIC. It also includes the immediate-mode line. The format of these lines is described in figure 1.

STMCUR 8A,8B Current statement-This pointer is used by BASIC to reference particular tokens within a line of the statement table. When BASIC is waiting for input, this pointer is set to the beginning of the immediate-mode line.

STARP 8C.8D

String/Array area—This block contains all the string and array data. String characters are stored as 1-byte ATASCII entries. Therefore, a string of 20 characters will require 20 bytes. Arrays are stored with 6-byte BCD numbers for each element. A 10-element array requires 60 bytes.

This area is allocated and subsequently enlarged by each dimension statement encountered, the amount being equal to the size of a string dimension or six times the size of an array dimension.

RUNSTK 8E,8F

Run-time stack—This software stack contains GOSUB and FOR/NEXT entries. The GOSUB entry consists of 4 bytes. The first is a 0 byte indicating GOSUB, followed by the 2-byte integer line number on which the call occurred. This is followed by the offset into that line so that the RETURN can come back and execute the next statement.

The FOR/NEXT entry contains 16 bytes. The first is the limit the counter variable can reach. The second byte is the step or counter increment. Each of these quantities is in 6-byte BCD format. The thirteenth byte is the counter variable number with the MSB set. The fourteenth and fifteenth bytes are the line number; the sixteenth is the line offset to the FOR statement.

MEMTOP 90.91

Top of application RAM—This is the end of the user program. Program expansion can occur from this point to the end of free RAM, which is defined by the start of the display list. The FRE function in BASIC returns the amount of free RAM by subtracting MEMTOP from HIMEM (pointed to by locations hexadecimal 2E5 and 2E6). Note that the BASIC MEMTOP is not the same as the OS variable called MEMTOP.

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Expiration Date_ Authorized Signature Listing 3: Quick manipulation of a graphics player within Atari BASIC. By setting a string variable to point to the 512-byte area reserved for a player and manipulating that string, a player can be moved around the screen faster than is otherwise possible in BASIC. This program creates a small rectangle that glides across the video screen, changing direction when it nears the boundary of the video display.

100 REM PLAYER/MISSILE EXAMPLE

110 DIM A\$(512),B\$(20)

120 X = X + 1:READ A:IF A < > -1 THEN B\$(X,X) = CHR\$(A):GOTO 120

130 DATA 0,255,129,129,129,129,129,129,129,129,255,0, -1

140 REM B\$ CONTAINS PATTERN FOR PLAYER SHAPED LIKE SMALL BOX

2000 POKE 559,62:POKE 704,88

2020 I = PEEK(106) - 16:POKE 54279,I

2030 POKE 53277,3:POKE 710,224

2040 VTAB = PEEK(134) + PEEK(135) + 256:REM VALUE OF VVTP POINTER

2050 ATAB = PEEK(140) + PEEK(141) + 256:REM VALUE OF STARP POINTER

 $2060 \text{ OFFS} = I \cdot 256 + 1024 - ATAB$

2070 HI = INT(OFFS/256):LO = OFFS - HI + 256

2090 POKE VTAB+2,LO:POKE VTAB+3,HI:REM A\$ POINTS TO P/M AREA

3000 Y = 60:Z = 100:V = 1:H = 1

4000 A\$(Y,Y+11) = B\$:POKE 53248,Z:REM VERT AND HORIZ POSITION CHANGED

4010 Y = Y + V:Z = Z + H

4020 IF Y > 213 OR Y < 33 THEN V = -V

4030 IF Z > 206 OR Z < 49 THEN H = -H

4420 GOTO 4000

Text continued from page 110:

source string into the first byte of the destination string, then the second, third, and so on. By making the destination string the second byte of the source (A\$(2) refers to the substring of A\$ from its second through its last character), the same character can be stored throughout the entire string.

Delete lines of code—By using a feature of the operating system, a program such as listing 2 can delete or modify lines of code within itself. The screen editor can be set to accept data from the screen without user input. The POKE in line 50 causes the Atari screen editor device to do a "forced read" of the information on the screen, while the POKE in line 60 restores control of the computer to the keyboard. (For more information, see the section on the screen editor within the "I/O Subsystem" chapter of the Atari Personal Computer System Operating System User's Manual and Hardware Manual.) Thus, by first setting up the screen, positioning the cursor to the top, and then stopping the program, BASIC gets the commands that have been printed on the screen.

Player/missile graphics with strings-A fast way to move player/missile graphics data is shown in listing 3. This program places a small box on the screen (a player) and causes it to bounce around the screen. A dimensioned string A\$ has its string/array area offset value changed to point to the player/missile graphics area. Writing to this string with an assignment statement now writes data into the player/missile area at assembly-language rates.

In particular, the first statement in line 4000 moves the player image in string B\$ up or down the vertical "strip" that the player occupies. The second statement changes the horizontal position of the "strip." When the box reaches the vertical limits of 33 or 213 (line 4020) or the horizontal limits of 49 or 206 (line 4030), the direction of the box movement is reversed.

Next Month

We will next take a look at the sound-generating capabilities of the Atari 400 and 800 computers.■

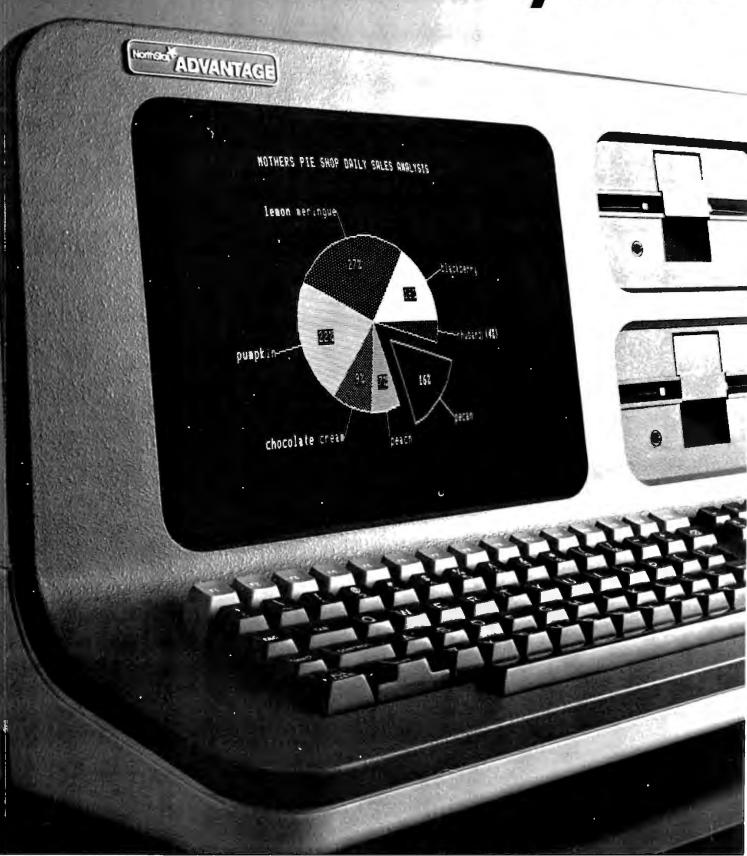
More detailed information on several of the subjects discussed here is contained in the Atari Personal Computer System Operating System User's Manual and Hardware Manual. This manual (part C016555) can be ordered for \$27 plus \$3 shipping and handling from Atari Customer Service, 1346 Bordeaux Dr., Sunnyvale, CA 94086. California residents must add 61/2% sales tax.

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The Input/Output Primer

Part 1: What Is I/O?

Steve Leibson Auto-trol Technology Corporation 12500 North Washington St. POB 33815 Denver, CO 80233

A modern computer can process incredible amounts of information or make thousands of decisions each second. Without communication to the outside world, however, the computer's work is of little use. Here's where input/output comes in; it links the computer to operators or processes that require its problemsolving powers.

Input/Output (I/O) is the term used to describe communication with the outside world. To describe the various means used to effect these communications, I'll start with the core of the system, the computer itself, then work outward toward the rest of the world.

A general-purpose computer has two main components: processor and memory. The processor, the system's engine, follows sequences of instructions that cause it to process data. Instructions and data are stored in memory for the processor's use.

Three sets of electrical lines, called buses, link the processor and memory: the address bus, the data bus, and the control bus. Computer memory is organized into thousands of locations, each with a unique address and the capability of storing one piece of data or one instruction in a

This article is the first in Steve Leibson's six-part series, The Input/Output Primer. The series will explain the way in which computers talk with the world. Upcoming articles will discuss interrupts and direct memory access; parallel and HPIB (GPIB) interfaces; BCD and serial interfaces; character codes; interrupts, buffers, grounds, and signal degradation. "An I/O Glossary," which follows this article, is a valuable reference for the entire series.

sequence. The processor differentiates between instructions and data.

The processor can access information in memory by placing the proper signals on the address bus. These signals represent an address that specifies the memory location of interest to the processor. The processor must also signify whether it wishes to extract information from the selected location (to read) or to place information in it (to write).

The advantage of memory-mapped I/O: existing processor Instructions serve the dual purpose of Interfacing to memory and to I/O devices.

This signaling is performed on the control bus, which also contains signal lines that synchronize the processor and memory. In read and write operations, information passes between memory and processor over a data bus.

Since data and instructions pass over the data bus, the processor must correctly interpret the information. The processor's internal timing cycles enable it to distinguish data from instructions. To obtain its next instruction, the processor performs an instruction fetch. Then the processor performs operations necessary to execute the instruction.

The location currently being accessed for instructions is held in a register or program counter within the processor. The instruction addressed by the program counter may cause the processor to access memory again, this time to obtain data or to place data in memory. Such operations result from execution of memory reference instructions.

We've now described all the computer operations needed to run a program: the computer can obtain instructions from memory, access memory for data, process data, and place processed data back into memory. Two questions now arise: how do the program and data get into the memory, and how does the operator obtain the results of the processing? The answer: through the input/output devices.

A complete computer system, such as a Hewlett-Packard desktop computer, is not composed of a processor and memory alone. Making a system requires adding peripheral devices such as a keyboard, display, printer, and magnetic tape unit. These peripheral devices connect the computer to the outside world. The keyboard, display, and printer allow communications with a human operator, while the tape storage device provides storage and retrieval of programs and data.

How are peripheral devices connected to the processor/memory combination inside the computer? Two methods are currently in use. The first places these devices on the memory bus already discussed; peripheral devices thus "appear" to the processor as memory locations. The processor can send data to, or obtain data from, the peripherals by using memory-reference instructions. This approach is called memorymapped I/O because it allocates some FEH FRIMITIME

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portion of computer memory space to peripheral devices. The Motorola 6800 and 68000 microprocessors use memory-mapped I/O.

The advantage of memory-mapped I/O is that existing processor instructions serve the dual purpose of interfacing to memory and to I/O devices. The disadvantage is that the full range of memory is not available for program and data storage. In other words, memory-mapped I/O reduces the computer's maximum memory size. For 8-bit microprocessors with only about 64,000 possible memory locations, this loss of available memory can be a real limitation.

The Intel 8080 and Zilog Z80 microprocessors use a slightly different scheme. I/O devices are connected to the processor by the memory data bus, but special I/O instructions and signals on the control bus are used for the I/O process. Full memory capacity is available to the processor because special I/O addressing is used. Though the I/O devices are on the memory bus, they are in I/O space rather than in memory space. Figure 1 illustrates how I/O devices are connected to processors on the memory bus.

The second method of implementing I/O in a computer is to create a totally new bus, the I/O bus, which resembles the memory bus. The I/O bus has an address bus (called the peripheral-address bus to differentiate it from the memory-address bus), a second set of data lines, and a peripheral-control bus. The signals on the I/O bus may or may not

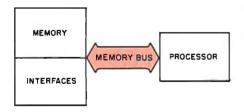


Figure 1: A computer system with memory-mapped I/O (input/output). The I/O interfaces communicate with the processor over its memory bus. As a result, the processor has less memory space available for its own use, but there's no need for I/O instructions in its instruction set.

resemble those of the memory bus. This system has the advantage of full memory capability but pays the price of creating a new set of instructions, called *I/O instructions*, and a second bus, the *I/O* bus. Figure 2 shows an *I/O* bus system.

Let's briefly discuss instructions before continuing. The memory-reference and I/O instructions belong to a class of instructions called *processor* or *machine* instructions. This class of instructions controls computer operations at the very lowest level. Each instruction can initiate only the simplest tasks, such as obtaining one piece of information from memory or dispatching one character to a peripheral device.

Programmers would face a tremendous task if they had to solve all problems by writing programs at this level of complexity. Therefore, the computer supplier usually provides a systems program or operating system which, in effect, provides a new set of instructions with far greater power. The new set of instructions is called a high-level language because the instructions, now referred to as statements, allow programming at a much higher level of complexity.

Digital Signals

We've briefly discussed the sets of lines called buses and have stated that the processor and other systems components send signals along these buses. Buses, of course, consist of metallic carriers upon which voltages may be impressed and currents made to flow.

The simplest signal that might travel along such a conductor is the presence or absence of voltage or current flow. This is a binary signal because it can assume only two states: present or absent. With a voltage-related signal, the voltage either is or isn't there: the voltage is either k volts or zero volts. Voltages

are measured with reference to a zero point, usually called *ground*, which is often a heavy conductor interconnecting all components in a computer system.

Binary signals are the primary means of communication in computer systems because the circuitry required to generate and detect mere presence or absence of a signal is much simpler to construct than circuits concerned with "how much" signal is present. Simplified circuitry allows construction of highly complex processors because binary circuits require much less space than other types. This is the key to construction of LSI (large-scale integrated) circuitry, which incorporates thousands of circuits on a small silicon chip.

Buses are simply sets of parallel conductors upon which binary signals can be impressed. The most common binary signal at present is the TTL level set. TTL (transistor-transistor logic) is a family of integrated circuits which constitute the building blocks for many of today's computers. These digital circuits not only define presence or absence of signal as valid binary signals but also define regions of voltage for proper levels. Those regions are:

High region = 2 to 5 volts
Undefined region = 0.8 to 2 volts
Low region = 0 to 0.8 volts

Voltages in the undefined region mean neither high nor low.

As long as the circuits that send and receive signals agree on the levels to be used, we have a hardware system for transmitting signals. We will see that one of the tasks of I/O circuits is to convert signal levels used by one portion of the system to those used in another. Unfortunately, not all peripheral devices use TTL levels. All the computer buses that we will discuss do use these levels.



Figure 2: A computer system with an I/O bus in addition to a memory bus. Building in a separate I/O bus frees all the memory-address space for the processor's own use.

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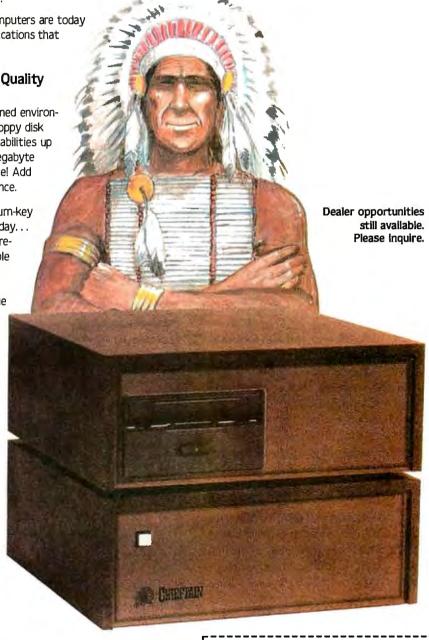
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Data Representation

After establishing signal levels, we must reach an agreement on what the various signals represent. What will be the digital representation of the character "A" or the number "123"? The alphabet can assume any of 26 values. Numerals can assume an infinite number of values. How can two levels—on and off—represent all these values?

The answer is to use more than one signal line, thus creating a bus. If we use eight lines, each of which can assume one of two levels, then we can represent 2 raised to the eighth power or 256 values. This is sufficient to represent all of the characters in the alphabet (both uppercase and lowercase) and the other printable characters and punctuation marks on a typewriter, along with a few special characters.

Communication is possible with eight lines as long as the sender and receiver agree on what each of the 256 values represents. The second task of I/O is to assure agreement between sender and receiver or at least to convert from one set of values to another.

In addition, not all devices communicate on the same number of lines. Some use a single wire (plus ground) and send one bit (binary digit) of information at a time. The receiver reassembles these sequential bits of information into a "parallel" representation (e.g., eight bits of data stored on eight parallel data lines). Some devices need only send numerals, which can be represented with ten values and require only four digital signal wires (because binary 1010, which has four bits, is decimal 10). Other forms of representation may require 16, 24, 32, or 64 lines, complicating interconnection. Interfacing among these devices must somehow adapt one system of representation to another.

The I/O Bus

We've just discussed several basic concepts relating to computer systems and I/O. Now we can give the programmer a means of questioning the computer and the computer a means of answering those questions.

The first step is to create an I/O bus leading from the processor to the outside. As stated earlier, the I/O bus is a set of conductors carrying signals that represent the information the computer is trying to transmit from the processor to the peripheral.

In addition, several conductors carry control signals that let the computer signal the recipient that the data on the bus is valid and should be accepted. The recipient must have some signals to notify the processor of the recipient's readiness to accept data and of its operational status. Finally, since we want the computer to be able to receive and transmit data, a signal is needed to dictate the direction of the data flow on the I/O bus.

The I/O bus shown in figure 3 has a number of connections. The top-most connection, with arrowheads at both ends, represents a group of 16 data lines. This connection is the data bus; the arrowheads indicate that the data bus can carry data in either direction, depending on the processor's immediate need. Beneath the

data lines is a single wire labeled "strobe." The strobe is the bus synchronizer; the computer uses the strobe to indicate that data is ready to be accepted.

The next wire in figure 3 is labeled "I/O" and controls the direction of the data on the data bus. The I/O wire is the traffic cop of the I/O bus, allowing bidirectional data flow in only one direction at a time. The peripheral signals the computer on wires labeled "status" and "flag." Status is a simple signal indicating presence or absence of a peripheral to receive data. After all, a computer can't communicate with a device that's not there.

Flag is a more complex signal. To understand flag, we need to study speed. Computer processors are very fast; the only moving parts inside them are the speedy electrons carrying digital signals. On the other hand, devices with which computers communicate are often mechanical. Disk and tape mechanisms, printers, and plotters all have moving parts that

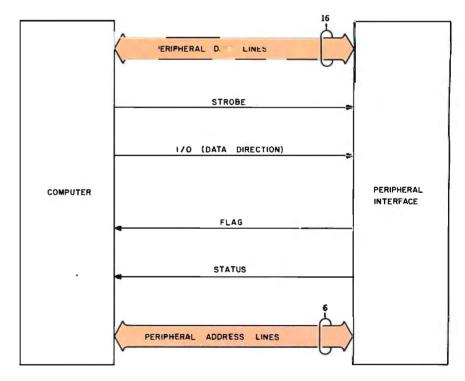


Figure 3: An I/O bus like that used by Hewlett-Packard. The bidirectional data lines carry information between the computer and the peripheral-device interface. The computer uses the strobe line to tell the peripheral device that data is ready to be accepted. The I/O line informs the peripheral of the direction of data transfer. The peripheral device uses the flag line to ask the computer to delay sending more data. The status line tells the computer whether or not the peripheral device is attached.

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Take a printer for example. Let's study an interchange between a computer and a piece of paper. The computer first addresses the printer interface using the last set of wires in the I/O bus diagram, the *peripheral-address lines*. If there's a device at that address, it will respond by signaling the computer on the status line. If the response is positive, the computer sets the I/O line to

"output" (direction is always from the processor's perspective), places data on the data lines, and causes the strobe line to indicate the data's availability. If the printer is working, it accepts and prints the data.

A serial impact printer, much like a typewriter, must select the proper character, activate some mechanism to strike the paper, and then move to the next character position.

These steps may take 10 milliseconds (0.01 seconds) or so to perform. That may not seem like a long time, but the processor takes about one microsecond (0.000001 seconds) to send the command to print. From the processor's perspective, the printer takes forever.

Fortunately, computers are patient and will obey if told to wait. In our example, the computer will not send another character until the printer has printed the current one. The flag line carries the printer's signal asking the processor to wait.

That completes our discussion of computer input/output. As we've seen, the computer remains firmly in control of the entire process. Next month, we'll look at those cases in which the I/O peripheral takes control of the computer: interrupts and direct memory access.

An I/O Glossary

Learning the terminology and jargon is one of the most difficult problems encountered when entering a new technical field. Every discipline has its own unique vocabulary, and the world of computer input/output is no exception. This glossary should help the reader who is unfamiliar with the computer terms in the I/O Primer, although the glossary is not comprehensive and its definitions are not necessarily universal.

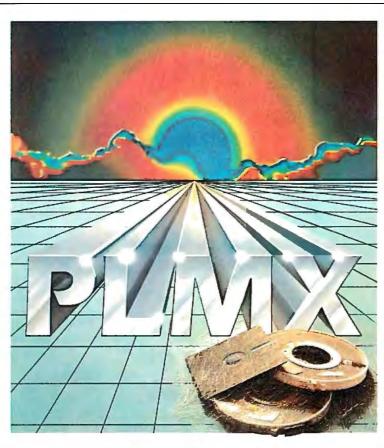
accumulator: a register inside the computer processor that stores operands and receives the results of operations. A computer may have several accumulators.

alphanumeric: representing letters

ASCII (American Standard Code for Information Interchange): a 7-bit code capable of representing letters, numbers, punctuation marks, and control codes in a form acceptable to machines.

analog: varying continuously rather than in steps. Contrast this with digital. A rheostat is an analog device; an on-off switch is digital.

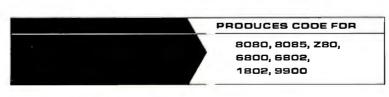
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Suite 304 121 S. Wilke Road Arlington Heights, Illinois 60005 A to D, ADC, or A/D): the conversion of continuously varying phenomena (e.g., voltages) into discretely varying or "stepped" phenomena.

APL: a high-level computer language considered by many to be the strongest language for mathematical procedures and algorithms. APL uses specially developed arithmetic operators.

assembly language: a low-level computer language for implementing higher-level functions. One assembler statement produces one machine instruction.

asynchronous device: a unit that operates at a speed not associated with any particular portion of the system to which it is connected; it therefore is not a time-critical component. Not to be confused with the asynchronous serial interfaces which are synchronous devices.

asynchronous data communications: a serial I/O protocol in which each byte transmitted is self-sufficient and bears no exact time relationship to preceding or succeeding bytes.

background program: that portion of the resident computer program that is run when the system has no other needs for the processor. Found only in multitasking systems.

base: the radix or number of characters in a particular number system. The decimal number system is base 10, since 10 numerals (0 through 9) are used.

BASIC (Beginners All-purpose Symbolic Instruction Code): a high-level language that is particularly easy to learn. Although this is the native language of most microcomputers today, there are many incompatible dialects.

baud rate: term often used to mean bit rate or data rate, the rate in bits per second at which information is transmitted over a serial link. In data transmission over analog channels such as the phone line, the baud and data rates may not be the same.

BCD (binary-coded decimal): a 4-bit system of coding the

numerals 0 through 9. The 6 most significant codes of the 4-bit system are unused because 4 bits can represent 16 different numbers.

benchmark: a test program used to compare a feature, usually speed, of two or more systems.

bidirectional lines: lines that may carry information in either direction but not in both simultaneously.

binary: the base-2 number system, which uses only the numerals 0 and 1.

bipolar: an integrated-circuit technology characterized by high speed, medium power requirements, and wide availability.

bisync (binary synchronous): a synchronous, serial data-communications protocol that is byte-oriented. Created by IBM.

bit (binary digit or binary integer): a single digit of a binary number. bit rate: see baud rate.

bus (plural buses): a group of hardware signal wires used to interconnect several devices for communication.

byte: a group of 8 bits.

character: a pattern which is meaningful in a semantic system and which does not consist of smaller meaningful units; an "atom" of meaning.

character set: a group of characters that, taken as a whole, can express all the information desired in a particular system.

checksum: a quantity used in several error-checking schemes. The checksum usually follows a string of characters.

chip (also integrated circuit): an electronic component made up of many basic devices, such as transistors, all combined on a single piece of silicon.

CMOS (complementary metaloxide semiconductor): a logic family of integrated circuits characterized by extremely low power requirements, medium speed, wide availability, and susceptibility to static discharge.

clock: a periodic signal used throughout a system for timing and synchronization.

compiler: a program that takes a high-level language as its input and produces machine code for output. compute-bound: adjective describing a program that is speed-limited by the computations being performed rather than by the I/O taking place.

control character: a character that produces some action in a device other than the printing or displaying of a character. A normal character may become a control character in some systems by being prefixed with a control character or characters.

controller: the device that dictates the sequence of events in a system. control line: a signal line used to sequence the flow of information over a data link.

CRT (cathode-ray tube): a term often used synonymously with video-display terminal, of which the CRT is a part; a popular display device used to show multiple lines of text and/or graphics. data bus: a set of signal wires that

data bus: a set of signal wires that carries data or characters between devices in a system.

data communications: generally taken to mean serial data I/O but may include any I/O between digital devices.

data set: Bell Telephone's name for a modem. Used to transmit digital data over voice telephone lines.

data terminal: a class of devices with keyboards and video displays, a video-display terminal. decimal: pertaining to the base-10 number system.

digital: a method of representing information with discrete numbers.

digital-to-analog (also D to A, or DAC, or D/A) conversion: a technique for converting a digital representation into a simulated analog signal.

DMA (direct memory access): an I/O technique for transferring data between a device and memory without the aid of the computer processor. A very high-speed method that requires special hardware to control memory.

DTL (diode-transistor logic): a

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logic family, compatible with TTL and nearly extinct.

EBCDIC (extended binary-coded decimal interchange code): a special IBM character set seldom used in microcomputers.

emulator: a program or circuit that imitates another program or circuit in real time. Usually, the emulator provides testing and monitoring capabilities beyond those of the program or circuit being emulated. erasable programmable read-only memory (also EPROM): an integrated circuit that can store programs or data which can later be erased. Information is stored, with or without power, until the erase procedure is activated. There are two types of EPROM: ultravioleterasable EPROM, and electrically erasable programmable ROM (EEPROM). EPROMs are common in development work because they can be reused.

exponent: the power of 10 of a number expressed in scientific notation. The exponent of the number

 1.245×10^{15}

is 15.

fan in: the electrical load a logic circuit places on a signal line.

fan out: a measure of the drive capability of a logic circuit.

firmware: a program (software) placed in ROM. Many microcomputers have firmware operating systems and language interpreters. flag line: a signal line used in a data link to signal the status of a device connected to the data link.

foreground job: a program that has the highest priority and runs on the computer processor whenever possible. Found only in multitasking systems.

full duplex: (in a communication channel) capable of simultaneous transmission in both directions. The term is also used (incorrectly) to describe data terminals that do not "self-echo" on their screens the characters they send, relying instead on the remote terminal to echo each character sent. Contrast

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with half duplex.

gate: the minimal logic element; a circuit with more than one input but only one output, which is energized by a certain combination of inputs. Basic gate types are AND, OR, Exclusive OR, and NOT.

GIGO (garbage in, garbage out): the usual answer to the question "Why doesn't my program work?" ground, earth or safety: a wire that is (or is supposed to be) at earth

potential. Intended to reduce or eliminate shock hazard in an electrical device.

half duplex: (in a communication channel) capable of transmission in both directions but in only one direction at a time. The term is also used (incorrectly) to describe data terminals that "self-echo" on their screens each character they send. Contrast with full duplex.

handshake: a signaling protocol for transferring information between devices in a synchronized manner at a rate acceptable to both devices: may be in either hardware or software.

hardware: the electronic circuitry in a system.

hardware buffer: a register or set of registers used to store information temporarily, usually to act as a transfer medium between a fast device and a slow one.

hardware driver: a circuit used to impress a signal on a conductor.

hardware interrupt: a mechanism that can quickly obtain the computer processor's attention for a task of higher priority than the one executing.

Hewlett-Packard Interface Bus (also HPIB, GPIB, IEEE-488 bus); a hardware interface similar to an 8-bit parallel interface but standardized in IEEE standard 488-1978.

high-level language: a computer language characterized by powerful statements and great ease of programming but both at the expense of execution speed.

HPL (High Performance Language): a high-level interpretive language found only in the Hewlett-Packard 9820, 9821, 9825, and 9826 desktop computers. Has extensive I/O capabilities.

IEEE (Institute of Electrical and Electronics Engineers): a professional organization that has defined several I/O standards.

initialization: a process that sets the starting values in a device to a known state. Often entire systems need to be initialized when powered up.

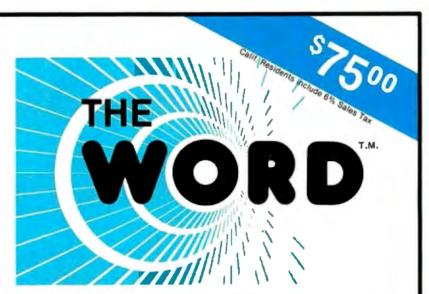
input: the process of transferring information into a computer.

input/output (I/O): a set of processes for information transfer into and out of a computer.

interface: the boundary between two devices or programs.

interface card: a device that converts signals from a computer bus into signals needed by a peripheral device. Voltages, signal speeds, and signal formats may be converted.

interpreter: a program that directly



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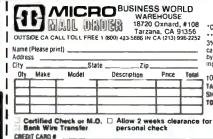
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executes a high-level language.

interrupt: a disruption in a process's normal flow.

inverter: a logic element or gate that outputs a 1 for a 0 input and a 0 for a 1 input. Also called a NOT gate.

I/O-bound: adjective describing a program whose speed is limited by the information interchange between devices in a system rather than by the computation being done.

K: abbreviation for 1024, typically used to specify memory size because 1024 is a power of 2.

k: abbreviation for 1000, typically used to specify resistor values and computer prices.

kluge: a concoction of hardware and software, usually extensively, patched together and not easily manufactured. Most commercial computers have several kluges.

latch: a logic device that transfers input data to output during a clock-signal transition and holds the data after the clock transition, regardless of whether or not the input data changes; used for memory.

LCD (liquid-crystal display): a display device characterized by high visibility in high light levels and no visibility in darkness.

LED (light-emitting diode): a display characterized by high visibility in darkness and less visibility at higher light levels.

logic: a group of circuits that performs Boolean arithmetic and memory functions.

logic ground: the reference level for all the digital signals in a system. Not necessarily connected to, or at the same potential as, the earth ground.

LSI (large-scale integration): highly dense logic circuits on single chips. Microprocessors are LSI devices.

machine code: the instructions directly executed by the processor. mainframe: term originating in large data-processing installations where sometimes small, remote processors are connected to a large, central "mainframe" com-

puter. Often used now to refer to the central control and interface unit of any computer, not including devices attached by external cabling.

mantissa: the significant digits of a number expressed in scientific notation. The mantissa of the number

 1.245×10^{15}

is 1.245.

mass storage: a device for storing large amounts of data or programs in a readily retrievable, non-volatile form.

MOS (metal-oxide semiconductor): an integrated circuit technology characterized by high density, medium speed, and medium power consumption. Two types of MOS exist: NMOS and PMOS, in addition to the related CMOS technology.

modem: see data set.

multitasking: a mode of computer operation in which several processes seem to take place simultaneously. In a multiprocessor system, simultaneous operation is truly possible. In a single-processor system, the processes timeshare the processor, and, although they appear to be happening simultaneously, they are actually occurring in a sequential manner. Multitasking operation allows a computer to make computations while waiting for slower I/O processes to take place. Also called *overlap*.

negative-true logic: a logic system in which a low voltage represents a logic 1 and a higher voltage represents a logic 0.

network: a term used in serial data communications to describe devices that have varying amounts of intelligence interconnected to form a large system.

noise: in a communication system or circuit, a disturbance which conveys no information and may interfere with the flow of information or meaningful signals.

nonvolatile: capable of retaining information even when a device is switched off; ROMs, disks, and tapes are nonvolatile.

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nybble: half a byte or 4 bits. BCD data is packed into nybbles.

object code: a program in machine code. The ultimate form a program must take to run on a processor.

octal: a base-8 number system using the numerals 0 through 7. Applied in the creation of machinecode programs and helpful in visualizing bit patterns.

one's complement: the inversion of each bit of a binary number. All 1s become 0s and all 0s become 1s.

one's-complement arithmetic: a binary arithmetic'system in which negative numbers are created by inverting individual bits in the corresponding positive-number representation. There are two 0s: all binary 0s (+0) and all binary 1s

open collector: a type of output structure found in certain bipolar logic families. The device has a transistor that enables it to output to a low-voltage level only. When the device is inactive, an external

resistor holds the device's output at a high-voltage level. Open collector devices are useful when several devices are to drive a single bus line (such as the IEEE-488 bus).

operating system: the software that controls and coordinates all the hardware elements in a computer system.

output: transfer of information from a computer to another device.

overlap: see multitasking.

packed data information that has been compressed to make optimal use of data storage. Four BCD.) digits may be packed in one 16-bit word.

paper tape: one of the oldest, slowest, and cheapest methods of storing information in a computer system. Data is stored in punchedhole sequences on a paper tape. Still the only universal medium of interchange between computer sys-

parallel I/O: the fastest, simplest

method of interconnecting two devices; requires the least circuitry. Data is transferred in bitparallel format, with the width of the interconnect bus generally equal to the word size of the processor or the peripheral. Eight-bit parallel interfaces are common and ideal for character transmission.

parity: an error-detection method used in I/O in which noise is a possible problem. Parity is determined by counting the number of 1s in a data word. If the number of 1s is odd, the word has odd parity; if the number of 1s is even, the word has even parity.

Pascal: a computer language that is popular for its structure and data types but has relatively primitive I/O statements.

peripheral: a device connected to a computer for providing data to, or accepting data from, the external environment.

peripheral processor: an auxilliary processor used to interface to external devices. Generally provided to increase system performance by allowing simultaneous computation by the main processor and I/O by the peripheral processor.

polling: a technique that discerns which of several devices on an I/O connection is trying to get the processor's attention. In a simple form, the processor may periodically interrogate each peripheral device to determine its

positive-true logic: a logic system in which a logic 0 is represented by a low voltage and a logic 1 by a higher voltage.

priority interrupt: an interrupt structure in which devices with higher priority may interrupt the servicing of devices with lower priority. In other systems, priority may only be used in the arbitration of simultaneous interrupts, disallowing interruption of an inprocess interrupt-service routine.

program: a series of statements defining a process or procedure in a form that can be executed by a computer.

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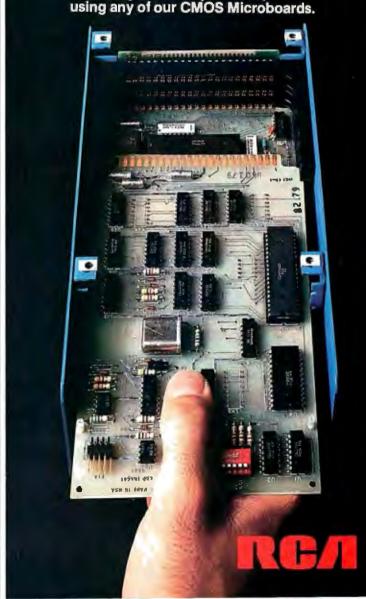
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Disk Utility for Apple DOS 3.3

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programmable read-only memory (PROM): a logic circuit that may be programmed once in a PROM programmer; stores data and/or instructions that are unlikely to need change. Also comes in erasable models (EPROMs).

protocol: a set of conventions for transfer of information between devices. The simplest protocols define only the hardware configuration. More complex protocols define timings, data formats, error-detec on and correction techniques, and software structures for running the interface. The most powerful protocols define each level of the transfer process as a layer separate from the rest, so that some layers, such as the interconnecting hardware, may be changed without affecting the other layers.

> st of processes " equential order or ot inblocks to be processed

schem ic: a c wing t t shows the interconnections of circuitry to form a device. Generally needed when interfacing two devices that plug-to-plug compatible and sometimes when interfacing those that are.

SDLC (synchronous data-link control): a protocol specifying a layered, bit-oriented approach to serial data communications. serial I/O: a type of interconnection in which information is transferred one bit at a time. The most common serial I/O hardw schemes are the S-232 stance and the 20-mA current loop. I are pseudo-standards because r devices using them work similarly but are 10t plug-to-plug compati-

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synchronous data communication: a serial I/O protocol in which the transmitter and receiver are synchronized to a common clock signal.

synchronous device: a device that transfers information at its own rate, not at the convenience of any other interconnected device. Synchronous devices, such as disks, must be serviced when they request service, or data is usually lost.

synchronous transfer: an I/O transfer that takes place in a certain amount of time without regard to feedback from the receiving device. The receiver must always be faster than the transmitter for such transfers to work properly.

threshold: the point of transition between two logic states. For example, 4.5 V might be a threshold for low/high transitions.

transceiver: a circuit or device

capable of transmitting *and* receiving.

transistor-transistor logic (TTL): a logic family characterized by high speeds, medium power requirements, and wide use.

Tristate (or three-state; Tristate is a trademark of National Semiconductor Corporation): an output configuration, found in several logic families, capable of assuming three states: logic high, logic low, and high-impedance. Useful for interconnecting many devices on the same set of wires in such a way that only one device at a time controls the levels on the lines while the other devices are in the high-impedance state.

two's complement: a one's complement to which 1 is added.

universal asynchronous receiver/ transmitter (UART): a logic device used to convert from parallel to serial and serial to parallel in the asynchronous serial data communications format.

universal synchronous/asynchronous receiver/transmitter (USART): a UART with additional capability for synchronous serial data communications.

vectored interrupt: an interrupt scheme in which each interrupting device causes the operating system to branch to a different interrupt routine, thus saving the time otherwise required for a poll to determine the interrupting device's identity. The Zilog Z80 has an advanced vectored-interrupt scheme.

voice channel: a transmission channel originally designed for voice transmission, such as the telephone line. Modems can transmit digital information over these channels for long-distance data communications.

word: the smallest unit of information that may be handled conveniently ("addressed") by a computer. Most microprocessors use 8-bit words called bytes. Some of the latest microprocessors, however, use 16-bit words. Usually, the larger the word size, the faster data may be processed.

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FIT—A Federal Income Tax Program in UCSD Pascal

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Does Uncle Sam withhold too much from your paychecks all year and then send you a refund without paying you interest on the excess amount withheld? Do you miss deductions when you make out your tax forms because you forget some items or fail to keep records in a way that makes deductions easy to find? Do you miss other tax breaks by choosing investment strategies without analyzing the tax consequences?

If you have access to a computer that runs UCSD Pascal, FIT, my federal income tax program, can help you with these problems. First, FIT will estimate your correct tax during the year. This will enable you to adjust the amount of withholding in order to increase your takehome pay, minimize your refund, and earn interest on income that Uncle Sam would routinely withhold. If interest rates are 15 percent, your loss during the year from excess withholding is about (.15)×(9/12)×(REFUND). A \$1000 refund means you lose \$112.50 in interest—almost enough for a new board, a modem, or some useful software.

FIT also provides a convenient way to collect tax data as they arise. With April 15 swiftly approaching, you won't have to spend hours searching for and organizing data. Also, since FIT makes calculating your taxes easy, you can use it to see how different kinds of investments would affect your obligations to Uncle Sam.

What FIT Does

FIT lets you enter tax data for all the lines on form 1040 and Schedules A and B. (Schedule A is for itemized deductions; Schedule B for dividends and interest income.) At your option, you can enter data sequentially

BYTE has made no independent evaluation of the accounting sufficiency of FIT. We also note that future changes in the tax laws should be reviewed for changed data and computational requirements.

without entering the line numbers, or you can type a line number to enter data for a single line or to correct an entry. FIT permits multiple entries for each line. That saves you the trouble of adding totals for each line before entering data. For joint returns, FIT lets you assign a data entry to either the husband or wife.

FIT then processes the data, consolidating Schedules A and B in form 1040, making all adjustments, and calculating the tax according to your filing status and number of dependents. FIT makes calculations for individuals, married persons filing separately, or married persons filing jointly.

FIT displays data on either the console or the printer. The program stores data in disk files for retrieval. It will also store multiple files under different names so that you can save tax data for different years, taxpayers, or scenarios. The ability to store multiple files is what makes FIT a good tool for analyzing the tax consequences of different investment strategies.

How to Use FIT

FIT starts by displaying the following prompt:

FIT COMMAND--> P)rint E)dit C)alculate R)ead W)rite Q)uit

The ")" indicates that the preceding letter is typed to invoke the desired command. Unless you are using the program with data previously stored in a disk file, you should begin with the Edit command. Just type E.

Editing

Typing E after the main prompt brings the editing prompt:

EDIT COMMAND--> A)sched A B)sched B Z)Form 1040 F)Filing Status Q)Quit

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The Starwriter uses industrystandard 96-character print wheels and ribbons, so there are no supply problems to worry about. Plug compatible with all major daisy wheel printers, it requires no changes in software or hardware and is available with a Centronics Parallel or RS 232C Serial interface. Optional Accessories: Bi-Directional tractor, Single Sheet Feeder.

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Listing 1: Sample data for line 8 of form 1040 as produced by FIT, a federal income tax program. The line at the top presents options to the user. Pressing < ESC> accepts the data, pressing control D deletes them, and pressing N, A, or W permits change of the name, amount, or assignment (to husband or wife).

LINE NUMBER 8

WAGES, SALARIES, ETC

GF INDUST

HUSBAND

AMOUNT

24590.00

To enter the taxpayer's name, the tax year, the filing status, and the number of dependents, type F. After you complete the entries under filing status, the EDIT COMMAND prompt line reappears. Choosing A, B, or Z brings the prompt:

EDIT COMMAND--> S)equentially I)ndividual lines O)uit

Sequential editing lets you enter data for one line at a time, skipping the lines that represent calculations based on data from other lines. FIT automatically fills in the calculated values later. If you choose I for editing individual lines, this prompt appears:

ENTER LINE NUMBER TO BE CHANGED 0) for help

Entering 0 causes the display of a list of the names and numbers of the lines on the form you are using. When you enter a line number, FIT displays each current entry for that line. You will see the prompt:

COMMAND--> ESC to continue \(\Lambda \) D)elete Change-N)ame A)mount W)hose

The screen also shows:

- the number and description of the line
- the name of the previous entry
- to whom the entry was assigned (husband or wife)
- the amount

You can accept the entry by pressing ESCAPE, delete the entry by pressing control D, or change the name, amount, or assignment of the entry by pressing N, A, or W. If the filing status is other than married, FIT won't show assignment of the item to husband or wife. Listing 1 shows an example of data displayed for line number 8.

When no data have been previously entered for a line, or when all the entries have been displayed, FIT asks:

DO YOU WANT TO ADD AN ITEM Y/N

Answering Y results in a prompt to input data.

Answering N brings a display like the one in listing 2, which shows a summary of the data for the current line. If you are doing sequential editing, the program proceeds to the next line number. If you are editing individual items, the screen asks whether you want to continue editing or quit.

The Edit mode takes you from form to form until you have had an opportunity to fill in all the items. Whether doing sequential editing or individual-line editing, you leave the Edit mode by typing Q for Quit.

When you leave the Edit mode, you again see FIT's main prompt line:

FIT COMMAND--> P)rint E)dit C)alculate R)ead W)rite Q)uit

Calculating

To calculate the taxes for an individual, just press C at the main prompt. If the filing status is "married," however, FIT asks whether to calculate your taxes for a married couple filing jointly, a married couple filing separately, or two unmarried individuals. (The law doesn't give married couples the option to file as two unmarried individuals, but a couple may want to see what their taxes would be if they were single.)

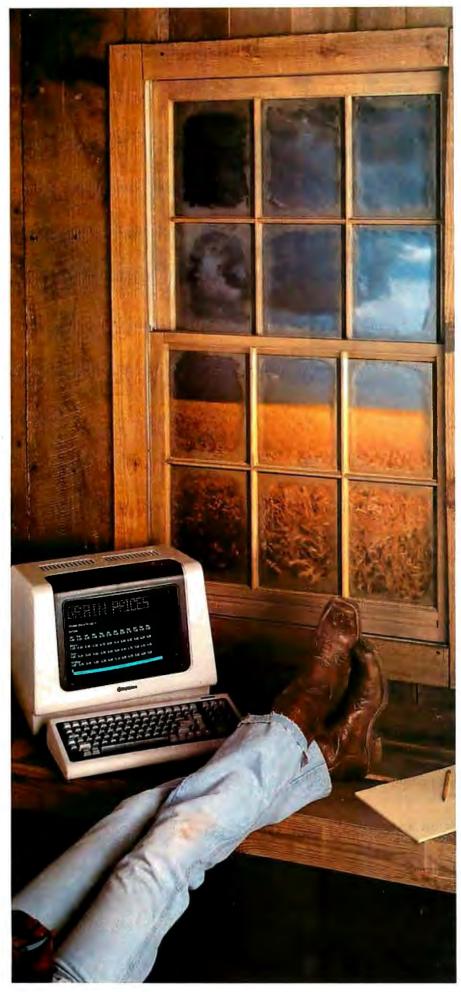
FIT does all the calculations for Schedules A and B and enters the results in form 1040. Then it does the calculations for form 1040 itself. The tax is calculated using the correct tax table for the filing status entered. The calculation takes only about 1.5 seconds and then you return to FIT's main prompt.

Printing

Typing P at the main prompt brings the prompt:

PRINTER COMMAND--> A)schedule A B)schedule B Z)Form 1040 #)for detail

You can print any of the three forms, with totals for each line, by pressing the letter indicated. If you want to see all the data entries for each line in addition to the totals, you press # (for detail) before selecting a form. Whether or not you choose detail, you are asked to direct the output to the printer or the console screen.



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Listing 2: A summary of the FIT data for line 8 of form 1040. FIT is running in the individual-line editing mode. Typing Q takes the user out of the Edit mode. If the user chooses to continue, FIT asks for the number of another line to edit.

DO YOU WANT TO --> C)ontinue Q)uit

LINE NUMBER 8 WAGES, SALARIES, ETC

HUSBAND 24590.00

WIFE 18500.00

TOTAL 43090.00

Listing 3 shows a sample printout for form 1040, listing 4 shows a printout for Schedule A, and listing 5 shows a printout for Schedule B. Listings 3 and 4 show totals only, but listing 5 was produced with the # option to show detailed entries for each item. FIT's printout of form 1040 adds a line at the end, MAXIMUM TAX BRACKET, to tell you the percentage used to calculate the last dollar of tax.

Reading and Writing

We've now seen all the commands in FIT's main prompt except for the Read and Write commands. If you want to read in a file of data or write a file, FIT asks for a file name (8 characters in the primary name; no extension

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required). If you use the Write command and enter the name of an existing file, FIT lets you choose a different file name or overwrite the existing file.

How FIT Works: Data Structures

The best way to learn how a program works is to look at the data structures first. Pascal conveniently puts them at the beginning of a program or procedure. FIT's main data structure is a record—a collection of a fixed number of related data items—named TLINE. TLINE, declared on the first page of listing 6, is a record of type variant. Records of type variant may contain variables that differ in the number and type of their components. The most important variant in the record TLINE is variant 1. It contains three long integers: one for amounts assigned to the husband, one for amounts assigned to the wife, and one for amounts assigned to the total for husband and wife. Variant 1 also contains a pointer to a data type called ITEM (these are discussed later).

Variant 2 holds data on the filing status, and variant 3 holds the name of the taxpayer.

FIT has one TLINE record for each line in form 1040, Schedule A, and Schedule B. An array called TLINES contains all the TLINE records. I put all the records for the three forms in a single array in order to speed access to data on disk. The index of the array—the number used to reference items in the array—is an integer between 1 and maxline. Here is how the TLINE records are stored in the TLINES array:

Form 1040 INDEX IN [1 TO 66]
Schedule A Schedule B INDEX IN [66+1 to 66+41]
INDEX IN [107+1 to 107+8]

I wanted the program to let me enter individual data items for each line, rather than make me sum all the individual data items myself and then enter the sum. One way to provide this multiple-entry feature is to construct an array for each line number to hold all its data items. This approach would require placing a reasonable limit on the number of data items per line, and then reserving memory space for that number of items for each line. If I set a maximum of 20 data items per line, the program

Text continued on page 162



for CP/M

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**	**********	*****	*****	
MAK	Y % JOE MICRO TAX YE ING STATUS 2 EXEMPTIONS 3	AR 1980		FORM 1040
F 1 L	ING STATUS 2 EXEMPTITING 3	alle to the total to		6 Mar 1981
***	*************************** WAGES, SALARIES, ETC INTEREST INCOME DIVIDENDS INCOME TAX REFUNDS ALIMONY RECEIVED BUSINESS INCOME CAPITAL GAIN CAPITAL GAIN CAPITAL GAIN DIST SUPPLEMENTAL GAINS TAXABLE PENSIONS & ANNUITIES PENSIONS, RENTS, ROYS, PARTNER	*****	************	
0	MAGEC CALABIED ETO	HUSBAND	WIFE 18500.00	TOTAL
8 9	WAGES, SALARIES, ETC	24590.00	13500.00	
10	INTEREST INCOME	622.50	150.00	
	DIVIDENDS	3/5.50	575.50	
11	INCOME TAX REFUNDS ALIMONY RECEIVED	0	125.25 2000.00	125.25
12	ALIMUNI RECEIVED	0705 00	2000.00	2000.00
1.3	BUSINESS INCOME	-2385.00	0 150.00	-2385.00
1.4	CAPITAL GAIN	-250.00	150.00	
15	CAPITAL GAIN DIST	0	0	0
1.6	SUPPLEMENTAL GAINS	0	0	0
1.7	TAXABLE PENSIONS & ANNUITIES	0	0	0
18	PENSIONS, RENTS, ROYS, PARTNER	5.60.00	0	560.00
1.9	FARM INCOME	U	V	0
20	UNEMPI.OYMENT	0	0	0
21	OTHER INCOME	0	0	0
22	TOTAL INCOME	23513.00	21500./5	45013.75
23	MOUTING EYPENCE	0	0	()
24	HOVING EXPENSE EMP BUSINESS EXPENSE PAYMENTS TO IRA	0		
25	PAYMENTS TO IRA	0	0	- -
	CAMENTO TO MECON	0	0	0
26	FAYMENTS TO KEOGH	•		125.00
27	INTEREST PENALTY	125.00		
28	ALIMONY PAID	4000.00	0	4000.00
29	DISABILITY INCOME	0	0	0
30	TOTAL ADJUSTMENTS	4125.00		4125.00
31	ADJUSTED GROSS INCOME	19388.00	21500.75	40888.75
32	AD HISTED GROSS INCOME	19388.00	21500.75	40999.75
33	DEDUCTIONS	4025.15	241.70	40000173
a a	PEDOC 11043	0023.13	201.70	0200+03
34	32-33	13342.85	21239.05	34601.90
35	TAY	2272.34	5215.77	4830.37
36	ΔΠΠΙΤΙΟΝΔΙ ΤΔΥΕς	22/2:07	02101//	0000107
50	ADDITIONAL TRACO			
37	ADJUSTED GROSS INCOME ADJUSTED GROSS INCOME DEDUCTIONS 32-33 TAX ADDITIONAL TAXES TOTAL TAXES	2272.34	5215.77	6830.37
====		=========	=======================================	
	*******************		*****	
MAR'		AR 1980		FORM 1040
	ING STATUS 2 EXEMPTIONS 3			6 Mar 1981
***	***************	********	*******	*****
		HUSBAND	WIFE	TOTAL
38	POLITICAL CONTRIBUTIONS	50.00	30.00	100.00
39	CREDIT FOR ELDERLY	0	0	0
40	CHILD AND DEPENDENT	0	0	0
41	INVESTMENT CREDIT	0	Ō	Ö
42	FOREIGN TAX CREDIT	0	0	Ō
43	WORK INCENTIVE	0	Ō	Ö
44	JOBS CREDIT	0	Ō	Ŏ
45	ENERGY CREDITS	175.80	Ö	175.80
46	TOTAL CREDITS (lines 38 to 45)	532.80	30.00	275.80
47	BALANCE (line 37 - line 46)	2046.54	5145.77	6554.57

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48 SELF EMPLOYMENT TAX		0	0	0
49 MINIMUM TAX		0	0	0
50 TAX FROM PRIOR YEAR I	NU-CREDIT	0	0	0
51 FICA AND RRTA TAXES		0	0	0
52 TAX ON IRA		0	0	0
53 ABVANCE EIC PAYMTS RE	CEIVED	0	0	0
EA DALANCE dide a AT Am	 	2046.54	5165.77	6554.57
54 BALANCE (lines 47 to	337		7107 + / /	7.000
55 TOTAL FICA WITHHELD		3590.00	3010.25	6600,25
	VHENTO	3370100	30.0+23	0000120
56 1980 ESTIMATED TAX PA	INEKIS	0	0	0
57 EARNED INCOME CREDIT	4070	0	0	0
58 AMOUNT PAID WITH FORM		0	0	0
59 EXCESS FICA AND RRTA		0	0	0
60 CREDIT FOR FED TAX ON		0	0	0
61 REGULATED INVESTMENT	CO CREDIT	0	0	0
62 TOTAL (line 55 to 61)		3590.00	3010.25	6600.25
63 OVERPAID		1543.46	0	45.68
64 TO BE REFUNDED TO YOU		0	0	0
45 APPLIED TO EST 1981]		0	0	0
66 BALANCE DUE		ō	2155.52	Ö
=======================================		=========		=======================================
MAXIMUM TAX BRACKET		32	43	37

Listing 4: A sample FIT printout of Schedule A, itemized deductions.

****	***********	*******	**********	******	**********	**
MAR	Y % JOE MICRO ING STATUS 2	TAX	YEAR 1980 3		SCHEDULE A	
FIL	ING STATUS 2	EXEMPTIONS	3		6 Mar 198	1
***	***********	********	*******	*****	*****	**
			HUSBAND 85.00 92.95	WIFE	TOTAL	
1	50 % OF MEDICAL INS PREM	15	85.00	0	85.00	
2	MEDICINE AND DRUGS		92.95	78.75	171.70	
3	MEDICINE AND DRUGS 1% OF LINE 31 FORM 1040		193.88	215.00	408.88	
	DOD LOTHE TIME OF YIME Y		0	0	0	
15	BALANCE OF INS PREMS		85.00	0	85.00	
6	OTHER MEDICAL AND DENTAL	-	85.00 230.50	517.70	768.20	
	TOTAL (lines 4 to 6) 3% OF LINE 31 FORM 1040 LINE 7 - LINE 8		335.50	517.70	853.20	
8	3% OF LINE 31 FORM 1040		581.64	645.00	1226.64	
9						
10	TOTAL MED & DENTAL		85.00	0	85.00	
44		:======= ,				==
11	STATE & LOCAL INCOME TAX REAL ESTATE TAXES GENERAL SALES TAXES	(408.85	480.45	939.30	
12 13	REAL ESTATE TAXES		1840.90	0	1840.90	
1.4	PERSONAL PROPERTY TAXES		. 150.90	250.50	401.40	
15	OTHER TAXES		0	0	0	
1.0	OTTER THALS					
16	TOTAL TAXES lines 11 to	15	2450.65	730.95		
17	HOME MORTGAGE INTEREST					
18	CREDIT & CHARGE CARDS		225,50	350.75	574.25	
19	HOME MORTGAGE INTEREST CREDIT & CHARGE CARDS OTHER INTEREST		0	0	0	
20	TOTAL INT (lines 17 to 1	L9)	3875,50	350.75	4226.25	

:: :: = =	. =====================================		.=======	.=========
21	CASH CONTRIBUTIONS	659.00	770.00	1429,00
22	OTHER CASH CONTRIBUTIONS	0	0	0
23	CARRYOVER	0	0	0
24	TOTAL CONTRIBUTIONS	659.00	770.00	1429.00
25	LOSS BEFORE INSURANCE	1500.00	0	1500.00
26	INSURANCE REIMBURS EMENT	895.00	0	895.00
27	LINE 25 - LINE 26	605.00	0	605.00
28	\$100 OR LINE 27	100.00	0	100.00
29	TOTAL CASUALTY OR THEFT	100.00	0	505.00
30	NNION DUES	^	110.00	110 00
	OTHER MISC DEDUCTIONS	150.00		
32	TOTAL MISCELLANEOUS	150.00		
ನಾವ ಮಹಾಹಾಣ		130.00		200.00
33	TOTAL MEDICAL & DENTAL	85.00	0	85.00
	TOTAL TAXES	2450.65	730.95	
	TOTAL INTEREST		350.75	
36	TOTAL CONTRIBUTIONS	659.00	770.00	1429.00
37	TOTAL CASUALTY ()R THEFT	505.00	0	505.00
38	TOTAL MISCELLANEOUS	150.00	110.00	260.00
39	SUM (lines 33 to 38)	7725.15	1961.70	9686.85
40	AIJUSTMENT	1700.00	1700.00	3400.00
41	LINE 39 - LINE 40	6023.15	261.70	6286.85
## = :				

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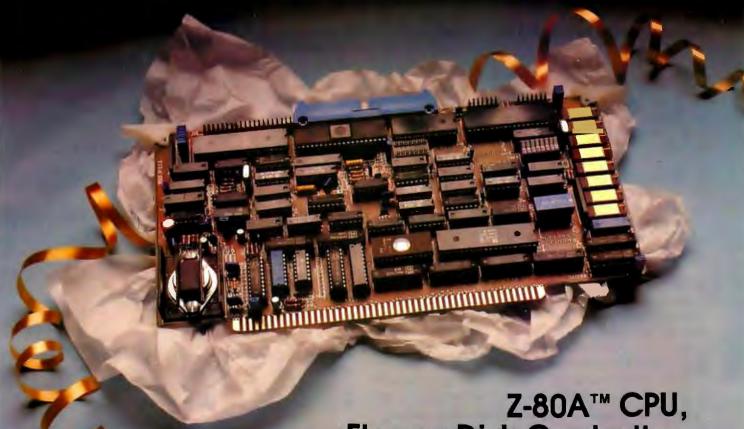
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Listing 5: A sample FIT printout of Schedule B, interest and dividend income. To obtain this printout, which shows detailed entries rather than just totals, the user typed # before typing B on the printer command line.

	• • • • • • • • • • • • • • • • • • • 	~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ 	· · · · · · · · · · · · · · · · · · ·	
MARY 3 JOE MICRO		TAX YEAR 1980 .,		SCHEITULE B
FILING STATUS 2	EXEMP	TIONS 3		6 Mar 1981
*****	*******	*******	*****	
		HUSBAND	NIFE	TOTAL
1 INTEREST INCO	ME			
LAST NAT	HUS	125.85		
LAST NAT	WIF	•	150.00	
QW L I CO	HUS	22,90		
AS CRED U	HUS	350.90		
DES INS CO	HUS	122.85		
TOTAL		622.5Ú	150.00	772.50
		*		
3 DIVIDEND INCO	ME.	•		
FG INDUST	HUS	250.00		
GF INDUST	WIF		450.00	
AF MOTORS	HUS	225.50		
AP MOTORS	WIF	,	225.50	
TOTAL	₩ ♣ !	475.50	675.50	1151.00

Text continued from page 154:

would require about 35K bytes of random-access read/write memory (RAM) based on the calculation: 115 lines \times 20 items \times 15 bytes per item. Most of this memory space would be wasted because most lines would have only a few entries.

To conserve memory space, I decided to store data entries for each line in a linked list. I constructed the list as

follows. I defined the structured data type ITEM as a packed record containing:

- the name of an item
- a 9-digit integer for the amount of the item
- the assignment of the item (to husband or wife)
- the line number associated with the item
- a pointer to the next item in the list

Defining a record as packed advises the compiler that you want it to store the data internally in a way that conserves memory space; you sacrifice some speed of access because of the time required for packing and unpacking the data.

A pointer is a variable that holds the storage address of a related item of data; the compiler doesn't assign memory space to these related data items once and for all, as the compiler does for other variables. The pointer in the record TLINE points to the first ITEM in the list of data ITEMs for each line number. The pointer in ITEM links the ITEMs in the list. Use of the pointers in this way assures that memory space will be consumed only when necessary.

FIT contains other important data structures. TITLES is a one-dimensional array of strings that holds the names of the lines on all three tax forms. TAXRAY is a three-dimensional array used to hold the four factors required to calculate the tax. These factors are:

- the lower income level for the bracket
- the upper income level for the bracket
- the minimum tax for the bracket
- the tax rate for income in excess of the lower level

There are 16 brackets. I defined the data type FACTORARRAY as a two-dimensional array of the 16 brackets \times four factors. Since each filing status requires

Text continued on page 394



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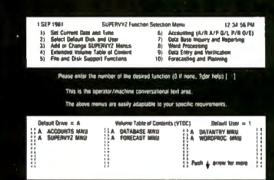
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Listing 6: The main FIT program, which also contains the support procedures. The support procedures perform basic tasks, such as handling input of string data, used in other procedures. The main body of FIT, at the end of the listing, calls the five segmented procedures START, EDIT, RW, PRINTER, and CALCULATE. The segmented procedures do most of the work of FIT.

PROGRAM FIT; {federal income tax rrogram} f by edward hesman ₹. 300 center hill rd } { centreville de 3. 19807 } -{ CONST MAXLINE = 115; MAXTLINE = 66; MINALINE = 67; MAXALINE = 107; MINBLINE = 108; MAXBLINE = 115; ESC ≈ 27; TYPE LONGINT=INTEGER[9]; FILENAME = STRING[15]; INTSTR=STRING[12]; NAMESTR=STRING[26]; FILING_STATUS = 0..5; TLINE_NUM = 1..MAXLINE; TLINESET = SET OF TLINE ... NUM; = (H_OWN,W_OWN,T_OWN); OWNER FOINTER = "ITEM; ITEM = PACKED RECORD NETR : POINTER; NAME : STRING[10]; TMA : INTEGER[9]; WHOSE : OWNER) MUNIT : TLINE_NUM; END; TLINE = PACKED RECORD CASE TAG : INTEGER OF 1 CIPTR : POINTER; HUS : INTEGER[9]; WIF : INTEGER[9]; TOT : INTEGER[9]); : (D1,D2,D3:INTEGER; 2 TAXYEAR : STRING[4]; FS : FILING_STATUS; EXEM : INTEGER); 3 1 (NAME : NAMESTR) # ENDI TLS = PACKED ARRAY[1., MAXLINE] OF TLINE; TAXTABLE #4 (X,Y,YS,Z); TAXFACTORS = (LOWER, UPPER, BASE, PERCENT) # FACTORARRAY = ARRAY [1..16, TAXFACTORS] OF LONGINT; VAR CH : CHAR! TTABLE : TAXTABLE

Listing 6 continued on page 166

FSTAT : FILING_STATUS#

SCREEN, SINGLE, SAME, QUIT : BOOLEAN

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```
DAY, MONTH, YEAR: INTEGER;
        SPECSET, DLINESET, SLINESET, SPAGESET, CALCSET : TLINESET;
        TAXRAY : ARRAY [TAXTABLE] OF FACTORARRAY;
        TITLES : ARRAY [1..MAXLINE] OF STRING[30];
        TLINES : TLS;
        MAX_TAX : ARRAY [OWNER] OF LONGINT;
            ! FILE OF CHAR!
PROCEDURE MEM; FORWARD;
FUNCTION READINT (LEN:INTEGER) : INTEGER; FORWARD;
PROCEDURE CLEAR FORWARD;
PROCEDURE ELINE; FORWARD;
PROCEDURE EEOL; FORWARD;
PROCEDURE EEOS; FORWARD;
PROCEDURE WAIT; FORWARD;
PROCEDURE POOL(DOL : LONGINT; VAR STOOL : INTSTR); FORWARD;
PROCEDURE CENTER (ST : STRING; SCREEN : BOOLHAN); FORWARD;
PROCEDURE READDOL (LEN:INTEGER; VAR DOLREAD: LONGINT); FORWARD;
PROCEDURE NAMER(TITLE : NAMESTR ; VAR ST : STRING ;L:INTEGER);FORWARD;
PROCEDURE LINE(CH:CHAR;LONG:INTEGER);FORWARD;
($ITAXSTART.TEXT)
₹$ITAXRW.TEXT3
C$ITAXPRINT.TEXT3
{#IT AXEDIT.TEXT}
PROCEDURE MEM!
  REGIN
    URITELN('MEMORY AVAILABLE ', MEMAVAIL)
  ENLE
PROCEDURE LINE ((CH:CHAR; LONG: INTEGER));
  VAR
          J:INTEGER:
  BEGIN
    FOR J:=1 TO LONG DO WRITE(P+CH)
  END; Cline>
PROCEDURE NAMER ((TITLE : NAMESTR ; VAR ST : STRING ; L:INTEGER));
Rused to permit string data input TITLE is a prompt .L is the max length
                                                         of the returned string }
  DEGIN
    REPEAT
      GOTOXY(0,6);
      WRITE('ENTER ',TITLE,' -->
                                  ();
      EEOLS
      READLN(ST);
      IF (LENGTH(ST)>L)
        THEN REGIN
               WRITE('NAME CANNOT EXCEED ', L, ' CHARACTERS');
               WAIT
               GOTOXY(0,7); EEOL;
             END;
    UNTIL (LENGTH(ST)<=L);
    WRITELNS
  ENDS
FUNCTION READINT {(LEN:INTEGER) : INTEGER};
{ a long winded routine to allow input of an integer of LEN digits}
  CONST
```

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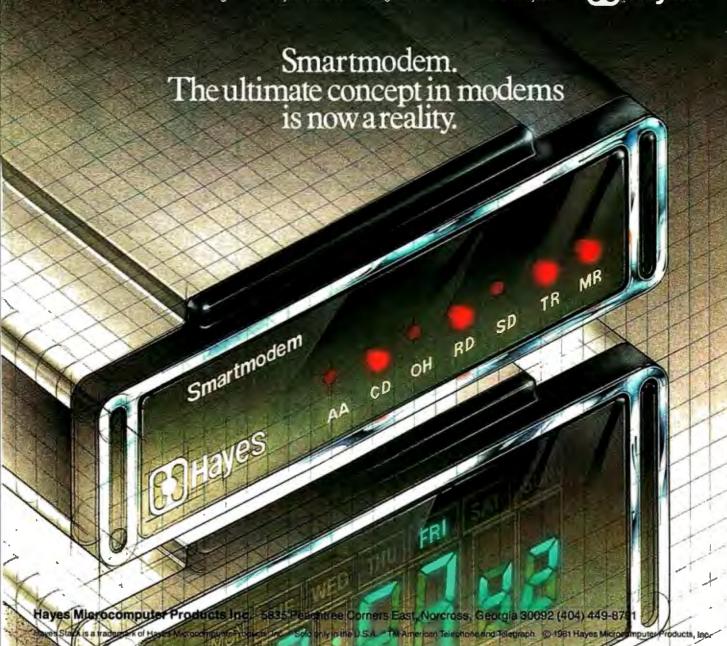
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```
PERJOD=.'.';PLUS='+';MINUS='-';DOL='$';BS=8;LF=10;FF=12;CR=13;DEL=127;
         SPACE=32; EEOL =4;
VAR
         CHARRAY: ARRAY [1..10] OF CHAR;
         READINTEGER: INTEGER;
         POSITION: 1..9;
         NEG: BOOLEAN?
         DIGITS: SET OF CHAR;
BEGINGREADINT)
         DIGITS:=['0'..'9'];
         FOR POSITION:=1 TO LEN DO
                 WRITE('_');
         FOR POSITION:=1 TO LEN DO
                 WRITE(CHR(BS));
         POSITION: #1;
         WHILE POSITION = 1 DO
           BEGIN
             READ(KEYBOARD, CHARRAYEFOSITIONI);
             IF (CHARRAYCEOSITION) IN DIGITS+CELUS, MINUS)) THEN
                      BEGIN
                      WRITE(CHARRAY[FOSITION]);
                      POSITION: =POSITION+1;
                      END; {if}
             END; {while}
         WHILE POSITION <= LEN DO
           BEGIN
             READ (KEYBOARD, CHARRAY (POSITIOND);
             IF (CHARRAYEPOSITION) IN DIGITS) THEN
                      BEGIN
                      WRITE(CHARRAY[FOSITION]);
                      FOSITION: = FOSITION+1;
                      END
             ELSE
                      BEGIN
                      IF CHARRAY[POSITION]=CHR(BS) THEN
                              REGIN
                              WRITE(CHR(BS));
                              POSITION:=POSITION-1;
                              END; (IF)
                      IF (CHARRAYEPOSITION) IN ECHR(SPACE), CHR(CR))
                              THEN LEN: = FOSITION-1;
                      END; {else}
     END; {WHILE}
     READINTEGER: = 0;
     IF CHARRAY[1]='-' THEN NEG:=TRUE else NEG:=FALSE;
     FOR POSITION:=1 TO LEW DO
          BEGIN
           IF (CHARRAYEFOSITION) IN DIGITS) THEN
           READINTEGER:=10*READINTEGER+ORD(CHARRAYCFOSITION])-ORD('O');
          END# (for)
     IF NEG
            THEN READINT: = -READINTEGER
            ELSE READINT: = READINTEGER;
END; (READINT)
PROCEDURE EEOS; {erase to end of screen}
  REGIN
    WRITE(CHR(2));
  END; {eeos}
PROCEDURE CLEAR; {clear the screen}
  BEGIN
```

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Softlights

By Fred Huntington

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Write for information on the niftiest piece of business software to come out in a long time - VersaForm from Applied Software Technology. It's a business forms processor which is a sophisticated, yet simple to use transactional management program.

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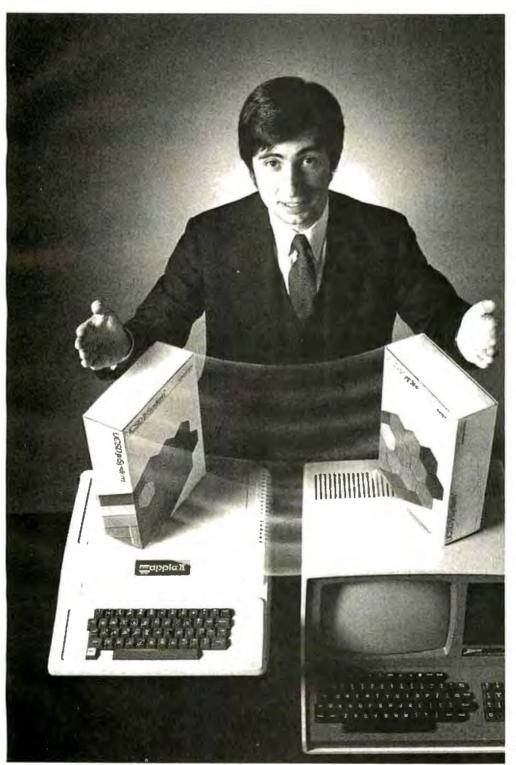


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WRITE(CHR(12))
   END;
PROCEDURE ELINE; {erase line}
   BEGIN
     WRITE (CHR (14))
   END;
PROCEDURE EEOL; {erase to end of line}
     WRITE(CHR(04))
   END;
PROCEDURE WAIT;
₹routine used to halt program while user examines output}
   VAR
          CH : CHAR;
   REGIN
     GOTOXY(10,23);
     WRITE('ENTER <ESC> TO CONTINUE');
     REPEAT
       READ(CH)
     UNTIL CH = CHR(27)
   ENDS
PROCEDURE CENTER ((ST : STRING; SCREEN : BOOLEAN));
{routing to print a string in the center of the line}
  VAR
                X,Y : 0..132;
                CH : CHAR9
  BEGIN
    CH := ' ';
    IF SCREEN THEN Y := 40 ELSE Y := 66;
    X := Y - (LENGTH(ST) DIV 2);
    WRITELN(CH:X,ST);
  END;
PROCEDURE POOL {(DOL : LONGINT; VAR STOOL : INTSTR)};
  BEGIN
    STR(DOL,STDOL);
    INSERT(',',STDOL, PRED(LENGTH(STDOL)));
  ENI);
PROCEDURE READDOL ((LEN:INTEGER; VAR DOLREAD:LONGINT));
{routine to permit entry of long integer of LEN digits}
 CONST
        BS = 8; FLUS = '+';
                                 MINUS = '-';
 VAR
         FOSITION: 1..10;
         NEG: BOOLEAN;
         ESC : CHAR;
         CHARRAY: ARRAY [1..10] OF CHAR;
         DIGITS:SET OF CHAR;
BEGIN(resddol)
        SAME := FALSE;
        QUIT := FALSE;
        ESC := CHR(27);
        DIGITS:=['0'..'9'];
        FOR POSITION:=1 TO LEW DO
                URITE(/m/)#
        FOR POSITION:=1 TO LEN DO
                WRITE(CHR(RS));
```

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```
FOSITION:=1;
        REPEAT
            READ(KEYBOARD, CHARRAY[POSITION]);
        UNTIL (CHARRAYCFOSITION) IN DIGITS+CFLUS, MINUS, ESC, 'Q', 'Q'));
        IF (CHARRAYEPOSITION) = ESC) OR (CHARRAYEPOSITION) IN ['Q','@'])
          THEN IF (CHARRAY[POSITION] IN ['Q','Q'])
            THEN BEGIN
                 QUIT := TRUE;
                 EXIT(READDOL);
               END
            ELSE BEGIN
                 SAME := TRUE;
                 EXIT(READDOL);
               END
          ELSE BEGIN
                 WRITE(CHARRAYEPOSITION3);
                 FOSITION: = FOSITION+1;
                 ENDIGITS
        WHILE POSITION <= LEN DO
          BEGIN
            REPEAT
              READ(KEYBOARD, CHARRAYEPOSITIONI);
            UNTIL (CHARRAYCPOSITION) IN (DIGITS + ['.', CHR(BS)]));
            IF (CHARRAY[POSITION] IN DIGITS ) THEN
                    WRITE(CHARRAYEPOSITIONI);
                    POSITION:=POSITION+10
                    END
            ELSE
                    BEGIN
                    IF CHARRAY[POSITION]=CHR(BS) THEN
                             BEGIN
                             WRITE(CHR(BS));
                             POSITION:=POSITION-1;
                             END; CIFS
                     IF (CHARRAY[POSITION] = '.')THEN
                             BEGIN
                               WRITE(('.');
                               LEN:=POSITION+1;
                             END;
                    END; {else}
   END; {WHILE}
   DOLREAD: = 0;
   IF CHARRAY[1]='-' THEN NEG:=TRUE ELSE NEG:=FALSE;
   FOR POSITION:=1 TO LEN DO
         BEGIN
          IF (CHARRAY[POSITION] IN DIGITS) THEN
          DOLREAD:=10*DOLREAD+ORD(CHARRAYEPOSITION))-ORD('O');
         END; (for)
    IF NEG THEN DOLREAD: - DOLREAD;
END; {readdol}
BEGIN(fit main)
  START;
 WRITELNS
  MEMS
  WAIT ;
  REPEAT
    CLEAR
```

Listing 6 continued on page 176

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```
WRITE('FIT COMMAND --> P)rint E)dit C)alculate R)ead W)rite Q)wit ');
    REPEAT
      READ (CH)
    UNTIL (CH IN ['E','e','C','c','R','r','W','W','F','F','R','R','C','C']);
    CASE CH OF
        'E','e' : EDIT;
         'R','r' : BEGIN
                     RW('R');
                     FSTAT := TLINES[7].FS;
                     IF FSTAT IN [2,3] THEN SINGLE := FALSE;
                   END:
                 : RW('W');
                 : PRINTER;
         'C','c' : CALCULATE;
      END; {case}
  UNTIL (CH IN E'Q', 'a'l);
END. (fit main)
```



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```
SEGMENT PROCEDURE START;
                                         {sets up the variables}
  PROCEDURE INITIALIZE;
  {inserts nul values in TLINES}
    VAR
          I : 1..MAXLINE;
          EMPTY : TLINE;
    BEGIN
      WITH EMPTY DO
        BEGIN
          IPTR := NIL;
          HUS := 0;
          WIF := 0;
          TOT := 0;
        END;
      FOR I := 8 TO MAXLINE DO
              BEGIN
                TLINESCID := EMPTY;
                TLINESCID. TAG 1- 1
              END;
      WITH TLINESC73 DO
        BEGIN
          D1 := 1; D2:=1; D3:=80;
         · TAXYEAR :# / /#
          FS :=0; EXEM := 0;
        END;
      WITH TLINES[6] DO NAME := ' ';
    END; {initialize}
    PROCEDURE READFACTORS;
    {reads the tax factor file into the array TAXRAY}
                     TFILE : FILE OF FACTORARRAY;
      VAR
                    TTABLE : TAX...TABLE;
      BEGIN
        RESET(TFILE, 'FACTORS, FTAX');
        FOR TTABLE := X TO Z DO
          BEGIN
            TAXRAYCTTABLE] := TFILE^;
            WRITE((,');
            GET (TFILE)
          END;
        CLOSE(TFILE);
      END; {readfactors}
    PROCEDURE READNAMES;
      {reads the line names into the array TITLES}
                  T=ARRAYC1..MAXLINED OF STRINGE303;
      TYPE
                  TNAMES: FILE OF T;
      VAR
      BEGIN
        RESET(TNAMES, 'LINENAMS.FTAX');
        TITLES := TNAMESO;
      END;
    PROCEDURE GETDATE;
      {sets the date from the disk in drive 4}
        VAR
            DUMMY: PACKED ARRAY [1..22 ] OF CHAR;
            HIGH, LOW : INTEGER;
        BEGIN
```

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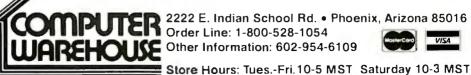
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```
UNITREAD( 4, DUMMY, 24, 2);
            HIGH != ORD ( DUMMY E 22 3 );
            LOW := ORD ( DUMMY E 21 ] );
            DAY := ( HIGH MOD 2 ) * 16 + LOW DIV 16;
            MONTH := LOW MOD 16;
            YEAR := HIGH DIV 2;
         END9
BEGIN{start}
   GETDATE;
    (the following set contains line numbers of lines requiring calculation)
    CALCSET := [9,10,22,30,31,32,33,34,35,37,46,47,54,62,63,64,65,66,69,70,73,
               74,75,76,82,86,88,90,93,94,95,98,99,100,101,102,103,104,105,106,
               107,109,111,114,1153;
    SINGLE := TRUE;
                        {needs a value to start}
    SCREEN : TRUE;
                        {most times it is}
                        {zero TLINES}
    INITIALIZE;
    READFACTORS;
                        {fill tax factor array}
    READNAMES;
                        {fill line number array}
END: {start}
```

Listing 8: The FIT segment procedure EDIT. EDIT enables the user to enter and correct data for form 1040, Schedule A, and Schedule B. EDIT lets the user work on all lines sequentially (procedure ED-SEQUENT) or on an individual line requested by number (procedure ED-INDIVIDUAL). Both these procedures call the procedure EDIT-TLINE to do the real editing of any line.

```
SEGMENT PROCEDURE EDIT;
                LN : TLINE ... NUM;
VAR
                                          findex to ARRAY TLINES>
                 INT : INTEGER)
                EDIT CHAR, CH : CHAR;
    PROCEDURE EDIT_SPEC;
    tenter taxpayers name, the tax year, filing status and number of dependents)
      VAR
                   H,W : INTEGER;
                    INT, EXEMPS : INTEGER;
                    LN : TLINE_NUM;
      PROCEDURE FILINGSTATA
        BEGIN
          WITH TLINES[7] DO
          BEGIN
            GOTOXY(0,4); EEOS;
            WRITELN('
                         1) Single'); WRITELN;
            WRITELN('
                         2) Married filing Jointly'); WRJTELN;
            WRITELN('
                         3) Married filing Separately'); WRITELN;
            WRITELN( '
                         4) Head of household'); WRITELN;
                         5) Widow(er)/);WRITELN;
            WRITELN('
            REPEAT
               INT := READINT(1)
            UNTIL INT IN [1.,5];
            FS := INT;
            IF FS IN [2,3] THEN SINGLE : FALSE;
          END; {with}
        END; {filingstat}
```

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```
Listing 8 continued:
```

```
BEGIN
     LN := 7;
     CLEAR; GOTOXY(0,2);
     WITH TLINES[7] DO
        BECIN
          CENTER (TITLES[5], SCREEN); WRITELN;
          NAMER('NAME', TLINES[6], NAME, 26);
          NAMER('TAX YEAR', TAXYEAR, 4);
          FILINGSTAT;
          EXEM := 0;
          CLEAR; GOTOXY (0,2);
          WRITE('ENTER CORRECT LETTER');
          GOTOXY(0,4);
          CENTER (TITLES[7], SCREEN); WRITELN;
          WRITELN('
                      Y)oursel?');WRITELN;
          WRITELN('
                       O)ver sixtsfive'); WRITELN;
          WRITELN('
                      B)lind();WRITELN;
          WRITELN('
                      T) over 65 and blind();
          REPEAT
            READ(CH)
          UNTIL CH IN ['Y', 'g', '0', 'o', 'R', 'b'];
          CASE CH OF
           1Y1,191
                       : H ::: 1;
                       : H :- 2;
           101,101
           /B/,/b/
                       : H :: 2;
           /T/,/t/
                       : H := 3;
             END; {case}
          IF NOT SINGLE
            THEN BEGIN
                    CENTER(TITLESCLN], SCREEN); WRITELN;
                    GOTOXY(0,6); EEOS;
                    WRITELN('
                                 S) rouse'); WRITELN;
                    WRITELN('
                                 O)ver sixtufive'); WRITELN;
                                B)lind');WRITELN;
                    WRITELN('
                    WRITELN('
                                 T) over 65 and blind');
                    REFEAT
                      READ(CH)
                    UNTIL CH IN ['S','s','0','o','B','b'];
                    CASE CH OF
                     1511/81
                              : W := 1;
                     101,101
                             : W := 2;
                     'B','b'
                              1 W :- 2;
                     /T/,/t/
                             : W := 3;
                       END: {case}
                  END (IF)
                ELSE W := 0;
          CLEAR; GOTOXY(0,6);
          WRITE ('ENTER NUMBER OF OTHER DEPENDENTS ');
          EXEMPS := READINT(2);
          EXEM := H + W + EXEMPS;
        END; {with}
  END; {editspec}
PROCEDURE EDIT_TLINE(LN : TLINE_NUM);
{main data input routine}
  VAR
                 HSUM, WSUM, DOL : INTEGER (91);
                 NEXTETR, PTR, LASTETR : POINTER;
                 TL : BOOLEAN;
                 CH : CHAR?
PROCEDURE VIEWS
{display contents of TLINES[LN]}
```

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```
VAR
              SCREEN : BOOLEAN;
                 OBJ : INTSTR;
  BEGIN
    SCREEN := TRUE;
    GOTOXY(0,3);
    EEOS;
    IF NOT SINGLE
      THEN BEGIN
              GOTOXY(0,8);
              PDOL(TLINESCLNJ.HUS,OBJ);
              WRITE('HUSBAND':20, OBJ:20);
              GOTOXY(0,10);
              PDOL(TLINESCLNJ.WIF,OBJ);
              WRITE('WIFE':20,08J:20);
           END;
    GOTOXY(0,12);
    PDOL(TLINESCLN].TOT,OBJ);
    WRITE('TOTAL':20,0BJ:20);
  END;
PROCEDURE SUMS #
{add all ITEMs and place values in TLINES[LN]}
  BEGIN
    WITH TLINESCLND DO
      BEGIN
        HUS := 0;
        WIF := 0;
        TOT := 0;
          IF IPTRISONIL
            THEN BEGIN
              NEXTETR := IPTR;
              REPEAT
               IF NEXTPIRA, WHOSE = H_OWN THEN HUS : HUS + NEXTPIRA, ANT
                                           ELSE WIF := WIF + NEXTPTR^.AMT;
               NEXTETR := NEXTETR^.NETR
              UNTIL NEXTETR = NIL;
              TOT := HUS + WIF
            END; {if}
       END; {with}
  END; {sums}
PROCEDURE WHO (PTR : POINTER);
{assign item to husband or wife}
  BEGIN
    WITH PTR1 DO
      BEGIN
        GOTOXY(0,12);
        WRITE('ASSIGN TO H)USBAND W) IFE ');
        REPEAT
          READ (CH);
        UNTIL (CH IN ['H', 'h', 'W', 'w']);
        IF CH IN ['H','h'] THEN WHOSE := H_OWN
                 ELSE WHOSE := W...OWN;
      END; {with}
   END; {who}
FUNCTION VIEWITEM(PTR : POINTER ) : POINTER;
{display and edit an ITEM then return pointer to next item}
  VAR
                ST : STRING;
                 CH : CHAR9
                OBJ : INTSTR;
```

```
BEGIN{viewitem}
 CLEAR
 WRITE('COMMAND --> <ESC> to continue
                                           'D)elete ');
 WRITE(' Chanse --> N)ame A)mount');
 IF NOT SINGLE THEN WRITE(' W)hose ');
 WITH PTR" DO
   BEGIN
      VIEWITEM := NPTR;
      GOTOXY(0,4);
      WRITE('LINE NUMBER ');
      IF LN <= MAXILINE
        THEN WRITE(LN : 2)
        ELSE IF LN <= MAXALINE THEN WRITE(LN-MINALINE+1 : 2)
                                ELSE IF LN <= MAXBLINE
                                    THEN WRITE(LN-MINBLINE+1 : 2);
      WRITELN(' ',TITLESELN3:40);
      GOTOXY(O,6);
      WRITE(NAME); EEOS;
      GOTOXY(0,8);
      CASE WHOSE OF
       H_OWN : WRITE('HUSBAND');
        WLOWN : WRITE('WIFE');
       TLOWN : WRITE('TOTAL');
      END; {case}
     GOTOXY(0,10);
     PDOL(AMT, OBJ) 9
     WRITE('AMOUNT ',OBJ:12);
     REPEAT
       REPEAT
         GOTOXY(77,0); READ(CH);
          IF CH = CHR(4)
                                {delete routine}
            THEN BEGIN
                   IF TL
                                {if pointer was from TLINES[LN]}
                           THEN TLINESCLND. IPTR := NPTR
                          ELSE LASTETRO.NETR := NETR;
                   EXIT(VIEWITEM);
                 END;
       UNTIL ( CH IN ['N','n','W','w','A','a',CHR(ESC)]);
        IF CH IN ['N', 'n', 'W', 'w', 'A', '3']
              THEN BEGIN
                                 {chanse a value in ITEM}
                     WITH PTRO DO
                       REGIN
                         CASE CH OF
                             'N', 'n'
                                       * NAMER('NAME', PTR', NAME, 10);
                             'A', 'a'
                                       : BEGIN
                                           SOT0XY(0,10);
                                           READDOL (9, AMT);
                                           WRITELN#
                                         END;
                                      # WHO(PTR)#
                             'W', 'W'
                           END; {CASE}
                        GOTOXY(77,0);
                                         {return cursor to command line}
                    END: (WITH)
                 ENTIF
              UNTIL CH = CHR(ESC);
      END; (with)
 TL := FALSE; {parent of rointer is no longer TLINES[LN]}
  LASTPTR := PTR;
END(viewitem);
```

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Circle 259 on inquiry card.

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```
BEGIN(edit_tline)
  HSUM := 0;
  WSUM := 0;
  WITH TLINESCLND DO
    BEGIN
                      {if any ITEMs exist}
      IF IPTR <> NIL
        THEN BEGIN
                TL := TRUE;
                                {parent of pointer is TLINES[LN]}
                NEXTPTR := VIEWITEM(IPTR);
                                                 {det first ITEM}
                       {while am ITEM exists det it}
                WHILE (NEXTERR <> NIL) DO NEXTERR := VIEWITEM(NEXTERR);
                       {no ITEMs left}
              END# (if)
  REPEAT
                       {add ITEMs or leave}
    CLEAR!
    GOTOXY(0,2);
    WRITE('LINE NUMBER ');
    IF LN <= MAXTLINE
      THEN WRITE(LN : 2)
        ELSE IF LN <= MAXALINE THEN WRITE(LN-MINALINE+1 : 2)
ELSE IF LN <= MAXBLINE
                                     THEN WRITE(LN-MINBLINE+1 : 2);
                  ',TITLESCLNJ:40);
      WRITELN('
      WRITE('DO YOU WANT TO ADD AN ITEM Y/N');
      REPEAT
        READ (KEYBOARD, CH)
      UNTIL ( CH IN ['Y', '\', 'N', 'n']);
      ELINE;
      IF CH IN ['N','n'] THEN BEGIN
                                  SUMS; {add the ITEMs and put in TLINE(LN]}
                                  VIEW; {display the contents of TLINES[LN]}
                                  EXIT(EDIT_TLINE);
                                END:
                          {begin the addition of a new ITEM}
      NEW (PTR);
      IF IPTR * NIL THEN IPTR := PTR
                                          - (if its the first ITEM of TLINESCLND)
                     ELSE LASTPTR - NPTR := PTR;
      LASTPTR := PTR;
      WITH PTR^ DO
                          {besin actual data entry}
        BEGIN
          NPTR := NIL;
          TLNUM : LN;
          NAMER('NAME', PTR', NAME, 10);
          GOTOXY(0,8);
          WRITE('ENTER AMOUNT ');
          READDOL(9,AMT);
          IF SINGLE THEN WHOSE := H_OWN
                     ELSE WHO(PTR);
        END! (with PTR")
   UNTIL (CH='Q');
  END; {with tlines[ln]}
END; {edit_lines}
FUNCTION EDIT ... WHAT : CHAR;
{select a schedule to edit}
  VAR
                 CH : CHARF
  BEGIN
    CLEARS
    WRITE ('EDIT COMMAND --> A)schedule A
                                                B)schedule B
                                                                    Z)form 1040 ');
    WRITE (' F)iling status Q)uit ');
    REPEAT
      READ(CH)
    UNTIL ( CH IN ['A', 'B', 'B', 'b', 'Z', 'F', 'f', '\(\f', '\(\f', '\(\forall '\));

Listing 8 continued on page 388
```

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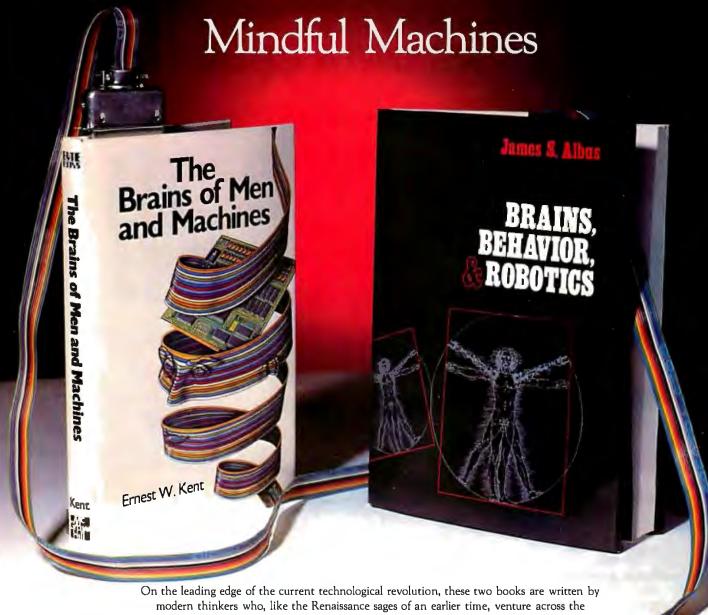
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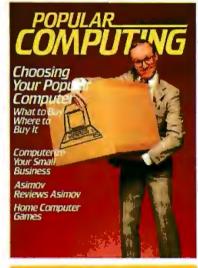
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Build an EPROM Emulator

Eric C. Rehnke 1067 Jadestone Lane Corona, CA 91720

Remember the last time you developed a program, "burned" it into (stored it in) an EPROM (erasable programmable read-only memory), and then discovered one or two bugs? And then, as a result of fixing one of the bugs by burning the EPROM again, several more showed up? It's happened to me more than once. And since it takes quite a bit of time to erase and reprogram EPROMS, a whole evening can be wasted without accomplishing much. After several of these frustrating sessions, I decided that there had to be a better way. After all, aren't computers supposed to save time?

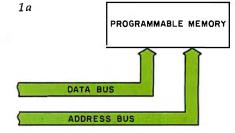
Clearly, a device was needed that would "look" like an EPROM to an EPROM socket and be quickly accessible from the program-development system. In this way, code could be verified before burning it into an EPROM. This becomes even more of a necessity if you're developing code for a small, dedicated controller and don't have any means of trying it before programming the EPROM.

About this time, I saw an ad for a Debug Memory Board (DBM-1) from Pragmatic Designs of Mountain View, California. The DBM-1 was exactly what I was looking for, but, unfortunately, it was meant to be used with an S-100 system. Since my system was 6502-based and didn't use the S-100 bus (there are a few of us out here), I ended up designing my own board. I call it an EPROM emulator because emulating is what it's doing.

Dual-Port Memory

The emulator gives my software-

development system a "window" into whatever system the EPROM is normally plugged into. It does this bit of



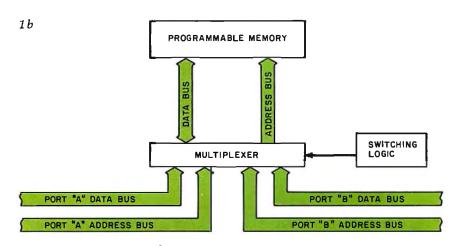


Figure 1: Types of programmable memory. Figure 1a shows the common single-port memory, with a single set of data and address buses. Figure 1b is a block diagram of dual-port memory; it allows access by two separate sets of buses.

TRS-80* COMPUTING EDITION

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The Percom Peripheral

35 cents

Percom's DOUBLER II tolerates wide variations in media, drives

GARLAND, TEXAS — May 22, 1981 — Harold Mauch, president of Percom Data Company, announced here today that an improved version of the Company's innovative DOUBLER® adapter, a double-density plug-in module for TRS-80* Model I computers, is now available.

Reflecting design refinements based on both theoretical analyses and field testing, the DOUBLER II³⁸, so named, permits even greater tolerance in variations among media and drives than the previous design.

Like the original DOUBLER, the DOU-BLER II plugs into the drive controller IC socket of a TRS-80 Model I Expansion Interface and permits a user to run either single- or double-density diskettes on a Model I.

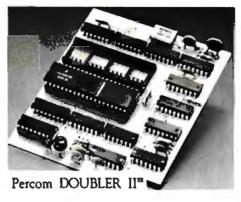
With a DOUBLER II installed, over four times more formatted data — as much as 364 Kbytes — can be stored on one side of a fiveinch diskette than can be stored using a standard Tandy Model I drive system.

Moreover, a DOUBLER II equips a Model I with the hardware required to run Model III diskettes.

(Ed. Note: See "OS-80": Bridging the TRS-80" software compatibility gap" elsewhere on this page.)

The critical clock-data separation circuitry of the DOUBLER II is a proprietary design called a ROM-programmed digital phase-lock loop data separator.

According to Mauch, this design is more tolerant of differences from diskette to diskette and drive to drive, and also provides immunity to performance degradation caused by circuit component aging.



Mauch said "A DOUBLER II will operate just as reliably two years after it is installed as it will two days after installation."

The digital phase-lock loop also eliminates the need for trimmer adjustments typical of analog phase-lock loop circuits.

"You plug in a Percom DOUBLER II and then forget it," he said.

The DOUBLER II also features a refined Write Precompensation circuit that more effectively minimizes the phenomena of bit-and peak-shifting, a reliability-impairing characteristic of magnetic data recording

The DOUBLER II, which is fully software compatible with the previous DOUBLER, is supplied with DBLDOS¹³, a TRSDOS -

compatible disk operating system.

The DOUBLER II sells for \$250, including the DBLDOS diskette.

Owners of original DOUBLERs may purchase a DOUBLER II upgrade kit, without the disk controller IC, for \$30.00. Proof of purchase of an original DOUBLER is required, and each DOUBLER owner may purchase only one DOUBLER II at the \$30.00 price.

The Percom DOUBLER II is available from authorized Percom retailers, or may be ordered direct from the factory. The factory toll-free order number is 1-800-527-1222.

Ed. note: Opening the TRS-80 Expansion Interface may void the Tandy limited 90-day Circle 281 on inquiry card. warranty.

All that glitters is not gold OS-80 Bridging the TRS-80* software compatibility gap

Compatibility between TRS-80* Model I diskettes and the new Model III is about as genuine as a gold-plated lead Krugerrand.

True, Model ITRSDOS" diskettes can be read on a Model III. But first they must be converted and re-recorded for Model III operation.

And you cannot write to a Model I TRSDOS diskette. Not with a Model III. You cannot add a file. Delete a file. Or in any way modify a Model I TRSDOS diskette with a Model III computer.

Furthermore, your converted TRSDOS diskettes cannot be converted back for Model I operation.

TRSDOS is a one-way street. And there's no retreating. A point to consider before switching the company's payroll to your new Model III.

Real software compatibility should allow the direct, immediate interchangeability of Model I and Model III diskettes. No read-only limitations, no conversion/re-recording steps and no chance to be left high and dry with Model III diskettes that can't be run on a Model I.

What's the answer? The answer is Percom's OS-80® family of TRS-80 disk operating systems.

OS-80 programs allow direct, immediate interchangeability of Model I and Model III diskettes.

You can run Model I single-density diskettes on a Model III; install Percom's plug-in DOUBLER® adapter in your Model I, and you can run double-density Model III diskettes

There's no conversion, no re-recording. Slip an OS-80 diskette out of your Model I and insert it directly in a Model III.

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OS-80 is the original Percom TRS-80 DOS for BASIC

programmers.

Even OS-80 utilities are written in BASIC. OS-80 is the Percom system about which a user wrote, in Creative Computing magazine, ". . . the best \$30.00 you will ever spend."†

Requiring only seven Kbytes of memory, OS-80 disk operating systems reside completely in RAM. There's no need to

dedicate a drive exclusively for a system diskette.

And, unlike TRSDOS, you can work at the track sector level, defining and controlling data formats — in BASIC to create simple or complex data structures that execute more quickly than TRSDOS files.

The Percom OS-80 DOS supports single-density operation of the Model I computer— price is \$29.95; the OS-80D supports double-density operation of Model I computers equipped with a DOUBLER or DOUBLER II; and, OS-80/ III — for the Model III of course — supports both single- and double-density operation. OS-80D and OS-80/III each sell for \$49.95. Circle 282 on inquiry card.

Circuit misapplication causes diskette read, format problems.

High resolution key to reliable data separation

GARLAND, TEXAS — The Percom SEPARATOR⁴⁸ does very well for the Radio Shack TRS-80* Model I computer what the Tandy disk controller does poorly at best: reliably separates clock and data signals during disk-read operations.

Unreliable data-clock separation causes format verification failures and repeated read retries.

CRC ERROR-TRACK LOCKED OUT

The problem is most severe on high-number (high-density) inner file tracks.

As reported earlier, the clock-data separation problem was traced by Percom to misapplication of the internal separator of the 1771 drive controller IC used in the Model I.

The Percom Separator substitutes a highresolution digital data separator circuit, one which operates at 16 megahertz, for the lowresolution one-megahertz circuit of the Tandy design.

Separator circuits that operate at lower frequencies — for example, two- or fourmegahertz — were found by Percom to provide only marginally improved performance over the original Tandy circuit.

The Percom solution is a simple adapter that plugs into the drive controller of the Expansion Interface (EI).

Not a kit — some vendors supply an untested separator kit of resistors, ICs and other paraphernalia that may be installed by modifying the computer — the Percom SEPARATOR is a fully assembled, fully tested plug-in module.

Installation involves merely plugging the SEPARATOR into the Model I El disk controller chip socket, and plugging the controller chip into a socket on the SEPARATOR.

The SEPARATOR, which sells for only \$29.95, may be purchased from authorized Percom retailers or ordered directly from the factory. The factory toll-free order number is 1-800-527-1222.

Ed. note: Opening the TRS-80 Expansion Interface may void the Tandy limited 90-day warranty. Circle 280 on inquiry card.

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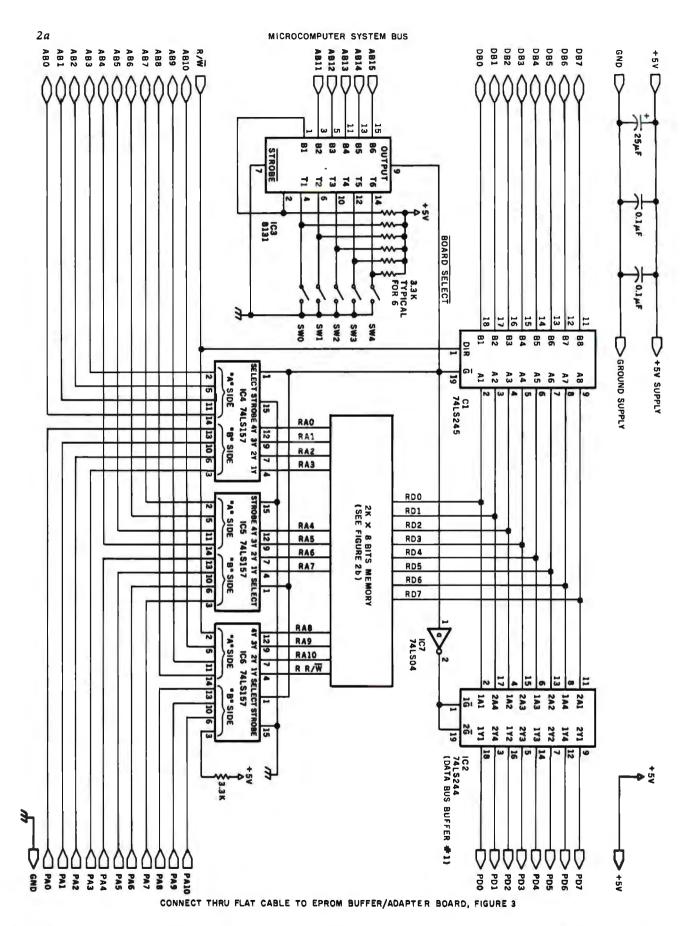


Figure 2a: A schematic diagram of the logic section of the EPROM emulator dual-port memory circuit. The 8131 address comparator generates the signal BOARD SELECT, used to allow either the development system or the EPROM socket access. See figure 2b for the programmable-memory portion of this circuit.

Whitesmiths, Ltd. is now shipping Pascal Compilers for 10 (count'em ten) different operating system families:



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Whitesmiths, Ltd.

magic by using dual-port memory. This is a block of random-access memory that can be accessed from two separate system buses (or ports). Each port has its own address and data bus, and incorporates logic that switches control between the two ports.

Since normal programmable memory has a single address and data bus, it can be called a single-port device (see figure 1a). To turn that memory into a two-port device, it is necessary to multiplex another data and address bus in by adding some

Number	Туре	+5 V	GND
IC1	74LS245	20	10
IC2	74LS244	20	10
IC3	8131	16	8
IC4	74LS157	16	8
IC5	74LS157	16	8
IC6	74LS157	16	8
IC7	74LS04	14	7
IC8	2114	18	9
IC9	2114	18	9
IC10	2114	18	9
IC11	2114	18	9

switching logic (see figure 1b).

Physically, the EPROM emulator consists of a circuit board containing the dual-port memory that plugs into the microcomputer developmentsystem bus (see figure 2), and an umbilical cable that leads out to a buffer module and 24-pin header plug (see figure 3). The buffer module is located as close as possible to the 24-pin header plug that is installed in the EPROM socket because it is used to increase the drive capability of the signals between the EPROM socket and the development system. I haven't done any testing to determine what the maximum length of the cable should be before delays and signal degradation cause the system to malfunction. Mine worked fine with a 3-foot long cable. Therefore, I didn't try any other lengths.

As you may have already guessed, the development system hooks into one port of the dual-port memory; the EPROM socket gets connected to the other.

The development system can read from and write to this memory through its port without any idea that there is anything different about it; it appears to be just an ordinary block of programmable memory. Whenever the development system isn't accessing the dual-port memory board, control is passed to the address and data bus of the EPROM socket. Whenever the EPROM socket is accessed, data are read just as if they were in an EPROM plugged into that socket.

As the schematic diagrams of figure 2 and figure 3 show, the design is straightforward. The 8131 address comparator (IC3, figure 2a) can be considered the "brains" of the system because it switches control back and forth between the two ports. When AB15 through AB11 have the same bit pattern as switches SW4 through SW0, the BOARD SELECT line from pin 9 of the 8131 goes low and several things happen simultaneously. The 74LS245 system data-bus buffer (IC1,

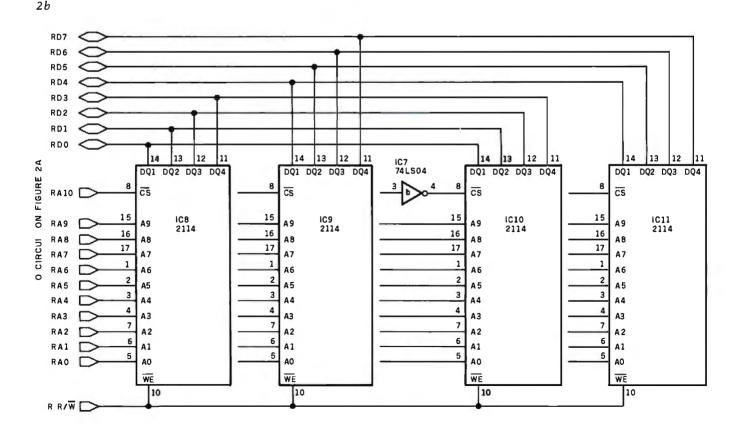
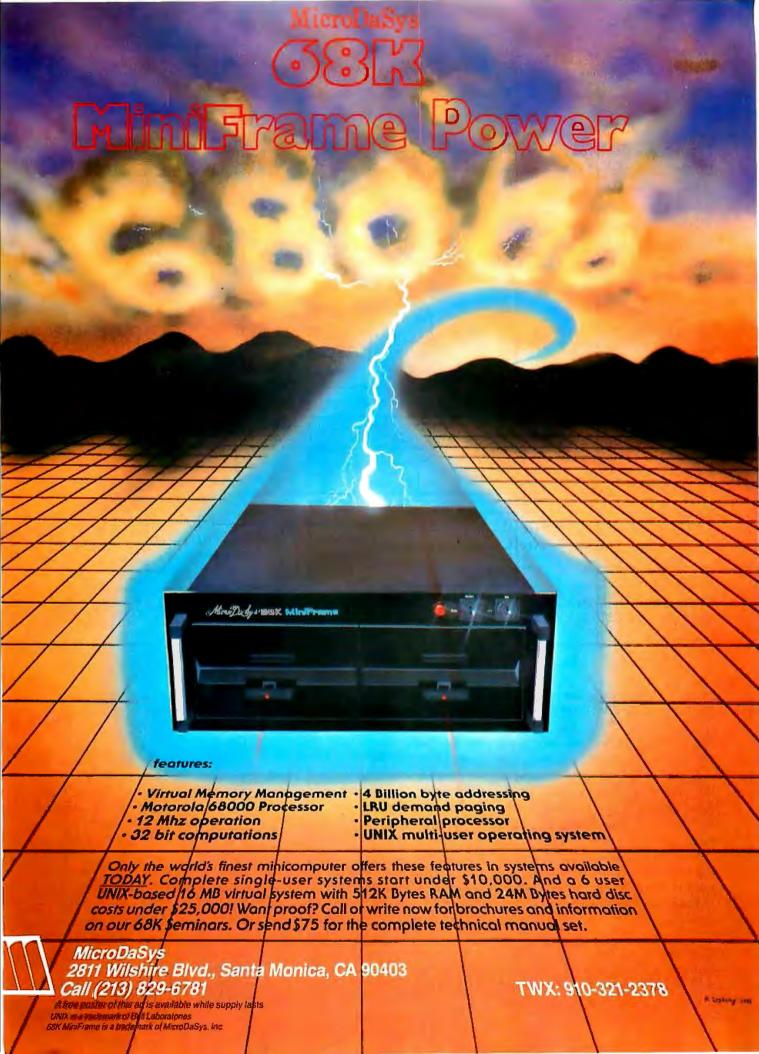


Figure 2b: A schematic diagram of the programmable-memory portion of the EPROM emulator dual-port memory circuit. The entire circuit (figures 2a and 2b) is connected via ribbon cable to the buffer/adapter board of figure 3.



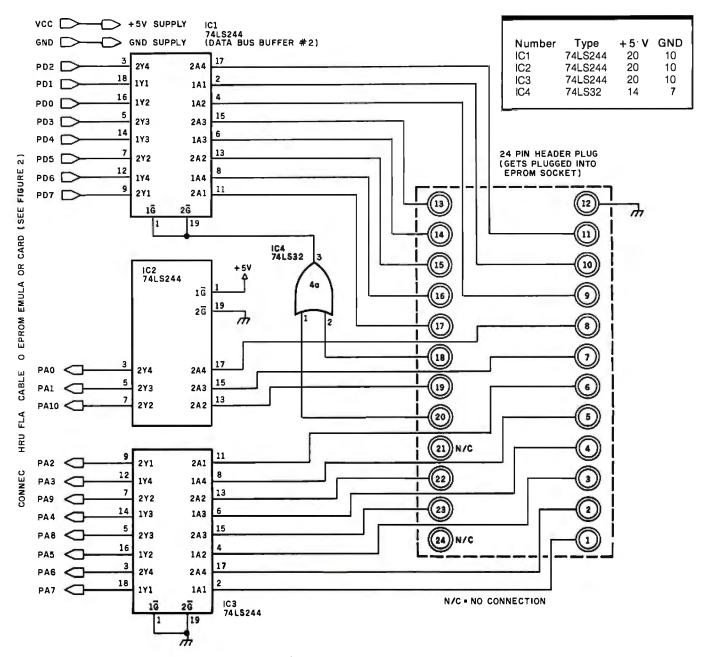


Figure 3: Schematic diagram of the buffer/adapter board. This segment of the emulator system is used to strengthen the drive capabilities of the EPROM socket to insure that signals are transmitted through the ribbon cable adequately.

figure 2a) is enabled, as well as the "A" side of the 74LS157 address-line multiplexers (which gives control of the dual-port memory over to the development system), while the EPROM data-bus buffer #1 (IC2, figure 2a) is disabled.

The development system is now in full control of dual-port memory access. If the EPROM socket tried to gain access to the board at the same time, the EPROM data-bus buffer #2 (IC1, figure 3) would be selected. However, since the #1 buffer (IC2, figure 2a) was deselected, no good data would be read. The 74LS32 gate

on the buffer board (IC4, figure 3) makes sure that the #2 buffer doesn't get enabled until the EPROM \overline{CE} and \overline{OE} signals (pins 20 and 18) from the target system are both low.

Whenever the BOARD SELECT line is high, the 74LS245 data-bus buffer (IC1, figure 2a) is disabled, while the 74LS244 EPROM data-bus buffer #1 is enabled, along with the "B" side of the 74LS157 address-line multiplexers. This gives the EPROM socket access to the dual-port memory during the times that the development system isn't accessing the board.

Details

This circuit was designed to reside in a 6502-based development system and emulate the Intel 2716 EPROM. The development system is built around the MOS Technology KIM-1 with hardware expansion accessories (48K bytes of memory, an 8-inch floppy-disk drive, and a 15-slot motherboard) from Hudson Digital Electronics (POB 120, Allamuchy, NJ 07820, (201) 362-6574). The emulator was built on a wire-wrap prototyping card (also from Hudson) using normal digital-construction techniques.

The EPROM buffer module in

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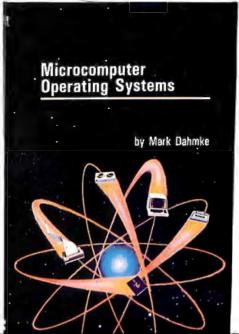
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> Price \$15.95 ISBN 0-07-033356-4

photo 1 is an earlier version designed to emulate the 2708 or the TI or Intel 2716. Since I ended up using only the Intel 2716-style part, I eliminated the switching feature from the design presented here. This simplified the circuitry quite a bit.

A situation may arise where the 2K-byte dual-port memory board may need to reside at a different physical address in the development system than that of the EPROM socket in the target system. In this case, the system assembler must be able to assemble code that runs at one location but actually resides at another.

Say, for example, that the emulator resides at C000 hexadecimal in the development system, while the EPROM socket is located at F800 hexadecimal in the target system. The system assembler must then be able to assemble object code to operate from the F800 address (so that it can run in the target system), but physically reside at C000 (so that it can be assembled into the emulator). This feature is usually called assembly with offset. It is included in the assembler from Hudson, as well as most good assemblers. If your assembler doesn't have this feature, you may be able to assemble to disk (or tape) and reload with an offset, Of course, if the emulator is located at the same physical address as the EPROM socket, you don't have to worry about any of these offset problems.

Users of the 6800 system should have little difficulty adapting the emulator to work with their machines. Users of Z80/8080 equipment will only have to redesign the interface to the development-system side of the emulator.

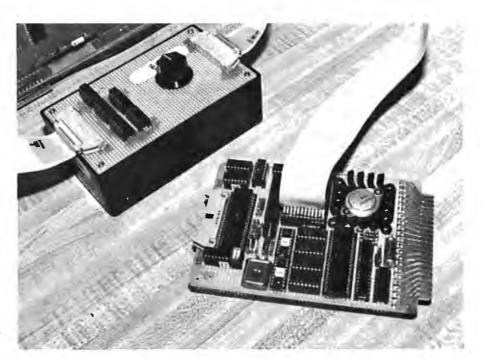
The emulator can easily be expanded to handle the newer 4K-byte EPROMS, with the addition of more memory and another multiplexer.

Another Use for the Emulator

How would you like a programmable character generator for your video board? Just plug the emulator into the character-generator socket (you may have to modify the connection to make it compatible) and load your character set into the dual-port memory. Anytime the video circuit is commanded to display a character, it reads the dual-port memory and displays the character you have programmed.

I also use the board for loading programs into my Rockwell AIM-65, Synertek SYM-1, and Apple II computers. Since the AIM-65 and SYM-1 only have cassette mass storage, I can usually save time and trouble by just saving everything on the floppy disks in the development system.

The EPROM emulator has proven itself to be a worthwhile addition to my arsenal of system-development tools and has paid for itself several times over.



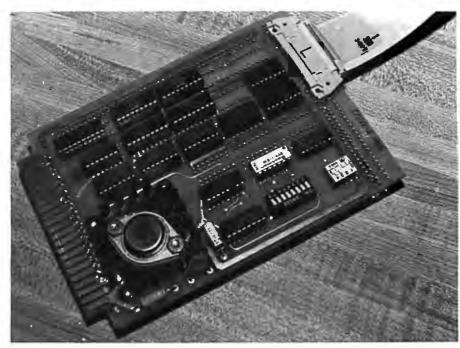


Photo 1: Close-ups of the parts of the EPROM emulator. The top photo is the early version of the buffer module, with its 24-pin header installed in the EPROM socket of the 6502-based single-board computer under development (the target system). The bottom photo shows the dual-port memory, built on a wire-wrap card.

Software Review

Two Tax Aids

Aardvark Individual Tax Plan and Howardsoft Tax Preparer

Mary Jo Kvam 13 Foliage View West Lebanon, NH 03784

Before I compare two income-tax programs, the Individual Tax Plan by Aardvark Software and the Tax Preparer by Howard Software, let's take a look at the process of creating a tax return.

Income-tax preparation has three phases that you must complete in order to come up with a finished product by April 15.

Phase 1 is record keeping. You must keep records of all the necessary tax facts and figures for the year.

Phase 2 is planning. It involves making certain key decisions so that when you fill out the forms and schedules, your tax position is optimized. These decisions might include whether to file joint or separate returns, how much stock to sell to maximize your tax advantage on long-term capital gain or loss, whether to use the 10-year averaging method for lump-sum distributions,

About the Author

Mary Jo Kvam has worked for eight years in data processing and is currently engaged in consulting and freelance writing.

At a Glance

Name

Individual Tax Plan

Туре

Income-tax-planning software

Manufacturer

Aardvark Software Inc. 783 North Water Street Milwaukee, WI 53202 (414) 289-9988

Price

\$250

Format

Two 51/4-inch floppy disks—one program and one data disk

Language Used

Apple Pascal Language System

Computer Needed

Apple II or Apple II Plus with 48K bytes of memory; CP/M System; one or more disk drives (DOS 3.3); printer (known to work with Anadex 9500 and 9501, Epson MX-80, NEC 5530, Okidata 22, most others)

Documentation

3-ring binder, 44 pages

Audlence

Professional tax planners

and other considerations.

Phase 3 is the paperwork of actually filling out the tax return to be submitted to the IRS. This phase is compulsory, of course, but your work here will be supported and strengthened by the completion of the other two noncompulsory phases.

The two tax programs reviewed here have different goals and are aimed at different audiences. The Individual Tax Plan will simplify and speed up your work in Phase 2. The Tax Preparer will assist you through Phase 1 and ease you through Phase 3. Both programs run on Apple II disk systems; see the At a Glance text boxes for the specific requirements.

The Aardvark Individual Tax Plan

The Aardvark Individual Tax Plan (AITP) helps you to determine systematically your best tax alternative. You enter a variety of income and expense items to create different tax situations. AITP does the calculations and allows you to isolate the tax results attributable to the

At a Glance

Name

Tax Preparer

Type

Income-tax record-keeping software for creation of IRS-acceptable forms and schedules

Manufacturer

Howard Software Services 6713 Vista Del Mar La Jolla, CA 92037 [714] 454-5079

Price

599

Format

Two 51/4-inch floppy disks—one program and one storage disk

Language Used

Applesoft BASIC

Computer Needed

Apple II Plus with 48K bytes of memory; one or more disk drives (DOS 3.2 or 3.3); printer optional—most parallel-port printers are suitable.

Documentation

3-ring binder, 22 pages

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Individuals and tax professionals

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ready-to-mail itemized bills, monthly Office Management Summary, Aged Receivables Report. An Accounts

List is also built into the system.

Complex transactions recording is reduced to a minimum because the LBS system is based on daily timesheets prepared by each timekeeper with

a complete system for coding client matters and expenses. The attorney auditing the pre-billing review form can choose various predetermined rates, or bill on retainer, contingency fee or an adjusted basis.

The Office Management Summary provides a financial analysis of each attorney's billings, aging of his accounts receivable and an analysis of the work effort of each timekeeper and total for the firm. The Accounts List summarizes current activity and status of each client.

The LBS is designed so that even first-time computer operators can install the system without expert help.

System/documentation-\$895

Demonstration System-\$ 75 Documentation alone-\$ 40

MICRO-TAX

Micro-Tax provides in-house computerized tax capability for the tax practitioner or serious investor. The system is designed to accept information, summarize data, compute tax and print the returns required by the Internal Revenue Service. The system's immediate response capability gives both tax specialist and clients immediate results of the computation.

The system reduces time required to complete a return while also minimizing the tax obligation of the taxpayer within the limit of the law. Three levels of tax prepara-

tion systems are available:

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handles multiple
clients, and prints
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Level 3 — All of Level 1 plus partnership schedules and forms.

State tax computation for the following states is available at additional cost: Arizona, California, Illinois, Ohio, Oregon, Maryland, New York,

Utah, Virginia and Washington, D.C.

Other states and municipalities are being added. Prices:

Level 1 — \$250 Level 2 — \$1,000

Level 3 — \$750

Level 2 plus Level 3 — \$1,500 State Tax — Call for prices Demonstration system — \$75



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variables entered. By comparing the outcomes, you can determine the most advantageous tax situation.

Step by step, AITP assists you in setting up your tax case. You are prompted for the number of alternatives you want; the maximum is 5 per file. AITP will then prompt you for up to 72 input values (besides spouse entries) to be used in determining the tax due (see table 1). You need not enter all this data, nor even be prompted for all of it. As shortcuts, AITP offers special function keys designed to provide freedom of movement through the data-entry section.

Once you've completed the data-entry section, you give your file a name and save it. It is now an old file, which can easily be reviewed, changed, or deleted. To see

all of the tax results for a case, the calculations are performed and the results are displayed on the screen and printed as hard copy. You can set up an additional file that provides more alternatives for the same case by using a different file name. You can create this file from scratch or make changes to an existing file and give the modified file a new name.

System Configuration

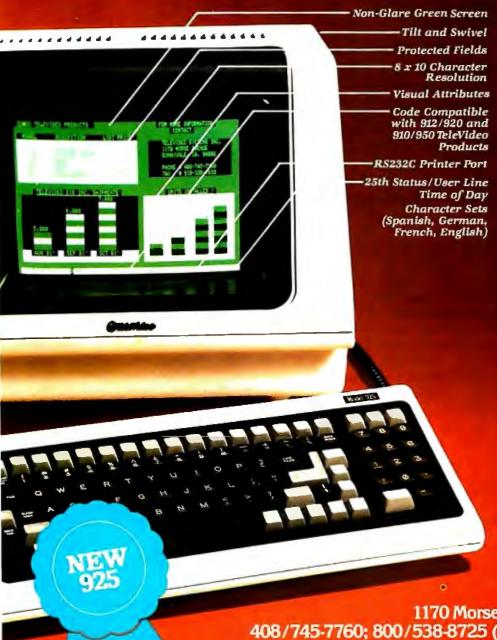
AITP requires an Apple II or II Plus with 48K bytes of memory and one or more disk drives using either DOS 3.3 or the Apple Pascal Language system. The disk-controller card must be installed in slot 6 and the printer-interface card in slot 1. Without the printer-interface card

1	Filing Status		Other Taxes
2	Exemptions		
_		41	Self-Employment Tax
	Income		Recapture of Investment Credit
			Other Taxes
3	Wages, Salaries		
4	Interest After Exclusion		Payments Payments
5	Dividends After Exclusion		i aymonto
6	Short-Term Capital Gain/Loss	44	Federal Income Taxes Withheld
	•		Estimated Federal Income-Tax Payments
7	Short-Term Capital Cain Sale of Principal Regidence	46	
8	Short-Term Capital Gain—Sale of Principal Residence	40	Onier i aymento
9	Long-Term Capital Laga Carriages		Schedule G
10	Long-Term Capital Loss Carryover		Conedule a
11	Long-Term Capital Gain—Sale of Principal Residence	47	1980 Form 1040, Line 34
12	Partnership Income	48	1980 Form 1040, Line 34 1979 Form 1040, Line 34
13	Other Income/Loss—A	48 49	
	Other Income/Loss—B	50	1978 Form 1040, Line 34
15	Other Income/Loss—C		1977 Form 1040, Line 34
16	Other Income/Loss—D	51	1980 Exemptions
17	Adjustments to Income		1979 Exemptions
		53	1978 Exemptions
	Deductions	54	1977 Exemptions
		55	1980 Foreign Income
18	Medical Insurance Premiums	56	3
19	Medicine and Drugs	57	1978 Foreign Income
20	Other Medical and Dental Expenses	58	1977 Foreign Income
21	State Income Taxes Withheld		Amounts Received Subject to Section 72(m)(5) Penalty
22	Estimated State Income-Tax Payments	60	Excess Community Income
23	Other Taxes		
24	Interest Expense		Form 4625—Minimum Tax
25	Charitable Contributions—20%		
	Charitable Contributions—50%	61	
27	Charitable Contributions Carryover—50%	62	Tax on Premature Redemption of Individual Retirement
28	Charitable Contributions—30% (Fair Market Value)		Bonds
29	Charitable Contributions—30% (Enter Gain If 50% Election	63	
-	Is Applicable)	64	Minimum Tax Deferred from Earlier Years
30	Charitable Contribution Carryover—30%		
31	Casualty Loss		Form 4726—Maximum Tax
32	Miscellaneous Deductions—A		· Offit 4720 Maximum Tax
33	Miscellaneous Deductions—A Miscellaneous Deductions—B	65	Personal Service Net Income
33	Miscellaneons Dennetions—D	00	1 Croonal Colvice Net Income
	Additonal Taxes		Form 6251—Alternative Minimum Tax
	Additorial Lakes		Tom 5251—Attemative william rax
34	Form 5405	66	Foreign Tax Credit Adjusted for Alternative Minimum Tax
35	Forms 4970, 4972, 5544, and Section 72(m)(5) Penalty Tax		Calculation
] 33	Totale Hore, Horz, Gott, and Geotion (Ziming) Femalty Tax	67	Other Credits Allowed Against Alternative Minimum Tax
	Credits	٥,	Sales Stocke Allowed Against Alternative William Tax
	Sidulid		Form 4972—10 Year Avg. Method
36	Political/Elderly/Child Care/Residential Energy Credits		Total Total Mag. Motiod
37	Investment Credit	68	Capital Gain Portion of Lump-Sum Distributions
38	Foreign Tax Credit		Ordinary Income Portion of Lump-Sum Distributions
39	WIN Credit		Current Actuarial Value of Annuity
40	Jobs Credit	71	Exclusion
] ~~	ood ordan		Federal Estate Tax Attributable to Lump-Sum Distribution
	3 a 4 1 . C.L mo		T Di . 1

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Table 1: A list of the 72 input values used in Aardvark's Individual Tax Plan to determine the income tax due.

The Performance Leader ... Model 925 Model 925



Now you can have it all with TeleVideo's new 925. Code compatible with our 910 and 950 terminals, the 925, with its 6502 microprocessorbased control board can emulate our 912/920 models while operating at speeds up to 19.2K baud. This allows you to grow within the TeleVideo family of terminals, from the conversational to the smart.

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The Value Leaders

in slot 1, AITP will not run. I have no printer at home, so I used a modem card in slot 1 and that worked fine. Aardvark claims that the Individual Tax Plan will interface successfully with most standard printers. A minor hardware modification may be necessary for printers that use the Centronics Parallel Card.

If you have a one-drive system, you will need to make extra copies of the program disk. All of your tax plan cases will be saved on these disks, and Aardvark estimates that between 20 and 30 tax-plan files can be saved on each disk. With a two-drive system you will need to make extra copies of the data disk, as well as a backup of the program disk. Aardvark estimates that between 50 and 75 tax-plan files can be saved on each data disk.

Documentation

The documentation for AITP is well packaged in a 44-page, 3-ring binder. The sheets are printed on one side only, making them good for notes. The documentation is easy to follow, complete, and concise. I had only to skim through the binder once to become familiar with the layout and feel comfortable with it as a tool.

The documentation has six sections. First, an introduction gives an overview of the program, hardware requirements, etc. The second section teaches you how to use AITP by walking you through two different sample cases. I found this section really helped me become comfortable with the software. It's a kind of "blind faith" approach, because you are setting up cases without knowing a lot about the software, but it works. The third section explains the screen menus, what every choice on every menu will do, and how the menus fit together. Section four describes the auto-entry keys and special function keys, which provide unique shortcuts for entering tax data. The fifth section defines the 72 tax inputs, and the appendixes give input work sheets and illustrations of the inputs and printouts of the two sample cases from section two. Everthing you need to run AITP is included in the documentation. If it weren't for a few minor errors, I would have rated it excellent.

Using the Program

For the most part, AITP is a pleasure to use. The hierarchical menu structure is easy to use and understand. Even during my first session of entering new cases and revising old ones, I knew where I was in relation to the overall program. AITP's error handling is well designed. The program will not crash when given improper input values; it simply refuses to accept them. Screen management is well done too. The screens are crisp and clear, and when there are separate sections on the same screen, they are well partitioned.

AITP could be improved a bit in a few areas. Some menu choices don't really make sense for certain processing paths. When selected, such choices may temporarily cause a slightly jumbled display. This flaw might have been remedied by tailoring the menus to the processing paths. And why prompt for spouse information in cases

involving single taxpayers? This situation causes no real harm, but if you're not married you must hit the F (Forward) key a bit more often.

According to Aardvark, this version of AITP will have been superseded by the time this review is published. The new version will reflect the new tax law and include adjustments for tax revisions through 1986. One of the enhancements that the new version will include is a projection capability, so you will be able to determine future tax consequences. You will be able to see the results of your tax planning for the base year plus the next four years.

Also, at an additional cost, you can obtain software designed for state tax planning. Only selected states are available (contact Aardvark for details). Note that the Aardvark Individual Tax Plan is now available to run on CP/M-based microcomputers.

The Howardsoft Tax Preparer

The Howardsoft Tax Preparer (HTP) actually prepares the forms and schedules that comprise the tax return. You enter information for your tax return just as you have always done, but you only need to enter information once. Repetitious inputs and complex procedures are eliminated. HTP takes care of all calculations, and the results are reflected on all lines of all forms where they are needed. An itemization feature allows HTP to be used for tax record keeping throughout the year in preparation for the next filing deadline.

The Process

Howardsoft suggests using the 1040 income-tax form as a guide for structuring your data entry. To create a new tax return, you give your return a name and select the 1040 as the form (file) you want to fill out. You enter data until you reach a line that requires a result from a yet uncompleted form or schedule. At this point, you must go to the end of the 1040 form. You can do this by scrolling or by exiting at the end of a section. After you save the interim results of the 1040, you select the form or schedule that you must complete before continuing with the 1040. Once that form or schedule is completed, you save those results and return to the 1040 form you started by requesting it by file name. This process continues until the 1040 and all other applicable forms and schedules are finished.

Granted, this may not be the fastest way to complete your tax return, but I agree with Howardsoft that it is the most foolproof. Revisions to any form or schedule can be made easily; however, every time you make an adjustment to a form or schedule, you *must* scroll through every other form or schedule that uses that data to ensure proper updating.

HTP creates printed versions of all of the forms and schedules that it handles, and, except for the 1040 form, these can be filed directly with the IRS. Preprinted 1040 forms must be used to meet IRS requirements, and HTP will print directly on the preprinted forms.



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Circle 222 on inquiry card.

System Requirements

HTP requires an Apple II Plus with 48K bytes of memory and one or two disk drives using DOS 3.2 or DOS 3.3. You'll need a printer to prepare the hard copy forms and schedules. Howard Software informs me that HTP will interface successfully with most standard printers. I used an Integral Data Systems 460G with satisfactory results.

The HTP package contains two disks—a program disk and a storage disk. If you have a one-drive system, your storage disk will need to contain label files in order to avoid the inconvenience of frequent switching between the program disk and the storage disk. A label-copying program is provided as part of HTP. The switching of disks then becomes minimal. In the case of a two-drive system, Howardsoft estimates that the storage disk can hold between 7 and 15 extensive returns.

Documentation

The documentation for HTP is in an attractive, durable package, but its content is only in the fair-to-average range. The documentation provides the information you will need to run HTP properly, but it does not make a very useful reference tool. It is unclear and did not help me much in seeing the whole picture. The manual is split into seven separate chapters, but the material is presented in such a way that I rarely knew where to turn for an answer.

The manual is also a bit sparse—for example, a few more forms and schedules in the appendix would have been a great help. And the documentation should do more than just tell you how to look at the sample case on the program disk. It should contain a walk-through for setting up a sample return from beginning to end. As it stands, the documentation needs rewriting to become a worthwhile resource.

Using the Program

HTP is not the easiest program to use. To some extent, this shortcoming can be traced back to the design of the software, but another reason for the program's complexity is that HTP undertakes quite a bit. The software allows you to enter tax data in its rawest and most familiar form, eliminates duplication of input, performs all calculations, and prints out forms and schedules acceptable to the IRS.

I discovered a flaw in HTP that could cause the tax return to be incorrect. The problem concerns capital gains distributions. The amount is entered on Schedule B, but HTP does not automatically carry this figure over to Form 1040 or to Schedule D. You must enter it again manually on either Form 1040 or Schedule D to properly compute your tax return. I did not hit upon any other critical problems, but the depreciation section was confusing and in need of improvement.

HTP could use quite a bit of tailoring. For example, when data for a new tax return are being entered, you face the same routine used for changing data on an

existing return. Every entry must be input as if it were changing old data. This means extra steps for each new entry, a time-consuming process. An adjusted routine for new cases is needed.

Some other refinements are also necessary. HTP lets you exit from a form or schedule by entering an "N" at the end of a section. Since you are apt to be going back and forth between various forms and schedules, this exiting capability should also be made available at those points where it is necessary to switch to another form or schedule. Also, the scrolling method for updating is cumbersome.

HTP screen management needs some work; more often than not, the screen seems cluttered. I would sacrifice the flashing statements and inverse displays for the clarity that some open space would provide.

A good feature of the printing routine is that you can enter as many returns as you want and then walk away after you get it going. You'll appreciate this when you're running off a few forms and schedules at the same time.

By the time this review is published, HTP will have been substantially upgraded, and many of the weak spots will have been corrected, according to Howardsoft. For example, the problem with capital gains distribution should be remedied, and Howardsoft plans to replace the scrolling update method with an automatic update method and improve the documentation. Some general software refining should be evident and a tax-planning facility should be added. In addition, Howardsoft will be offering separate interrelated software for preparing the state income-tax return for certain states.

Comparisons

Neither Aardvark nor Howardsoft provides a warranty on the results of its software. This means the IRS will hold *you* responsible for inaccuracies, not the software houses.

AITP stores uncalculated results. The calculated results are not filed on the disk, but are printed directly from memory, which ensures that the results are consistent with the input. In HTP, calculation results are filed on the disk and all printing is done directly from the disk. Thus, it is possible to change an input and then print an incorrect form because the calculations are based on the old input. The HTP documentation warns against this possibility.

The only way to exit from AITP is to shut off your Apple II. You cannot use Apple system commands or do anything else while you're running AITP. HTP, written in Applesoft BASIC, can be terminated to return control of your Apple II to you. You can use Apple system commands and modify the program if you want.

AITP requires organizational work before you can actually input data, and the bulk of the tax calculations must also be done prior to inputting data. The nonprofessional tax planner may have difficulty in deciding which figures should be included as part of which inputs. On the other hand, nonprofessional tax preparers will *not* find

The Logo Language is Here for the Apple II

TO SQUIRAL :ANGLE :DISTANCE
IF °:DISTANCE > 200 THEN STOP
FORWARD :DISTANCE
RIGHT :ANGLE
SQUIRAL :ANGLE :DISTANCE + 3
END

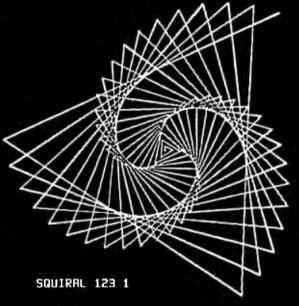
Terrapin, the Turtle Company, brings you the Terrapin Logo Language for the Apple II with Turtle graphics, now ready for immediate delivery.

The Terrapin Logo language is a sophisticated and powerful language that is easy for anyone to use. Although originally intended for children, the Logo language is one that the most advanced programmers will enjoy using too. It includes many features common to artificial intelligence research languages permitting programs of great power to be written quickly and easily. Writing comparable programs in other languages is usually much more difficult and time consuming.

The Turtle graphics is fun and easy. With simple commands such as FORWARD, RIGHT, and PENUP you can draw in six hi-res colors. In just a few short sessions you can learn to create figures more complex than the one above whether you know how to program or not.

But the Terrapin Logo language is more than just a graphics language. It supports:

- list structure, allowing easy manipulation of words (strings) and lists
- user defined procedures which can be used exactly as if they were part of the language.
- fully integrated screen editor for procedures and text
- · floating point and integer arithmetic
- a total of 120 primitives (commands) including 30 graphics commands
- recursion
- assembly-language interface capability



The Terrapin Logo language was developed by the Artificial Intelligence lab at the Massachusetts Institute of Technology.
Terrapin is now authorized by MIT to distribute the results of its 12 years of research to you. To provide quality support for the language, Terrapin has assembled a team that includes two of the three authors who developed the Logo language for the Apple II at MIT, as well as Dr. Feurzeig, the originator of the Logo language.

Every copy of the Terrapin Logo language comes with complete documentation. To run the language, a 48K Apple II with a 16K RAM card or a language card, and one disk drive is required.

Terrapin also offers the robot Turtle, and the following books: Turtle Geometry, Special Technology for Special Children, Mindstorms, Katie & the Computer, and Apple Logo from Byte Books.

Suggested retail price: \$149.95 To order or for more information, call or write:



Terrapin, Inc.678 Massachusetts Avenue
Cambridge, MA 02139
(617) 492-8816

Form -	Description
Form 1040	U.S. Individual Income Tax Return
Schedule A	Itemized Deductions
Schedule B	Interest and Dividend Income
Schedule C Schedule D	Profit (or Loss) from Business or Profession Capital Gains and Losses
Schedule E	Supplemental Income Schedule
Schedule F	Farm Income and Expenses
Schedule G	Income Averaging *
Schedule R&Rp	Credit for Elderly
Schedule SE	Computation of Social Security Self-
	Employment Tax
Schedule TC	Tax Computation Schedule
Form 2106	Employee Business Expenses
Form 3468	Computation of Investment Credit
Form 4562	Depreciation
Form 4726	Maximum Tax on Personal-Service Income
Form 4797	Supplemental Schedule of Gains and Losses
Form 5695	Energy Credits
Form 2210	Underpayment of Estimated Tax by
	Individuals

These additional forms are offered in a special supplement for those who need them.

Form 2119	Sale or Exchange of Principal Residence
Form 4625	Completion of Minimum Tax—Individuals
Form 6251	Alternative Minimum Tax Computation

Table 2: A list of all the forms and schedules handled by Howardsoft's Tax Preparer.

HTP above their level of tax expertise. Inputs need no prior handling if you use the itemization routine, and you make entries as if you were manually completing the return. There is nothing extra to be concerned about and a lot of the bother is taken away. (See table 2 for the forms and schedules which HTP emulates and prints out.)

Both Aardvark and Howardsoft offer updated software to reflect necessary revisions due to changing tax laws. Aardvark makes new versions available to its users within weeks of the passing of tax legislation. Howardsoft publishes its software revisions in January of the next year, because the IRS does not publish the final versions of its new forms and schedules until the end of the calendar year. Both software houses offer these revisions to their customers at a fraction of the cost of the original software. Aardvark and Howardsoft are also periodically expanding and enhancing their software at a reasonable cost.

Conclusions

- Neither Aardvark's Individual Tax Plan nor Howardsoft's Tax Preparer is for the novice. AITP is clearly aimed for use by the tax professional. HTP can be worthwhile for the nonprofessional as well as the professional, but it does require some tax knowledge.
- •AITP is a polished product. It is well structured, clear in its documentation, and easy to use. HTP is an ambitious product, but some refinements would make it easier to use.
- AITP and HTP perform as advertised, and the printouts produced are in accordance with the documentation.
- •AITP is tax-planning software. HTP does tax record keeping and prepares and prints the tax return. The two programs are not in direct competition. Together they include all phases of tax preparation.

Acknowledgments

My thanks to Robert Strohsahl of Chips Microcenter, Hanover, New Hampshire, and to C. Bennett Brown, Jr., CPA, of Smith, Batchelder & Rugg, Hanover, New Hampshire, for their kind assistance.

Tax Tips for Computer Owners

Melvyn Feuerman 46-15 Westminster Rd. Great Neck, NY 11020

Melvyn Moller, CPA 25 West 43rd St. New York, NY 10036

The Economic Recovery Tax Act of 1981, signed into law by President Reagan on August 13, 1981, provides the largest tax reduction in our nation's history. We will focus on the tax breaks the new law provides to individuals using computer systems in their trade or business.

One of the major objectives of the Tax Act of 1981 was to encourage companies to invest in capital equipment (such as new computer systems) by simplifying and speeding up the depreciation of equipment and by providing a research and development (R&D) tax credit. Some new business deductions became effective retroactively to January 1, 1981. The R&D tax credit went into effect July 1, 1981.

Business Deductions

The new tax law simplifies the method for computing depreciation on equipment, such as computers used in your business. Effective January 1, 1981 (this tax year!) you may use the new Accelerated Cost Recovery System (ACRS) to compute the amount of depreciation you can take each year. For computer

STOP SOFTWARE FAILURES

Using a micro in a product <u>sounds</u> easy...

One piece of software can make the difference between success and failure.

What do you do when the software doesn't work? Over the years, we have seen many good products fail, either before or after they reached the market, because the microprocessor software did not do its job.

WHAT WENT WRONG?

Many of the failures occurred because the people programming the micro did not know how to organize a large control program. Those responsible for the product implementation were wizards at hardware design and had easily coded small micro control programs before. But the programming techniques that worked for less than 2K bytes of code simply fell apart as the program grew beyond 4K bytes.

Unfortunately, the loops and tests and flags that work so well for a small program get out of control very rapidly as the program grows. Pretty soon, some of the things the program must do are not being done fast enough. The code gets too complicated, difficult to modify and unreliable. The result: another software failure!

Fortunately, these problems can be avoided by using a program manager. You can divide your complex control program into a number of separate, more manageable programs, called *tasks*, each designed to do one job. For example, a Keyboard Task might handle user input; a Printer Task might generate reports. Each task can be written and tested separately and then combined to form a reliable, finished system.

The program manager, called a multitasking executive, supervises the orderly execution of these tasks, assuring that the most important jobs always get done first. Tasks appear to be executing simultaneously. It's almost like having a separate CPU for each task! That is why professional software designers are now turning to AMX as the starting point for their product and system designs. They know that AMX will shield them from the difficulties of managing the micro, freeing them to concentrate on their application.



AMX is our **multitasking executive** for the 8080, 8085, Z80 and 6809 processors. We're rather proud of it. We made AMX compact, very fast, and ROMableto meet our own application needs. Even though the AMX nucleus is less than 1400 bytes in size, it features multiple task priorities, intertask message passing with priority queuing, external event synchronization, and interval timing with 32-bit precision. Each feature is clearly explained in the AMX Reference Manual.

RELIABILITY BUILT IN

We don't know anyone who can write an executive without errors, so we thoroughly tested AMX in real applications before ever offering it as a product. That is why not one system malfunction has ever been attributed to AMX. That kind of reliability just isn't an accident.

HARDWARE INDEPENDENCE

AMX does not require a particular hardware configuration. Of course, it does need a microprocessor, but even there we offer you a choice. You control your environment. You pick the I/O method. You decide the most optimum interrupt service technique for your system. AMX will support your choice.

High level language interface modules are available separately to allow AMX to be used with most popular programming languages including PASCAL, C, PL/M and FORTRAN. Of course, you can also code in assembly language if required.

Users of the CP/M and FLEX Operating Systems can utilize our AMX interface modules to access information on diskette in real time.

COMPLETE DOCUMENTATION

AMX can be judged by the quality of our documentation. The positive response from our users has exceeded our expectations. Our manuals are especially valuable to those just being introduced to real-time multitasking. More experienced users will appreciate the fact that we deliver AMX source on diskette to permit AMX to be moved to the software development system of your choice.

HOW TO ORDER

A specification sheet and price list are available, free. Your check or money order for \$75 will purchase the AMX Reference Manual for immediate evaluation (specify 8080, 8085, Z80 or 6809 processor). Add \$25 for postage and handling outside USA and Canada. The standard AMX Multitasking Executive package, including source code, is available for \$800 after signing our liberal license agreement.

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Vancouver, B.C., Canada V6J 1Y5 Telephone (604) 734-2796 Telex 04-55670 equipment purchased in 1981 the applicable recovery percentages are:

> Year 1 15% Year 2 22% Year 3 21% Year 4 21% Year 5 21%

For example, if you purchased a computer in November 1981 for \$5000 you can depreciate \$750 ($$5000 \times 0.15$) in 1981. You can also get an investment tax credit of 10 percent (\$500) on the purchase of the computer. (It is interesting to note that the socalled "half-year" convention works to the advantage of the taxpayer who buys a computer near the end of 1981. He gets the entire tax deduction and tax credit, although the computer will be used for only a short time in 1981.)

You do not have to use the new ACRS to compute depreciation. You still have the option of computing depreciation using the straight-line method.

The Tax Act did repeal one tax break—the first-year extra depreciation allowance of 20 percent of the cost of the equipment. Equipment that you purchased prior to January 1, 1981 should be depreciated using the same rules that were in effect before the new law.

Hardware and software developers should take note that R&D equipment that they purchased after January 1, 1981 receives special treatment. They get a special tax break that allows them to depreciate R&D equipment over a three-year period. The applicable recovery percentages are:

> Year 1 25% Year 2 38% Year 3 37%

Beginning in 1982, owners of computers (or any capital equipment) will have the option of deducting up to \$5000 for hardware and software purchases made in 1982. This tax break will have the very positive effect of encouraging those budding software and hardware entrepreneurs who work full time and have plenty of W-2 income to purchase a computer system to start their own businesses. This break should be very important to developers of software for the new IBM Personal Computer.

Research and Development Tax Credit

Another perhaps more significant new tax break for software and hardware developers is the Research and Development Tax Credit, which retroactively went into effect July 1, 1981. You won't find too much about this credit in your new 1040 instruction manual from the IRS, but a new Form 6765—Credit for Increasing Research Activities—will help you on lonely nights around April 15, 1982.

The R&D Tax Credit applies if you are launching a new computer product or significantly improving an

existing computer product and you are having additional R&D expenses as compared to the last three years. You can get a tax credit of 25 percent of the increase in R&D expense. You will also have the option of taking all of the R&D expense in one year.

For example, let's assume that you have a software business and that between July 1, 1981 and December 31, 1981 you spent \$15,000 developing a new computer product, such as a new mailing-list program or an improved electronic spreadsheet. Also assume that you spent \$10,000 on R&D between July 1, 1980 and December 31, 1980. Then, if your business is a sole proprietorship you can take the \$15,000 as a business expense on Schedule C and you can take a tax credit of \$1250 (25 percent of the \$5000 R&D increase) as an R&D Tax Credit on form 1040.

The R&D Tax Credit is of less value to companies that have had little R&D expense in prior years. For example, the R&D Tax Credit for a new business is only 12.5 percent of R&D expenses.

New Penalties

One final comment on the depreciation and R&D tax credits that we have outlined above. They can be used only if you are using your computer in a trade or business. This can be a part-time business, but it cannot be a hobby!

The Tax Act of 1981 also contains additional penalties for taxpayers who file false information, are negligent in their underpayment of taxes, or "pad" or overstate certain deductions. For example, if you underpay your tax because you took too large a deduction for depreciation, you will have to pay a special penalty. Furthermore, interest payments on money you owe the IRS will accumulate at the prime rate of 20 percent established on October 15, 1981. Clearly it is in your best interest to select a competent and honest tax adviser to help you prepare your tax return!

Conclusion

The Tax Act of 1981 should have a very positive effect on the growth of the computer industry. The Tax Act provides incentives for business to purchase computers, and, perhaps most important, it encourages the development of the "cottage industry" of software developers by providing them with R&D tax credits. ■

About the Authors

Melvyn Feuerman is currently the computer systems coordinator for Damson Oil Corporation, one of the nation's largest independent oil and gas companies. Prior to working for Damson, Feuerman was data-processing director of the E.K. Leaton Company, an insurance and pension consulting company. He was also a computer project manager in charge of developing time-sharing tax and financial planning programs for Peat Marwick and Mitchell & Co. He has a BA from CCNY and an MBA from Baruch College.

Melvyn Moller is a Certified Public Accountant who has his own practice in New York City.

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The A40 is a price/performance alternative to the Disk II. With 40 tracks, you get an additional 20K bytes, and faster track-to-track access. The A40 is intended for use in dedicated DOS, CP/M and Pascal applications, and as a companion drive for the A70. The A40 is Micro-Sci's most cost-effective

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Book Reviews

Beyond Games: Systems Software for Your 6502 Personal Computer

Ken Skier BYTE/McGraw-Hill New York, 1981 433 pages, softcover \$14.95

Reviewed by Bob Katz 248 East 90th St. Apt. 3B New York, NY 10028

At last! An assembly-language programming book that develops useful, real-world tools, has *no* mathematical routines, and is written in plain English. In fact, *Beyond Games* not only teaches you how to write programs, it's entertaining.

If you own an Apple II, Ohio Scientific

Challenger I-P, PET 2001, or Atari 800, you'll be able to make *direct* use of the routines developed in this book. But owners of other 6502-based machines (such as KIM, SYM, AIM, etc.) need not despair—Ken Skier's routines interface directly with a microprocessor's *software*, not with any system-specific hardware.

For example, Skier develops a textediting program step by step. One of the first things this program must do is find the ASCII value of a key that has been pressed. Skier teaches us that calling a subroutine is a sound programming technique to perform the maneuver. He gives this subroutine the name GETKEY. All microcomputers that have keyboards already contain the housekeeping routines used to get the value of a key. Some computers call it GETKEY, others may call it by a different name, e.g., GETCHR for "get character." But essentially this subroutine always reduces to a single ROM (read-only memory) address which may be called from Skier's main program.

Skier has researched this calling ad-

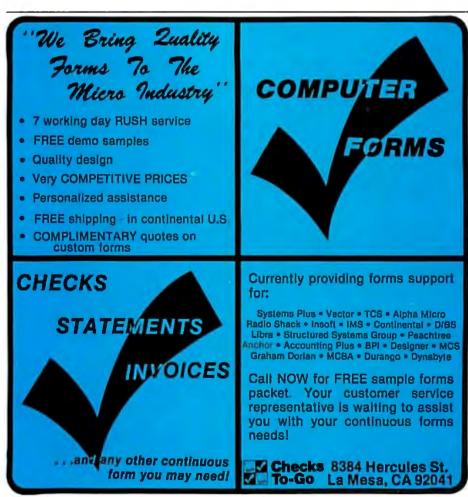
dress, as well as the addresses of all other necessary subroutines within the Apple II and the other computers. Beyond Games contains specific Apple, Atari, PET, and OSI versions of a machine-language texteditor program, visible-monitor program, print utilities, and screen-management utilities. These programs are identical in their assembly-language source-code form, regardless of the computer. Thus, owners of other 6502-based computers who wish to use Skier's programs can look up the addresses of their GETKEY or other routines, then substitute these addresses. The documentation provided with a computer should give the addresses of important ROM subroutines.

You may wish to develop an assembly-language or machine-language program on your own, or alter some of the routines for a specific computer not directly supported by the book. You should have no trouble doing this. Skier teaches how to structure a program using the "top down" technique and how to deal with problems in little pieces—in other words, how to proceed logically through the writing of an assembly-language program.

A word about the specific routines. Skier's text editor is very basic and is not designed to be a word processor. It is designed to write and edit text for inserting (and deleting) strings of any size into any memory location. Even if you don't need any of the routines he provides, the exercise of reading Beyond Games will teach you just how a text-editing program is constructed. That alone is worth the price of the book.

If you do decide to use his routines, Skier provides several means to load them into your computer. The easiest (and most expensive) method is to order a data cassette directly from Skier. The next easiest is to key in the machine-language programs from BASIC by using data statements and Skier's object-code loader. The latter program contains checksums to protect you from entering mistakes into memory. With care you can also load routines directly into memory as hexadecimal bytes.

In conclusion, those programmers who wish to learn how to write such mathematical routines as 16-bit arithmetic and logarithms should look elsewhere; those who wish to learn how to turn on the relay that controls their lawn sprinkler should also look elsewhere. But anyone who wants to learn to create logical machine-language programs, debuggable programs, or well-documented programs, should read *Beyond Games*:



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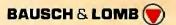
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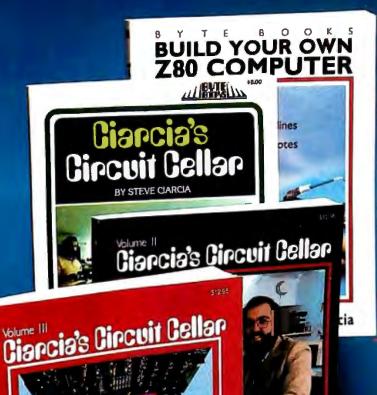
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Hardware Review

Dithertizer II

loe Tomas Computer City 1525 South Willow St. Manchester, NH 03103

The Dithertizer II, a new video-digitizer interface for the Apple II computer, creates high-resolution digitized images that can be printed on any printer that has graphics capability. Most Apple users have probably seen graphics demonstrations with pictures of Winston Churchill, Albert Einstein, or soccer balls. These "pictures" were all created by a video digitizer.

Designed by David Hudson of Computer Stations Inc., the Dithertizer II uses a video camera with external synchronization to load any image that can be captured by the camera into the memory (high-resolution-graphics pages) of an Apple II. The Dithertizer II is a "framegrabber," direct-memory-access-type (DMA) digitizer, requiring only one frame or 1/60 second to capture a binary image. The software lets you create pictures in either of two ways: (1) as a "dithered" gray scale built from multiple binary (black-and-white) images, or (2) as imageintensity contours, using image subtraction from two frames. The number of frames required to create a dithered image is dependent on the dither matrix size. which is selectable via the software. You must use game paddles to adjust the contrast and density of the image being created and view the results on the monitor.

Installation

I ran into a slight problem when I installed my Dithertizer II. The Dithertizer II interface card, which is inserted into slot 7 of the Apple, has two cables attached to it. The first cable has a 6-pin DIN-type connector that attaches to a Sanyo video-camera cable. The second cable is a two-conductor wire with a "piggy-back" IC (integrated circuit) socket at its end. The instructions told me to remove the 74LS34 IC at location C-14 on the Apple's motherboard and replace it with the adapter socket. The instructions placed great emphasis on the orientation of pin 1 when inserting the adapter socket. Next, I reinserted the 74LS34 IC into the adapter, which completed the installation.

After checking the installation, I was ready to go. I mounted the camera on a tripod, aimed it at myself, and booted the software. According to the instructions, the display monitor should have displayed a dithered image. Unfortunately, Murphy's law prevailed—all I saw on the video display screen was diagonal scan lines. Turning the system off, I double-checked the installation. It seemed odd that when the adapter socket was inserted at location C-14, the two-wire cable should extend out the front of the socket rather than the back, especially since the interface card was located behind the socket. Even though pin 1 was properly oriented, I removed the 74LS34, reversed

At a Glance

Name

Dithertizer II

A high-speed frame-grabber, DMA-type video digitizer designed to create computerized images or pictures.

Manufacturer

Computer Stations Inc. 11610 Page Service Dr. St Louis, MO 63141

Dithertizer II interface, \$300.00; Sanyo VC1610X Video Camera, \$410.00; Package System Price, \$650.00.

Hardware required

Apple II or Apple II Plus, 48K bytes of user memory, one floppy-disk drive with controller, game paddles, video monitor or TV with RF (radio-frequency) modulator, one of the following printers with appropriate interface: Integral Data Systems models 225, 440G, 445G, 460G, 560G, NEC Spinwriter models 5510 or 5520. Anadex models DP9500 or DP9501.

Software required

Dithertizer software included.

Software options

Computer Stations Enhanced Graphics Software for the appropriate printer, Price: \$44.95.

Documentation

17-page hardcover notebook-style manual.

Home hobbyists, photo studios, attention getter for trade shows, motion detection.

the socket, and replaced the IC. Holding my breath, I again turned the system on and behold: it worked. Obviously, the adapter had been miswired. Fortunately, no damage occurred.

The Dithertizer II software contains machine-language



Figure 1: A "dithered" image of the author, as rendered by the Dithertizer II.

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routines for frame-grabbing, dithering, and contouring. It includes a demonstration program, written in BASIC, that shows the use of all three routines. The software is supplied in DOS 3.2.1 format, and I had no problem in MUFFINing it to DOS 3.3 format.

Implementation

Using the Dithertizer II is very simple. Game paddles are used to adjust the displayed image. Paddle 0 sets the black level, while paddle 1 adjusts the contrast or gray tones. Other options, selectable via single-keystroke commands, allow dithering, contouring, freezing the image, saving image to disk, printing the image, and more. Pressing H (for HELP) will display a menu listing all commands and options.

The documentation is short, but it is complete and easily understood. After reading it, I started experimenting, and it took me only a few minutes to become accustomed to image processing. The only part I had difficulty with was determining the amount of gray scale required to create a well-balanced or shaded image. With a little trial and error, I was soon printing good-quality images.

Focusing the camera is important in order to create a sharp image. The Sanyo camera is not a conventional video camera as used on VCRs (video-cassette recorders). but a commercial camera like those used in closed-circuit systems. Unlike VCR-type cameras, the Sanyo does not have through-the-lens viewing to facilitate focusing. The focusing-adjustment ring on the lens is calibrated reasonably well; however, it is difficult to obtain accurate focusing at close range. To overcome this problem, I attached a cable to the RF (radio-frequency) output connector of the camera and then connected it temporarily to the input of my video monitor. This allowed me to focus the camera accourately. Then I disconnected the cable and plugged the monitor back into the Apple. Incidentally, you can make close-up shots (as close as two to three inches) by carefully unscrewing the camera lens to change its focal length. Also, use a white background if you plan to do portrait or high-contrast work (see figure 1). A white background allows better resolution and

Despite the fact that the Sanyo camera is designed for black-and-white images, I found that I was able to achieve better gray scale and shading by using a color video monitor. The color monitor displayed some gray shades as "blue over gray." This enabled me to determine differences in gray scale, which ultimately resulted in higher-resolution images. A black-and-white monitor made this slightly more difficult to accomplish.

As supplied, the software does not have print routines installed. Assuming you have a printer with dot-graphics capability, you must either write your own print drivers or purchase Computer Stations' Enhanced Graphics Software. This software is available for Integral Data Systems Paper Tiger printers as well as for the NEC Spinwriter models 5510 and 5520 and Anadex models DP9500 and DP9501. The addendum I received with the

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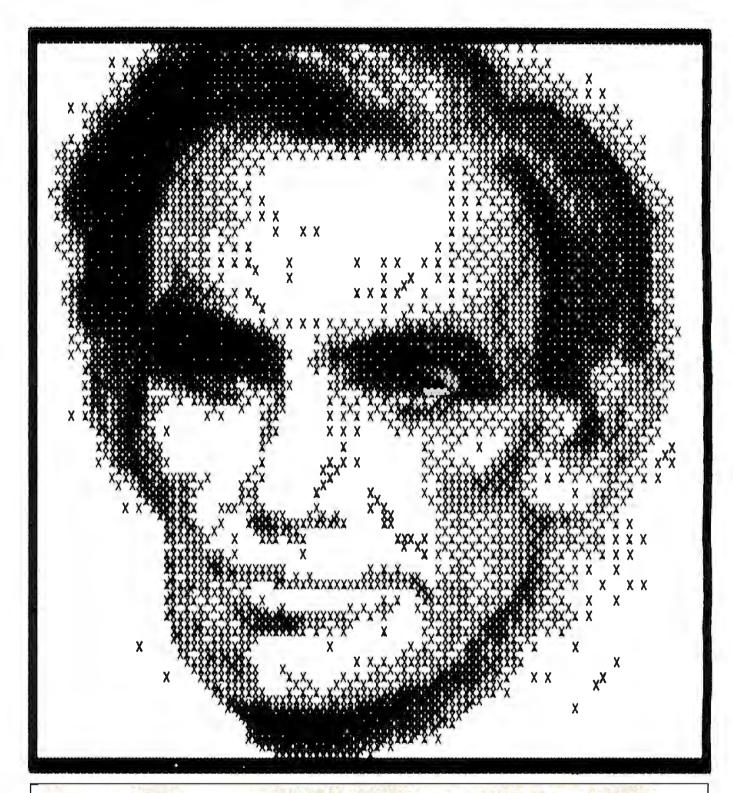
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Printer Driver Packages

Several software packages allow Apple II high-resolution graphics to be printed out as hard copy. The pictures accompanying this article were printed with Computer Stations' software drivers for the IDS Paper Tiger. Computer Stations also sells the Enhanced Graphics Software package for the Epson MX-80 dot-matrix printer. Pictures can be created with a graphics tablet or with the Dithertizer II and are saved as binary disk files. This package requires an MX-80 equipped with the Graftrax 80 high-resolution option, costs \$44.95, and is available from Computer Stations, 11610 Page

Service Dr., St Louis, MO 63141.

Progressive Software has released its Graphics Printing System for the Diablo and NEC full-character printers. The program prints the graphic image from the high-resolution screen to the printer via the Apple High Speed Serial Interface card (or equivalent). The picture above of Abraham Lincoln is an example of the Graphics Printing System's output. The package can be used with a Diablo 1620 or 1640 or with a NEC Spinwriter 5510 or 5520, costs \$109.95, and is available from Progressive Software, Suite 323-Blue Bell West, Blue Bell, PA 19422.

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Figure 2: The cover of BYTE, November 1980. Both figure 1 and figure 2 were created on an Integral Data Systems 460G dotmatrix printer.

routine. Additional information concerning the various machine-language routines used is included to assist you in writing your own special-application programs.

Conclusions

The Dithertizer II is a well-constructed video digitizer that does all that its manufacturer claims. The interface card consists of seven ICs, plus a handful of other components, and is very clean in construction. At first glance, the Dithertizer II seems a little overpriced, considering the number of components on the circuit board. However, when you take the developmental costs into consideration, the price seems quite reasonable.

Preliminary releases of the Dithertizer II had only a seven-page instruction manual; it was easily understood and quite complete. George Baltzell of Computer Stations has informed me that new, expanded documentation is now being shipped with the product.

Practical applications? Aside from hobbyist uses, other applications might include motion detection for security systems, an attention-getter for trade shows, advertising, artwork layout (see figure 2), and photo-studio uses. My primary reason for getting the Dithertizer II was for promotional and publicity-type advertising. (I offer a free portrait to any of my customers.) All in all, I have been quite pleased with the product, and we plan to put it to use not only here, but in the grand openings in several of our new stores.

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A Guided Tour of Apple Pascal Units and Libraries

Ross M. Tonkens MD **Suite 1185-W** 8635 West Third St. Los Angeles, CA 90048

One of the most powerful features of Apple Pascal is its extensibility via a unit. Similar in structure to Pascal programs, units have peculiarities that can render them mysterious to UCSD Pascal newcomers.

To clear up some of these mysteries, we will begin by considering what a unit does and how it differs from both a program and an external procedure or function, and then we'll study two units that have markedly different purposes. Next, we will examine the process of compiling and linking these units and binding them to your SYSTEM.LIBRARY.

In addition, I have provided a listing of a Pascal program that, when saved on the system disk as SYSTEM. STARTUP, places a color test pattern and the system date on the screen when the Apple/UCSD system is booted up (see listing 2). This program uses the CALENDAR unit (discussed later), as well as the Pascal

About the Author

Dr. Tonkens is a cardiologist with a background in small-computer systems. In 1980 he was engaged in full-time research on computerassisted image-enhancement techniques for real-time two-dimensional echocardiographic images. He continues to act as a consultant for private industry on medical-image processing and database management.

units, **TURTLEGRAPHICS** and APPLESTUFF, that are already resident in the SYSTEM.LIBRARY.

Anyone who first learned programming in BASIC probably finds the lack of direct access to absolute memory one of the few frustrations of Pascal. For those who are unfamiliar with UCSD Pascal (University of California, San Diego), and Pascal in general, the language cannot express the concept of absolute addressing. (BASIC accomplishes this with the CALL < address > statement.) Even assembled machine-code external procedures called by the Pascal host program are automatically relocated at the time of their linkage to the host. (The host program is the Pascal program that calls an externally compiled or assembled subroutine.)

Some Definitions

Let me clarify two terms that will be used frequently throughout the remainder of this article: source files and object files. When we refer to a source file, we mean the English-like representation of a program, external subroutine, or unit. The source file is the text you type in through an editor like the one in the Apple Pascal operating system.

If this text file conforms with cer-

tain syntax rules, the compiler or assembler will turn this text file into the code form that the computer actually executes at run time. This code file is called the object file; it contains object code that is generally not human readable. The object code is called p-code (pseudocode) if derived from a UCSD Pascal source file, or 6502 machine language if derived from an assembly-language source file through use of the system's assembler. The important point is that the source file is what you write, and the object file is what the computer executes at run time. Both are versions of the same program, external subroutine, or

Most of the time, UCSD Pascal's automatic memory management is convenient and frees the programmer from worrying about such things as overstepping allotted memory boundaries and inadvertently erasing parts of the system program. But what if you have a useful EPROM (erasable programmable read-only memory) with no source file, and many of the machine-language routines on that EPROM could be of tremendous use in your Pascal programs if only they could be accessed? There is no way to specify the absolute address of that EPROM, or of a routine within it, from a standard

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P.O. Box 3297 Santa Ana, CA 92703 Phone: 714/731-4338 TWX: 910 595 1146 Pascal host program.

Similarly, the Apple II contains many software "switches" of great use to the BASIC programmer that are available via PEEKs and POKEs, but are inaccessible from Apple Pascal.

The UCSD Pascal operating system allows for extensibility of the language by the user in order to fill special needs (like direct addressing of memory) through the use of units. A unit is a compiled subroutine (or more usually a collection of compiled subroutines) that essentially adds new commands to off-the-shelf UCSD Pascal. For instance, a computer musician might have use for a unit that added commands for producing notes of specified pitch. Indeed, UCSD Pascal was customized for the Apple II, through the use of units, for implementing such special functions as producing high-resolution graphics (TURTLEGRAPHICS) and reading the game paddles and generating sound (APPLESTUFF).

There are also commercial units for sale, and soon you will be able to choose from a selection of "canned" units for specialized programming purposes.

Two sample unit listings are shown in listing 1. The first, called WINDOW, provides access to the Apple II's memory by adding PEEK, POKE, and CALL instructions to your Apple's Pascal vocabulary. The second, called CALENDAR, reads the area of the system disk where the system date is stored and makes it accessible to the programmer.

The Power of a Unit

Let us look a little more closely at a unit. Unlike a standard Pascal procedure or function, a unit can exist separately from the body of the main program text and still be incorporated within a Pascal program's object code at run time. But if this were the whole story, a unit would have no advantage over an external procedure.

The power of a unit lies in its ability to house multiple (hopefully related) procedures or functions, both in Pascal and in assembly language, under one roof. All of these proce-

Text continued on page 234

Listing 1: Two sample units for Apple Pascal. In listing 1a, WINDOW provides access to the Apple's memory by absolute address through the BASIC-like instructions PEEK, POKE, and CALL. In listing 1b, CALENDAR reads the date from the system disk and makes it accessible to the user.

```
listing 1a
                                                                      -32767.,32767*
       INTRINSIC UNIT WINDOW
                                                    *NOTE THAT THIS UNIT ACCEPTS OUT*
                                                     *OF RANGE DATA (O > DATA > 255) *
                                                     *BY STORING =>ABS(DATA MOD 256)*
***********
                                                     ************
      (* ROSS M. TONKENS, M.D. *)
                                                   PROCEDURE CALL(ADDR: INTEGER);
                                                    (*VER.01.09.81*)
                                                     *EMULATES BASIC'S "CALL" COMMAND*
              ( *$S+ * )
  (*SWAPPING ON FOR UNIT COMPILATION*)
                                                     *THIS IS A "FRONT END" FOR
                                                     *INSTALLING ASSEMBLY LANGUAGE *
                                                            .PROC CALL.ASSY
UNIT WINDOW; INTRINSIC CODE 23 DATA 24;
                                                     *IN THIS INTRINSIC UNIT.
                                                     ************
INTERFACE
                                                    IMPLEMENTATION
*PROVIDES A "WINDOW" FROM UCSD/PASCAL *
*INTO ADDRESSABLE MEMORY, THIS ALLOWS*
                                                    TYPE BYTE = PACKED ARRAY [0..1] OF 0..255;
*MANIPULATION OF DATA AT THE BYTE
                                                        DIRTY = RECORD
*LEVEL AS WELL AS CALLS TO MACHINE
                                                                 CASE BOOLEAN OF
*CODE ROUTINES AT ABSOLUTE LOCATIONS *
                                                                  TRUE : (INT: INTEGER);
*(AS IN A ROM) DIRECTLY FROM PASCAL.
                                                                  FALSE: (PTR: ^BYTE);
                                                                 END:
*IN ESSENCE THIS UNIT ADDS THE
                                                     (*THIS DEFINES A VARIANT RECORD WHICH
*FAMILIAR BASIC COMMANDS:
                                                      WILL MAP TO AN ABSOLUTE HARDWARE
                                                      ADDRESS IN THE APPLE
       PEEK, POKE, AND CALL
*TO UCSD PASCAL.
                                                    VAR TRICK : DIRTY;
                                                    PROCEDURE CHECK(VAR DATA: INTEGER);
PROCEDURE POKE(ADDR, DATA: INTEGER);
                                                    FORWARD:
( ************
 *EMULATES BASIC'S "POKE" COMMAND*
                                                   PROCEDURE POKE;
 *INVOCATION => POKE(ADDR, DATA) *
                                                    CHECK(DATA);
                                                    TRICK. INT: = ADDR;
FUNCTION PEEK(ADDR: INTEGER): INTEGER;
                                                    TRICK.PTR^[0]:= DATA
                                                   END:
 *EMULATES BASIC'S "PEEK" COMMAND*
 *INVOCATION => DATA:= PEEK(ADDR)*
                                                   FUNCTION PEEK;
 *******************************
                                                   BEGIN
                                                    TRICK.INT:= ADDR;
                                                    PEEK:= TRICK.PTR^[0]
( ****************************
 *BOTH ADDR AND DATA MUST BE *
                                                   PROCEDURE CHECK;
 *INTEGER VARIABLES NOT CONSTANTS*
                                                    (*THIS ASSURES ONLY VALID DATA
 *ADDR MUST BE IN THE RANGE :
                                                      WILL GET POKED.
```



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Listing 1 continued:
 DATA: = ABS(DATA MOD 256);
END:
PROCEDURE CALL;
 EXTERNAL;
BEGIN
 ( *DUMMY INITIALIZATION*)
END.
     .TITLE "*PROCEDURE TO EMULATE BASIC'S 'CALL'*"
I
     ROSS M. TONKENS, M.D.
      VER.01.09.81.13
2
     .MACRO POP
                    ; POPS 16 BIT ADDRESS
     PLA
     STA
                 21
     PLA
     STA
                 81+1
     . ENDM
     .MACRO PUSH
                    ; PUSHES (RETURN) ADDRESS BACK ONTO STACK
     LDA
                 81+1
     PHA
     LDA
                 81
     DHA
     .ENDM
     .PROC CALL,1
 PROGRAM TO CREATE A CALL FUNCTION FOR
 PASCAL IN THE APPLE II
 USE THIS ASSEMBLY LANGUAGE PROGRAM TO
 CALL PROGRAMS THAT ARE NOT NORMALLY
  ACCESSIBLE FROM PASCAL.
 TO USE: ASSEMBLE THIS PROGRAM
           AND SAVE THE CODE FILE ON
           <YOURDISKNAME> AS
               CALL.ASSY, CODE
           THEN
           EITHER
             LINK TO INTRINSIC UNIT "WINDOW"
             LINK DIRECTLY TO YOUR HOST PROGRAM
             AS FOLLOWS:
           1. DEFINE A PROCEDURE IN YOUR
             PROGRAM:
7
             PROCEDURE CALL(ADDR);
             EXTERNAL;
              (ADDR MUST BE AN INTEGER VARIABLE.)
```

Listing 1 continued on page 230

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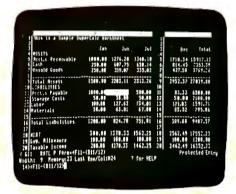
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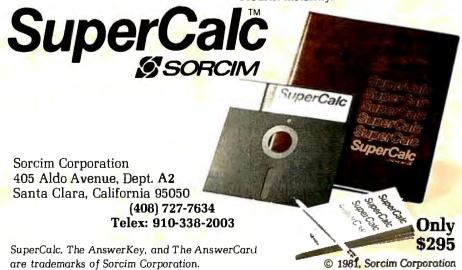


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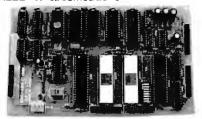
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```
Listing 1 continued:
           2.COMPILE YOUR PROGRAM, AND THEN RUN
             THE LINKER.
7
           3. WHEN ASKED FOR THE LIB. NAME, TYPE:
              cYOURDISKNAME>:CALL.ASSY.CODE
  WARNING: ANY PROGRAM WHICH CHANGES MEMORY
           LOCATIONS MAY INTERFERE WITH
           THE PASCAL OPERATING SYSTEM.
RETURN
        . EOU
                o
YRCALL
       . EQU
1
3
        POP
                RETURN ; SAVE PASCAL RETURN ADDRESS;
        POP
                YRCALL ; SAVE OUR CALLING ADDR;
        PUSH
                RETURN ; PUT BACK ON STACK;
        JMP
                GYRCALL; VECTOR TO PASSED ADDRESS PARAMETER
        . END
listing 1b
( *$S+,R-*)
( *RANGE CHECKING OFF BECAUSE ONLY BYTE #11, WHICH IS UNITREAD
( *BLOCK #2 CAN BE COUNTED ON TO COMPLY WITH RANGE CONSTRAINTS
       INTRINSIC UNIT CALENDAR
**********
       (* ROSS M. TONKENS, M.D. *)
          (*VER.O1.19.81.03*)
UNIT CALENDAR; INTRINSIC CODE 25 DATA 26;
INTERFACE
 *PASSES CURRENT SYSTEM DATE INTO THE *
 *VARIABLES:
               THISDATE:
                               1..31
               THISMONTH:
                                1..12
               THISYEAR:
                                1..99
 *AND RETURNS DATE AS A STRING WITH
 *LEADING AND TRAILING BLANKS AS THE
 *GLOBAL VARIABLE, "TODAY," WHICH HAS
 *THE FORM:
  <SP><MONTH><SP><DAY><, 19><YEAR><SP>*
                 OR
         <SP>JAN 20, 1981<SP>
 *THIS IS ACCOMPLISHED AUTOMATICALLY
 *AT RUNTIME FOR ANY PROGRAM USING THIS*
```

*UNIT, SO THAT FOR ALL PRACTICAL PUR- *

POSES THE PROGRAM "WAKES UP" WITH ALL

*THE ABOVE VARIABLES PREINITIALIZED. *

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END. (*INITIALIZATION*)

```
Listing 1 continued:
VAR
THISDATE
               : 1..31;
THISMONTH
               : 1..12;
THISYEAR
               : 1..99;
TODAY
               : STRING[14];
PROCEDURE DUMMY:
(*A PROCEDURE IS EXPECTED BY COMPILER AT END OF ANY INTERFACE SECTION*)
IMPLEMENTATION
TYPE
DATE
               = PACKED RECORD
                                MONTH
                                       : 1..12;
                                DAY : 1..31;
                                YEAR
                                      £ 0..99;
                        END:
VAR
BLOCK
               : ARRAY[0..10] OF DATE;
MONTHNAME
               : STRING[3];
DY, YR
               : STRING;
   PROCEDURE DUMMY;
     BEGIN
       ( *DUMMY* )
     END:
BEGIN (*INITIALIZATION*)
   UNITREAD(4, BLOCK, SIZEOF(BLOCK), 2);
   (*PACKED ARRAY, "BLOCK," IS MAPPED ONTO FIRST 11 BYTES*)
   (*OF BLOCK 2 ON BOOT DISK IN FILE UNIT #4. ARRAY HAS *)
   (*SIZE OF 11 BYTES BECAUSE THE DATE IS IN 11TH BYTE OF*)
   (*DISK BLOCK #2, AND WE NEED A WAY OF INDEXING TO THE *)
   (*ELEVENTH BYTE.
   WITH BLOCK[10] DO
     BEGIN
       THISMONTH: = MONTH;
       THISDATE := DAY;
       THISYEAR := YEAR
   CASE THISMONTH OF
                      1: MONTHNAME: = 'JAN';
                      2: MONTHNAME: = 'FEB';
                      3: MONTHNAME:= 'MAR';
                      4: MONTHNAME: = 'APR';
                      5: MONTHNAME: = 'MAY';
                      6: MONTHNAME: = 'JUN';
                      7: MONTHNAME:= 'JUL';
                      B: MONTHNAME := 'AUG';
                      9: MONTHNAME: = 'SEP';
                     10: MONTHNAME: = 'OCT';
                     11: MONTHNAME: = 'NOV';
                     12: MONTHNAME: = 'DEC';
   END; ( *CASE*)
   STR(THISDATE, DY);
  STR(THISYEAR, YR);
  TODAY:= CONCAT('', MONTHNAME, '', DY, ', 19', YR, '')
```

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Text continued from page 226:

dures and functions are available from within a Pascal host program just as if they and their related constants, types, and variables had been declared globally within the host program itself. As a matter of fact, units may even be nested (ie: one unit may employ another unit in its construction).

In order to graft the procedures and functions declared within a unit onto a Pascal host program, you need only include the reserved word USES, followed by the name of the unit, after the program heading (assuming the unit has been installed in SYSTEM.LIBRARY on the system disk; otherwise, see page 69 of the Apple Pascal Language Reference Manual).

Units come in two varieties: regular and intrinsic. While a regular unit becomes incorporated into the code file of the host program at compile time, it must be explicitly linked at the time of compilation. (Linkage can be thought of as the process of grafting an external subroutine onto a Pascal host program.) In this sense a

regular unit is quite similar to an external procedure or function, except that it allows you to link many procedures and functions simultaneously. Once linked, a copy of the regular unit's object code actually resides within the host program's object-code file. Thus a regular unit, once linked, need no longer be present in the system at the time the host program is run because a copy has already become part of the host program.

On the other hand, an intrinsic unit must reside in a special file called SYSTEM.LIBRARY on the system disk when a host program calling it is executed. This is because an intrinsic unit is linked to the host program and loaded into memory with it at the time the host program is run. (In the latest update of Apple/UCSD Pascal Version 1.1, the programmer can even specify that a portion of a program reside in main memory only while it is actually executing.) The Pascal host program contains no image within it of any intrinsic units it employs, and it expects to find

those intrinsic units in SYSTEM, LIBRARY.

The advantage of this is that linkage is accomplished automatically at run time. When you debug a Pascal program, you are continually revising the source code and recompiling. This process can be tedious enough, especially if the program is long, but recurrent relinking can render it unbearable. Even though the RUN command invokes an attempt at automatic relinking of all external procedures and functions, linking still takes a lot of time. Intrinsic units, on the other hand, are essentially "prelinked" and waste not a second at compile time-a real blessing if you do a lot of programming.

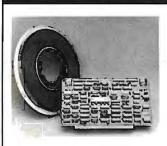
In comparison to the hardware domain, an intrinsic unit is like a computer peripheral with a standard plug configuration through which it communicates with the computer. You simply plug it into the computer to make it work. A regular unit is more like a peripheral to which connections from the computer must be individually soldered at the time of interfacing.

A Specific Example

Like a Pascal program, a unit is a set of algorithms draped over an orderly superstructure. This superstructure is illustrated in the WINDOW unit of listing 1. We will study the general structure of units through this example.

First, note that the compiler SWAPPING option must be enabled, (*\$\$+*), in order to compile any unit. Next, the heading, UNIT WINDOW, identifies this text to the compiler as a unit, as opposed to a program or external procedure.

INTRINSIC designates this as an intrinsic unit; that is, one that is "prelinked." Returning to the hardware analogy, CODE 23 and DATA 24 are a way of specifying which "pins" on a "standard intrinsic unit connector plug" are active. If you wish to write your own unit, or are just curious about how these CODE and DATA segment numbers are assigned, you can refer to the "Program Segmentation" section of the Addendum to the



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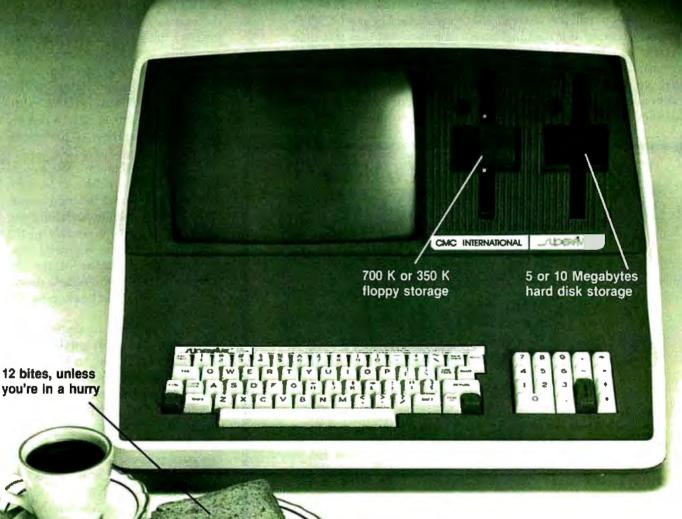
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The interface section of a unit is the only internal detail that is visible from the outside. It is comparable to the socket on the side of a computer peripheral. The interface defines the manner in which the unit can communicate with the UCSD Pascal host program. All the variables in the interface section will be shared with any host program as if they had been declared as global variables within the host. The same holds true for any label, constant, or type declaration within the interface section. If any variables are declared within the in-

terface of an intrinsic unit, a data segment must be declared in addition to an obligatory code segment (see page 76, in the Apple Pascal Language Reference Manual).

The procedure and function declarations of the interface are really the core of the unit. The names of these procedures and functions will become, in essence, new words in the vocabulary of any UCSD Pascal host program that uses that unit.

Through the use of units, there is virtually no practical limit on the number of new commands you can teach your system to recognize. The interface's procedure and function

declarations are abbreviated to the procedure or function name plus parameters, as if they were FORWARD declarations in a standard Pascal program.

One peculiarity of units is that Apple/UCSD Pascal assumes you are writing the unit for the explicit purpose of declaring procedures and functions in the interface. Therefore, the manuals never mention that the interface must contain at least one procedure or function declaration. (If, like me, you always manage to stumble on the exception to the rule—as in UNIT CALENDAR in listing 1—then you must insert a dummy procedure declaration at the end of the interface.)

The implementation section contains any label, constant, type, variable, procedure, and function declarations that are private to the unit and not intended to be accessible to the Pascal host program. Following this, we find the expansion of the abbreviated (FORWARD-like) procedure and function declarations of the interface section.

Finally, we come to the initialization section, which is similar to the main part of a Pascal program. This section is optional, and, as long as the last END; of the last procedure or function is followed by an additional END. statement (note the period), the compiler will remain quite happy. The usual purpose of the initialization section is to perform some sort of housekeeping or setup task in preparation for use of the unit's new commands by the host program. The initialization is executed first, before any of the host program's own code, as soon as the host program is invoked. An example given in the Apple Pascal Language Reference Manual is the table of trigonometric values that the initialization section of the TRANSCEND unit generates in main memory for later reference by the trigonometric functions this unit adds to standard UCSD Pascal.

If included, the text for the initialization section is sandwiched between a BEGIN and the unit's final END. (whose period signals the end of text to the compiler). I have in-

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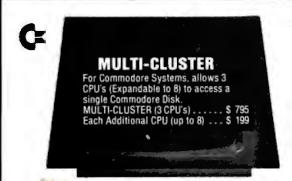
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Computer Furniture and Accessories, Inc. 1441 West 132nd Street Gardena, CA 90249 (213) 327-7710 cluded a dummy initialization section for illustrative purposes in the listing of WINDOW.

Using Units

It is instructive to compare the initialization section of the CALEN-DAR listing with the dummy version in the WINDOW listing. In CALEN-DAR, the initialization section is used to read an area of the system disk and load data from this area into public variables declared in the interface section. No procedures or functions are declared in the interface section of this unit (except for a dummy procedure, as described previously). Thus, when any program that employs CALENDAR begins execution, the first action undertaken is a reading of system date information from the system disk and storage of the information in variables that can be accessed immediately by the host program. To the host program, these preinitialized variables look the same as constants since they already contain values before the main program even begins execution.

As an aside, a unit can be built within a skeleton program designed to exercise and test it. Just substitute the expanded unit terminated by an END; (note the semicolon) where the USES. <unitname> declaration would normally appear. When the surrounding program runs as expected, the unit may be "shelled" out like a peanut, recompiled (after exchanging the final semicolon for a period), and used as is or bound into a collection of units (called a *library file*) on disk.

This brings us to the task of compiling the listed units and binding them into the SYSTEM.LIBRARY. If you have only one disk drive you would be best served by reading and understanding the following, but also sending for a disk with all of the files on it (see the information in the text box on page 244). This will save an inordinate amount of juggling to fit many obligatory files on one 5-inch disk. If you have two or more drives, and have never had the experience of compiling and linking a unit and installing it in a library, I heartily re-

commend that you type in all the text from the listings and see the instructions that follow. (You should be seated at a Language-Card-equipped Apple II as you read the remainder of this article.)

To begin, enter the UCSD editor and type in the text file for the INTRINSIC UNIT WINDOW. Compile it, and save both text and code files on disk APPLE2, as U.WINDOW. TEXT and U.WINDOW.CODE. Next, type in the assembly-language listing, CALL, assemble it (by typing A from the command level), and save text and code files on disk APPLE2 as CALL.ASSY.TEXT and CALL.ASSY.CODE.

Now you must link the external procedure, CALL.ASSY.CODE, to the host unit, U.WINDOW.CODE. Type L from the command level to invoke the linker. You should ultimately see the question:

HOST FILE?

Type APPLE2:U.WINDOW.CODE and then hit the Return key (the .CODE suffix may be omitted when using the updated Pascal version 1.1). Next, you will be asked:

LIB FILE?

to which you should answer, CALL.ASSY.CODE and hit the Return key. The question will be repeated. This time you simply hit the Return key. The next question:

MAP FILE?

asks where you wish to send messages concerning the progress of the linking process. You might find it instructive to reply CONSOLE: so you can read the linker messages on the screen. Finally, you will be asked for the name of the object-code file to which you wish the finished, linked version sent with the prompt:

OUTPUT FILE?

Answer with APPLE2:U.WINDOW. CODE, followed by Return. At this

Text continued on page 244



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Listing 2: Apple Pascal program to display a high-resolution color test pattern and the system-disk date.

```
STARTUP
             (*$S+*)
    (* ROSS M. TONKENS, M.D. *)
       (*VER.01.24.81.01*)
( *************
 *PRODUCES A SIX COLOR HIGH RESOLUTION*
 *COLOR BAR TEST PATTERN WITH THE
*SYSTEM DATE DISPLAYED IN THE CENTER *
*ALONG WITH ANY GREETING OR MESSAGE *
*THE USER MAY DESIRE.
 *WHEN THIS PROGRAM IS SAVED ON THE
 *BOOT DISKETTE AS
          "SYSTEM.STARTUP"
 *THE APPLE WILL "WAKE UP" DISPLAYING *
 *A COLOR TEST PATTERN AND WHAT IT
 *BELIEVES TO BE THE CORRECT DATE.
 *THUS SAVING THE USER FROM HAVING TO *
 *INVOKE THE FILER TO CHECK THE DATE *
 *AFTER BOOTING. THIS IS ACCOMPLISHED*
 *BY BLOCKREADING THE AREA OF THE BOOT*
 *DISK WHERE THE SYSTEM DATE IS STORED*
```



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```
*AND DISPLAYING THIS INFORMATION ON
 *THE HIRES SCREEN. THE METHOD IS
 *THEREFORE VALID BOTH FOR MANUAL
 *UPDATE SYSTEMS AS WELL AS FOR THOSE *
 *SYSTEMS CONTAINING A CLOCK WHICH
 *AUTOMATICALLY UPDATES THE SYSTEM
 *DATE ON THE BOOT DISKETTE.
 USES TURTLEGRAPHICS, APPLESTUFF, CALENDAR;
  YOU SHOULD FIRST BIND THE UNIT, "CALENDAR,"
  TO THE SYSTEM.LIBRARY (SEE ACCOMPANYING
  ARTICLE) BEFORE COMPILING THIS PROGRAM.
  THIS IS BECAUSE "SYSTEM.LIBRARY" IS WHERE
  THE COMPILER EXPECTS TO FIND ALL "INTRINSIC"
CONST
MINX
                       O; (*HIRES SCREEN BOUNDS*)
MINY
                       0; (* "
MAXX
                     279; (*
                     191; (* *
MAXY
                       7; (*HIRES CHAR WIDTH
CHARWD
                                                * )
CHARHT
                       8; (*HIRES CHAR HEIGHT
VAR
LEFT,
RIGHT,
TOP.
BOTTOM,
COLOR.
INC
        : INTEGER;
 PROCEDURE BAR:
  (*DRAWS THE VERTICAL COLOR BARS ON THE SCREEN*)
  (*ONLY 5 COLORS USED SINCE BORDER AND TEXT
  (*WINDOWS ARE IMPLICITLY BLACK, THE 6TH COLOR*)
  VAR
  COLR: SCREENCOLOR;
    BEGIN
      CASE COLOR OF
                    1: COLR:= WHITE;
                    2: COLR:= BLUE;
                    3: COLR:= ORANGE;
                    4: COLR:= GREEN;
                    5: COLR:= VIOLET
      VIEWPORT(LEFT, RIGHT, TOP, BOTTOM);
      FILLSCREEN(COLR);
      IF COLOR < 5 THEN
        BEGIN
          LEFT: = LEFT + INC:
          RIGHT:= RIGHT + INC
        END
                            Listing 2 continued on page 242
    END;
```

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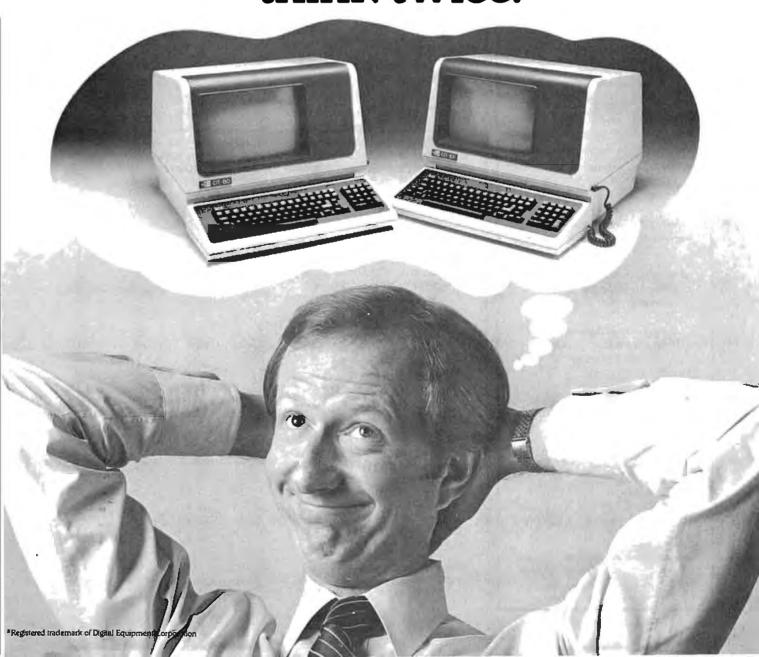
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```
Listing 2 continued:
      PROCEDURE MESSAGE;
      (*"LOADS" PROCEDURE SAYIT WITH USER MESSAGE STRING*)
      VAR
      MSSG
                      : STRING;
      VTAR
                      : 1..24;
      CH
                      : CHAR;
    ( *TODAY
                      : STRING;
                                    PREDECLARED IN "UNIT CALENDAR"*)
    PROCEDURE SAYIT:
    ( *CALCULATES COORDINATES FOR CENTERING USER*)
    (*MESSAGE ON THE HIRES SCREEN AND PRINTS IT*)
    VAR
    X, Y: INTEGER;
      BEGIN
        X:= ROUND((280 - LENGTH(MSSG) * CHARWD)/2);
        Y := MAXY - VTAB * 8;
        VIEWPORT(X - CHARWD, X + LENGTH(MSSG) * CHARWD + 2 * CHARWD.
                 Y - CHARHT, Y + 2 * CHARHT);
        FILLSCREEN( BLACK );
        MOVETO(X,Y);
        WSTRING( MSSG );
      END:
      SUBSTITUTE YOUR MESSAGES AND VTABS FOR THE
      ONES BELOW. OF COURSE YOU WILL WANT TO KEEP
      THE DATE WHICH IS STORED IN THE PREDECLARED
      STRING VARIABLE "TODAY" FROM "UNIT CALENDAR."
      BEGIN
      MSSG:= ' GOOD DAY, DR. TONKENS! ';
      VTAB:= 8; SAYIT;
      MSSG:= ' WELCOME TO APPLE/UCSD PASCAL 1.1 ';
      VTAB:= 10; SAYIT;
      MSSG:= CONCAT(' THE DATE IS', TODAY);
      VTAB:= 12; SAYIT;
      MSSG:= ' DIGIT ALICE AT YOUR DISPOSAL ';
      VTAB:= 16; SAYIT;
      MSSG:= ' HIT <RETURN> WHEN READY ';
      VTAB:= 22; SAYIT;
      VIEWPORT(MINX, MAXX, MINY, MAXY)
    END;
BEGIN (*STARTUP*)
  INITTURTLE;
  LEFT:= 0; RIGHT:= ROUND(MAXX/5) - 1;
```

```
BEGIN (*STARTUP*)
  INITTURTLE;
  LEFT:= 0;RIGHT:= ROUND(MAXX/5) - 1;
  TOP:= MINY; BOTTOM:= MAXY;
  INC:= RIGHT + 1;
  FOR COLOR:= 1 TO 5 DO
    BAR;
  MESSAGE;
  REPEAT UNTIL KEYPRESS;
  TEXTMODE
END. (*STARTUP*)
```

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Text continued from page 238:

point, WINDOW (currently saved as APPLE2:U.WINDOW.CODE) is ready to be bound to SYSTEM.LIBRARY.

However, before installing WIN-DOW in SYSTEM.LIBRARY you should enter and compile CALEN-DAR from its listing and save the text and code files as APPLE2:U.CALEN-DAR.TEXT and APPLE2:U.CALENDAR.CODE.

At this point a few words are in order about a library file. All objectcode files in UCSD Pascal can be visualized as residing within a "cabinet" having sixteen shelves. Each shelf can hold only one item, called a segment. A segment represents one stand-alone piece of object code. A unit, even one which invokes external assembly-language subroutines, still represents only one segment, since the subroutine, once linked to the unit, becomes an integral part of that unit's object code. The only time a unit occupies more than one "shelf" in the cabinet is when that unit is an intrinsic unit with both code and data segments. (This subject was briefly examined in the discussion of WINDOW.) Pascal programs use only one shelf. This is because any program, no matter how lengthy, is still one stand-alone piece of object code. There are exceptions to this rule if the program is so lengthy that it has to be broken up into pieces, but this subject is beyond the scope of our current discussion (see the "Program Segmentation" section of the Addendum to the Apple Pascal Language Reference Manual).

A library is merely one of these "cabinets" whose shelves contain useful collections of precompiled subroutines instead of a program. If we wish to fill two of the empty "shelves" in SYSTEM.LIBRARY with the WINDOW and CALENDAR units, we begin by executing APPLE3:LIBRARY from the command level. To the prompt:

OUTPUT CODE FILE ->

reply APPLE1:SYSTEM.LIBRARY followed by Return. When

LINK CODE FILE ->

appears, again reply, APPLE1: SYSTEM.LIBRARY and hit Return. Now, when

SLOT TO LINK INTO?

appears, reply = to initiate automatic copying of all the old units into the new library.

Be sure to watch the screen during this process, as you can actually see a dynamic depiction of units being stored in the new library's code slots. You will again be prompted:

SLOT TO LINK INTO?

to which you should reply: N (for new file). Again, you will also be asked:

LINK CODE FILE ->

which you answer with APPLE2: U.WINDOW.CODE Return. Type the following: 1 7 2 8 N. You will see the by now familiar prompt:

LINK CODE FILE ->

Reply, APPLE2: U.CALENDAR. CODE Return. Now to the question:

SLOT TO LINK INTO?

reply as follows: 1 9 2 10 Q.

You will be prompted with the question:

NOTICE?

so that, if you wish, you may type in a copyright or the current date on which you appended this library. This message will then be embedded in the library file on disk for later retrieval through the LIBMAP utility on disk APPLE3. The next Return (with or without a NOTICE) will terminate execution of LIBRARY, returning you to the command level, and replace the old copy of SYSTEM.LIBRARY on disk APPLE1 with your new, appended verison.

If you want a copy of the interface sections of the units in the new SYS-TEM.LIBRARY, simply execute APPLE3:LIBMAP. Answer Y to all

(Y/N)? prompts after specifying APPLE1:SYSTEM.LIBRARY when asked to:

ENTER LIBRARY NAME:

Answer, PRINTER: or CONSOLE:, Return, to the request:

MAP OUTPUT FILE NAME:

and hit Return when asked again, in order to return to the command level.

Conclusion

The extensibility of UCSD Pascal through units is one of its most powerful features, one that is similar in concept to using one of a genii's three magic wishes to ask for more magic wishes.

I hope this article will encourage readers to explore the power of the unit and investigate some of its mysteries.■

Acknowledgments

The author wishes to acknowledge the work of Daniel D. Sokol (see "Notes on Absolute Location Interfaces to Apple Pascal," September 1980 BYTE, page 324), from which many of the programming examples in this article were taken.

For those with only one disk drive (or an aversion to typing) a disk is available with copies of the following files:

- U.WINDOW.TEXT and U.WIN-DOW.CODE
- •CALL.ASSY.TEXT and CALL. ASSY.CODE
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OCTOBER AND JUNE, AND TELL ME HOW MUCH THE LITTLE SALESMEN MADE IN THE MIDDLE-SIZED DEPARTMENTS sophisticated user.

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successful firm is the one with the highest stook price when the simulation ends.

FLIGHT SIMULATOR (Available for all computers)

A realistic and extensiv mathematical simulation of take off, flight and landing. The program utilities aerodynamic equations and the characteristics of a real artford. You can practice insurament approaches and navagation using radiab and compass headings. The more advanced typer can also perform loops, half-rolls and similar aerobatic maneuvers. Although this program does not employ graphics, it is exciting and very addictive. See the roll-toware review in CONPUTENDICK. Runs in KK Altat.

YALDEZ (Available for all computers)

YALDEZ (Available for all computers)

Price: \$15.95 Casatter \$18.95 Disketed VALDEZ is a computer simulation of supersanker navigation in the Prince William Sound/Valder Narrows region of Alaxka lockeded in this simulation is a realistic and extense 25 & 25 de meent rang, portions of which may be viewed using the ship's sphanameric radar display. The motion of the ship itself is accurately modelled mathematically. The simulation also contains a model for the tidal patterns in the region, as well as other verific tougoing tankers and drifting inchergal. Other your course from the Gulf of Alaska to Valdez Harbort See the software reviews in 80 Software Critique and Personal Computing.

BACKGAMMON 2.0 (Atarl, North Star and CP/M only)

This program tests your backgammon skills and well also improve your game. A human can compete against a computer or against another human. The computer can even jux against itself. Either the human or the computer and onlother or generate die rolls. Board positions can be created or saved for replay. BACKGANMON 20 plays in accordance with the official rules of backgammon and its sure to provide many facationistic presents of backgammon and its sure to provide many facationistic presents of backgammon play.

CHECKERS 3.0 (PET only)

Price: \$16.95 Casette/\$20.95 Unkerte
This is one of ite most challenging checkers programs available. It has 10 levels of play and allows the user to change skill
fee'ds at any time. Although providing a very tough game at level 4.8, CHECKERS 3.0 is practically unbratable at levels 9 and
10.

CHESS MASTER (North Star and TRS-80 only)

This complete and very powerful program provides (five levels of play, it includes castling, en passant captures and the pion of pars was, Additionally, it board may be preset before the start of play, permitting the examination of "book" pla maximize execution speed, the program is written in assembly language (by SOFTWARE SPECIALIST'S of California graphics are employed in the TRS-30 version, and two widths of abhanumeric display are provided to accommodate. Price: \$19.95 Cassette/\$23.95 Diskette

LEM LANDER (32K Apple Disk only)
Prior
Prior your LEM LANDER to a safe landing on any of mine different surfaces ranging from amouth to treach
paddless are used to control craft attitude and thrust. This is a real-time high res challenge!

FOREST FIRE! (Atari only)

Using excellent graphics and sound effects, this simulation puts you in the middle of a forest fire. Your job is to direct operations to put out the fire while compensating for changes in w md, weather and terrain. Not protecting waitable structures can result in starting pensities. Life-like variables are provided to make FOREST FIRE! verysuspenseful andchallenging. No two games have the same setting and there are 3 levels of difficulty.

SPACE EVACUATION! (Apple, Atarl and TRS-80 only)

Can you colonize the galaxy and execute the Earth before the sun explodes? Your computer becomes the ship's computer as you explore the universe to redeate millions of people. This simulation is particularly interesting as it combines many of the exciting elements of classic space games with the mystery challenge of ADVENTURE.

MONARCH (Alari only)

MONARCH is a fascinating economic simulation requiring you to survive an 8-year term as your nation's leader. You determine the amount of acreage devoted to industrial and agricultural use, how much food to distribute to be populace and how much should be spent on pollution control. You will find that all decisions involve a compromise and that it is not easy to make everyone lapply. Runsin MSK Atari.

CHOMPELO (Atari only)

CHOMPELO is really two challenging games in one. One is similar to NIM; you must bite of part of a cookie, but avoid taking the pulsoned portion. The other game is the popular board game REVERSL it fully uses the Atail's graphics capability
and is hard to beat. This package will run on a 16K system.

SPACE LANES (Available for all computers)

SPACE LANES is a single but existing space transportation game which involves up to four players finduling the com

The object is to form and expand space transportation companies in a competitive environment. The goal is to a mass m
worth than your opportent. The economics include stock purchases and company mergers. Watch your wealth growth

AVAILABILITY

DVNACOMP software is supplied with complete documentation containing dear explanations and examples. Unless otherwise specified, all programs will now within tick program memory space (ATA BIL requires 240). Except where noted, program are wait painted to the program are wait, able on ATABI, PET, TRS-80 (Level II) and Apple (Applesch) existent and diskert as well as North Star single density (double density compatible) diskerte, Additionally, most programs can be obtained on standard (BIM 3740 ingle density/double density) compatible formal [8" CPAM floopy disks for systems running under MBASIC (for example, Allos, Xerox 820 and many others). 5% "CPAM diskertes are available for the North Star and Obstonce computer systems.

*ATARI. PET/CBM, NORTH STAR, CP/M, IBM, OSBORNE and XEROX are registered tradenames and/or trade marks

**Except where noted, all TRS-80 Model I software is available on classific (only) for the TRS-80 Model III. Except VALDEZ, CRIBBAGE, GRAFIX, CHESSMASTER. TRS-80 diskettes are not supplied with either DOS or BASIC.

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- 24 hour order phone

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STARTREK 3.2 (Available for all computers)

This is the classic Startet simulation, but with several new features. For example, the Kingsons now shows at the Enterprise without warming while also attacking starbasts in other quadrants. The Kingsons also stack with both light and heavy cuttiest and move when shot at The situation is thereive then the Enterprise is besigned by three heavy crutiers and a starbase S.O.S. is received? In Kingson ago to star Circles and Amore Merchandising.

BLACK HOLE (Apple only)

This is an exciting praphical simulation of the problems involved in closely observing a black hole with a space probe. The object is to enter and maintain, for a prescribed time, an orbit close to a small black hole. This is to beaching with a small probe of the control of the craft is realistically simulated using side jets for rotation and main thrusters for acceleration. This program employs like-keep gradient and is clusteriously a well as challenging.

SPACE TILT (Apple and Alari only)

Price: \$10.95 Cassette/\$10.95 Diabette
Use the game paddles to tilt the plane of the TV screen to "roll" a ball into a hole in the screen. Sound simple? Not when the
hole gets mandler and smaller! A boil-in timer allows you to measure your skill against others in this hald-forming action.

ESCAPE FROM VOLANTIUM (Atari only)

Bring the action and excitement of an areade into your home with ESCAPE FROM VOLANTIUM? To escape you must maneover your spaces this paround obstacles and laser blast the dragon (without being catent). If he is killed with a direct shot (not just a leg lopped off), a door opens to the outside. However, the door does not stay open indefinitely. If you fail to escape in time, the door does not not end or dragon appears. Sometimes you can amabat through the door by repeatedly chippingsavay at it. Other times it is impervious. At the higher levels of play more obstacles and dragoniappear, adding to the excitement. Uses high resolution graphics and sound. Runs in 16K2.

AI.PHA FIGHTER (Alari only)

Two excellent graphics and action programs in one! ALPHA FIGHTER requires you to destroy the alient starships passing through your sector of the galaxy. ALPHA BASE is in the path of an allen UPO invasion; let five UPO's got yand the game ends. Both games require the joystick and get progressively more difficult the higher you score! ALPHA FIGHTER will run on 16K systems.

THE RINGS OF THE EMPIRE (Atarl only)

The empire has developed u new battle station protected by rotating rings of energy. Each time you blast through the rings and destroy the station, the empire develops a new station with more protective rings. This exciting game runs on 16% systems, employs extensive graphics and sound and can be played by one or two players.

INTRUDER ALERT (Atari only)

This is a fast paced graphics game which places you in the middle of the "Dreadstar" having just stolen in plans. The droids have been elected and are directed to destroy you at all costs. You must find and enter your ship to escape with the plans. Five levels of difficulty are provided. INTRUDER ALERT requires a joystick and will run on 16K systems.

MIDWAY (Atarl only)

Price: \$14.95 Cambette/\$1

MIDWAY is an exicting extension of the game of Battleship. It mixes the challenges of strategy and chance. Ye can be another human or the computer. Color graphics and sound are both included. Runs in 1645.

TRIPLE BLOCKADE (Alari only)

TRIPLE BLOCKADE is a two-to-three player graphics and sound action game. It is based on the classic video are degame
which-millions have enjoyed. Using the Atari joysticks, the object is to direct your blockad applies round the screen without
running into your opponent(s). Although the concept is simple, the combined graphics and sound effect lead to "bigh
anxiety".

GAMES PACK I (Available for all computers)

GAMES PACK I contains the classic computer games of BLA.CKJACK, LUNAR LANDER, CRAPS, HORSERACE,
SWITCH and more. These games have been combined into one large program for one in loading. They are individually accessed by a convenient menu. This collection is worth the price just for the DYNACOMP version of BLACKJACK.

GAMES PACK II (Available for all computers)

GAMES PACK II (Available for all computers)

GAMES PACK II includes the games CRAZY EIGHTS, JOPTO. ACEY-DUCEY, LIFE, WUMPUS and others. As with
CAMES PACK, II all the games are loaded as one program and are called from a menu. You will particularly enjoy
DYNACOMP's version of CRAZY EIGHTS.

Why pay \$7.95 or more per program when you can buy a DYNACOMP collection for just \$10.95?

MOON PROBE (Alarland North Star only)

This 'san extremely challenging 'lunar lander' program. The user must drop from orbit to land as a predetermined target on the moon's surface. You control the thrust and orientation of your craft plus direct the rate of deternt and approach angle. Runsin 16K Atari.

SPACE TRAP (Atari only, 16K)
This galactic "shoot'em up" aread game places you near a black hole. You control your spacered flusing the joystick and at-tempt to blast as many of the alien ships as possible before the black hole closerabout you.

CHIRP INVADERS (PET/CBM only)

Price: \$14.95 Cassette/\$18.95 Disket
CHIRP INVADERS is an addictive game using action graphics. A Federation space station must be reached before the Chire
conquer the Earth. Stationary obstacles, moving meteors, and the attacking Chirps must all be availed for a success
journey. Cond luck.

ADVENTURE

CRANSTON MANOR ADVENTURE (North Star and CP/M only)

At last A comprehensive Adventure game for North Star and CP/M systems, CRANSTON MANOR ADVENTURE takes you inton mysterious CRANSTON MANOR where you attempt to gather fabulous tressures. Luthing in the manor are wild animats and robots who will not give up the treasures without a fight. The number of rooms is greater and the associated descriptions are much more claborate than the current popular series of Adventure programs, making this game the top in its class. Play can be stopped at any time and the status stored on diskette. Not available in 5½ "CP/M format.

GUMBALL RALLY ADVENTURE (North Star only, 46K)

Take part in this outlaw race from the east coast to the west coast. The goal is to find your way to the finish line white maintaining the highest possible speed. You may choose one of five cars available at the garage. The choice will affect your speed and trange. Remember to take spare parts and don't get caught speeding!

UNCLE HARRY'S WILL. (North Star only, 40%)
Unde Harry has died and has left you everything. However, he has neglected to mention where everything is finite and, his will consist of a gone which contains dues. You will have to travel all over the United States both by car and on foot to solve the puzzle, and there are over 300 locations to probe. Be careful and watch out for red herrings!

SPEECH SYNTHESIS

DYNACOMP is now distributing thenew and revolutionary TYPE-'N-TALKTM (TNT) speech synthesizer from Votrax, Simply connect TMT to your computer's serial interface, enter text from the beyloade and hear the words spoken. TNT is the essiess-to-program speech synthesizer on the market. It uses the least amount of memory and provides the most Petable vocabulary available.

List price \$375. DYNACOMP'S price \$329.95. Please add \$5.00 for shipping and handling.

TALK TOME (T'N'T Atarl only, 24K)

Price: 514.95 Cassette/518.95 Diskette
This program presents a superb (utorial on speech synthesis using the Atarl 800 and TYPE'N TALK TOME will
Bioutrate normal word generation as well as phoneme generation. The documentation includes many helpful programming
tips.

MISCELLANEOUS

Pricet 5 9.95 Cassetter/\$13.95 Diskette
A unique algorithm randomly produces fascinating graphics displays accompanied with tones which vary as the patterns are
built. No two patterns are the same, and the combined effect of the sound and graphics are memorizing. CRYSTALS has been
used in local stores to demonstrate the sound and color features of the Atari. Runs in MK. Atari.

PUTER STAD SOURCES. CRYSTALS (Atari only)

NORTH STAR SOFTWARE EXCHANGE (NSSE) LIBRARY DYNACOMP now distributes the 21 volume NSSE library. These dialettes each contain many programs and offer an outstanding value for the purchase price. They should be part of every North Star user's collection. Call or write DYNACOMP for details regarding the contents of the NSSE collection.

Price: \$9.95 each/\$7.95 each (4 or more) The complete collection may be purchased for \$149.95

BUSINESS and UTILITIES

MAILMASTER (Atari diskette only)

Price:\$39,95 Diskette
MAILMASTER is a very versalite software package for managing and manipulating mail lists and min data bases. Each disk
can hold over 700 customer extrices containing name, address, three 3-letter key words and a phone number. The diply
by marked so that emrics may be made and addied with ease. The status (e.g., disk space left, options, etc.) is shown all times.
Labbis may be printed: [1, 20 7 up, and all storing rigs code and alphabetic) is performed by a fast marchine hanguage potarum.

SORTIT (North Star only)

Price: \$39.95 Dishette
SORTIT is a general puspose sorting program written in 8080 assembly language. This program will nort sequential data files
generated by NORTH 5TAR BASIC. Primary and optional secondary keys may be numeric or one to nine character strings.
SORTIT is easily used with file generated by DYNACOMP's MAIL LIST program and is very versatile in its capabilities for
all other BASIC data file sorting.

PERSONAL FINANCE SYSTEM (Alari and North Star only)

Peter: \$39.95 Diskette
PFS is a single diskette, menu-oriented system composed of ten different programs. Besides recording your expenses and tax deductible items, PFS will sort and summarize expenses by payee, and display information on expendentiers by any of sure defined codes by month or by payee, PFS will even produce monthly bar graphs of your expenses by category! This pongriful package requires only one disk drive, minimal memory (24K Asia), 23K worth Sixy-and will store up to 600-records pick (and over 1000 records per disk by making a few's implethanges to the programs). You can record checks plus each expenses so that you can finally see where your money goes and eliminate guesswork and tedious hand calculations. Contains high speed machine language sort.

AMILY BUDGET (Apple and Atari only)

Prict: \$34.95 Diskette
FAMILY BUDGET is a very convenient financial record-keeping program. You will be able to keep track of cash and credit
expenditures as well as income on a daily basis. You can record tax deductible items and charitable demantion. FAMILY
BUDGET also provides a continuous record of all credit transactions. You can make daily cash and charge entiries to any of 21
different expenses occounts as well as to 5 payroll and tax accounts. Data are easily retrieved giving the user complete control
over an otherwise complicated (and unorganized) subject.

DVET AR DIRECTION COMPRISED AND ADDRESS AN

LA LEULIUR II (CP/M)

This is the second release version of DYNACOMP's popular TEXT EDITOR I and contains many new Centures. With TEXT EDITOR II and contains many new Centures. With TEXT EDITOR II you may build test elife-in chunks and assemble them for later display. Blocks of test maybe approached, inserted or deleted. Files may be saved on disk/diskrete in right jamifiend/centered format to be later printed by either TEXT EDITOR II or the CP/M ED facility. Fether, ASCI CP/AN files (including BASIC) and assembly language programs) may be read by the editor and processed. In Facil, test files can be built using ED and later formatted using TEXT EDITOR II. All in all, TEXT EDITOR II is an increase, easy to use, but very flexible editing system.

TLE (Atari and North Standard). TEXT EDITOR II (CP/M)

DFILE (Alari and North Star diskettes only)

This handy program allows North Star and Atari disk users to maintain a specialized data base of all files and programs in stack of disks which invariably accumulates. DFILE is easy to set up and use. It will organize your disks to provide efficilocating of the desired file or program.

FINDIT (North Star only)
This is a three-in-one prog
Commercial (eg: plumbers INVESTIGATION OF THE CONTROL OF THE

Price: \$12.95 Cassette/516.95 Distance and the supermarket. Before going shopping, it will remind you of all the things you might need, and then display (or optionally print) your shopping list and the total cost. Adding, deleting, changing and storing data is very easy. Runs with 16K. SHOPPING LIST (Atari only)

TAX OPTIMIZER (North Star only)

Price \$59.95 Ulkette
The TAX OPTIMIZER is an easy-to-uge, ment oriented software package which provides a convenient means for analyzing
various income to strategies. The program is designed to provide a quick and easy data entry. Income tax is compared by all
ax methods (regular, income averaging, maximum and alternate minimum ass). The tuser may immediately observe the tax
effect of critical financial decisions. TAX OPTIMIZER has been thoroughly field tested in CPA offices and comes complete
with the current tax tolles is in that tails. TAX OPTIMIZER is tax deductible!

UTIL (Apple only, 48K)

Price: \$19.95 Dialectic

UTIL is a disk-oriented utility system which permits examining and changing of the contents of DOS 3.2 and 33 diskettes at the bit (nibble or byte) level. With UTIL you can easily examine the contents of a diskette sector by socion, restructure become prointers, reallocate sectors (e.g. bad sectors may be "hidden"), and perform many other sophisticated operations. For the experienced organization.

URNNEY AND MENU (Alari only)

TURNKEY is a utility program which allows you to create autoboot/autorum diskettet exaily. Simply load and nin TURNKEY, load the program diskette to be modified, and answer the questions! The TURNKEY diskette alone once with DOS 2,0
and includes another program. MENU. MENU lists the contents of your diskette alphabetically, and permits the running of
any BASIC program on the diskette by typing a single key, TURNKEY and MENU provide you with the ability to run any
program on your diskette by simply turning on the computer and pressing a single key.

Price: \$39.95 Dial (Alari only)
STOCKAID (Alari only)
STOCKAID provides a powerful set of tools for stock market analysis. With STOCKAID you can display point and for affairs, as well as but charts with oscillators. You can also examine long term movins averages and on-balance volume feat STOCKAID allows you to input daily data with a single diskets storage capability of 239 days × 16 stocks. includes stock deficient and geta daysiment capabilities. A very professional package!

EDUCATION

HODGE PODGE (Apple only, 48K Applesoft or Integer BASIC)

Price: \$19.95 Cassette /\$22.95 Delactic
Let HODGE PODGE be your child's teacher. Pressing any key on your Apple will result in a different and intriguing 'llappening'' related to the letter or number of the chose key. The program 'spaphics, color andsound are a delight for 'dildren' from ages 19/10-7, HODGE PODGE is a non-intimidating teaching device which brings a new dimension to the use of computers in decated to.

TEACHER'S AIDE (A tark only)

TEACHER'S AIDE (Continue of thee basic modules contained in one program. The first module provides addition and subtraction exercises of varying levels of difficulty. The second module consists of multiplication problems in which the state
may be tested both on the final answer and/or on the subtotal answer in the long hand procedure. Several levels of complexity
are provided here as well. The third module consists of division problems; one particularly nice leasure of the division nade
is that the long hand division steps can be displayed along with the remainder in order to deaty demonstrate the procedure by
which the remainder is deviced. Using TEACHER'S ADE is not merely a citil, but table a kanning experience.

ORDERING INFORMATION

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STATISTICS and ENGINEERING

DIGITAL FILTER (Available for all computers)

Price: 539.95 Cassette/543.95 Diskete
DIGITAL FILTER is a comprehensive data processing program which permits the user to design his own filter function or choose from a menu of filter forms. The filter forms are subsequently converted into non-recursive convolution coefficients which permit rapid data processing. In the exploit design mode the shape of the frequency transfer function is specified by directly entering points along the desired filter curve. In the menu mode, ideal low pass, high pass and bandpass filters may be approximated to varying degrees according to the number of points used in the calculation. These lifters may optionally be supported to the price of the control o

DATA SMOOTHER (Not available for Atari)

This special data amoultaing program may be used to rapidly derive useful information from noisy business and engineering data which are equally spaced. The software features choice in degree and range of Ft, as well as smoothed first and second derivative calculation. Also included is automatic plotting of the input data and smoothed results.

FOURIER ANALYZER (Available for all computers)

Price: \$19.95 Cassetter \$23.95 Diabette

Use this program to examine the frequency spectra of limited duration signals. The program features automatic scaling and
plotting of the input data and results. Practical applications include the analysis of complicated patterns in such fields as electronics, communications and business.

TFA (Transfer Function Analyzer)
This is a special software puckage which may be used to evaluate the transfer functions of systems such as thi-II amptiff sets and filters by examining their response to pulsed inputs. TFA is a major modification of FOURIER ANALYZER and contains an engineering oriented decibel versus log-frequency plot as well as data editing features. Whereas FOURIER ANALYZER is designed foreducational and scientific use, TFA is an engineering tool. Available for all computers.

HARMONIC ANALYZER (Available for all computers)

Price: \$24,95 Canaster;538,95 Disk

HARMONIC ANALYZER was designed for the spectrum analysis of repetitive waveforms. Features include data file gent iden, editing and storage-retrieval as welled a stand spectrum plotting. One particularly unique facility is that the input aneed not be equally spaced or in order. Theoriginal datais sorted and a cubicspline interpolation is used to create the data required by the FFT algorithm.

FOURIER ANALYZER, TFA and HARMONIC ANALYZER may be purchased together for a combined price of \$49.95 (three cassettes) and \$39.95 (three diskettes).

REGRESSION I (A-vallable for all computers)

REGRESSION I is a unique and exceptionally versable one-d'unensional least squares "poly-comial" curve fitting program.

Features include verybing accuracy; an automatic degree determination option; an extensive intensi library of fitting function; data editing, automatic data, curve and residual plotting; a statistical analysis (as; standard deviation, correlation certificient, etc.), and much more. In addition, new fits may be tried without reentering the data. REGRESSION I is certainly the cornerstone program in any data analysis software library.

REGRESSION II (PARAFIT) (Available for all computers)

Price: 519.95Cassette/523.95 Diskette
PARAFIT is designed to handle those cases in which the parameters are imbedded (possibly nonlinearly) in the fitting functhan. To sucy imply inserts the functional form, including the parameters (A(I), A(2), etc.) as one or more BASIC statement
lines. Data, results and residuals may be manipulated and plotted as with REGRESSION I. Use REGRESSION I for
polynomial fitting, and PARAFIT for those complicated functions.

MULTILINEAR REGRESSION (MLR) (Available for all computers)

MLR is a professional software package for analyzing data sets containing two or more linearly independent variables. Besides performing the basic represention calculation, this program also provides easy to use data entity, storage, retrieval and ediding functions. In addition, the user mayinterrogate the solution by supplying values for the independent variables. The number of variables and data size is limited only by the valiable memory.

REGRESSION 1, II and MULTILINEAR REGRESSION may be purchased together for \$51.95 (three co

ANOVA (Nol available on Atarl cassette or for PET/CBM)

Price: \$39.95 Cassette/\$4.395 Disterte
In the past the ANOVA (analysis of variance) procedure has been limited to the large mainframe computers. Now
DYNACOMP has brought the power of this method to small systems. For those convergant with ANOVA, the DYNACOMP
software package inclutes the 1-way, 2-way and N-way procedures. Also provided are the Yates 2^{N-V} factorial designs. For
those unif nalidae with ANOVA, do not worty. The accompanying documentation was written in an utural fastion thy sofession in the subject) and serves as an excellent introduction to the subject. Accompanying ANOVA is a support program for
building the data bases. Included are several convenient features in cluding datasetting, deleting and appending.

BASIC SCIENTIFIC SUBROUTINES, Volumes I and 2 (Not available for Atari)
DYNACOMP is the exclusive distributor for the software keyed to the popular texts BASIC SCIENTIFIC SUBROUTINES,
Volumes I and 2 by F. Ruckelschel (see advertisements in BYTE magazine). These subroutines have been assembled according
to chapter, included with each collection is a menu program which selects and demonstrates each subroutine.

Volume 1
Collection #1: Chapters 2 and 3 - Data and function plotting; complex variables and functions.
Collection #2: Chapter 4 - Extended matrix and vector operations.
Collection #3: Chapters 3 and 6 - Random number generators (Poisson, Gaussian, etc.); series approxin
Price per collections 134-95 Cassette #38.95 Diskette
All bree collections are available for \$3.995 (three cassites) and \$49.95 (three diskettes).

United Occupation 1. Chapter 1 - Linear, polynomial, multidimensional, parametric least squares.

Collection 82: Chapter 2 - Series approximation techniques (economization, inversion, reversion, shifting, etc.).

Collection 82: Chapter 3 - Functional approximations by iteration and recursion.

Collection 84: Chapter 4 - CORDIC approximations to trigonometric, hyperbolic, exponential and logarithmic functions.

Collection #3: Chapter 5 - ChADNE approximations to trigonometric, hyperbolic, exponential and logarithmic functions.

Collection #6: Chapter 5 - Table interpolation, differentiation and integration (Newton, LaGrange, splines). Collection #6: Chapter 6 - Methods for finding the real-roots of functions.

Collection #7: Chapter 7 - Methods for finding the complex roots of functions.

Collection #8: Chapter 8 - Optimization by steeperst descent.

Price per collection: 514-95 Castetto/318-95 Diskette

All eight collections are available for 578-99.95 (eight assetted) and \$129.95 (eight diskettes).

Because the texts are a vital part of the documentation, BASIC SCLENTIFIC SUBROUTINES, Yolumes I and 2 are available from DYNACOMB.

BASIC SCIENTIFIC SUBROUTINES, Vol 1 (319 pages): \$19.95 + 75¢ postage BASIC SCIENTIFIC SUBROUTINES, Vol 2 (790 pages): \$23.95 + \$1.50 postage

See reviews in KILOBAUD and Dr. Dobbs.

ROOTS (Available for all computers)

In a nuthell, ROOTS simultaneously determines all the zeroes of a polynomial having real coefficients. There is no limit on the degree of the polynomial, and because the procedure is iterative, the accuracy is perenally very pool. No initial guesses are required as input, and the calculated roots are substituted back into the polynomial and the residuals displayed.

required as input, and the calculated roots are autostituted stack title the polynomial and the residuate staphysical.

ACTIVE CIRCUIT ANAL (SISI (ACAP) (848 Apple only)

ACAP is the analog circuit designer's answer to LOGIC SIMULATOR. With ACAP you may analyze the response of an active or passive component circuit (e.g., as transitive amplifier, band pass filter, etc.). The circuit may be probed at equal state in frequency, and the resulting complex (i.e., real and imaginary) voltages at each component juncture examined. By plotting the magnitude of these voltages, the frequency response of a filter or amplifier may be completely deternined with respect both amplitude and phase. In addition, ACAP prints a statistical analysis of the range of voltage response which result from tolerance variations in the components. ACAP is easy to learn and use. Simply describe the circuit in term of the chremens and their placement, and execute. Circuit descriptions may be aveed onto essette or diskette to be recalled at a later time for execution or editing. ACAP should be part of every circuit designer's program library.

LOGIC SIMULATOR (Apple only; 48K RAM)

Prict: \$24.95 Chanetter/\$2.8.95 Diskette
With LOGIC SIMULATOR you may easily test your complicated digital logic design with respect to given set of inputs to
determine how well the circuit will operate. The elements which may be simulated include multiple input AND, OR, MOR,
EXOR, EXNOR and NAND gates, as well as inverters; J-K and D (lipt-flops, and one-vibost. The response of the system is
available every lock cycle, laptus may be docked in with varying clock cycle (neight/displacements and delays may be introduced to probe for gatches and race conditions. At the user's option, a timing diagram for any givens set of modes may be plotted using HIMES graphics. Such every our breatsborneling until the circuit is checked by LOGIC SIMULATOR.

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STATSORT consists of several menu selected programs which allow the user to create (build, edit, merge), format and print files, (menchies) sort there one not prifeld, and numerically analyze (maximum, minimum, neverage, variance, sandoard deviation) tabulated data. STATSORT is well documented and easy to use. The casactic version can also be employed to create a data type which can be rend by the Radio Shack Advanced Statistics! Package.

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Technical Forum

A Fast Approximation for Fast Fourier

Mark H. Polczynski Eaton/CCSD 901 South 12th St. Watertown, WI 53094

Two articles in BYTE have presented approximations for rapidly calculating $M=\sqrt{}$. Richard Lord in "Fast Fourier for the 6800" (February 1979 BYTE, page 108) approximates M by M'=L+S, where L is the larger of the quantities a and b, and S is the smaller. Bob Leedom in a "Technical Forum" (June 1979 BYTE, page 188) points out that the approximation can be greatly improved by letting M'=L+KS and choosing K to minimize the error of approximation, E=M-M'.

The optimum value of *K* depends on the user's requirements. Four strategies for optimizing *K* suggest themselves:

- 1. minimize the peak-to-peak error
- 2. minimize the average magnitude of the error
- 3. set the average positive error equal to the average negative error
- 4. set the average error equal to zero

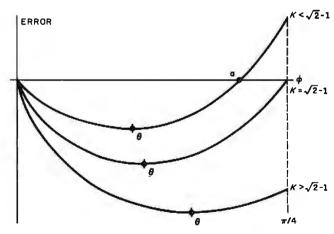


Figure 1: Generalized error curve for $E = 1 - \cos(\phi) - K \sin(\phi)$.

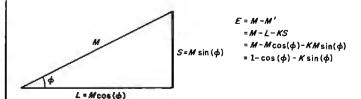


Figure 2: Constructing E = M - M'.

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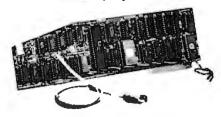
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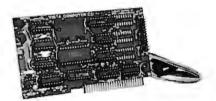
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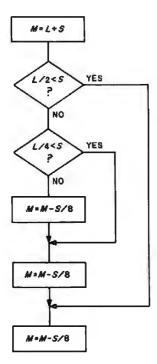


Figure 3: Possible flowchart for strategy five.

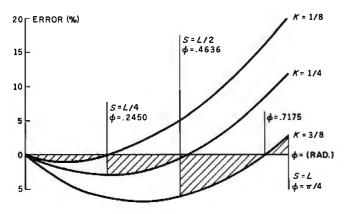


Figure 4: Error curve for strategy five.

Strategy	κ	Peak to Peak Error (%)	Average Magnitude of Error (%)
1	.4142	8.23	5.48
2	.3157	11.9	3.24
3	.2811	13.3	3.09
4	.2673	13.9	3.11
-	3/8	9.57	4.24
	1/4	14.7	3.20
-	1/8	21.2	5.64
5	1/8, 1/4, 3/8	8.98	2.09

Table 1: Solutions and errors for various strategies and values of K.

Equations for analytically deriving values of K which satisfy these strategies can be developed with the aid of the generalized error curves for E=M-M' shown in figure 1. The error curves are developed by constructing the diagram in figure 2 and observing that $E=1-\cos\phi-K\sin\phi$. The equations which describe strategies one through four are:

1. minimize:
$$E(\phi=\Theta)$$
; $\sqrt{2}-1 < K < 1$
 $E(\phi=\pi/4)-E(\phi=\Theta)$; $0 < K < \sqrt{2}-1$

2.
$$\frac{d}{dK} \left(\int_{\alpha}^{\pi/4} E d\phi - \int_{\alpha}^{\alpha} E d\phi \right) = 0$$

3.
$$\frac{1}{(\pi/4-\alpha)} \int_{\alpha}^{\pi/4} E d\phi = \frac{-1}{\alpha} \int_{0}^{\alpha} E d\phi$$

$$4. \qquad \int_{0}^{\pi/4} E \mathrm{d}\phi = 0$$

Solutions to these equations are given in table 1. Note that for strategy one, the solution for K is $\sqrt{2}-1$.

As Leedom points out, the problem with these strategies is that multiplication by the optimized value of K is still rather time-consuming. The process can be speeded up if K is set equal to values such as 1/4, 3/8, or 1/8. This allows the multiplication to become a simple shift (and possibly add) process. A decrease in accuracy accompanies the increase in speed, as shown in table 1.

A fifth strategy exists which is slightly more lengthy than a straightforward shift and add, but which is more accurate than any of the other strategies. For this approach, the value of K used in the approximation depends on the relative magnitudes of L and S. The algorithm is as follows:

$$L/2 < S \le L$$
; K=3/8
 $L/4 < S \le L/2$; K=1/4
 $S \le L/4$; K=1/8

The other strategies require that a decision be made as to which of the quantities a or b is larger. This strategy requires that two additional decisions be made, but since S is compared to L/2 and L/4, the decisions are based on the result of simple shift operations. Note also that once the decisions are made, multiplication by K is a shift and add operation. A possible flowchart for this strategy is shown in figure 3. The error curve for strategy five is shown in figure 4, and the improved accuracy for this strategy is demonstrated in table 1.

Technical Forum is a feature intended as an interactive dialog on the technology of personal computing. The subject matter is open-ended, and the intent is to foster discussion and communication among readers of BYTE. We ask that all correspondents supply their full names and addresses to be printed with their commentaries. We also ask that correspondents supply their telephone numbers, which will not be printed.

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BYTE February 1982 251

Software Review

Omniterm: Smart Terminal Program for the TRS-80

Bob Liddil POB 66 Peterborough, NH 03458

The addition of communications capabilities to a computer inaugurates a new concept in personal computing. With a modem, a telephone, and an intelligent terminal program, a microcomputer becomes an instrument for external data collection or transmission. With these tools, you can communicate with similarly equipped computers throughout the world.

The most critical of these tools is the terminal program. True, an inferior modem or faulty telephone line can cause problems, but the terminal program can open ing on its features (or lack of them). Omniterm, a new product from a small company in

endless possibilities or cause severe limitations, depend-

Massachusetts, has most of the possible features of a smart terminal program. But even a novice user, normally overwhelmed by complex programs, can easily adjust to Omniterm.

A popular use of terminal programs is the bulletin board network, which consists of approximately 400 automatically answered, electronic-message centers around the country. You can dial any of these numbers and leave a message for someone in that area or take advantage of local features such as receiving public-domain programs or sending electronic mail.

Since all bulletin board systems do not operate on the same type of computer, your terminal program should be able to adjust to different system requirements.

Omniterm seems equal to the demands placed on it. As long as I stayed on TRS-80-based bulletin board systems. I had no difficulty with elementary tasks when using the inexpensive (\$24,95) terminal program from Instant Software called Terminal 80. But when I tried Modem Over Manhattan, an interesting service in New York, or ABBS (Apple Bulletin Board System) in Cleveland, or even the TRS-80-based Big Byte system in Cincinnati, Terminal 80 fell apart. Omniterm worked flawlessly with all these services.

Omniterm's command mode, accessible any time during its use, gives fingertip control of everything you need when communicating with another system. Onekeystroke entries make it easy.

At a Glance

Name

Omniterm

Type

Intelligent terminal program

Author

David Lindbergh

Manufacturer

Lindbergh Systems 49 Beechmont St. Worcester, MA 01609

Price

595

Language

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Format

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93 81	MC10161 MC10162	4.77	6.28 6.28	4.14 4.14	5.51 5.51	Binary to 1-8 Decimal (Low) Binary to 1-8 Decimal (High) Error Det. Correction Cir.	
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77 19 19 77	MC10170	6.01	7.74	5,53	7.16	9+2 Bit Parity Ckr Dual Binary to 1-4 Dec (Low) Dual Binary to 1-4 Dec (High)	
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Ρ	PRINTER	}		IS:	OFF	X	SYSTEM COMMANDS	
R	SCREEN	REFORM.	ATTING	IS:	54	Т	CHANGE/EXAMINE TABL	_ES
С	CR SUPP	PRESSION		IS:	OFF	Ų	CHANGE UART SETTING	aS.
L	LF SUPP	RESSION		IS:	ON	Α	SEND CONTROL-A & QU	JIT
D	DUPLEX			IS:	FULL	@	SEND "AT" SYMBOL &	QUIT
Ε	ECHO			IS:	OFF	В	SCROLL BACK DISPLAY	
G	CR/LF G	ROUPING		IS:	OFF	Z	ZERO REAL-TIME CLOCK	<
1	INPUT T	O BUFFER	7	IS:	OFF	F	FILL BUFFER FROM DIS	iκ
0	OUTPUT	FROM BI	JFFER	IS:	OFF	S	SAVE BUFFER TO DISK	
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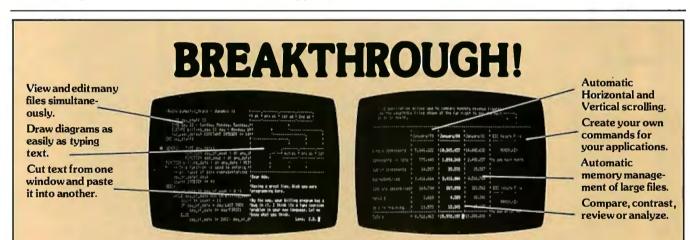
Figure 1: The command menu as it appears on the screen in Omniterm. The menu is displayed by pressing the @ key twice. Return to the active telecommunications mode is accomplished by pressing the < break > key. Displaying the menu does not interrupt the flow of data through the program.

The printer is accessible during communications. While using one service, I activated the printer while the instructions were coming on the screen; this gave me a reference sheet, saving valuable long-distance time. In the command mode, a status indicator lets you know whether the printer function is on or off. A buffer lets the printer fall behind the screen if it is not fast enough to keep up. Omniterm buffers 2048 characters of data before it runs out of room.

Some bulletin board or "information utility" systems are not set up for the TRS-80 64-column screen. Apple or Atari 40-column and Videotext 32-column units can cause problems with the video display. Omniterm allows you to reformat the screen from the command table. This gives you a 64-column screen, regardless of what your computer is receiving. The status of this function is displayed in the command mode.

For additional screen-format control, you can select carriage-return suppression, line-feed suppression, and carriage-return/line-feed grouping.

Omniterm also lets you determine the communications protocol (baud rate, bits per data word, stop bits, parity,



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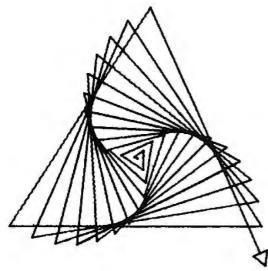
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TO POLYSPI :SIDE :ANGLE :INC

FORWARD :SIDE

RIGHT : ANGLE

POLYSPI :SIDE+:INC :ANGLE :INC

END

POLYSPI 1 123 3

This drawing was made by this program using LOGO's "turtle graphics".

The turtle is a Logo-controlled "cybernetic toy" that draws lines as it moves across the TV screen. Directing the turtle to construct graphic designs, programmers simultaneously confront aesthetic and mathematical issues.

Logo is more than turtle graphics. Logo was designed to put some of the powerful ideas of computer science at your disposal— ideas like procedure, process, local and global variables, list processing, recursion, etc. Its syntax is simple enough that beginners can write procedures in a first session, yet Logo is extensible and provides the means to tackle advanced and sophisticated projects.

Logo has often been described as a language for children. It is so, but in the same sense that English is a language for children, a sense that do s not preclude its being ALSO a language for poets, scientists, and philosophers.



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368 Congress St. Boston, Mass U.S.A. 02210 (617) 451-2646 full or half duplex, and automatic character echo). This gives you much flexibility for dealing with the various bulletin board and information services available.

Superior file handling separates Omniterm from less "intelligent" terminal programs. File capabilities include sending, receiving, and saving to and retrieving from disk. Omniterm has a file-transfer buffer of 27,644 bytes. You can input to the buffer from the remote computer and save to disk, or input to the buffer from the disk and output to the remote computer. It's easy to use these functions. To test them, I loaded a simple program from Forum-80 in Nashua, New Hampshire, saved it to disk, and executed it afterward to make sure it ran. I sent a BASIC adventure game to a youngster in Massachusetts; I received a BASIC adventure he had written for me, saved it to disk, and communicated via the keyboard and screen in between file transfers. It worked, even though I'm no professional.

Other useful command features are the special system commands that, among other things, allow you to save any communications protocol permanently to disk, to be called from the command mode whenever you need it. Another unique feature is the ability to backtrack into a special buffer and reconstruct what has appeared on the screen before a disconnect—useful for retrieving and reviewing pertinent data without using the printer or making another telephone call.

A novel item is a graphics "bell" that appears on the screen when a control-G is received. If an audio amplifier

is attached to the system via the cassette port, you'll also get an audible beep.

Omniterm comes with a 61-page instruction book, punched to fit in a binder. It is written so the beginner can understand the workings of the program. However, it is not too simplistic; there are technical explanations for the expert.

David Lindbergh has obviously spent much time and care on this project. His knowledge of the subject and professional presentation enhance the product considerably. Its \$95 price tag places Omniterm in competition with Lance Micklus's ST80 series of terminal programs, including ST80III, currently regarded by many as the standard for this type of program.

Conclusions

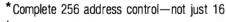
The program is very easy to use and works well. Most of the information you need is available on the menu, which can be displayed at any time without breaking connections to the host computer.

All the screen-formatting controls and communications conventions are software selectable, which means you can use the program with a wide variety of host computer systems.

The clearly written instructions and documentation are complete.

These features, coupled with its competitive price, make Omniterm a contender for the title of best in its class.■

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Voice Synthesis for the Color Computer

Third in a Series

William Barden Jr. 28122 Orsola Mission Viejo, CA 92692

Would you believe that using three resistors, an inexpensive integrated circuit (IC), two capacitors, a plug, a \$1.59 microphone, and some software you can record and play back your voice on a TRS-80 Color Computer with 16K bytes of RAM? What if I told you that the quality is better than that of Texas Instruments' Speak & Spell?

In this article I'll show you how to take any sound input, digitize it, store it in memory, and play it back on request, all with the few components mentioned above! The catch is that the 16K bytes of RAM will allow you to record only about 11/3 seconds of sound. However, by sacrificing some reproduction fidelity you may be able to extend the recording time to 13 seconds or more. This article is meant primarily to show you how to capture the sounds, record them, and play them back. I'll leave the improvements up to you. [This is the third in a series of articles describing hardware and software projects for

About the Author

William Barden Ir. has written many books on microcomputer programming and design. He is a member of the Association for Computing Machinery (ACM) and the Institute of Electrical and Electronics Engineers (IEEE).

the Radio Shack TRS-80 Model I, Model III, and Color Computer. For a list of previous titles in the series, see the references at the end of this article . . . Ed.]

Voice-Frequency Parameters

The range of hearing for humans is from 20 to 20,000 hertz (Hz), or cycles per second. In fact, the upper limit for most people is considerably lower than 20,000 Hz. The average telephone circuit has an upper frequency limit of 3500 Hz, and voice clarity suffers surprisingly little. Amateur radio operators, to increase their transmitters' average power output, restrict audio frequencies even further, to 3000 Hz or so. To reproduce acceptable voice, therefore, I need to design circuits capable of playing back frequencies up to 3500 Hz. First, of course, I have to capture the voice data. A fundamental rule of digital recording is that the sampling rate must be at least twice the maximum frequency to be recorded. Voices, then, must be recorded at rates of 7000 Hz or better. In other words, the voice input must be converted to digital form at a rate of 7000 samples per second or better.

Analog-to-Digital Conversion

To convert the voice signal to digital form, I will use an analog-todigital converter (ADC), which takes the analog voice input and converts it to a digital value (see figure 1). The larger the number of bits in the sample, the finer the resolution in the digital representation of the analog value. If the ADC offers six bits of data, for example, each digital value will be within 2-6, or 1/64, of the analog input value. A 5-bit ADC will produce values within 1/32 of the analog input value, and so on. When the digitized form of the input is replayed, the output waveform will approximate the original by a series of square waves. The higher the sampling rate and the resolution of the ADC, the more the output will resemble the original, as shown in figure 2.

For hardware reasons explained later, I'll use a 6-bit ADC. To avoid wasting bits, I could pack four 6-bit values into three 8-bit bytes. However, it's less trouble and faster simply to put a 6-bit ADC value in each byte and ignore the two unused bits, as shown in figure 3. A sampling rate of 7000 Hz, therefore, will fill 7000 bytes of memory for each second of recorded sound.

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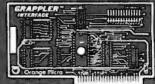
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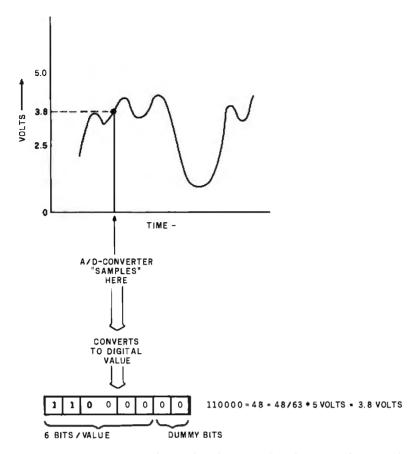


Figure 1: An ADC converts an electrical analog, such as voltage, to a binary value.

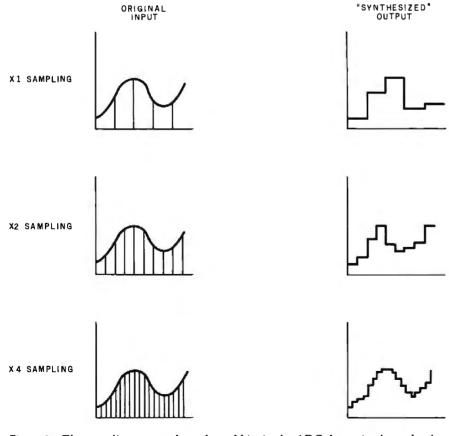


Figure 2: The sampling rate and number of bits in the ADC determine how closely the input signal can be reproduced.

In commercial voice-synthesis integrated circuits, many techniques are used to reduce the amount of storage required for audio data. Texas Instruments, National Semiconductor, and other companies produce hardware that can synthesize voices using only a few hundred bytes of data per second of speech. In these circuits, the voice-reproduction processor uses silent periods, symmetry of waveforms, and replication of patterns to compress the data. Fourier waveform analysis and other advanced techniques are used as well. The result of all this processing is a compact, specially encoded form of the voice data for the special hardware involved. However, I'll stick with the "brute force" approach for the time being. Later in the article, I'll discuss ways to cut down on the storage requirements.

To play back digitized sounds, I need the inverse of an ADC, a digital-to-analog converter (DAC). The DAC will take in as data each digitized value and produce as output a voltage level proportional to that value. A sequence of all these voltage levels will simulate an analog waveform. If the data was originally captured by a 6-bit ADC, then a 6-bit DAC is required to reproduce each sample.

In theory this brute-force voice capture and synthesis process is simple: take an analog voltage as input from the audio source, sample it 7000

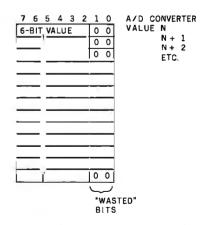
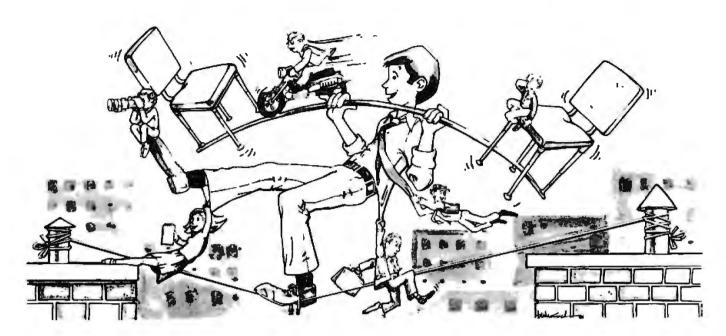


Figure 3: Although 25 percent of the storage space is wasted in storing 6-bit ADC values in 8-bit bytes, it is efficient in terms of storage speed.

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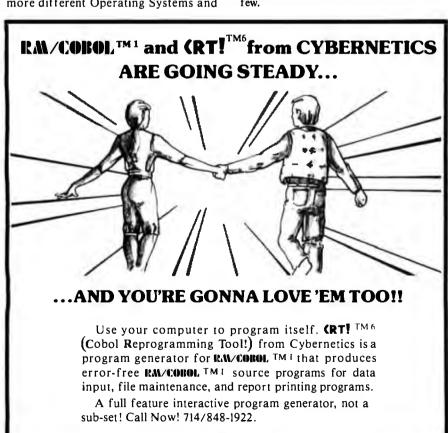
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times per second with an ADC, store the digitized ADC output values in the memory of a digital computer, and then play back the values from memory with a DAC. The process is illustrated in figure 4.

Color Computer Hardware

The Color Computer has a built-in 6-bit DAC and ADC circuit (see reference 2). Under normal use, the DAC synthesizes sine waves for recording cassette data and generating musical tones. The ADC exists partially in hardware and partially in software and is used to perform analog-to-digital (A/D) conversion on the joystick positions.

Color Computer DAC. The DAC (figure 5) is a 6-bit circuit that operates as fast as data can be output to it. I'll have to use assembly-language coding, however, to get the required output rates of 7000 or more bytes per second. BASIC would only allow several hundred operations per second, far too few for my purpose.

Each 6-bit digitized value can be output to hexadecimal address \$FF20, the PIA (peripheral interface adapter) for the DAC. [In accordance with 6809 microprocessor conventions. numbers in hexadecimal form are prefixed with a dollar sign . . . Ed.] The value will be held in the PIA until overwritten by the next value. The output of the DAC is very rapid (less than a microsecond), and so it appears that the DAC is no problem in my timing scheme. The output of the DAC goes to a radio-frequency/ audio modulator that converts the signal to a television picture with audio. Audio from the DAC, therefore, will be heard through the audio circuits of the television used with the Color Computer.

Color Computer ADC. The ADC is shown in figure 6. It uses a comparator IC, which compares two inputs. The output of the comparator is either 1 or 0 depending upon whether the plus input is lower or higher than the minus input. The output rate of the comparator is extremely fast. To get the comparator output, I read address \$FF00 and look at bit 7 of that value.

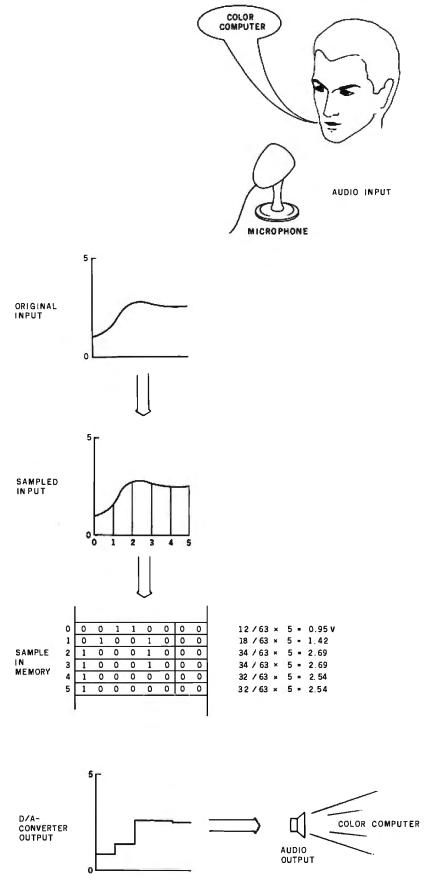


Figure 4: Brute-force voice synthesis samples input to digitize it, stores the ADC values in memory, and then outputs the values from memory to a DAC.

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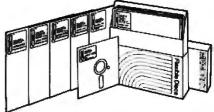






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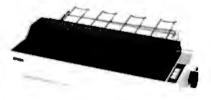
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One of the inputs to the comparator is from the external joystick connector. This should be a voltage level from 0 to +5 volts (V). The joystick input can be a voltage from the joystick potentiometer, or it can be any voltage in that range from any external device including an audio amplifier. The second input to the comparator is from the DAC and is also 0 to +5 V. A/D conversion is accomplished by rapidly changing the DAC output and checking the comparator output until I find the two values that bracket the voltage from the joystick input.

The Color BASIC ROM (read-only memory) provides a machine-language subroutine to accomplish this. It uses a type of binary search to converge on the joystick input value (for details, see reference 2). However, the subroutine processes four input values: right joystick X and Y and left joystick X and Y. In addition, the routine compares the current value of each channel with the previous one until they match. All of this overhead allows sampling rates of only 600 to 700 per second, too slow

for my needs. I need a high-speed ADC!

Voice-Synthesis Software

INPUT Routine. The software for such a high-speed ADC is shown in the text box with listing 1. It may not be the fastest ADC routine around, but it *does* allow conversion of about 7733 samples per second. One technique used in the routine is "linear coding" without loops, eliminating the loop overhead. The logic is explained in detail in the text box.

The INPUT routine takes $6 \times 19.1 + 14.6$ microseconds (μ s) for each ADC conversion, allowing 7733 samples per second. Note that during each 129.2- μ s conversion, the input voltage may change and the final value may be off by 25 percent or more, as shown in figure 7. In the majority of cases, however, the result is fairly close for these high sampling rates of audio frequencies.

The RAM buffer is 10,300 bytes long, providing for about 11/3 seconds' worth of recording.

OUTPUT Routine. The OUTPUT routine (listing 2) is considerably

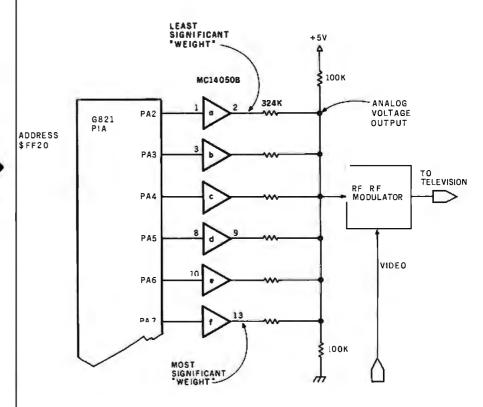


Figure 5: The Color Computer uses a 6-bit DAC to convert the six values from output port \$FF20 to an analog voltage. In this project, output is routed to an RF (radiofrequency) modulator.

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- 3) Allentries must be at least 6"x4" and no larger than 8"x10" in size.
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- 5) All entries will be judged by an independent panel of judges on their creative merit, originality and best use of computer equipment. Decision of the judges is final.
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- 9) Entries will be maintained on file at Epson America, Inc. until January 1, 1983.
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simpler than the input routine. The routine points to the beginning of the buffer, delays about $\frac{1}{1000}$ second, fetches a value from memory (LDA,X+), outputs the value to the DAC (STA \$0FF20), tests for the end of the buffer (BUFEND), and then returns for the next value if there are more data remaining.

SELECT Routine. The SELECT routine connects the right joystick X

channel to the ADC and routes the DAC output to the television's builtin speaker. SELECT is executed once at the beginning of both INPUT and OUTPUT.

BASIC Driver. The 6809 assembly-language subroutines shown in listings 1 and 2 are *relocatable*, that is, they can be placed and run anywhere in memory and still operate properly. Listing 3 shows the same

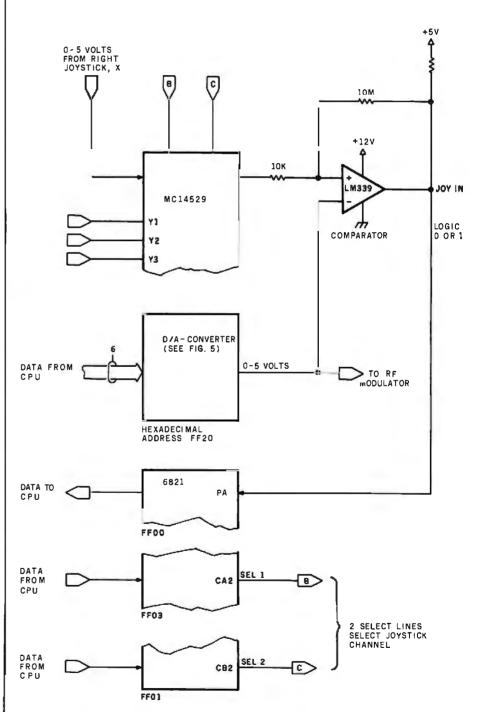


Figure 6: The Color Computer ADC uses a comparator, the DAC, and software to bracket the joystick input value.

Listing 1: The INPUT routine is coded in 6809 assembly language with a minimum of branch instructions to maximize execution speed. The routine performs 7733 A/D conversions per second.

							The second secon
1728			00100		ORG	\$172B	

						SIS PROG	
			00130				ONDS WORTH OF INPUT *
			00140			REQUEST	*
			99159			T TO REC	
			99169			UT TO PLI	
			00170		******	*****	******
			06180		ECA I	* 1000 11	and the second s
		1704		BUFFER	EQU	\$4000-10	
4700	4.7	SFFF		BUFEND	EQU	\$3FFF	END OF BUFFER
172B		9985	90210	INPUT	LBSR	SELECT	SELECT RIGHT/X
	198E		99229		LDY		LOAD INPUT PIA ADDRESS
1732		1704	00280	TUDOOF	LDX		LOAD BUFFER PHTR ADDRESS
1735	C6 F7	56 2		INP005	LDB	#\$80	LOAD START VALUE
1737 1738		FF20	5/0250 00260		STB	\$0FF20	OUTPUT FIRST VALUE
1730	28	A4 04	99279		LDA Bi1I	, Y INP015	INPUT COMPARATOR GO IF TOO LOW
173E		401	99289		SUBB	#\$40	
1740		04	00290		BRR	##40 INP020	SUBTRACT DELTA GO TO SECOND ITERATION
1742		40		INP015	ADDB	#\$40	ADD DELTA
1744	20	99	99310	TIMESTO	BRA	INP020	GO TO SECOND ITERATION
1746	F7	FF20	99329	INP020	STB	\$0FF20	OUTPUT SECOND VALUE
1749		R4	003:30	1111 020	LDA	, Y	INPUT COMPARATOR
174B	2B	24	00340		BMI	INPG25	GO IF TOO LOW
174D	Ca	20	99359		SUBB	##20	SUBTRACT DELTA
174F	20	94	99369		BRA	INPOSO	GO TO THIRD ITERATION
1751	CE	261	00370	INFØ25	ADDB	#\$20	ADD DELTA
1753	20	99	00380	525	BRA	INP030	GO TO THIRD ITERATION
1755	F7	FF20		INP030	STB	\$0FF20	OUTPUT THIRD VALUE
1758	R6	R4	00400		LDA	, Y	INPUT COMPARATOR
1758	28	94	00410	•	BI1I	INP035	GO IF TOO LOW
175C	CØ	10	00420		SUBB	#\$10	SUBTRACT DELTA
175E	20	94	00430		BRA	INP040	GO TO FOURTH ITERATION
1760		10		INP035	ADDB	#\$10	'ADD DELTA
1762	20	00	00450		BR:A	INF040	GO TO FOURTH ITERATION
1764		FF20		INP040	STB	\$8FF20	CUTFUT FOURTH VALUE
1767	A6	A4	99479		LDA	, Y	LOAD COMPARATOR
1769		94	00480		IMB IMB	INP045	GO IF TOO LOW
1768	CØ	ଡଚ	00490		SURB	#8	SUBTRACT DELTA
1760		04	99599		BR:A	INP050	GO TO FIFTH ITERATION
176F	CB	98	99519	INF'045	ADDB	#8	ADD DELTA
1771	20	88	99529	TI (5.050	BRA	INP050	GO TO FIFTH ITERATION
1773 1776	F7 86	FF20		INP050	STB	\$0FF20	OUTPUT FIFTH VALUE
1778	28	A4 04	99549		LDA	, Y	INPUT COMPARATOR
177A		94	99559 99569		BMI SUBB	INP055	GO IF TOO LOW
	20	04	99579		BRA	#4 INP060	SUBTRACT DELTA GO TO SIXTH ITERATION
177E		94	99539 99579	INP055	BDD:8		ADD DELTA
1730	20	99	00520	1191 000	BR:A	#4 INP060	GO TO SIXTH ITERATION
1782	F7	FF20	20600	INPEGO	STB	\$0FF20	OUTPUT SIXTH WALUE
1735	R6	R4	99619	1111 2000	LDA	, Y	INPUT COMPARATOR
	2B	04	99629		BMI	ÍNP065	GO IF TOO LOW
1789		92	99639		SUBB	#2	SUBTRACT CELTA
178B	20	04	00640		BRA	IMF'070	GO FOR NEXT VALUE
173D	CB	02	99659	IMP065	ADDB	#2	ADD DELTA
178F	20	90	99669		BRA	INP070	GO FOR NEXT VALUE
1791	E7	80		INFOZO	STB	, X+	STORE VALUE
1793	BC	3FFF	90689		CMPX:	#BUFEND	TEST FOR END OF BUFFER
1796	26	iPB	896.98		BNE	INP005	GO IF NOT END
1798	39	(1.1)	00700		RTS		END-RETURN

The INPUT Routine

For those of you not acquainted with assembly language, the input routine shown in listing 1 is not as imposing as it looks. The datum on the extreme left of the listing is the hexadecimal location in memory where the instruction is found. The next two columns represent the machine code of the instruction in hexadecimal. The fourth column is simply a line number. The remaining four columns are the assembly-language program containing the optional label, the op-code mnemonic, the operand, and comments, respectively. The dollar sign (\$) is used to signify a hexadecimal value.

The pound sign (#) indicates that the operand is an "immediate" value to be used by the op code, rather than a variable in memory.

Six sections of the code are virtually identical. Each one starts with STB \$0FF20 and ends with BRA INPxxx.

In each section the value in the B register is output to the DAC by STB \$0FF20. The DAC immediately changes this value to a voltage level. The output of the comparator is then loaded into the A register by LDA, Y. The Y register was previously loaded with the address of the comparator output, \$0FF00. If the value in A has bit 7 set, a branch on minus (BMI) is done, and a delta value (one-half of the

present range) is added to the value in the B register. If the value in A has bit 7 reset, the SUBB #\$xx is done to subtract the delta value.

The six sections taken together constitute a binary search to find the input value. At INP070, the B register holds the final value. It is stored in the next memory location pointed to by the X register. The ",X+" form of the instruction automatically increments the X register by 1 to point to the next location after the current store. The X register is then compared to BUFEND, the last location for storing digitized values. If there is space left, the routine branches back to INP005 to sample the next value.

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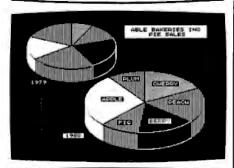








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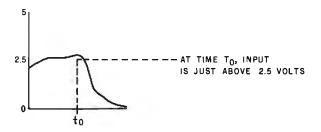


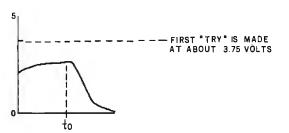


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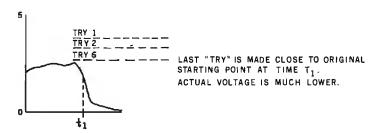


Figure 7: By the time the software has bracketed a given voltage sample, the true voltage has often changed significantly, as shown in this sequence. However, as long as the sampling rate is at least twice the highest frequency to be measured, the magnitude of the error will be acceptable.

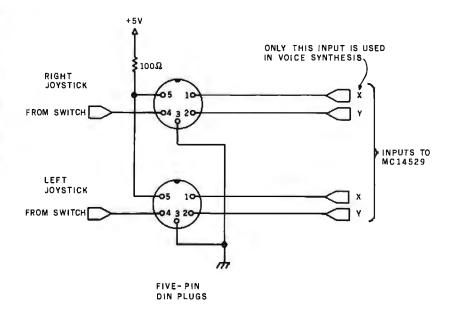


Figure 8: The Color Computer's joystick inputs allow four channels of data. Only the X input of the right channel is used in this project.

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Listing 2: The OUTPUT routine is coded in 6809 assembly language. It retrieves values stored in memory and reproduces the original input by outputting the data at the original input rate. Data is output to the television audio modulator.

1798 8 1790 8 1780 8 1785 4 1786 8 1786 8 1780 2 1783 8 1786 8 1788 8 1788 8 1788 8 1788 8	36 37 37 11 36 11 36 16 37 36 37 36 37 36 37 36 37 36 37 37 37 37 37 37 37 37 37 37 37 37 37	FF23 FF23 F7C4 FF20 FFF6 FF61 FF01 FF03 FF03	00710 00720 00730 00740 00750 00760 00760 00760 00610 00660 00660 00660 00660 00660	OUTPUT OUTØ10 OUTØ20 SELECT	BSR LDA STA LDA LDA DECA BNE LDA STA LDA STA LDA STA LDA STA LDA STA LDA STA LDA STA LDA STA LDA STA LDA STA LDA STA LDA STA LDA STA LDA STA LDA STA LDA STA STA LDA STA STA LDA STA STA LDA STA STA LDA STA STA LDA STA STA LDA STA STA LDA STA STA LDA STA STA LDA STA STA STA LDA STA STA STA STA STA STA STA STA STA ST	\$ELECT #\$3C \$0FF23 #BUFFER #19 OUT020 ,X+ \$0FF20 #BUFEND OUT010 \$0FF01 #\$0FF \$0FF03 #\$0FF03 #\$0FF03	SELECT DAC OUTPUT LOAD INITIALIZATION VALUE INITIALIZE PIA FOR OUTPUT POINT TO BUFFER DELAY COUNT DELAY LOOP DELAY GET VALUE OUTPUT TO DAC TEST FOR END OF DATA GO IF NOT END END-RETURN GET PIA CONFIGURATION RESET LSB OF MUX SELECT STORE GET PIA CONFIGURATION RESET MSB OF MUX SELECT STORE RESURN
		1000	ののものの		C110		

Listing 3: A BASIC program that loads the INPUT and OUTPUT routines into memory, defines them as external USR calls, and allows the user to store and play back up to 11/2 seconds of speech.

```
100 PCLEAR 1:CLEAR 10,8H1720
110 REM VOICE SYNTHESIS PROGRAM IN BASIC FORM
120 DATA 247,255,32,166,164,43,4,192,0,32,4,203,0,32,0
130 DATA 23,0,133,16,142,255,0,142,23,196,198,128
140 DATA 231,128,140,63,255,38,157,57,141,24,134,60,183,255,35
150 DATA 142,23,196,134,19,74,38,253,166,128,183,255,32
160 DATA 140,63,255,38,241,57,182,255,1,132,247,183,255,1,182,255,3
170 DATA 132,247,183,255,3,57
180 FOR J≃0 TO 5
190 RESTORE
200 FOR I=&H1737+J*15 TO &H1745+J*15
210 READ A
220 POKE IJA
230 NEXT I
240 POKE &H173F+J*15,2^(6-J)
250 POKE %H1743+J*15,2^(6-J)
250 NEXT J
270 FOR I=&H172B TO &H1736
280 READ A
290 POKE IJA
300 MEXT
310 FOR I≃&H1791 TO &H17C3
320 READ A
330 POKE I A
340 NEXT I
350 DEFUSR0=&H172B:DEFUSR1=&H1799
360 INPUT "RECORD (R) OR PLAY (P)?";A$
370 IF A$="R" THEN A=USR0(0) ELSE IF A$="F" THEN A=USR1(0) ELSE GOTO 360
380 GOTO 360
```

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programs converted to DATA values in an Extended Color BASIC program. This BASIC program stores DATA values into memory locations \$172B through \$17C3. To condense the number of DATA values, the loop from 180 through 260 replicates the six sections of the INPUT routine six times. Values of 64, 32, 16, 8, 4, and 2 are POKEd for the delta values in two places. The following loops move the remaining values.

There are two entry points to the code, one at INPUT and one at OUT-PUT. In this fixed location for the program, INPUT is at location \$172B and OUTPUT is at location \$1799. USR0 calls the INPUT routine and USR1 calls the OUTPUT routine.

Building the Input Device

The normal joystick inputs are shown in figure 8. Each joystick plug is a 5-pin DIN jack. On each DIN jack, one pin is connected to the X

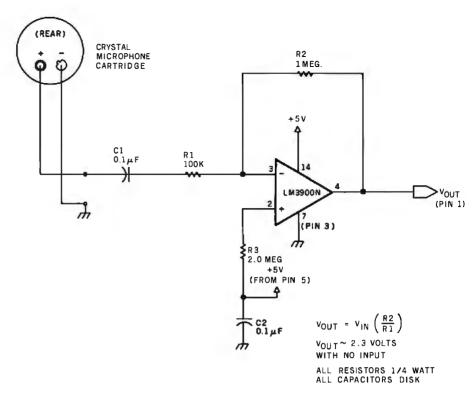


Figure 9: An op-amp serves as a " \times 10" amplifier to up the output from the crystal microphone to the voltage range of 0 to 4.6 V.

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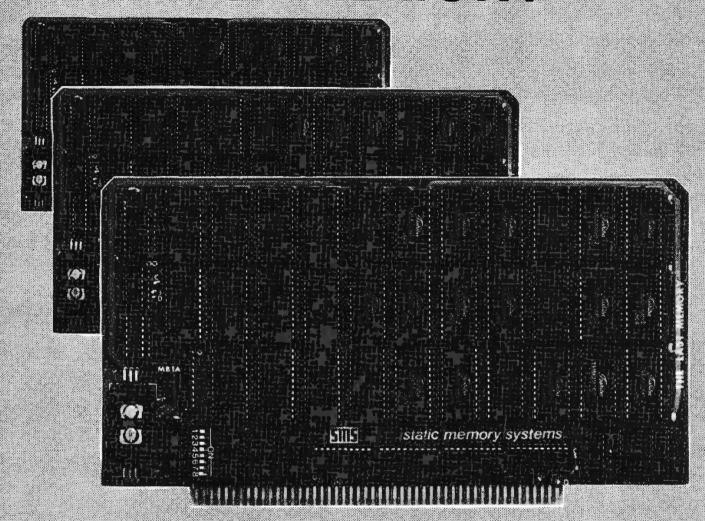
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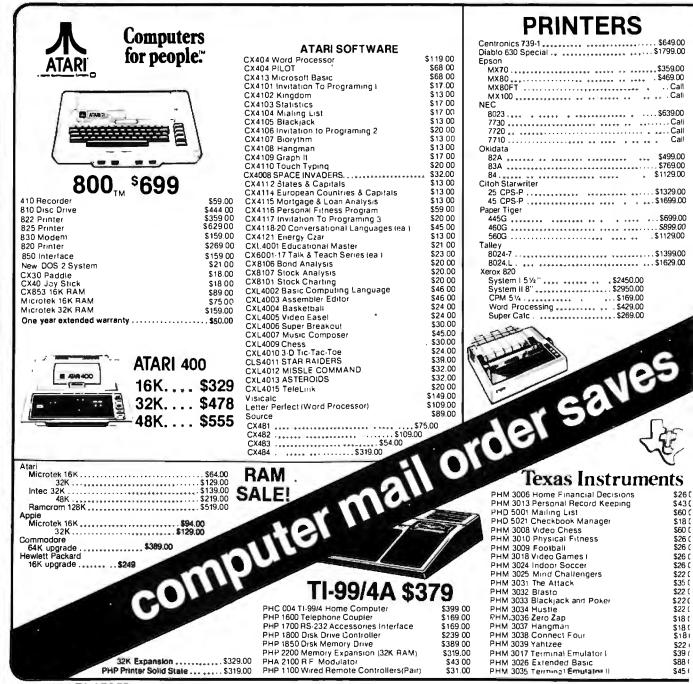
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In this application I'll be using only the X channel of the right joystick. I'd like to convert an audio signal, which is essentially an AC voltage, to a level of 0 to 5 V DC. This level can then be sampled, digitized, and stored in memory by the ADC hardware and software.

Figure 9 shows a simple voice-input circuit for connection to the Color Computer's right joystick jack. To convert the sound to an analog voltage, I use a crystal microphone. Its output is on the order of tenths of a volt. A simple "op amp" (operational amplifier) ups this voltage to the desired 0 to 5-V range. The amplifier's resting voltage, or bias, is

about 2.3 V. As sound is applied, this voltage fluctuates in the 0 to 5-V range.

Since the amplifier I'm using requires less than 0.004 amperes, I can power it with the 5-V DC supply available from pin 5 on the Color Computer's DIN jack. The only side effect this will produce is a 0.4-V drop across the 100-ohm resistor on the 5-V lead.

The easiest way to construct the amplifier is to mount the parts on a prototype board, as shown in figure 10. This board, which Radio Shack sells for \$6.49 (catalog number 276-175), consists of 23 rows of 12 holes each. The outer vertical columns on the left and right can be used for ground and power buses.

Figure 10 shows the arrangement of the components on the prototype board. The resistor and capacitor leads can be cut to length and then pushed into the proper holes without soldering or wire wrapping. The LM3900N op amp can also be pushed into the board—the holes are properly spaced.

The microphone used in this project is really a crystal microphone cartridge, available from Radio Shack for \$1.59 (catalog number 270-095). Two wires must be soldered to the cartridge. Then the other ends of the wires are coated with solder and plugged into the board as shown.

Three wires go from the board directly into the Color Computer's right joystick DIN jack, as shown in figure 10. One wire attaches to ground (pin 3), one attaches to +5 V (pin 5), and one attaches to the X channel (pin 1).

All parts are available from Radio Shack or other electronics stores and should cost under \$10. See table 1 for a parts list.

Operation of the Voice System

Now to see (er, hear) some results. Plug the completed circuit into the right joystick jack. Turn on the Color Computer and quietly execute the

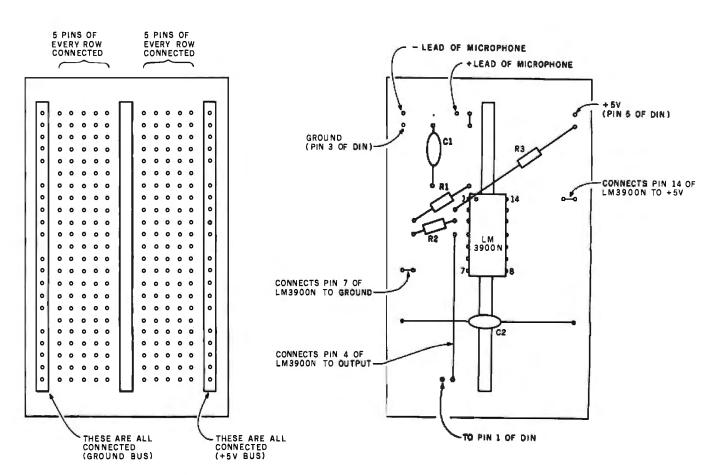


Figure 10: The project uses an inexpensive prototype circuit board, which allows the six components to be connected without soldering or wire-wrapping.

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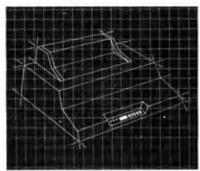
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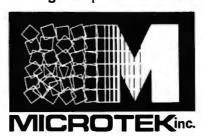
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You should now see a continuous display of a number close to 30. The number displayed represents the voltage input from the microphone circuit, in units of 4.6/64 V. Thirty multiplied by 4.6/64 is approximately 2.3, which is the correct voltage when you are not talking into the microphone. Actually, values from 26 to 34 indicate an acceptable bias level. If the displayed numbers are out of this range, the audio signals will be clipped on either the top or bottom. as shown in figure 11, resulting in distorted sound. If the value is greater than 34, decrease the value of R3 in figure 9; if it is less than 26, increase the value of R3.

Talk into the microphone while running the program. You should see the values change, although the pattern isn't predictable. Look for lows close to 0 and highs close to 63.

If everything looks satisfactory, load the program shown in listing 3 and execute it. When the message "RECORD (R) OR PLAY (P)?" is displayed, type R. At the same time, speak loudly into the microphone element while holding it close to your mouth. Speaking off to the side eliminates voice "pops." You have about 11/3 seconds to record the message. (Sorry, Texans, you'll have to adopt a speedy California vocal attitude here.) You'll have time for such messages as "Help! computer failure!" "Twas brillig and the slithy " and "Input error, dummy!"

The program will record the audio and then return to the prompt message again. Enter P to play back the message through the television audio. You can play back a recorded message repeatedly by looping back to the P USR call.

The fidelity of the sound played back is excellent, even though its duration is short. (Short but sweet, to coin a phrase)

Condensing the Data

That's the basic hardware and software for acquiring and playing back

Part	Number Required	
Crystal microphone cartridge (Radio Shack Cat. No. 270-095 or equivalent)	1	
LM3900N operational amplifier (Radio Shack Cat. No. 276-1713 or equivalent	1	
0.1-μF capacitor—C1, C2	2	
100-kΩ resistor—R1	1	
1-MΩ resistor—R2	1	
2-MΩ resistor—R3	1	
Prototype circuit board (Radio Shack Cat. No. 276-175 or equivalent)	1	
Table 1: Parts list for the microphone inp	out circuit.	

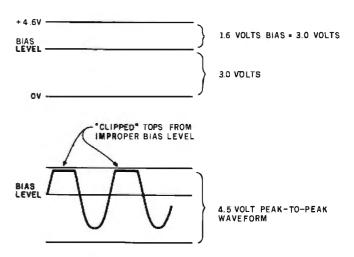


Figure 11: Clipping off the top or bottom of the waveforms may result from an improper bias setting. Bias should be set to approximately 2.3 V.

the data. Now comes the problem of condensing the data. Three approaches can be used here: altering the sampling parameters during acquisition of the data, processing the data after acquisition, and a combination of the two.

Altering the Sampling Parameters. The program just described records data at about 7700 samples per second. The rate can be reduced by putting in a time delay after the "STB ,X+" in the INPUT routine. A simple routine like the one shown in listing 4 would do the trick. It would delay the acquisition of data by about $5.62 \times X\mu s$. Sampling rates for various values of X are shown in table 2. The

	Samples per	
X	Second	
1	7410	
2	7114	
2 3	6841	
4	6587	
5	6414	
10	5390	
20	4137	
30	3357	

Table 2: The sampling rate of the input routine can be reduced by adding a time delay loop after the STB X + inINPUT (listing 1). A simple loop is described in the text. Rates as low as 6000 samples per second should still produce intelligible speech.



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PLEASE READ ORDERING INFORMATION ON PAGES 443 AND 109 **Listing 4:** A simple routine that puts a time delay after the "STB ,X+" in the INPUT routine.

	LDA	# X	CONSTANT
LOOP	DECA		DECREMENT
	BNE	LOOP	LOOP IF NOT ZERO

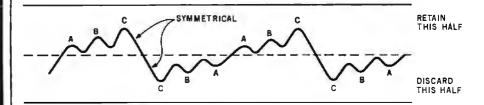
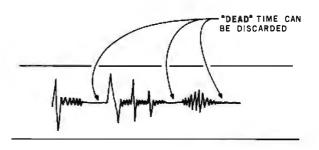


Figure 12: One method of data compression is to keep only the top or bottom half of the waveform; the other half can be synthesized by the OUTPUT program at the proper time.



" COLOR COMPUTER IS ... "

	V	LU	Ε		0	0
	VA	LU	E		0	0
1	1 1	1	1	1	1	1
	DE	LA	!	N	M S	3
	V	AL U	Ε		0	0
	VA	LU	Ε		0	0

LEGITIMATE A/D-CONVERTER VALUE LEGITIMATE A/D-CONVERTER VALUE FLAG WORD (WASTED BITS NOT 0) DELAY COUNT LEGITIMATE A/D-CONVERTER VALUE LEGITIMATE A/D-CONVERTER VALUE

Figure 13: Another method of compressing the data involves recognizing dead space between words. Instead of storing these silent periods, a flag-word may be stored in the data sequence, followed by a delay count to be used during the output process.

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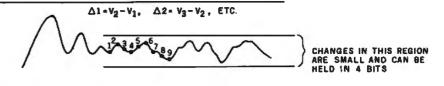
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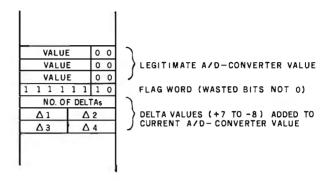


Figure 14: Data that repeat or change only minutely may be compressed by using 4-bit values. The values are added to the current ADC value to generate a new DAC output value.

program must be reassembled if this change is made, because the displacement values for the branches in some cases are no longer valid. Judging from the quality of the speech at the 7700 samples-per-second rate, sampling rates as low as 6000 per second will probably be acceptable.

Another parameter that can be varied in acquisition is the resolution of the ADC. I used a 6-bit ADC. allowing for 64 different levels. Certainly one or two bits could be deleted from this resolution without too much degradation. If two bits were deleted, twice as much data could be stored in memory by packing two nibbles per byte in memory. This would call for a little more overhead in the INP070 area as the values were stored, but the net effect would probably be to maintain the same sampling rate (or better), since the instructions from INP050 through INP070 could be deleted.

Data Processing after Acquisition. In most compression methods, the ADC values are post-processed by an analysis program. The waveforms are symmetrical about the horizontal axis. Therefore, I can keep one half and throw the other away, as shown in figure 12. The trick here is recognizing repetitions of the cycle.

Another possibility is to delete the dead time between words. In a string of words, large areas where there is no sound are a waste of storage. For such cases, the dead space could be stored as a special flag value, indicating that a delay of *n* milliseconds could be performed based on the value following the flag value, as shown in figure 13.

A third compression technique is to look for portions of the data that change slowly. Certain sounds, such as vowels, have a much lower level than consonants like "P" that almost explode over a wide dynamic range. If the change is small enough, it can be held in four bits instead of eight, further reducing memory requirements. Again, a flag value can be used on output to get into this "slow change" mode, as shown in figure 14.

I hope I've stimulated your imagination with this article. Half the battle is getting the data digitized. The rest is mere programming!

References

- Barden, William, Jr. "Color Computer from A to D," December 1981 BYTE, page 134.
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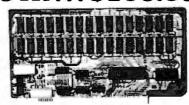
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Pascal NOW

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Pascal NOW sounds like an impassioned plea to adopt the Pascal language. While that would be a worthwhile topic, it is not the subject of this article. NOW (Negotiable Order of Withdrawal) is a term used to describe a wide variety of interest-bearing checking accounts.

Pascal NOW is a Pascal program designed to help manage one of these accounts. This article describes the program and some of the features of Pascal. I also provide a few hints to help a person who already knows BASIC begin to "think in Pascal." Such a person resembles one who knows the English system of weights and measures but wants to learn the metric system. The metric system is often learned as a translation system-one thinks in the English system, then converts to metric units. This is entirely different from "thinking in metric." The same problem can arise in learning Pascal. To capitalize on the features of Pascal, one must

About the Author

Thomas E. Doyle has taught computer programming at the technical college level for seven years.

begin to "think in Pascal" rather than "think in BASIC" and then translate to Pascal.

The difference between a regular checking account and a NOW account is that the latter earns interest. A personal finance program must include the capability of handling this additional income correctly. My first impulse was to modify a BASIC program I've been using to manage my checking accounts. I've also received several suggestions for improvements to the program, so I decided to rewrite the program in Pascal, incorporating those improvements.

Using the Program

Above all, a checkbook program should be easy to use. The program should provide the following functions:

- add items to the file
- remove items from the file
- sort the items by date
- dump the updated file to disk
- load the file from disk
- print the file contents
- balance the account and print totals by item category
- quit (return to operating system)

Each of the eight functions is specified by typing the first letter of the function name: A, R, S, D, L, P, B, or Q (upper or lowercase).

Each item in the file has five descriptors:

- 1. item number
- 2. dollar amount
- 3. date
- 4. description of item
- 5. item category

For checks, the item number would be the check number. You can assign sequential numbers to items such as deposits, NOW interest, or electronic funds transfers. Since most checks start numbering at or above 100, at least 99 numbers would remain for that purpose. This method works best if item numbers for noncheck transactions are recorded right in the checkbook.

Modification

The exact nature of the item category list will vary depending on your expenditures. Almost everyone

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BUSINESS 100 PROGRAM LIST

1 RULE78 Interest Apportionment by Rule of the 78's 2 ANNUI Annuity computation program 3 DATE Time between dates 4 DAYYEAR Day of year a particular date falls on 5 LEASEINT Intérest rate on lease 6 BREAKEVN Breakeven analysis 7 DEPRSL Straightline depreciation 8 DEPRSY Sum of the digits depreciation 9 DEPRDB Declining balance depreciation 10 DEPRDDB Double declining balance depreciation 11 TAXDEP Cash flow vs. depreciation tables 12 CHECK2 Prints NEBS checks along with daily register 13 CHECKBK1 Checkbook maintenance program 14 MORTGAGE/A Mortgage amortization table 15 MULTMON Computes time needed for money to double, triple, etc. Determines salvage value of an investment 16 SALVAGE 17 RRVARIN Rate of return on investment with variable inflows 18 RRCONST Rate of return on investment with constant inflows 19 EFFECT Effective interest rate of a loan 20 FVAL Future value of an investment (compound interest) 21 PVAL Present value of a future amount 22 LOANPAY Amount of payment on a loan 23 REGWITH Equal withdrawals from investment to leave 0 over 24 SIMPDISK Simple discount analysis 25 DATEVAL Equivalent & nonequivalent dated values for oblig-26 ANNUDEF Present value of deferred annuities 27 MARKUP % Markup analysis for items 28 SINKFUND Sinking fund amortization program 29 BONDVAL Value of a bond 30 DEPLETE Depletion analysis 31 BLACKSH Black Scholes options analysis 32 STOCVAL1 Expected return on stock via discounts dividends 33 WARVAL Value of a warrant 34 BONDVAL2 Value of a bond 35 EPSEST Estimate of future earnings per share for company 36 BETAALPH

60 COMPBAL 61 DISCBAL 62 MERGANAL 63 FINRAT 64 NPV 65 PRINDLAS 66 PRINDPA 67 SEASIND 68 TIMETR 69 TIMEMOV 70 FUPRINE 71 MAILPAC **LETWRT** 73 SORT3 74 LABELI 75 LABEL2 76 BUSBUD 77 TIMECLCK 78 ACCTPAY 79 INVOICE 80 INVENT2 TEL DIR 82 TIMUSAN 83 ASSIGN 84 ACCTREC 85 TERMSPAY 86 PAYNET 87 SELLPR 88 ARBCOMP 89 DEPRSF 90 UPSZONE 91 ENVELOPE 92 ACITOEXP 93 INSELE 94 PAYROLL2 95 DILANAL 96 LOANAFFD RENTPRCH 98 SALELEAS 99 RRCONVBD 100 PORTVAL9

Weighted average cost of capital True rate on loan with compensating ball required True rate on discounted loan Merger analysis computations Financial ratios for a firm Net present value of project Laspeyres price index Paasche price index Constructs seasonal quantity indices for company Time series analysis linear trend Time series analysis moving average trend Future price estimation with inflation Mailing list system Letter writing system-links with MAILPAC Sorts list of names Shipping label maker Name label maker DOME business bookkeeping system Computes weeks total hours from timeclock info. in memory accounts payable system-storage permitted Generate invoice on screen and print on printer In memory inventory control system Computerized telephone directory Time use analysis Use of assignment algorithm for optimal job assign. In memory accounts receivable system-storage ok Compares 3 methods of repayment of loans Computes gross pay required for given net Computes selling price for given after tax amount Arbitrage computations Sinking fund depreciation Finds UPS zones from zip code Types envelope including return address Automobile expense analysis Insurance policy file In memory payroll system Dilution analysis Loan amount a borrower can afford Purchase price for rental property Sale-leaseback analysis Investor's rate of return on convertable bond Stock market portfolio storage-valuation program

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47 EOQ

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45 SIMPLEX 46 TRANS

48 QUEUEI

38 OPTWRITE

54 FQEOQPB 55 QUEUECB

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As above but with shortages permitted As above but with quantity price breaks Cost-benefit waiting line analysis Net cash-flow analysis for simple investment

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Economic order quantity inventory model

Single server queueing (waiting line) model

Fixed quantity economic order quantity model

Option writing computations

Value of perfect information

Cost-volume profit analysis

Conditional profit tables

Opportunity loss tables

Value of additional information

Expected value analysis

Derives utility function

Bayesian decisions

Value of a right

Portfolio selection model-i.e. what stocks to hold

Profitability index of a project Cap. Asset Pr. Model analysis of project

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Circle 138 on inquiry card.

will have the common expense categories of food, shelter, transportation, and clothing. The program listing shows possible categories, but I'm sure everyone will want to modify it to reflect specific needs.

If you want to change specific category titles, modify the assignment statements in the procedure "initialize" (see listing 1). The program is set up for a total of 50 categories. To change the total number of categories, modify the assignment statement in the constant declaration statement that sets "max_codes" to 50. The first ten category codes are set up for items that will add to the

balance; the remaining codes are reserved for items that will reduce it. If you want more codes for income categories, change the constant declaration that sets "max_add_ code" to 10. The item category is accessed and stored by number, which speeds item entry and minimizes storage space requirements. If you need instructions, the program will list the item categories and their descriptions.

One important aspect of selecting item categories is deciding how specific to make the categories. For example, consider automobile expenses. Your first thought might be to lump

all auto-related expenses together. Another method would be to classify auto expenses in more specific categories: insurance, repairs, monthly payments, etc. By using the second method, it's easier to do other types of analysis. For instance, if you wanted to know how much you were spending on insurance policies, you could group auto with health, life, and other types of insurance. A good way to determine the exact nature of your expense categories is to review the checks you've written in the last vear or two.

The specific data file name "A:tom81" is set in the constant

Text continued on page 304

Listing 1: The source listing for Pascal NOW written in Pascal/MT+, version 5.2.

```
PROGRAM checks;
{ Pascal/MT+ Version }
CONST max_items = 300;
      max\_codes = 50;
      max\_add\_code = 10;
      disk_file = 'A:tom81';
TYPE
    item_data = RECORD
                    item_number : INTEGER;
                   month: INTEGER;
                   day: INTEGER;
                   year : INTEGER;
                   amount : REAL;
                   description : STRING[30];
                   code : INTEGER;
                END;
VAR command : CHAR;
    code_description : ARRAY [1..max_codes] OF STRING[15];
    items : ARRAY [l..max_items] OF item_data;
    item_last : l..max_items;
    data_file : FILE of item_data;
    lines_printed: 0..80;
    code_amount : ARRAY [1..max_codes] OF REAL;
    entry_year : INTEGER;
    swaped : BOOLEAN;
    answer : CHAR;
    result : INTEGER;
```

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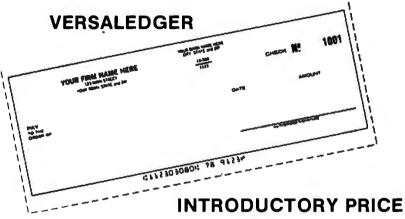
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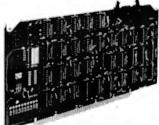
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Listing 1 continued:

```
PROCEDURE initialize:
{ set initial values }
VAR count : 0..max_items;
BEGIN
    item_last := 1;
    FOR count := 1 TO max_codes DO
                                                    ٠,
      code_description[count] := '
    code_description[1]
                             'Balance forward';
    code_description[2]
                              'Deposit
                          :=
    code_description[3]
                              'NOW interest
    code_description[11]
                          :=
                              'House payment
    code_description[12]
                              'Car payment
                          :=
    code_description[13]
                              'Gas & Electric
                          :=
    code_description[14]
                              'Gasoline
                         :=
    code_description[15]
                              'Credit cards
    code_description[16]
                              'Auto insurance
                              'Entertainment
    code_description[17]
    code_description[18]
                              'Telephone
    code_description[19]
                             'Auto maint.
                              'Subscriptions
    code_description[20]
    code_description[21]
                              'Clothing
    code_description[22]
                              'Computer parts
    code_description[23]
                              'Travel
    code_description[24]
                              'Contributions
                              'Misc. auto
    code_description[25]
    code_description[26]
                              'Investments
    code_description[27]
                              'Education
    code_description[28]
                              'Water & sewer
                          :=
    code description[29]
                              'Taxes
    code_description[30]
                              'Books
    code_description[31]
                              'Food
                              'Drugs
    code_description[32]
                          : =
    code_description[33]
                              'Medical service'
                          :=
    code_description[34]
                              'Tyme withdrawl
                          :=
                              'Misc. insurance
    code_description[35]
    code_description[36]
                         :=
                             'Dental
    code_description[37] :=
                             'Professional
    code_description[38] := 'Sewing/knitting'
    code_description[50] :=
                              'Misc. expenses
END:
PROCEDURE newpage;
{ print form-feed and 2 blank lines }
BEGIN
    WRITELN(CHR(12));
    WRITELN:
    WRITELN;
    lines_printed := 0;
END;
PROCEDURE instructions;
{ print description of program operation }
VAR answer : CHAR;
    count : INTEGER;
BEGIN
    newpage;
    WRITELN(' Checkbook program - T.E. Doyle ');
    WRITELN(' Version 1.23 ');
    WRITELN;
    WRITE(' Want instructions ? ');
                                   Listing 1 continued on page 296
```

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```
READ(answer);
   WRITELN;
   IF (answer = 'Y') OR (answer = 'y') THEN
      BEGIN
        newpage;
        WRITELN(' -- Commands --');
        WRITELN:
        WRITELN(' A -
                        Add an item');
        WRITELN(' R
                     - Remove an item');
        WRITELN(' P
                        Print all items');
        WRITELN(' B
                        Print balance');
                     - Sort by date');
        WRITELN(' S
                    - Dump to disk');
        WRITELN(' D
        WRITELN(' L
                        Load from disk');
        WRITELN(' Q - Quit'):
        WRITELN:
       WRITELN:
        WRITELN('Code
                                Description');
        FOR count := 1 TO 27 DO
          WRITE('-');
        WRITELN;
        FOR count := 1 TO 50 DO
          IF code_description[count] <> '
                                         ',code_description[count]);
             WRITELN(count:3,'
      END;
END;
PROCEDURE heading;
{ print heading for new page of item printout }
VAR count : 0..79;
BEGIN
    WRITE(' Item
                                 Amount
                                                     Description');
                      Date
    WRITE('
                              Code');
    WRITELN;
    FOR COUNT := 1 TO 79 DO WRITE('-');
    WRITELN;
END:
PROCEDURE item_print( count : INTEGER);
{ print data on one item }
BEGIN
    WITH items[count] DO
    BEGIN
    WRITE(item_number:5);
    WRITE (month: 5, '/');
    IF day < 10 THEN
       WRITE('0', day:1)
    ELSE
       WRITE (day: 2);
    WRITE('/',year:2);
    WRITE (amount:11:2);
    WRITE('
            ',description);
    WRITE('
                ',code_description[code]);
    END;
END;
PROCEDURE print_all;
{ print data for all items in file }
VAR count : INTEGER;
BEGIN
                                                             Listing 1 continued on page 298
  newpage;
```

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```
Listing 1 continued:
   heading:
    FOR count := 1 TO item_last-1 DO
     IF lines_printed = 55 THEN
       BEGIN
        newpage;
        heading;
       END;
     item_print(count);
     WRITELN;
     END;
  WRITELN:
 END;
 PROCEDURE balance;
 { Print totals by categories and net balance }
 VAR item : l..max_items;
     balance : REAL;
 BEGIN
   FOR item := 1 TO max_codes DO
     code_amount[item] := 0.00;
   balance := 0.00;
   FOR item := 1 TO item_last-l DO
     WITH items[item] DO
     code_amount[code] := code_amount[code] + amount;
   FOR item := 1 to max_add_code DO
     balance := balance + code_amount[item];
   FOR item := max_add_code+1 TO max_codes DO
     balance := balance - code_amount[item];
   newpage;
   WRITELN('
               Category
                                     Amount');
   FOR item := 1 TO 32 DO
     WRITE('-');
   WRITELN;
   FOR item := 1 to max_codes DO
     IF code_amount[item] <> 0.00 THEN
       WRITELN(code_description[item],' -',code_amount[item]:14:2);
   FOR item := 1 TO 32 DO
     WRITE('-');
   WRITELN:
  WRITELN('Balance
                    -',balance:14:2);
  WRITELN;
 END;
 PROCEDURE remove;
 { remove item from file }
 VAR remove : CHAR;
     found, item : INTEGER;
     item_remove : INTEGER;
 BEGIN
   found :=0;
   WRITELN:
   WRITE(' Remove item number - ');
   READ(item_remove);
   FOR item := 1 TO item_last-l DO
     IF items[item].item_number = item_remove THEN
       found := item;
   WRITELN;
   IF found <> 0 THEN
     BEGIN
       heading;
       item_print(found);
```

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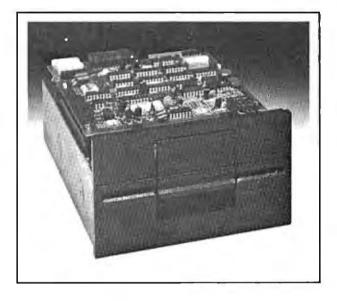
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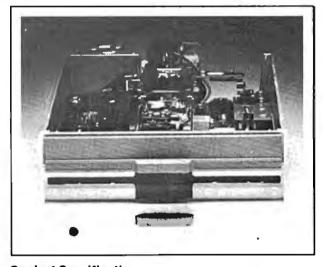
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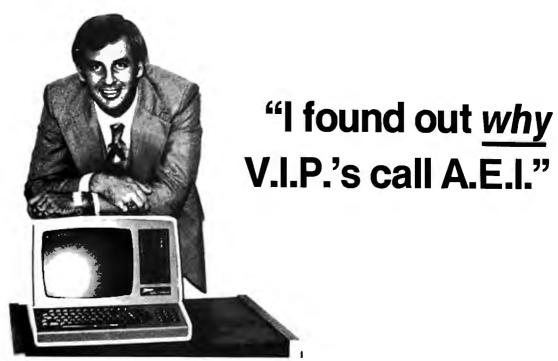
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```
WRITELN;
      WRITELN;
      WRITE(' Remove ? ');
      READ(remove);
      IF (remove = 'Y') OR (remove = 'y') THEN
        BEGIN
          FOR item := found TO item_last-l DO
             items[item] := items[item+1];
          item_last := item_last-l;
        END;
    END;
  IF found = 0 THEN
    WRITELN(' Item not in list ....');
END;
PROCEDURE entry;
{ console entry of check/deposit data }
VAR ch : CHAR;
BEGIN
  REPEAT
   WITH items[item_last] DO
     BEGIN
                                                         ٠,
       description := '
       WRITELN;
       WRITE(' Item number ? ');
READLN(item_number);
       WRITE(' Month ? ');
       READ (month);
       WRITE(' Date ? ');
       READ (day);
       WRITE(' Amount ? ');
       READ (amount);
       WRITELN('
                                                                  .');
       WRITE(' Description ? ');
       READLN(description);
       WHILE LENGTH (description) <> 30 DO
         description := CONCAT(description,' ');
       WRITE(' Code ? ');
       READ(code);
       year := entry_year;
       WRITELN;
     END;
   heading;
   item_print(item_last);
   WRITELN;
   WRITELN;
   WRITE(' Correct ? ');
   READ (ch);
  UNTIL (ch = 'y') OR (ch = 'Y');
  items[item_last+l] := items[item_last];
  items[item_last+l].item_number := 0;
  item_last := item_last+l;
  WRITELN;
END;
PROCEDURE swap_items(item : integer ; VAR swaped : BOOLEAN);
{ exchange file data at location with location+l }
BEGIN
  items[max_items] := items[item];
  items[item] := items[item+1];
                                                               Listing 1 continued on page 302
```



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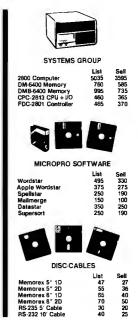
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```
Listing 1 continued:
  items[item+1] := items[max_items];
  swaped := TRUE
END;
PROCEDURE date_sort;
{ sort data file by date }
VAR finish , item : 0..max_items;
    date_first , date_second : REAL;
    item_first , item_second : INTEGER;
BEGIN
  finish := item_last-2;
  REPEAT
    swaped := FALSE;
    FOR item := 1 TO finish DO
      BEGIN
        WITH items[item] DO
          BEGIN
            date_first := year * 10000.0 + month * 100.0 + day;
            item_first := item_number;
          END;
        WITH items[item+1] DO
          BEGIN
            date_second := year * 10000.0 + month * 100.0 + day;
            item_second := item_number;
          END:
        IF date_first > date_second THEN
          swap_items(item, swaped);
        IF (date_first = date_second) AND (item_first > item_second) THEN
            swap_items(item, swaped);
      END;
    IF finish > 2 THEN
      finish := finish -1;
  UNTIL NOT swaped
PROCEDURE dump;
{ write file of item information to disk }
VAR count : INTEGER;
  ASSIGN(data_file, disk_file);
  REWRITE (data_file);
  FOR count := 1 TO item_last DO
    BEGIN
      data_file^ := items[count];
      PUT(data_file);
    END;
  CLOSE(data_file, result);
END;
PROCEDURE read_disk;
{ load data from disk to file }
BEGIN
  WRITELN;
  ASSIGN(data_file,disk_file);
  RESET(data_file);
  item_last := 1;
  REPEAT
    items[item_last] := data_file^;
    GET(data_file);
    WRITE('.');
    IF item_last MOD 10 = 0 THEN
      WRITELN:
```

```
item_last := item_last + 1;
 UNTIL items[item_last -1].item_number = 0;
    item_last := item_last -1;
   WRITELN:
    CLOSE(data_file, result);
END:
PROCEDURE prog_commands;
{ console entry of program command }
BEGIN
    WRITELN;
    WRITE(' Command ? ');
    READ(command):
    CASE command OF
      'A', 'a' : entry;
      'B', 'b' : balance;
      'P', 'p' : print_all;
      'R', 'r' : remove;
      'S','s' : date_sort;
      'D', 'd' : dump;
      'L','l' : read_disk;
      ELSE
      IF (command = 'Q') OR (command = 'q') THEN
        WRITELN(' Leaving Program')
        WRITELN(' Invalid command .....')
    END;
END;
{ mainline program }
BEGIN
    initialize:
    instructions;
    WRITELN:
    WRITE(' Enter year " 2-digit " for new entries - ');
    READ(entry_year);
    WRITELN;
    WRITELN:
    read_disk;
    REPEAT
      prog_commands;
    UNTIL (command = 'q') OR (command = 'Q');
    WRITELN;
    WRITE(' Save file ? ');
    READ(answer);
    IF (answer = 'Y') OR (answer = 'y') THEN
      dump;
END.
1>
Text continued from page 292:
```

statement to your specific file name. If you're keeping track of several convenient to compile separate ver-

declaration section. Change this load the data file automatically when the program is run. This poses a problem the first time you run it. NOW accounts, you'll find it more How do you load a file that doesn't exist? The best way to handle this sions of the program for each account problem is to first compile a version and maintain each version on a dif- of the program without the ferent disk. The program is set up to "read_disk" statement in the mainline section. Run this version, add one item to the file, and do a write to disk. Recompile the program with the "read_disk" statement in the mainline section and use that version thereafter. This may take a little extra effort initially, but it makes the program much more convenient.

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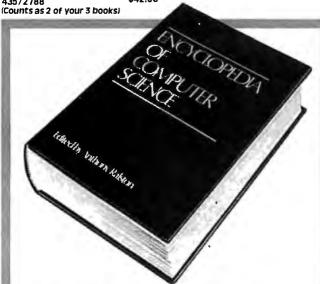
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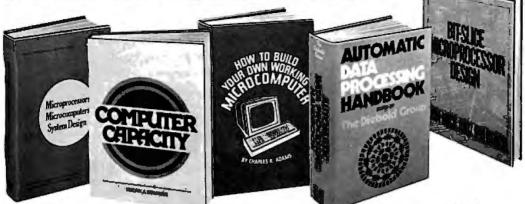
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Managing Data

An interesting aspect of data management programs is that, in most cases, a number of specific descriptors may refer to the same item. In the Pascal NOW program, five descriptors refer to each item. Four are numerical, and the fifth, "description," is a string of charac-

Consider these descriptors as hav-

ing two identities. The first consists of belonging to a group of similar descriptors (e.g., an item number belonging to the group of all item numbers). Most languages have the capability for this type of grouping through the use of arrays. Membership in a group of descriptors referring to a specific item, such as a check, forms the second identity. BASIC and many other languages do

not have ways to indicate this type of grouping.

In BASIC, you can indicate a general relationship of this sort by considering that array members with like index numbers refer to the same item. To illustrate, assume that the first element in the item-number array and that in the date array refer to the same check. This sort of grouping is an illusion. One realizes this when swapping items during a sorting. You cannot simply include a line in a BASIC program that will swap all the descriptors referring to one item with all the descriptors referring to another.

One way of circumventing this problem is to group all the descriptors into a long string, then pick out certain fields within the string to obtain the specific descriptor information. This enables the program to reference all descriptors that relate to a specific item. Unfortunately, the item descriptors lose their identity as being members of the similar descriptors' group. BASIC programs using this technique become cluttered with MID\$ statements.

Enter Pascal

Pascal has the RECORD data type to handle this problem. The easiest way to visualize the RECORD data type is to consider how most BASIC programs store descriptor information on disk. Descriptor information for a specific item is stored in a common record in the disk file. The commonality is lost when the data is read from the disk and the specific descriptor information is sent to the array. In Pascal, it is possible to maintain the relationship between descriptors through the use of a RECORD data type.

The Pascal NOW program defines "item_data" as a RECORD that consists of seven descriptors referring to a common item. There are actually seven descriptors, rather than the five mentioned earlier, because the date is broken down into month, day, and year. We then define a variable "items" as an array of "item_data". Notice that "items" is not simply seven arrays but is an array of

Text continued on page 318

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Listing 2: The changes needed in order to run Pascal NOW under Pascal/Z, version 3.0. Substitute listing 2a for all the material from TYPE until (but not including) the "initialize" in listing 1. Substitute 2b, 2c, and 2d for equivalent procedures within listing 1.

```
(2a)
TYPE
    item_data = RECORD
                    item_number : INTEGER;
                    month: INTEGER;
                    day: INTEGER;
                    year : INTEGER;
                    amount : REAL;
                    description: STRING 30;
                    code : INTEGER;
                 END;
    $string0 = string 0;
    STRING255 = STRING 255;
VAR command : CHAR;
    code_description : ARRAY [1..max_codes] OF STRING 15;
    items : ARRAY [l..max_items] OF item_data;
    item_last : l..max_items;
    data_file : FILE of item_data;
    lines_printed: 0..80;
    code_amount : ARRAY [1..max_codes] OF REAL;
    entry_year : INTEGER;
    swaped : BOOLEAN;
    answer : CHAR;
    result : INTEGER;
FUNCTION LENGTH(x:$STRING255) : INTEGER; EXTERNAL;
(2b)
PROCEDURE heading;
{ print heading for new page of item printout }
VAR count : 0..79;
BEGIN
    WRITE(' Item
                      Date
                                   Amount
                                                       Description');
    WRITE ('
                           Code');
    WRITELN;
    FOR COUNT := 1 TO 79 DO WRITE('-');
    WRITELN:
END;
PROCEDURE item_print( count : INTEGER);
{ print data on one item }
BEGIN
    WITH items[count] DO
    BEGIN
    WRITE(item_number:5);
    WRITE(month:5,'/');
    IF day < 10 THEN
       WRITE('0', day:1)
    ELSE
       WRITE(day:2);
    WRITE('/',year:2);
    WRITE (amount:14:2);
    WRITE(' ',description);
    WRITE(' ',code_description[code]);
    END;
END;
                                                              Listing 2 continued on page 310
```

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```
Listing 2 continued:
(2c)
PROCEDURE entry;
{ console entry of check/deposit data }
VAR ch : CHAR;
BEGIN
  REPEAT
   WITH items[item_last] DO
     BEGIN
                                                        ٠,
       description := '
       WRITELN;
       WRITE(' Item number ? ');
       READLN(item_number);
       WRITE(' Month ? ');
       READ (month);
       WRITE(' Date ? ');
       READ (day);
       WRITE(' Amount ? ');
       READ (amount);
       WRITELN('
                                                                 !);
       WRITE(' Description ? ');
       READLN(description);
       WHILE LENGTH (description) <> 30 DO
         APPEND (description, ' ');
       WRITE(' Code ? ');
       READ (code);
       year := entry_year;
       WRITELN;
     END;
(2d)
PROCEDURE dump;
{ write file of item information to disk }
VAR count : INTEGER;
BEGIN
  REWRITE(disk_file, data_file);
  FOR count := 1 TO item_last DO
    WRITE(data_file,items[count]);
END;
PROCEDURE read_disk:
{ load data from disk to file }
BEGIN
  WRITELN;
  RESET(disk_file, data_file);
  item_last := 1;
  REPEAT
    READ(data_file,items[item_last]);
    WRITE('.');
    IF item_last MOD 10 = 0 THEN
      WRITELN;
     item_last := item_last + 1;
  UNTIL items[item_last -1].item_number = 0;
    item_last := item_last -l;
    WRITELN;
END;
PROCEDURE prog_commands;
{ console entry of program command }
BEGIN
    WRITELN:
    WRITE(' Command ? ');
```

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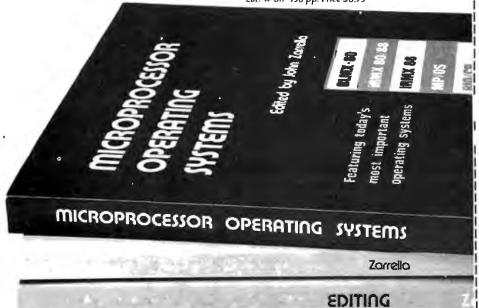
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Listing 2 continued:

```
READ(command);
CASE command OF
   'A','a': entry;
   'B','b': balance;
   'P','p': print_all;
   'R','r': remove;
   'S','s': date_sort;
   'D','d': dump;
   'L','l': read_disk;
   ELSE:
   IF (command = 'Q') OR (command = 'q') THEN
        WRITELN(' Leaving Program')
   ELSE
        WRITELN(' Invalid command ....')
END;
END;
```

Listing 3: A sample run of the Pascal NOW program.

```
Checkbook program - T.E. Doyle Version 1.23
```

Want instructions ? y

```
-- Commands --
```

```
A - Add an item

R - Remove an item

P - Print all items

B - Print balanc:

S - Sort by date

D - Dump to disk

L - Load from disk

O - Ouit
```

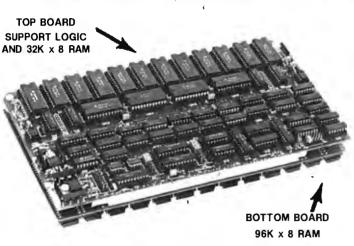
Code	Description
1 2 3 11 12	Balance forward Deposit NOW interest House payment Car payment
13	Gas & Electric
14	Gasoline
15	Credit cards
16	Auto insurance
17	Entertainment
18	Telephone
19	Auto maint.
20	Subscriptions
21	Clothing
22	Computer parts
23	Travel
24	Contributions
25	Misc. auto
26	Investments
27	Education

Listing 3 continued on page 316

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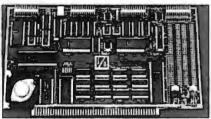
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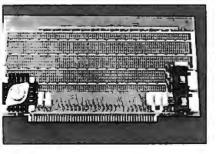
Bank readdress Phantom select/override Wait cycle select/deselect



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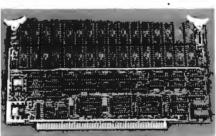
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Listing 3 continued:

28	Water & sewer
29	Taxes
30	Books
31	Food
32	Drugs
33	Medical service
34	Tyme withdrawl
35	Misc. insurance
36	Dental
37	Professional
38	Sewing/knitting
50	Misc. expenses

Enter year " 2-digit " for new entries - 81

Command ? p

Item	Date	Amount	Description	Code
	2/02/81		Balance from 1980	Balance forward

Command ? a Item number ? 2 Month ? 3 Date ? 3 Amount ? 18.00

Description ? Subscription to BYTE Code ? 20

Item	Date	Amount	Description	Code
2	3/03/81	18.00	Subscription to BYTE	Subscriptions

Correct ? y

Command ? b

Category		Λmount
Balance forward Subscriptions	_	100.00
Balance	_	82.00

Command ? a Item number ? 1 Month ? 1 Date ? 1 Amount ? 12.34

Description ? Movie tickets Code ? 17

Listing 3 continued on page 318

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Correc	t ? y			
Comman	d ? p			

Item	Date	Amount	Description	Code
1	2/02/81	100.00	Balance from 1980	Balance forward
2	3/03/81	18.00	Subscription to BYTE	Subscriptions
1	1/01/81	12.34	Movie tickets	Entertainment

Command ? s Command ? p

Item	Date	Amount	Description	Code .
1	1/01/81	12.34	Movie tickets	Entertairment
1	2/02/81	100.00	Balance from 1980	Balance forward
2	3/03/81	18.00	Subscription to BYTE	Subscriptions

Command ? b

Category		Amount
Balance forward Entertainment Subscriptions	-	100.00 12.34 18.00
Balance	_	69.66

Command ? w Invalid command

Command ? q Leaving Program

Save file ? y

Text continued from page 306:

records, with each element consisting of seven items. This concept is similar to multidimensional arrays. There's a major limitation to BASIC multidimensional arrays that would preclude their use in this application: they must have all elements of the same type. Integers, reals, and strings cannot be grouped into one array in BASIC.

Another advantage over multidimensional arrays is how elements are referenced. If you want to reference all the descriptors for a specific item, indicate "items[index]". To reference a specific descriptor of the item (e.g.,

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Program Operation

There are a few differences in operation between the Pascal/Z and Pascal/MT + programs. Pascal/MT + version 5.2 offers the choice of BCD or floating-point format for real numbers. For this program, I used BCD numbers. Pascal/Z version 3.0 offers only floating-point format; therefore, an error of a penny or two will show up occasionally. Input of data from the keyboard is a little different in Pascal than in BASIC. If there's a variable with the type CHAR, it can hold a single character. A READ statement awaiting this variable will be satisfied when a single character is typed in. Pascal/MT+ does not require a carriage return to indicate that the character has been typed. So, when a key is pressed for a singlecharacter command, the program will process the command immediately. Keyboard input in Pascal/Z is handled like keyboard input in BASIC. After you enter a single-character command, the program will wait for a carriage return. This variation has an interesting effect when entering the item description (a string with a maximum length of 30 characters).

In both versions of the program, typing a carriage **return** will terminate this string. In the Pascal/MT+ version, if the description is greater than 30 characters, the program will terminate the string when the 30th character is entered and then go on. In the Pascal/Z version, the string input is not processed until the carriage return is pressed. If the string entered is over 30 characters, Pascal/Z detects an error and abruptly terminates the program.

Observations: Basic vs. Pascal

One of the first things the BASIC user notices when using Pascal or other compiled languages is that compiling takes time. For example, when using Pascal/Z, the **program** must be compiled, assembled, **and** linked. For the Pascal NOW program, this process takes almost 8 minutes. When using Pascal/MT+, the program must be compiled and linked, a process

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that requires nearly 4 minutes. Both times are for a Z80-based system operating at 4 MHz.

In seven years of teaching computer programming, I've noticed a definite improvement in the quality of programs written by people using compiled languages. When working with BASIC, it's very tempting to write programs using the cut-and-try technique: if a program doesn't work, throw in a few GOTO statements to patch it up, then try it again. BASIC

program changes can be incorporated and evaluated very quickly. This characteristic almost encourages an inelegant technique.

With a compiled language like Pascal, you're more apt to think through a problem because of the relatively long time required to incorporate changes. The available versions of Pascal are evolving, so I'd encourage you to make a very careful comparison of each version's features before making a selection.
■

Pascal Standards

One of the problems plaguing BASIC is the lack of a standard. Pascal has a slightly different problem—it has several standards. At present, there appear to be three main "standards" for Pascal: the Jensen and Wirth standard, the UCSD standard, and the ISO standard. Some of the differences among these are very subtle, but other differences can hamper program transport between systems. I won't attempt to say which of these standards is "The Standard," but I will offer observations on the differences between some versions of Pascal.

While this program was being written, I had access to three versions of Pascal: Pascal/MT+, version 5.2, Pascal/Z, version 3.0, and UCSD Pascal, version 1.0 (pseudocode). The first two compilers are native code compilers, compiling the Pascal source code directly to 8080/Z80 machine code. The UCSD version is a pseudocode (p-code) compiler, compiling the Pascal source to an intermediate code (p-code) which is then interpreted. I ran a prime number program under all three versions as a benchmark and measured execution times. Because the p-code version took almost five times as long as the native code versions, I only wrote versions of the program in Pascal/MT + and Pascal/Z.

The main difference between Pascal/MT+ and Pascal/Z lies in how they handle character strings. Jensen and Wirth define strings in a very limited sense and do not define any string functions or procedures. UCSD Pascal has set a de facto standard for strings, and Pascal/MT+ has incorporated these UCSD string functions and procedures into its version of Pascal. Pascal/Z defines its own string functions and procedures, which are not directly compatible with those of UCSD Pascal.

Disk input/output (I/O) is another area where Pascal/MT+ and Pascal/Z differ. Pascal/MT+ has incorporated full file buffert, GET, and PUT I/O and has kept its file I/O as close as possible to ISO and Jensen and Wirth standards. Pascal/Z has not implemented standard file buffer1, GET, or PUT I/O, and as a result, the procedures that read and write to external files are a bit different. When printing real numbers, the field width specification for Pascal/Z did not work properly. Consequently, the sections of the program that print beadings and real numbers were modified. By the time this article is published, the problem should be remedied.

The CASE statement, as defined by Jensen and Wirth, does not allow for exceptions. Both versions of Pascal incorporate extensions to handle exceptions. Pascal/MT+ uses the statement ELSE as it is used in IF-THEN-ELSE statements to identify the exceptions. Pascal/Z uses ELSE: to identify exceptions. It considers the ELSE as another case and, as a result, follows it with a colon.

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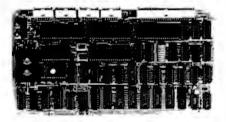


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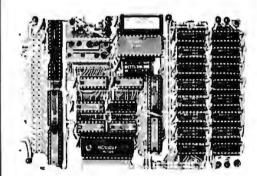
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BYTE's Bugs

Bugs Switch Photos and Figures

The two photographs on page 40 of Steve Ciarcia's article "Switching Power Supplies" were inadvertently transposed. (See the November 1981 BYTE.) The photograph above the caption for photo 3 is actually photo 4 and vice versa.

Gremlins also struck Chris Crawford's article, "The Atari Tutorial, Part 3: Player-Missile Graphics." (See the November 1981 BYTE, page 312.) The color portions of Chris's figures 1 and 2b, which represented the video images, were omitted, and figure 4 appeared upside down. The corrected figures are shown here.■

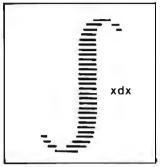
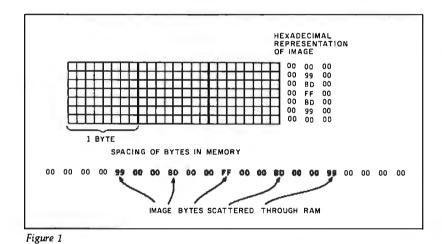


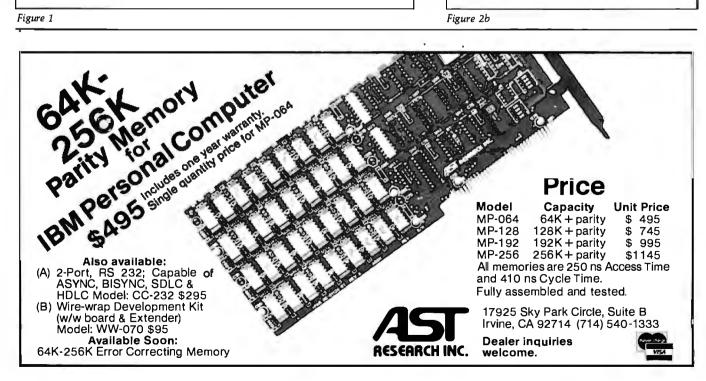
Figure 4



BYTE N ISSILE 1 MISSILE 3-MISSILE 2-IISSILE O Figure 2b

THESE BITS GIVE THE SHAPE AND VERTICAL POSITION FOR

SŞILE MISSILE MISŞILE MISSILE



BYTELINES

News and Speculation About Personal Computing

Conducted by Soi Libes

Random Rumors: An Ada compiler for Z80-based systems is said to be in development by Supersoft Associates, Champaign, Illinois. Versions for Intel's 8086/ 8088. Motorola's 68000. and Zilog's Z8000 are expected by year's end. The Z80 version, a subset of Ada (the Department of Defense has still not frozen the complete Ada standard), will be upgraded to a completely validated version in subsequent releases. The Z80 Ada package will sell for \$200 to \$300.... American Express will market the Sinclair ZX81 via its mailorder business. . . . Digital Research may be working on a Visicalc look-alike. . . . Tandy is rumored planning, on its TRS-80 Model II desktop computer, to incorporate two Tandon 8-inch "thinline" floppy-disk drives and a Winchester drive in the spot now occupied by two 8-inch drives. . . .

Apple may introduce its 68000 machine in the second quarter of 1982; Apple is reported to be trying to purchase one million 68000 microprocessors at \$10 each. Two versions of the 68000-based system are expected: a single-user desktop unit and a network controller for an Ethernet-type system.... Reports are that Intel is getting a mixed reception to the iAPX-432 32-bit microprocessor. In any event, the instruction set will be frozen, in microcode, early in 1982. Present owners of iAPX-432 chip sets will be able to trade them for the revised version... Heath is said to be working on a completely new generation of computers....

Several Japanese manufacturers are expected to introduce complete briefcasesize personal computers using CMOS (complementary metal-oxide semiconductor) and bubble memory.... Commodore's hoped-for Z80 processor board for the PET is a dead issue, as negotiations for an exclusive license from Small Systems Engineering, the supplier, have broken down.... Data General is rumored about to make available a CP/M-compatible version of its Enterprise system. . . . Corvus is reported about to introduce Xerox 820 and IBM Personal Computer interfaces for its Omninet local network system. . . . Alpha Micro may be developing a video-taperecorder interface as a Winchester disk drive backup market.

Random News Bits: Zilog Corporation, Cupertino, California, and Seeg Technology, Campbell, California, have announced plans to manufacture a 16K-bit EEPROM (electrically erasable programmable readonly memory). Samples are expected by the end of the second quarter of 1982. Later this year, Zilog plans to introduce versions of the Z8, Z80. and Z800 microprocessors with on-board EEPROM memory. No mention of the ROM size. . . . DEC (Digital Equipment Corporation) announced that earnings for the quarter ending in October 1981 increased 58% (\$88.8 million) on a 28% increase in sales (\$839.3 million).... Condesin, of Cupertino, California, claims it will soon introduce a 4M-bit non-volatile memory on a chip the size of a 64K-bit device using an "unpatterned charge-storage" technique. With an access time of 1 microsecond, it is viewed as a replacement for floppy disks. Condesin expects to be in production by the end of this year. It also expects to be able later to increase storage 16 times to 2³⁶ bits on a single chip. . . .

Panasonic has introduced a hand-held computer using the 6502 microprocessor and 8K bytes of memory. . . . Bell Laboratories is field-testing Getset, a combination telephone handset, speakerphone, keyboard, and video display that can be used for store-and-forward switching, electronic mail, directory and dialing assistance, and database and personal-information retrieval... Wolfdata, Ithaca, New York, has developed Wolfdata Artificial Intelligence Language (WAIL), which writes programs dynamically.... General Instrument Microelectronics. Hicksville, New York, has introduced a 16K-bit EEPROM requiring only one +5-volt supply. It is organized as 2K by 8 bits, can be erased in 10 milliseconds, retains data for 10 years, and features a pinout similar to the 2716 EPROM. Price is \$40....

The IEEE (Institute of Electrical and Electronics Engineers) has established a committee to draft a standand for the 8-bit STD bus. Currently 40 manufacturers produce STD-bus boards. The committee will also investigate 16-bit transfers on the bus and compatibility with the Eurocard format. . . . More than a hun-

dred firms have already been licensed by Xerox to use Ethernet. A license costs \$1200.... Radio Shack, preparing to launch its 16-bit computer, has increased its retail computer-marketing field force from 5 to 18 people... A jury in San Francisco found Data General guilty of violating federal antitrust laws by illegally tying the sale of its operatingsystem software to its hardware. Plaintiffs were Fairchild Camera and Instrument Corporation and Digidyne Corporation.... Oki Semiconductor, Santa Clara, California, takes the prize for the largest ROM in production: a 4M-bit ROM.

BM Watching: The most serious disadvantage of the new IBM Personal Computer is its limited disk storage. However, IBM is said to be working on adding 8-inch floppy-disk drives and a 14M-byte Winchester disk to the list of peripherals for the Personal Computer. IBM may also be working on a higher-density plug-in memory card to free one of the bus slots in the machine.

A few discount dealers are already offering discounts on the IBM system that are very small compared to discounts available for other systems. However, IBM is selling the system to its own employees at a 40% discount.

IBM will have to strengthen its distribution before it will have a serious impact on Apple and Tandy. After all, Apple and Tandy have extensive distribution systems that took several years to develop. Apple Computer Inc.

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¹The numbers can be superscripted or non-superscripted, at the user's option.

²At the user's option, the footnotes can also be removed from the text file to a separate note file.

³The default symbol "@" can be changed to any other symbol.

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⁵The user can modify, add, or delete text and notes and run **FOOTNOTE** again to re-number and reformat the edited

has 2500 dealers and over 300 companies selling hardware and software for the Apple. Tandy Corporation's distribution is even larger. To increase distribution, IBM is expected to open a large number of retail outlets this year and add a large number of new distributors. IBM is said to be negotiating with industrial distributors to carry the Personal Computer. Many of these distributors are already carrying the IBM 3101 ASCII terminal and the 8-inch Piccolo Winchester drive. However, this distribution route will probably not begin to function until the second quarter.

Further, IBM has reorganized its internal marketing and manufacturing organization. IBM sales reps will now be able to sell the entire range of IBM products, where previously they have been limited to one or two specific product lines.

Portia Isaacson and Egil Juliussen of Future Computing, Richardson, Texas, recently released a market-research study titled IBM's Billion-Dollar Baby: The Personal Computer (\$475 a copy), in which they predict that demand for the IBM Personal Computer will reach 100,000 units by the end of 1982, 250,000 units by the end of 1983, and 450,000 by the end of 1985.

Computing Market: Capitalizing on the fact that 250,000 DEC VT-100 video terminals are already in operation, Digital Equipment Corporation (DEC) has entered the personal computer market by introducing a kit to upgrade a VT-100 to a full-blown personal computer system. In doing this the firm accomplished three things: (1) it capitalized on a closed, ready market; (2) it provided

a system cost substantially below its competition (provided you already own a VT-100); and (3) it beat at least one company that was planning to introduce a VT-100 personal-computer upgrade to the punch. The \$2400 kit upgrades a VT-100 (which typically costs \$1300 to \$1500, depending on options) by adding a Z80 microprocessor with 64K bytes of memory on a plug-in board and a 514-inch floppy-disk drive (160K bytes of storage) in a separate cabinet. CP/M costs another \$250 and a second drive adds \$1275.

DEC will be selling the system through its distributors, by direct telephone order, and through its 25 stores. No plans were disclosed for sales via computer stores.

Battle of the Operat-Ing Systems: When IBM announced that Digital Research's CP/M-86 disk operating system (DOS) would be supported by the IBM Personal Computer, visions of plentiful software danced in the heads of many potential purchasers, who were thinking of the legion of programs that are available for use under CP/M-80, the operating system that has become the de facto standard for users of 8-bit 8080-, 8085-, and Z80based computers.

But the visions may soon be dancing to a different tune. Despite the similarity of the two DOSes, an operating system does not change the character of the hardware it runs on, and the hard fact remains that software written and compiled for the Z80 microprocessors cannot be immediately and easily run on the 8088 16-bit microprocessor. Programs must be converted and/or rewritten to be compatible, taking time and effort.

Meanwhile, confidence is increasing in IBM's Personal

Computer DOS, which was written for IBM by Microsoft Inc., of Bellevue, Washington. As of this writing, all of the application software announced by IBM runs under this DOS, and many program authors report that converting CP/M-80 programs to run under the Microsoft system is easier than converting them to run under CP/M-86.

Microsoft will be releasing the operating system, which it will call "MS-DOS," to be run on 16-bit computer systems from other manufacturers. And Lifeboat Associates of New York City, the world's largest distributor of 8-bit CP/M software, has committed itself to support Microsoft's MS-DOS, under the name "SB-86." for the 16-bit world. Lifeboat plans to make SB-86 available for a wide variety of machines in the same way that it made CP/M-80 available off the shelf for close to 40 different 8-bit computers. Lifeboat says it will convert all of its current software packages to run under SB-86.

There is no doubt that CP/M-80 will continue to dominate the 8-bit DOS market. But the 16-bit race for dominance is still on, and CP/M-86 is in the pack along with MS-DOS and the multiuser operating systems: Digital Research's own MP/M-86, Oasis-86 from Phase One Systems, Multi-OS from Infosoft Systems, and Microsoft's Unix-like Xenix operating system.

32-Bit Bus Spec Agreed On: While the IEEE-896 committee continues to haggle over a standard for 32-bit microprocessors, three manufacturers have announced agreement on a 32-bit bus. Motorola, Mostek, and Signetics/Philips have announced the VME bus. Thompson CSF has also an-

nounced its support for the bus. The VME bus is a Eurocard-compatible subset of Motorola's Versabus and includes some of the features from the IEEE-896 group. However, the three companies, all with a large stake in the 32-bit 68000 market, felt they could wait no longer.

The bus has 192 pins in its fully **expanded** configuration with 64 available for user-defined I/O. The IEEE-896 design has fewer pins, but uses multiplexing, which lowers the performance of the system.

idbits From Japan: The Japanese government is investing \$50 million in a program to develop a fifth-generation computer by 1985. The computer will offer more intelligent man/machine interfaces and will be more closely aligned with societal needs than its honorable ancestors. It will be based on VLSI (very-large-scale integration) devices, integration of new communications techology, parallel processing, software engineering, artificial intelligence, and pattern recognition.

Fujitsu has announced the development of a new highperformance integrated circuit using the company's **HEMT** (high-electron-mobility transistor) technology. The device has demonstrated a switching time of 17 ps (picoseconds, or 10^{-12} seconds) with a power dissipation of 0.96 milliwatts. This is about 30 times faster than conventional MOSFETs (metaloxide semiconductor fieldeffect transistors) and is comparable to the 13-ps time of Josephson-junction devices. Fujitsu engineers hope to reduce this time to well under that of Josephson devices. One advantage of the HEMT devices is that they require less cooling-only to -196°C (the temperature of

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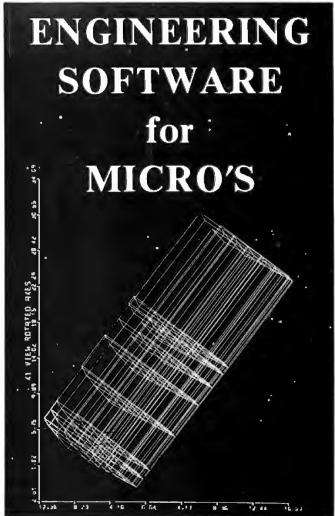
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liquid nitrogen) compared to -269°C (the temperature of liquid helium) for Josephson devices. Hence, HEMT-based computers should be more practical and less costly.

NEC (Nippon Electric Company) has disclosed that it is considering building a \$100 million plant in Roseville, California, for fabrication and assembly of integrated circuits and electronic equipment. The plant is tentatively slated to go into production at the end of 1983.

Dalsy-Wheel and Dot-Matrix Printer Status Report: In 1972, David Lee created the Diablo daisywheel printer. Until then, IBM dominated the wordprocessing impact-printer market with its Selectric printer. The daisy-wheel printer operated with many fewer parts, providing faster and more reliable operation. Further, sophisticated control electronics were added to provide intelligent printer operation.

Within a year, Xerox Corporation acquired the Diablo Company. Lee left the following year and formed Qume, which was later bought by Exxon. Qume introduced its own version of a daisy-wheel printer, and for the next five years Diablo and Qume shared the word-processing daisy-wheel market.

Then, in 1979, Ricoh, a Japanese supplier, entered the market as an OEM (original-equipment manufacturer) supplier to Tandy and Lanier. NEC (Nippon Electric Company) introduced a word-processing printer using a thimble-like printing element. And recently Fujitsu announced a daisy-wheel printer that operates at 80 characters per second, almost twice the speed of most U.S. models. Also, we

can shortly expect Pertec, Brother, and Canon to introduce daisy-wheel printers.

Diablo and Qume have responded to the foreign competition by introducing new daisy-wheel printers having fewer parts, operating at lower speeds, and hence costing less. The Diablo and Qume share of the market has dropped to about 50%. However, the market has been growing at a rate of about 40% per year, and their business has continued to increase even though their market share decreased.

One other consideration in the word-processor market is that the quality of dot-matrix printers has been improving, and they are more and more being used for word-processing work. This trend can be expected to continue.

Although Americans have long expected a "Japanese invasion" in the personal computing market, this has not occurred. What has happened might be called an "infiltration," with the Japanese moving into selected segments of the market. The area where they have already scored a great success is in the under-\$1000 dot-matrix printer market. (The low-cost floppy- and hard-disk markets could be next.)

The Japanese, who two years ago had virtually no U.S. printer sales, today have almost 75% of the under-\$1000 printer market, estimated at \$200 million (expected to grow to \$950 million by 1985). Epson America is now the market leader. U.S. manufacturers, such as Centronics, Anadex, Tally, and Dataproducts, have abandoned the under-\$1000 printer market and are now concentrating their efforts on the higher-speed, multi-mode (single-pass and multi-pass), and multi-font machines. The question is, "Will the Japanese be far behind?"

he Developing 16-Bit Market: What is faster than a speeding bullet and more powerful than a locomotive? The new Texas Instruments TMS99000 16-bit microprocessor, with 24-MHz clock rate and an instruction set that includes single-precision floating-point instructions, that sells for a modest \$65 (100-piece price). And National Semiconductor, after many doubts and delays, is finally beginning to make available samples of its 16032 16-bit microprocessor.

The biggest news of the month is that AMD (Advanced Micro Devices) has signed a 10-year licensing agreement with Intel for the 8088, 8086, and iAPX-432 16and 32-bit microprocessors. AMD was, until now, the prime second source for the Zilog Z8000 16-bit microprocessor and a developer of many of the Z8000 support chips. AMD has disclosed that, although it will continue to manufacture and support its current Z8000 products, it will not do any further development of them. Zilog had recently reduced prices on the Z8002 to \$19.90 in 1,000piece lots. The Intel 8086 is currently selling for \$58.50 in lots of 100, with prices rising to \$127.40 for the 10-MHz version. However, Japanese suppliers are entering the market with high-volume prices close to \$23 and, for delivery 6 months from now, are quoting \$14. Motorola is currently charging \$91 for the 68000 processor in 25 to 99 quantities, and prices rise to \$269 for a 10-MHz part.

The Zilog Z8000 appears to have been caught in a pincer movement between the 8086 and the 68000. The 8086's large base of software and support chips, large number of second sources, and attractive pricing, and the 68000's high-powered performance appear to be making

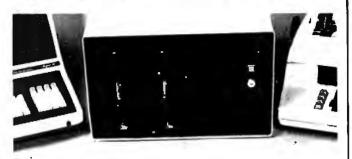
the 16-bit market a twodevice show, with the Z8000 getting a low third billing. It is rumored that Zilog's new 32-bit microprocessor will be a migration upward from the Z8000. This feature may prove attractive to system designers and put Zilog back in the race.

Floppy-Disk Format Chaos: The microcomputer industry has created a chaotic situation in 514-inch floppy-disk formats. The lack of a standard format has resulted in a multiplicity of disk formats such that disks created on one manufacturer's 514-inch disk system cannot be read on another manufacturer's 514-inch disk system. Thus, programs created using the CP/M operating system running on a Heath, Intertec, Apple, TRS-80, IBM, or North Star computer cannot be transferred easily from system to system. The problem is most acute for people who wish to copy public-domain software from the CPMUG and SIG/M user-group libraries.

Eight-inch floppy-disk users fortunately have a standard (the IBM 3740 format for single-density disks). Thus, 8-inch disk owners exchange software in singledensity format. However, there is no standard for double-density formatting, and 8-inch disk owners are forced to use single density when copying disks and then convert them to their particular double-density format. Virtually every 8-inch diskcontroller maker furnishes software for this converting process.

An additional problem has been created by manufacturers who have "improved" their versions of CP/M. In some cases these improvements cause the CP/M system to no longer be compatible

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BYTELINES_

with other CP/M systems.

The situation appears to be worsening because neither the IEEE nor the manufacturers appear to be concerned with the problem. Buyers of personal computers must be made aware that just because a particular computer uses the CP/M disk operating system, it does not mean that disks will be compatible with other systems that use CP/M. And if the system uses 5¼-inch disks, incompatibility is almost certain.

Amateur-Radio Computer News: The FCC (Federal Communications Commission) is presently considering authorizing amateur radio operators to transmit data not encoded in ASCII (American Standard Code for Information Interchange) or 5-bit (Baudot/Murray) code. This is being done in response to a petition from the ARRL (American Radio Relay League). The FCC is also considering allowing increased sending speeds for ASCII transmission within certain frequency bands.

The ARRL, AMRAD (Amateur Radio Research and Development Corporation), and AMSAT (Radio Amateur Satellite Corporation) recently conducted a conference on amateur-radio computer networking. The purpose was to recognize the innovative work already done by amateurs in the United States and Canada, to explore the possibilities of an integrated amateur packet network, and to set up the framework for orderly growth of a network.

According to Paul L. Rinaldo, chairman of the conference, a two-level approach to network organization is being planned. Local networks centering around VHF (very high frequency) repeater stations will be supplemented by more wideranging "backbone" net-

works. A backbone network is being formed along the eastern seaboard of North America from Norfolk, Virginia, to Montreal, Quebec, with a spur into the Boston, Massachusetts, area. Other centers of activity are Tucson, Arizona; San Francisco, California; and Vancouver, British Columbia.

Most of the testing has been done in the 2-meter and 220-MHz bands at a data rate of 1200 bps (bits per second). AMRAD is seeking a special temporary authorization from the FCC to experiment with higher data rates.

The proceedings of the conference are available for \$5 from AMRAD, 1524 Springvale Ave., McLean, VA 22101.

s "The Last One" The Last One? The Last One, the advertising claims, is "a computer program that writes computer programs" and, further, is "the last program you'll ever need."

The Last One asks the user programming questions and uses the answers to generate a "totally bug-free BASIC program" (to quote the ads). Versions that generate direct machine code and respond to continuous voice input are planned. The Last One was first demonstrated in April 1981 at the West Coast Computer Faire. The vendor, Al Systems, did not start filling orders until November 1981. It claims to have received orders for over 10,000 copies. worth over \$6 million (a single copy is \$600).

The question now is whether there can be a "last one." Al Systems says that it will require dealers to attend classes on the product and sign an agreement under which they will be fined if they misrepresent The Last One. The vendor admits that an unskilled user could make a mess of a program and that,

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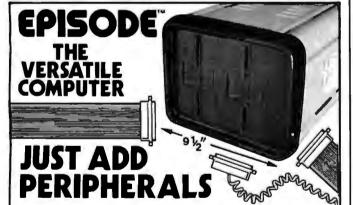
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- · Column position is displayed at all times.
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See full page ad in December issue (Page 61) of BYTE.

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GA. 912-377-7120 TM - TANDY CORPORATION FREE COPY OF WARRANTY UPON REQUEST although The Last One produces "error-free code," it may not produce an "error-free program." The vendor further admits that the manual requires considerable study, even for someone well versed in programming.

Hence, The Last One is really a program-generating tool. It does not solve a programming problem because it cannot define what it is that the user wants to do with the machine. Rather, it can, once a user is skilled in its use, substantially reduce coding time.

DEC Introduces Single-Chip LSI-11: Digital Equipment Corporation has made available a single-chip, 40-pin version of its popular 16-bit LSI-11 microprocessor (previously a 4-chip set). Unfortunately, hardware multiply and divide were not included. The device is used on a new single-board computer called the Falcon (or T-11). The board contains 4K bytes of read/write memory and sockets for 4K bytes more, as well as 32K bytes of ROM (or 16K bytes of ROM and 8K bytes of read/write memory). The board also contains two serial ports, 24 parallel I/O lines, a real-time clock, and DEC's standard LSI-11 bus interface

ntel Enters the Micro-computer Business: It was inevitable—Intel has finally entered the computer systems business. Intel has had all the components but has never integrated them into a complete system. Now it has finally formed an "OEM

Microcomputer Systems Division" to market the System 86/330. The complete system is intended to be sold by systems houses dealing in turnkey systems. In other words, Intel supplies everything but the actual application software.

The System 86/330 uses Intel's 8086 16-bit microprocessor in a Multibus housing with 320K bytes of programmable memory, 35M-byte Winchester disk, and 1Mbyte floppy-disk drive, all housed in a desktop unit. Options include interfaces to IEEE-488, RS-232C, RS-422, RS-449, Ethernet, and more. Disk operating systems include iRMX-86, CP/M-86, MD-DOS, or Unix. Performance is claimed to cover the range from the DEC PDP-11/23 up to the PDP-11/70 products. Prices to OEMs start at \$19,000 each. Watch out, DEC—Intel is coming on strong.

pple Doings: A. C. "Mike" Markkula, President of Apple Computer Inc., at a recent computer-conference panel discussion, shocked the audience by telling them that Apple Computer will try to "diligently eliminate what is now commonly referred to as 'software protection.' "He stated that "users should be allowed to have as many copies of a software program as necessary to do the application." Ironically, seated at the panel table was a representative from Atari, which has been advertising that it will pursue and legally prosecute anyone caught unlawfully copying its software.

Apple has also announced

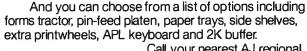
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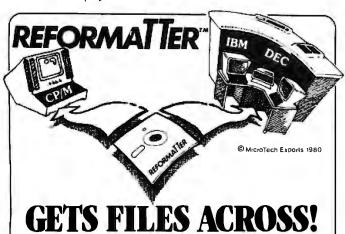
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a 237% year-end increase in income, to \$39.4 million on a 186% increase in sales (to \$334.8 million). Expenditures for research and development in fiscal 1981 were \$21 million, compared to \$7.3 million in 1980.

Radio Shack's Own Information Service: Tandy Corporation, parent company of Radio Shack, has begun to operate its own electronic information database service. The Tandy Videotex System is as yet offered only in Tarrant County, Texas (wherein lies Fort Worth, site of Tandy's headquarters), but it provides subscribers with continuously updated information, on demand, around the clock.

Tandy is inviting providers of specialized information to join the venture, while launching the service with the generalized staple diet familiar to users of other videotex systems; general news from local, regional, and national sources; sports news; special events; business and financial news; and weather forecasts.

During the initial marketing test period, the databases

will be maintained on TRS-80 Model II computers using the newly developed TRS-80 Communications Multiplex-

Tandy is also in the process of installing TRS-80 diskbased computer systems in each of its 4000 companyowned retail stores in the U.S. Each system will do detached processing and then communicate inventory and billing information to the firm's central computers in Fort Worth.

Luote of the Month:

"The current personal computer market is about the same size as the total potatochip market. Next year it will be about half the size of the pet-food market and is fast approaching the total worldwide sales of panty hose." James Finke, President, Commodore International Ltd.

MAIL: I receive a large number of letters each month as a result of this column. If you write to me and wish a response, please include a self-addressed. stamped envelope.

Sol Libes POB 1192 Mountainside, NJ 07092

BYTE's Bits

Software Authors' **Association Formed**

The Computer Writers' Association (CWA) has been formed to assist authors in situations involving legal rights, publishing standards, and a host of other difficulties that they confront when trying to sell software. The CWA is working on developing a standardized contract language between software writers and publishers, retaining legal counsel, publishing standards on plagiarism, and printing a regular newsletter. The CWA will offer new authors advice on how to break into the industry. A data bank will be established for members. Regular meetings will be held.

Anyone with resources, organizational skills and ideas should contact the Computer Writers' Association, POB 6312, Minneapolis, MN 55406, (612) 333-6060.

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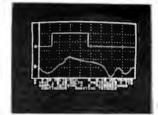
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BYTE February '82

System Notes

6809 Machine-Code Disassembler

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Any 6809-based system can use a resident disassembler whose purpose is to decipher various postbytes, relative addresses, and many op code mnemonics, thus making it easier for the assembly-language programmer to inspect the contents of memory. Although it produces no labels or machine-readable code that can be directly reassembled, the disassembler described here is fast and small (less than 2K bytes). In addition it is both reentrant and relocatable, allowing it to be placed anywhere in RAM (random-access memory) or ROM (read-only memory) while functioning normally. You can program this disassembler into an EPROM (erasable programmable read-only memory) and plug it into any EPROM socket with no change in operation.

A couple of techniques are used to make the program relocatable. First, program counter (PC) relative indexed addressing, rather than immediate addressing, is used to load the data-table starting addresses into an index register. During execution the index register is loaded with the program counter plus or minus the distance to the table, instead of with an absolute address. When relocating the program to another memory area, the program counter component of the address will still point to the table when added to the same offset. The assembler accomplishes the hard part of all of this—calculating the distance from the instruction to the table.

Another technique used for writing relocatable code is to store temporary variables on the stack rather than in absolute memory locations. The 6809, with its two stack pointer registers, makes this easy. First the user-stack register (U) is loaded with the current top-of-stack address. Next the system-stack pointer (S) is adjusted downward to leave room for the variables on the stack. This step is necessary to keep subroutine calls and interrupts from clobbering the variables on the stack. As long as the U register is not changed, variables can be referenced to their position on the U stack workspace simply by using

constant offset indexed addressing (i.e., LDA VARI-ABLE1,U). As much stack space may be reserved as necessary, as long as the computer has RAM available. Of course the user workspace must be **returned** to the system stack at the completion of the routine.

Since all of the temporary variables are on the stack, and assuming the stack can grow in size as necessary, the program can be interrupted in midexecution and called by another user program without **changing** any of the temporary variables. This reentrant feature allows the program to appear to service two or more users simultaneously under interrupt control. Of course, when using a disassembler in this mode, multiple output devices should be provided, or the outputs will be mixed and meaningless.

What does all of this cost? Well, like anything else there's the usual trade-off of speed and memory usage. While PC relative and constant offset indexed instructions operate somewhat more slowly than their immediate and extended or direct addressed counterparts, the speed penalty is not noticeable when the program is I/O (input/output) limited, as is this one. And while an additional byte is necessary for the indexed mode's postbyte, the postbye can sometimes include the constant offset, resulting in a saving of 1 byte of memory over extended addressing.

Using these techniques, the disassembler program in listing 1 was written as a subroutine which disassembles one machine-code instruction (1 to 5 bytes) and returns to its calling program—perhaps a monitor or software breakpoint routine. The sample output of listing 2 shows a portion of the disassembler working on itself. The memory address as well as the machine code are shown, followed by the mnemonic of the op code. The mnemonic's operand is deciphered to make offsets, target addresses, and addressing modes more readable.

Text continued on page 362

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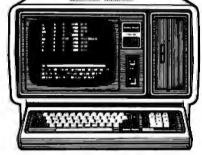
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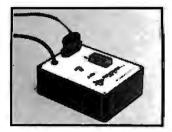
Listing 1: The 6809 machine-code disassembler program.

```
>**** DISAS9, 6809 MACHINE CODE DISASSEMBLER
>* VER 1.1, JUN 1981, J. DUBNER

>* THIS SUBROUTINE DISASSEMBLES 6809 MACHINE CODE TO THE
>* CONSOLE. IT IS COMPLETELY POSITION INDEPENDENT AND
>* REQUIRES NO RAM OTHER THAN ABOUT 90 BYTES ON THE STACK.

                                   ON ENTRY X:= ADDRESS TO BEGIN DISASSEMBLING, Y:= ADDRESS OF MONITOR'S OUTPUT ROUTINE.
ON EXIT X:= ADDRESS OF NEXT INSTRUCTION TO DISASSEMBLE,
                               ) *:
                              > * (+) AND (+) ARE MY PRINTER'S CHARACTERS FOR SQUARE
> * BRACKETS (ASCII $59 AND $50) AND SIGNIFY INDIRECT
> * ADDRESSING
                              > *** USER STACK ORGANIZATION
> * TEMPORARY STORAGE
> DUTCH RMB 2 MONI
                                                                       MONITOR'S OUTPUT CHARACTER ROUTINE
CURRENT DISASSEMBLY ADDRESS
WORKING ADDRESS
INSTRUCTION LENGTH
0000
                               CURADR
                                           RMB
0002
                                            RMB
0004
                               > WRKADR
0006
                               ) LENGTH
                                            RMB
                                                        1
                              ) PAGE
) DPCD
                                                                                    PAGE
Ø007
                                            RMB
                                                         1
                                                                       OP
                                                                           CODE
                                                                       OP CODE
0008
                                             RMB
                                                        1
Ø009
                               > POSTB
                                            RME
                                                        1
                                                                                    SECOND BYTE
                                                                       MSB OF OPERAND
                              >BYTE1
>BYTE2
Ø000A
                                            RMB
                                                        1
COOD
                                             RMB
                                                                       INDIRECT ADDRESSING FLAG
INDEXED ADDRESSING BYTE
NEXT AVAILABLE BYTE OF DUTPUT BUFFER
(2) (2) (C)
                               ) INDFLG
                                            RMB
                                                        1
WOOD
                               > INDBYT
                                            RMB
DODE
                              > NXTBUF
                                            RMB
                                * DUTPUT BUFF
                               ) BUFFER EQU
RMB
        0010
                                                                       START OF OUTPUT BUFFER
0010
                                                                       ADDRESS
                                                        4
0014
                                             RMB
                                                        12221
0015
                                             RMB
                                                                       PAGE HEX BYTES
                                            RIMB
                                                                       OPCODE HEX BYTES
POST BYTE HEX BYTES
ØØ19
                                             RMB
ØØ1B
                                             RMB
ØØ1 C
                                                        Žį.
                               ) HEXB
                                             RME
                                                                       OPERAND HEX BYTES
                                                        725121
0020
0022
0027
                                             RIMB
                                             RMB
                               ) MNEM
                                                                       OP CODE MNEMONIC
                                             RMB
0028
                                OPRAND
                                            RMB
                                                                       OPERAND PLUS CR, LF, EOL
        ØØ3D
                                                                       END OF BUFFER
                               > ENDBUF
                                            EQU
                                             DRG
                                                        $(2)
                               > **** INITIALIZATION
                                                        A,B,Y,U PRESERVE REGISTERS
OUTCH-ENDBUF,S
U,S SET UP WORKSPACE ON STACK
CURADR,U SAVE ADDRESS TO DISASSEMBLE
OUTCH,U SAVE OUTPUT CHAR ROUTINE ADDRESS
        34
33E8
                66
03
34
42
                                            PSHS
LEAU
                               DISAS
യമായ
0002
0005 1F
0007 AF
                                            TFR
0009 10AF
                 C4.
                                             STY
2000C 30
0000E C6
0010 6F
0012 5A
0013 26
                                            LEAX
                                                        LENGTH, U INITIALIZE TEMPORARY VARIABLES #BUFFER-LENGTH
                 45
                                            LDB
CLR
                 20A
                                INIT1
                 20
                                            DECB
                                                        INIT1
#$20
                                            BNE
                 20
2D
0015 SE
                                            LDA
                                                                       INITIALIZE BUFFER WITH BLANKS
0015 86
0017 06
0019 A7
0018 5A
0010 AE
0020 AF
                                                        #ENDBUF-BUFFER
                                            LDE
                 80
                                INIT2
                                            STA
                                            DECB
                 FB
42
                                            BNE
                                                         INIT2
                                            L.DX
                                                        CURADR, U INITIALIZE WORKING ADDRESS
                 44
                                            STX
                                                        WRKADR, U
0022 EC
                 46
                                             INC
                                                        LENGTH, U INSTRUCTION LENGTH AT LEAST 1 BYTE
                                *** MAIN PROCEDURE
                                            LDB
CMPB
Ø024 E5
                 80
                                                                       GET FIRST BYTE OF MACHINE CODE
PAGE 1?
YES
0026 C1
0028 27
002A C1
                                                         #$10
                 10
                 24
                                            BED
                                                        MAIN1
                                            CMPB
                                                        #:4:11
                                                                       NO.
                                                                              PAGE 2?
ØØ2C 26
                                            BNE
                                                        MAIN2
                 ØE
                                                                       NO, MUST BE OP CODE
002E E7
                                            STB
INC
LDB
                                                                       SAVE PAGE
LENGTH AT
GET OPCODE
                 47
                               >MAIN1
                                                        PAGE, U
                                                        LENGTH, U
                 46
                                                                                       LEAST 2 BYTES
ØØ32 E6
                 80
```

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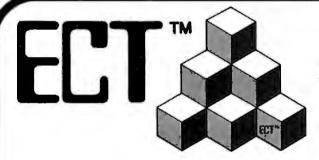
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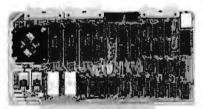
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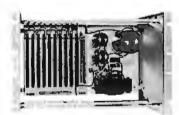
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004A 3D	Listing 1 continue	ed:				
0048 86 04	0036 E7 0038 C1 003A 24 003C C1 003E 25 0040 C4	48 80 88 40 88 87	MAIN2	STB CMPB BHS CMPB BLO ANDB	OPCD, U ##\$80 MAIN3 #\$40 MAIN4 #\$0F	SAVE OPCODE OPCODES \$80-FF? YES, CONVERT TO \$40-4F OPCODES \$40-7F? NO
MUL			× MAIN3			CONVERT TO \$40-4F
STX NXTBUF, U	004A 3D 004B 30ED 004F 30 0051 31C8 0054 C6 0056 A6 0058 A7 005A 5A	051C 8B 22 04 80 A0))))	MUL LEAX LEAY LDB LDA STA DECB	MNTAB, PC D, X MNEM, U #4 , X+ , Y+	X POINTS TO ENTRY IN TABLE Y POINTS TO SPACE IN BUFFER TRANSFER OPCODE MNEMONIC FROM TABLE
0065 81 2A			, ,			POINT TO OPERAND POSITION IN BUFFER
006B A6 48 > LDA OPCD,∪ 006D 81 C0 > CMPA #\$C0	ØØ65 81	2A	; } }	CMPA	# ⁷ :+:	ILLEGAL OPCODE?
	ØØ6D 81	CØ	, * SELE(} } } }	LDA CMPA	OPCD,U #\$CØ	OCESSING ROUTINES

Listing 1 continued on page 346

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```
Listing 1 continued:
0073 81
0075 103
0079 81
0078 24
                                       CMPA
              0182
       1024
                                       L.BHS
                                                 OP80
               40
                                       CMPA
                                                  #$40
                                       BHS
                                                  OPØØ
                                                 #$3Ø
DP3Ø
ØØ7D
       81
                                       CMPA
007F
       1024
               ØØF5
                                       LBHS
0083
0085
                                                 #$20
OP20
                                       CMPA
       1024
               00BD
                                       LBHS
0029 81
                                       CMPA
                                                  #$10
               10
008B 24
008D 20
                                       BHS
                                                 OPIØ
               ŌŌ
                                       BRA
                                                  OPØØ
                            **** OPCODES ØØ-ØF AND 4Ø-7F

** TRAP ILLEGAL OPCODES

OPØØ TST PAGE, U MUST
                           ;́* TRĂP
      5D
281
271
26
271
26
                                                 PĂĠĔŢŪ
008F
               47
                                                               MUST BE PAGE Ø
0091
0093
               Ø8
4E
                                                 OPØ1
                                       BNE
                                       CMPA
                                                  #$4E
                                                               $4E AND $5E NOT VALID
0095
               04
                                       BED
                                                  OPØ1
               5E
                                       CMPA
                                                  #$5E
0097
                                                  0902
ติดจจ
                                       BNF
               Ø284
                            OPØ1
                                       LBRA
                                                  ILEGOP
ØØ9B 16
                                                               ILLEGAL OPCODE EXIT
                            * REGISTER ADDRESSING
OPØ2 ANDA #$FØ
                                       ANDA
LDB
ØØ9E 84
               FØ
                                                 # A
#$40
ØØAØ CE
               41
       81
27
                                                               A-REG?
YES
                                       CMPA
ØØA2
               40
ØØA4
               ØE
                                       BED
                                                  OPØS
               50
07
42
25
       8i
26
                                                               B−REG?
NO
                                       ČMPA
                                                  #$50
OP04
ØØAE
SAGO
                                       ENE
                                                 #'B YES
MNEM+3,U PUT REGISTER INTO MNEMONIC
00AA CE
00AC E7C8
00AF 20
                                       LDB
                            OP03
                                       BRA
                               INDEXED ADDRESSING
04 CMPA #$E0
                            OPØ4
00B1 81
00B3 26
               60
               05
                                       BNE
                                                  OPØ5
00B5 17
                                                  INDEX
               0285
                                       LBSR
                                                               PROCESS INDEXED MODE
00BS 20
                                                  OPØ7
               ØC.
                                       BRA
                              EXTENDED ADDRESSING
PØS CMPA #$70
BNE OPØ6
00BA 81
00BC 25
00BE 17
                            OPØS
               70
                                                 ÖPØE
EXTEND
OPØ7
               05
                                       LBSR
                                                               PROCESS EXTENDED MODE
                               DIRECT ADDRESSING
0003 17
0006 16
                           ) OPØE
               03AE
                                       LBSR
                                                               PROCESS DIRECT ADDRESSING MODE
               041B
                           ) OPØ7
                                       LERA
                                                               FINISH UP
                           ) *** OPCODES
                                              10-1F
                                       ILLEGAL OPCODES
LDB PAGE, U
BEQ OP12
                           >* TRAP
0009 E6
0008 27
000D 16
               47
Ø3
Ø252
                           ) OP 10
                                                               MUST BE PAGE Ø
                                       LBRA
                                                  ILEGOP
                           ) OP11
                              PROCESS LONG BRANCHES
P12 CMPA #$16
BEQ OP13
0000 81
0002 27
0004 81
               16/
04
17
03
                           ) OP12
                                       CMPA
                                                  #$17
                                                  ÖP14
OP23
00DE 26
00DS 16
                                       BNE
               0080
                           )OP13
                                       LBRA
                                                               PROCESS LIKE 20-2F
                            * PROCESS CC INSTRUCTIONS OP14 CMPA #$1A
00DB 81
00DD 27
                                                  #$1A
OP15
               Ø9
                                       BEQ
                                                  #$1C
OP17
ØØDF
       81
                                       CMPA
00E1
        ŽĒ.
               10
                                       ENE
               43
26
23
03F0
00E3
00E5
00E8
                                       LDA
       38
                                                               FIX 'ANDCC'
       A7C8
86
                                                  MNĒM+4, U
                             OP15
                                       LDA
                                                  #1#
                                                  PUTCH
                                                               PRINT AS IMMEDIATE MODE
PROCESS LIKE DIRECT ADDRESSING
00EA
                                       LBSR
ØØED
                                       LBSR
                                                  DIRECT
Ø0FØ
               Ø3F1
                             OP16
                                       LBRA
                                                  FINISH
                             * PROCESS REGISTER TRANSFER INSTRUCTIONS
00F3 81
00F5 25
00F7 6C
00F9 E€D8
               1 E
F 9
                                       CMPA
                             OP17
                                                  #$1E
OP16
                                       BLO
                                                               PROCESS REMAINING 1-BYTE INSTRUCTIONS
                                                  LENGTH, U
                4E
                                        INC
               04
                                       LDB
                                                  +WRKADR, U+ GET POST BYTE
                                                  BYTE 1, U
#$88
ØØFC
                4A
ØØFE.
               88
                                        ANDE
                                                               CHECK BOTH REGISTERS SAME SIZE
```

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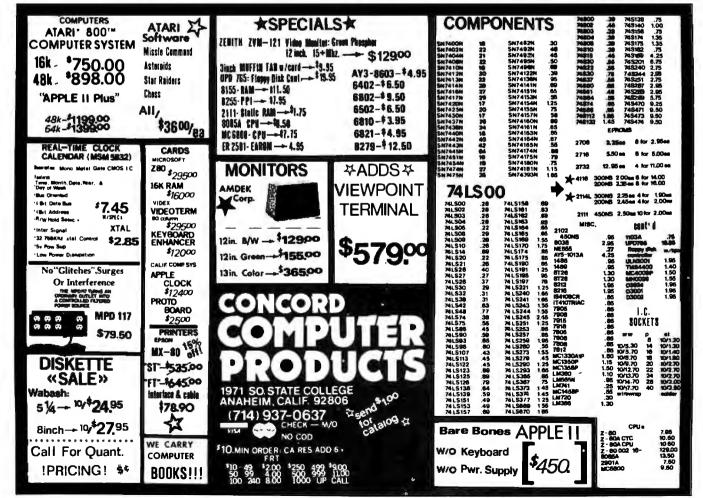
by Leslie Nelson, February 1982

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System N	lotes
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Listing 1 continued:			
0100 27 04 0102 C1 88 0104 26 C7	BEQ CMPB BNE	OP18 #\$88 OP11 ILLEGAL OPCODE IF NOT S	BAME
0106 E5 4A 0108 54 0109 54 0109 54 010A 54 010B 80 13 010E 81 2A 0110 27 BB 0112 85 0114 17 03 C6 0114 17 E6 0117 E8 0118 81 2A 011D 27 AE	DBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBB	REG GET SOURCE REGISTER CHECK FOR INVALID REGISTER PUT COMMA IN BUFFER PUTCH BYTE1, UREG GET DESTINATION REGISTER CHECK FOR INVALID REGISTER CHECK FOR CHECK FOR INVALID REGISTER CHECK FOR CHEC	ER
0136 26 04 0138 86 43 013A 20 06 013C C1 08 013C 26 05 0140 86 50	REG ANDB LEAR REG ANDB LEAR REG BNE AND	#\$0F MASK OFF HIGH NIBBLE REGTAB,PC B,X GET REGISTER NAME FROM PUTCH #\$05 REG1 #'C FIX 'PC' REG3 #\$0A REG2 #'C FIX 'CC' REG62 #'C FIX 'CC' REG64 #'P FIX 'DP' PUTCH	TABLE
0146 E6 47 0149 C1 11 0149 C27 08 014C 81 20 014E 2E 07 0150 C1 00 0152 27 03 0154 16 01CB	<pre>/**** OPCODES ; /* TRAP ILLEG /OP20 LDB / CMPB / BEQ / CMPA / BNE / CMPB / BEQ / CMP21 LBRA // CMP21 LBRA // CMPA // CMPB // CMPB // CMPB // CMPA // CMPB // CMPA // CMPB // CMPB</pre>	PAGE, U #\$11 MUST BE PAGE Ø DR 1 GP21 #\$2Ø 'BRA' MUST BE PAGE Ø OP22 #\$ØØ OP22 ILEGOP	
0157 C1 10 0159 26 18 0158 C6 03 015D 30C8 24 0160 A6 80 0162 A7 84 0164 30 18 0165 5A 0169 86 4C	>* PROCESS LOI > OP22 CMPB > BNE > OP23 LDB > LEAX > OP24 LDA > STA > LEAX > DECB > ENE > LEAX	#\$10 LONG BRANCHES ON PAGE : OP26 #3 CHANGE MNEMONIC TO LONG MNEM+2,U ,X+ X -2,X	



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Listing 1 continued:
                                          STA
LESR
                01
0342
0371
                                                     1,X
REL1E
Ø16B A7
Ø16D 17
                                                                    PROCESS RELATIVE ADDRESS
                              DP25
                                                     FINISH
                                          LBRA
                              * PROCESS SHORT BRANCHES OP26 LBSR RELB
Ø173 17
Ø176 2Ø
                Ø31E
                F8
                                          BRA
                                                      OP25
                              *** OPCODES 30-3F

* TRAP ILLEGAL OPCODES

OP30 LDB PAGE, U

CMPA #$3F

BEO OP301
Ø178 E6
Ø17A S1
Ø17C 27
Ø17E C1
Ø19Ø 27
                47
3F
67
                                                                    MUST BE PAGE Ø EXCEPT 'SWI'
                                                     #Ø
OP3
                00
                                          CMPB
                ØB
                                          BED
                                                      ILEGOP
                Ø19D
                                          LBRA
0182
                                 PROCESS 'LEA' INSTRUCTIONS
0185 81
0187 22
0189 17
018C 16
                33
Ø6
                                          CMPA
BHI
LBSR
                                                     #$33
DP34
                              OP32
                                                      INDEX
                Ø1B1
                                                                    CAN ONLY BE INDEXED MODE
                               OP33
                                                     FINISH
                                          LBRA
                              * PROCESS STACK INSTRUCTIONS
OP34 CMPA #$3C CHECK
BEQ OP302
CMPA #$37
BHI OP33 PROCE
                300
507
554
46
Ø18F
Ø18F 81
Ø191 2:7
Ø193 81
                                                                    CHÉCK FOR 'CWAI'
0193 81
0195 22
0197 60
0199 A6D8
0190 A7
0196 A7
                                          BHI
                                                                    PROCESS REMAINING 1-BYTE INSTRUCTIONS
                                                      LENGTH, U
                                                      <u> TWRKADR,U÷ GET POSTBYTE</u>
                04
                                          LDA
                                                      BYTE1, U
BYTE2, U
                4A
                                           STA
                                                                    TEMPORARY STORAGE
                48
                                          CLRB
LSL
BCC
Ø1AØ 5F
01A1 68
01A3 24
01A5 30ED
                               DP35
                                                      BYTE2,U
DP300
                4B
                                                                    SHIFT BIT
                                                                                    INTO CARRY
                                                                    NO REGISTER IF BIT NOT SET
                ØJBA
                                                      STRTAB, PC
                                          LEAX
01A9 A6
                                          LDA
                                                                    GET REGISTER FROM TABLE
                53
Ø7
25
Ø2
                                                                    DECIDE ON 'U' OR 'S' FOR STACK
Ø1AB 81
                                           CMPA
Ø1AD 26
Ø1AF A1C8
Ø1B2 26
Ø1B4 86
                                                      อัยรัย
                                          BNE
CMPA
                                                      MNEM+3,U COMPARE TO LAST CHARACTER
OP36 OF MNEMONIC
                                          BNE
                                                      #' U
                                                                    REPLACE REGISTER CHARACTER
                                          LDA
Ø186 17
Ø189 81
                0324
                                                      PUTCH
#1P
                               OP36
                                                                    FIX 'PC' AND 'CC'
                50
                                           CMPA
                                                      DP37
#'C
DP38
#'C
        27
                04
Ø18B
                                          BED
ØIBD 81
                43
Ø7
                                           CMPA
        26
86
17
20
Ø1BF
                                           BNE
Ø101
Ø103
Ø108
                               DP37
                                           LDA
                                                      PUTCH
OP39
                                           LBSR
                 Ø317
                 ØB
                                           BRA
                44
07
                                                                    FIX 'DP'
Ø108 S1
                               DP38
                                           CMPA
                                                      #7 ₽
#7 ₽
        26
86
17
20
Ø1CA
Ø1CC
Ø1CE
                                          BNE
                50
                                          LDA
                Ø3Ø0
                                          LESR
                                                      PUTCH
Ø1D1
                                                      DP39
                 ØØ
                                           BRA
Ø1D3 86
Ø1D5 17
                 20
                               OP39
                                           LDA
                                                                    PUT COMMA IN BUFFER
                0305
                                          LBSR
                                                      PUTCH
        50
01
26
                                           INCH
Ø1D8
                               0P300
Ø1D9
                Ø٤
                                           CMPB
                                                      #8
                C4
                                                      ÖF35
ИIDB
                                           BNE
        ABO A O
                4E
1F
4E
                                          LDX
LEAX
STX
                                                      NXTBUF, U REMOVE LAST COMMA FROM BUFFER
Ø1DD
Ø1DF
                                                      -1, X
ØÎĒ1
ØIE3
                                                      NŽTŘUF, U
OP33
                                           BRA
                 A7
                               * PROCESS 'SWI'
OP301 CMPR ±
01E5 C1
01E7 27
01E9 CB
01EB E7C8
01EE 20
                ØØ
A3
                                           CMPB
                                                      #Ø
DP33
#$21
                                                                    DONE IF PAGE Ø ADD $21 TO CON
                                           BED
                                                                       D $21 TO CONVERT PAGE INTO
ASCII CHARACTER
                21
25
90
                                          ADDB
STB
                                                      MNEM+3,U
OP33
                                           BRA
                                 PROCESS 'CWAI'
Ø1FØ 86
Ø1F2 17
Ø1F5 17
Ø1F8 16
                                          LDA
LBSR
                                                      #'#
PUTCH
DIRECT
                23
0268
0270
                              > DP30/2
                                                                    PRINT AS IMMEDIATE MODE
                                           BSR
                                                                    PROCESS LIKE DIRECT
                 02E9
                                           LBRA
                                                      FINISH
```



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750.00 636.00 2050.00 1889.00 325.00 256.00 215.00 171.00 385.00 244.00 95.00 44.95 125.00 189.95 26.95 | To 7225 | To 750.00 | 636.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 | 750.00 for 7225

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351

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Listing 1 continued:
                               /**** OPCODES 80-BF

>* PROCESS 'BSR' A

>OP80 LDB PAC

CMPA #$8
                                                         r AS SPECIAL CASE
PAGE, U
Ø1FB E6
Ø1FD 81
Ø1FF 26
                 47
3D
                                                          #$8D
                  11
                                              BNE
                                                          OPS1
01FF 25
0201 C1
0203 1026
0207 86
0209 A7C8
020C 17
020F 16
                  00
                                             CMPB
                                                          #$00
                                                                         MUST BE ON PAGE Ø
                 Ø11B
                                              LBNE
                                                          ILEGOP
                 42
22
Ø285
                                             LDA
                                                                         CHANGE 'JSR' TO 'BSR'
                                                          MNEM, U
                                              LBSR
                                                          RELS
                                                                         PROCESS LIKE SHORT BRANCH
                 Ø2D2
                                             LBRA
                                                          FINISH
                                    GET MNEMONIC AS REQUIRED BY PAGE
P81 ANDA #$BF
                                ) A:
Ø212 84
Ø214 81
                 8F
                                ) DP81
                                             ANDA
CMPA
BNE
                 83
20
00
                                                         #$83
OP83
                                                                         FIX SUBDICMPD/CMPU
        26
C1
27
86
2216
Ø218
                                             CMPB
0218 C1
021A 27
021C 86
021C 86
0221 86
0223 A7C8
0226 86
0228 A7C8
0228 86
0228 27
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                                                          #$00
                 4C
43
22
                                             BEQ
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                                             STA
                                                         MNĒM, U
                 4D
23
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                                                         MNEM+1,U
                                             LDA
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        C1
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                                             CMPB
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0231 86
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                                                         MNĒM+3,U
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Ø238
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Ø23C
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        81
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                                                         #$8C
                                                                         FIX CMPX/CMPY/CMPS
                 11 ·
00
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59
                                                         ÖPSØØ
#1' Y
                                             BEQ
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                 #$10
                                             CMPB
0244 27
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0248 A7C8
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                                             BEQ
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Ø24B 2Ø
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        81
25
C1
                 8E
                                > OP85
                                             CMPA
                                                          ##$BE
                                                                         FIX LDX/LDY AND STX/STY
                 11
                                                         ÖP86
                                             BLD
                                             CMPB
                                                          排$11
                                                                         CANNOT BE PAGE 2
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86
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                                                          MNEM+2, U
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Ø262 C:
                                 3890
                                             CMPB
                                                          #$1200
                                                                         ALL REMAINING OPCODES MUST BE
Ø264 1Ø26 ØØBA
                                             LBNE
                                                          ILEGOP
                                                                             ON PAGE Ø
                               ) *** JOINTLY PROCESS 80-BF AND CO-FF
) * TRAP ILLEGAL OPCODES
) OP800 LDA OPCD, U
Ø268 A6
Ø26A 94
                 48
BF
87
Ø26A
                                             ANDA
                                                          #$BF
        9292929
171715
                                                         ##87
OP801
25C
                                                                         STORE OPCODES NOT ALLOWED IN
加26日
加27回
                                             BEQ
CMPA
                 Ø8
                                                                             IMMEDIATE MODE
                 SD
                                                          #$8D
Ø272
Ø274
                 Ø4
8F
                                                          OP801
                                             BED
                                                         #$8F
0P802
                                             CMPA
Ø276
Ø278
                  ŏ٦.
        25
16
                                             RNF
                                             LBRA
                 เขี้ยัค7
                                ) OPSØ1
                                                          ILEGOP
                                   PROCESS EXTENDED ADDRESSING
P802 LDA OPCD, U
ANDA #$30
                                             LDA
ANDA
CMPA
BNE
0278 A6
027D 84
027F 81
0281 26
0283 17
                 48
30
30
                                 OP802
                                                         #$30
0P803
EXTEND
                 ЙE
        26
17
16
                 ØĭFC
                                             LBSR
Ø286
                  Ø25B
                                             LBRA
                                                         FINISH
                                                  INDEXED ADDRESSING
PA #$20
E OP804
                                    PROCESS
                                N. 244
0289 81
028B 26
028D 17
0290 16
                                             CMPA
BNE
LBSR
                 20
06
                                )OP803
                 พีพีAD
                                                          INDEX
```

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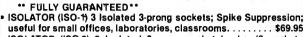
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The powerful screen-oriented Editor features finds, changes, moves, copys and much more. All keys have convenient auto repeat (typamatic), and since no line numbers are required, the full width of the screen may be used to generate wel commented code.

The Assembler features all of the following: complete 6809 instruction scomplete 6800 set supported for cross-assembly; conditional assembly; labels; assembly to cassette tape or to memory; listing to screen or printer; a mnemonic error codes instead of numbers.

The versatile ABUG monitor is a compact version of CBUG, tailored for debugging programs generated by the Assembler and Editor. It features examine/change of memory or registers, cassette load and save, breakpoints and more. SpSEOC Price: \$89.95

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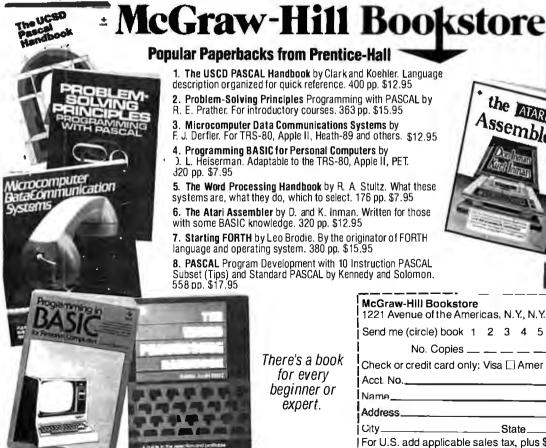
353

Listing 1 continued:

Ø295 1 Ø297 :	31 26 17 16	10 06 01DA 0247	>** PROCE >OP8Ø4 > >	ESS DIRE CMPA BNE LBSR LBRA	ECT ADDRES #\$10 OP805 DIRECT FINISH	SSING
029F 029A 029A 029A 029A 029A 029A 029A 029A	84 81 27 81 24 17 16	23 Ø23B 48 883 Ø6 Ø103 Ø103 Ø103 Ø102A	/* PROCI > OP805 > > > > > > > > > > > > >	ESSAR SDASAR LLBDASAAA LLBDADAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA	EDIATE ADI #,# PUTCD, PUTCD, ##883 OP886 #P806 #P806 FINTEND FINTEND FINTEND	DRESSING OPCODES 83 AND 8C-8F HAVE 2-BYTE OPERANDS PROCESS 1-BYTE OPERAND LIKE DIRECT PROCESS 2-BYTE OPERAND LIKE EXTENDED
02BC 02BE 02C0 02C2 02C4 02C6 02C6 02C6 02C0 02C0 02C0 02C0 02C0	E844 8812 8867 887 887 887 887 887 887 887 887 88	7FB980412484 JD		CM SMARTH ARCHARA DE DANNING AA A DE DANNING AA A DE DANNING AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA		TRAP ILLEGAL OPCODES CHANGE 'A' TO 'B' IN MNEMONICS FIX 'ADDD'

Listing 1 continued on page 356

:ORTH



5. The Word Processing Handbook by R. A. Stultz. What these systems are, what they do, which to select. 176 pp. \$7.95 6. The Atari Assembler by D. and K. Inman. Written for those with some BASIC knowledge. 320 pp. \$12.95 $\,$

8. PASCAL Program Development with 10 Instruction PASCAL Subset (Tips) and Standard PASCAL by Kennedy and Solomon.

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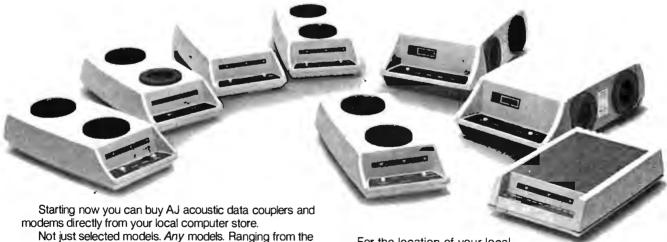
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System No	otes				
Listing 1 continue Ø2D8 3ØC8 Ø2DB AE	,	OPCØA	LEAX LDA_	MNEM+2,U	
Ø2DD 81 Ø2DF 27 Ø2E1 3Ø Ø2E3 6C	41 Ø2 Ø1 84	OPC1	CMPA BEQ LEAX INC	#'A OPC1 1,X X	CHANGE 'A' TO 'B' IN MNEMONIC
02E5 C1 02E7 27 02E9 20	00 36 37	OPC2	CMPB BEQ BRA	#\$ØØ OPC8 ILEGOP	MUST BE PAGE 0
02EB 81 02ED 22 02EF 26 02F1 86 02F3 87C8 02F6 86 02F8 87C8 02FB 86 02FD 86 0300 86 0302 87C8	21 16 53 22 54	OPC3	CMPA BHI BNE LDTA LSTA LSTA LSTA LSTA LSTA LSTA	#\$CD OPC6 OPC5 #'S MNEM, U #'T MNEM+1, U #'D MNEM+2, U #\$20 MNEM+3, U	FIX 'STD'
0305 20 0307 86 0309 A708 0300 86 030E 20	DE 4C 22 44 E8))) OPC5))	BRA LDA STA LDA BRA	#'L MNEM,U #'D OPC4	CHECK FOR PAGE Ø
0310 C1 0312 27 0314 86 0316 C1 0318 27 0318 27 031A 86 031C A7C8	11 ØE 55 ØØ Ø2 53 24)) OPC6)))))) OPC7) OPC8	CMPB BEQ LDA CMPB BEQ LDA STA LBRA	#\$11 ILEGOP #'U #\$00 OPC7 #'S MNEM+2,U OP800	PAGE 2 NOT ALLOWED FOR CE-CF FIX LDU/LDS AND STU/STS PROCESS LIKE 80-BF
0322 30ED 0326 31C8 0329 C6 0328 A6 0327 57 0327 57 03330 26 03330 26 03334 AF 03334 86 03338 A7 03338 16	0249 2249 224 80 80 80 81 41 41 46) >*** ILI > ILEGOP > ILOP1 > OF ILOP1 > OF ILOP1 > OF ILOP1 > OF ILOP1	LEGAX LEAY LDB LDCA STCCB BNE LEAX LDTA LDTA LDTA LDTA LDTA LDTA LDTA LDTA	PCODE ROUT MNILEG, PO MNEM, U #4 , X+ , Y+ ILOP1 1, Y NXTBUF, U #1 LENGTH, U FINISH	STORE '***' IN OPCODE MNEMONIC POINT TO NEXT AVAILABLE POSITION IN BUFFER AFTER OPCODE MNEMONIC SET INSTRUCTION LENGTH TO 1
033D 6C 033F AE 0341 E6 0343 AF 0345 E7 0347 E7	46 44 80 44 4D	> >*** PR > INDEX > > > >	OCESS I INC LDX LDB STX STB STB	LENGTH, U	DRESSING MODE BUMP WORKING ADDRESS POINTER AND GET POSTBYTE
0349 C4 0348 C1 034D 26 034F 63 0351 86 0353 17	90 90 07 4C 5B 0187	/ >* CHECI > > > > > >	K FOR I ANDB CMPB ENE COM LDA LBSR	INDIRECT AI #\$90 #\$90 IND1 INDFLG,U #'*	DDRESSING BITS 4 AND 7 SET? NO, NOT INDIRECT YES, SET FLAG OUTPUT '+'
0356 E6 0358 C4 035A C1 035C 25 035E C1	4D 8F 80)* AUTO > IND1 > > > >	INCREM LDB ANDB CMPB BLO CMPB	1ENT/DECRENT NDBYT, U #\$8F #\$80 IND5 #\$83	MENT ADDRESSING MASK OFF REGISTER AND INDIRECT BITS AUTO INC/DEC? NO

```
Listing 1 continued:
               00DS
                                        LBSR
                                                   GETREG
                                                                 PUT REGISTER INTO BUFFER
0376 86
                                        L.DA
       17
C1
26
Ø378
Ø37B
                                        LBSR
                                                   PUTCH
               Ø152
                                        CMPB
BNE
               81
                                                   #$81
                                                                  INCREMENT BY 2?
Ø37D
Ø37F
               ิ่งรี
015B
                                                    IND2
                                        LBSR
                                                    PUTCH
0382
       ΪĖ
                                        LBRA
               00E6
                            >IND2
                                                    INDEND
       86
17
12
17
17
0385
0387
038A
038C
038E
0391
                                        LDA
LBSR
CMPB
BNE
               2D
                             INDS
                                                                 AUTO DEC
                                                   ̈́UTCH
#$83
               0153
               83
03
                                                                 DECREMENT BY 2?
                                                    IND4
                                        LBSR
                                                   PUTCH
GETREG
               Ø14C
               ØØBA
                             IND4
                                                                 PUT REGISTER INTO BUFFER
0394
       16
               00D4
                                         LBRA
                                                    INDEND
                                ACCUMULATOR OFFSET
                            ) *
Ø397
Ø399
Ø39B
Ø39D
                                        LDA
CMPB
       86
                             IND5
                                                    #' A
       C1
27
86
               SĒ
                                                    #$8E
               0C
425
485
QE
                                         BEO
                                                    INDE
                                                    #' B
                                         LDA
039F
03A1
03A3
                                         CMPB
                                                    #$85
                                         BEO
                                                    INDE
       86
                44
                                         LDA
Ø3A5
       Ĉ1
                                         CMPB
                                                    #$8B
               SB
Ø3A7
                                         BNE
                                                    IND7
               ØE
               0131
20
0120
009A
                                        LBSR
LBSR
LBSR
LBSR
Ø3A9
Ø3AC
Ø3AE
       17
86
17
                                                   PUTCH
#',
                              INDE
                                                                 DUTPUT OFFSET REGISTER
                                                    #',
PUTCH
                                                                  OUTPUT INDEX REGISTER
                                                    GETREG
Ø3B1
                                                    INDEND
Ø384
                                         LRRA
                00B4
                            >* CONSTANT DFFSET FROM PC
> IND7 CMPB #$8D
Ø387
Ø389
        C1
27
                as
                24
24
                                         BEQ
                                                    IND8
        Ĉi
26
Ø3BB
                                         CMPB
                                                    #$8C
03BB
03BB
03BF
03C1
03C3
                                                    INDIØ
INDBYT,U GET POSTBYTE
                                         BNE
        AE
AZ
ØC
                4D
                              INDE
                                         LDA
                                         STA
                49
                                                    POSTB, U
                                                                  ACCOUNT FOR IT
                                         INC
                                                    LENGTH
                ØE
03C5
03C7
03C9
03CC
03CE
                                                    DS##
EDNI
        C1
27
                SD
                                         CMPB
                15
                                         BEQ
                                                                  PROCESS 8-BIT OFFSET OUTPUT ', PC'
                                                    RELS
                00C8
                                         LBSR
        17
                                                    #7
                                         LDA
                2C
Ø1ØC
                              INDSA
        86
                                         LBSR
                                                    PUTCH
        17
 03D1
03D3
03D6
03D6
        86
17
86
17
                50
                                                    #UTCH
                0107
                                         LBSR
                43
Ø1Ø2
                                         LDA
LBSR
                                                    PUTCH
                                                    INDEND
        16
                                         LBRA
 Ø3DB
                ØØ8D
                                                    REL16
                                                                   PROCESS 16-BIT OFFSET
 03DE 17
03E1 20
                                         LBSR
                             > IND9
                ØØD1
                                                     INDSA
                                         BRA
                             >* CONSTANT OFFSET (ZERO)
03E3 C1
03E5 26
03E7 4F
03E8 17
03EB 86
03ED 17
03F0 8D
03F2 20
                                         CMPB
                                                    #$84
IND12
                84
                             >IND10
                                          BNE
                ØD
                                         CLRA
                                                     PUTCH
                ÖÖDB
                              IND11
                                                                   7 , R7
                ŽČ
                                          LDA
                00ED
50
77
                                         LBSR
                                         BSR
                                                     GETREG
                                                     INDEND
                             >* 5-BIT OFFSET
 03F4 C5
03F6 26
03F8 6D
03FA 26
03FC E6
03FC C4
                                         BITB
                                                     #$80
                                                                   5-BIT OFFSET IF BIT 7=0
                80
                             >IND12
                                                     IND13
                18
4C
4F
                                          BNE
                                                     INDFLG, U INDIRECT ADDRESSING NOT ALLOWED INDIS
                                          TST
                                          BNE
                                                     ĪNDĒŸT, U
                                          LDB
                4D
                                                                   GET OFFSET BITS
TEST SIGN BIT
                                                     #$1F
                1F
10
                                          ANDB
                                                     #$10
 0400
                                          BITB
                                                                   POSITIVE
                                          BEQ
                                                     IND12A
 0402
                ØS
        $6
17
                 2D
 0404
                                                     PUTCH
                                          LBSR
 040E
                 00D4
                                                                   SET HIGH ORDER BITS
CONVERT TO POSITIVE NUMBER
 Ø4Ø9
Ø4ØB
Ø4ØC
        CA 0 1 2 0
                                          ORB
                                                     #$EØ
                ΕØ
                                          NEGR
                                                     B,A
IND11
                                          TFR
                 98
                               IND12A
                 DΞ
```

Listing 1 continued on page 358

```
Listing 1 continued:
                            >* 8-BIT OFFSET
                                        LDA
                                                    INDBYT, U GET POSTBYTE
Ø41Ø A6
Ø412 A7
Ø414 C1
Ø416 26
                            > INDIS
               4D
                                                   POSTB, U
#$88
                                        STA
               49
                                        ERMO
               88
                                        BNE
                                                    1ND15
                                         INC
                                                   LENGTH, U
                45
Ø418 EC
                                                   +WRKADR, U← GET DFFSET BYTE
BYTE1, U
041A E6D8
041D E7
041F 2A
               04
                                        I'''DB
                                        STE
               4A
Ø6
                                                   ĪŅD14
                                                                 TEST SIGN OF OFFSET
Ø421
Ø423
       26
                ŽĎ
                                        L_DA
                                                    РОТСН
               ØØB7
                                        LESR
                                                                  CONVERT TO POSITIVE NUMBER
0426 50
0427 1F
                                         NEGB
                              IND14
                                         TFR
                98
0429 20
                ED
                                         BRA
                                                    IND11
                            >x 16-BIT OFFSET
>IND15 CMPB #
042B C1
042D 26
042F 6C
0431 6C
0433 ECD8
0436 ED
0438 17
043B 17
                                                    #$89
                89
                100
                                         BNE
                                                    INDIE
                                                    LENGTH, U
                4Ē
                                         INC
                04
                                         ĹDD
                                                    *WRKADR, U+
                ÃA
                                         STD
                                                    BYTE1, U
                008B
                                         LBSR
                                                    PUT2H
                                                    B, A
IND11
                98
                                         TFR
       20
                                         BRA
Ø43D
                A3
                            > ★ EXTENDED INDIRECT
                                                    INDBYT, U
                4D
9F
                            > IND16
                                         LDA
CMPA
Ø43F A6
                                                    #$9F
Ø441 81
                                                    ĬŇĎ18
POSTB, U
Ø443 26
Ø445 A7
Ø447 8D
                ØE
                                         BNE
                49
                                         STA
                39
20
                                                                  PROCESS LIKE ENTENDED
                            ٠,
                                         RSR
                                                    EXTEND
                                                    INDEEND
0449 20
                                         BRA

→ TRAP ILLEGAL

                                                     INDEX MODES
                            > IND18 LBRA
                                                    ILECOP
Ø44B 16
                FED4
                                       INDEX
PSHS
                                                 REGISTER
Ø44E 34
Ø45Ø E6
Ø452 86
Ø454 C4
                              GETREG
                MA
                                                    B
                                         LDB
                                                    INDBYT, U GET POSTBYTE
                4D
                                                    #'X
#$EØ
                58
                                         LDA
        C4
27
                ĒØ
                                         ANDB
Ø45E
Ø458
                                         BEQ
                                                    ĞĔTŔ1
#'Y
                ØE
       ēЕ
                59
20
Ø45A C1
Ø45C 27
Ø45E 86
                                                    #$20
GETR1
                                         CMPE
                ØΞ
                                         BEQ
                55
                                         LDA
                                                    #1 U
0450 C1
0462 27
0464 86
                                         CMPB
BEQ
LDA
                                                    #$40
                023
75
04
                                                    GÉTŘ1
#'S
                                         ESR
PULS
                                                    PUTCH
0466 8D
0468 35
046A 39
                              GETR1
                                                                  OUTPUT REGISTER
                                         RTS
                                                INDEXED PROCESSING
INDELG,U INDIRECT MODE?
INDEN1 NO
#'*
                              * FINISH UP
INDEND TST
BEQ
046B 6D
046D 27
046F 86
                404
50
                                         LDA
                                                    PUTCH
 Ø471 8D
                                         BSR
                              INDEN1
 Ø473
        39
                                         RTS
                             >*** PROCESS DIRECT ADDRESSING MODE
Ø474 EC
Ø476 86
                46
24
63
                                                    LENGTH, U
#$24
PUTCH
                              DIRECT INC
                                         LDA
                                                                  PUT '$' IN BUFFER
 Ø478 8D
Ø47A AED8
Ø47D A7
Ø47F ED
Ø481 39
                Ø4
                                         LDA
                                                    +WRKADR, U← OUTPUT 1-BYTE ADDRESS
                4A
45
                                                    BYTE1, U
                                         STA
                                         RSR
                                         RTS
                              **** PROCESS EXTENDED ADDRESSING MODE EXTEND ESR DIRECT OUTPUT FIRST BYTE INC LENGTH, U
                                        BSR
INC
INC
Ø482 SD
                FØ
                45
45
2
Ø484 6C
        БČ
 Ø486
                                                    WRKADR+1, U
Ø488 26
Ø48A 60
                                         BNE
                                                    EXT1
                44
                                                    WRKADR, U
       8ďãŘ
                                                    +WRKADR, U+
BYTE2, U
PUT2H O
Ø48C
                04
                              EXT1
                                         LDA
                4B
33
Ø48F
        A7
                                         STA
0491
                                         BSR
        SD
                                                                  OUTPUT 2ND BYTE
0493
        39
                                         ŔŤŚ
```

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Listing I continued:

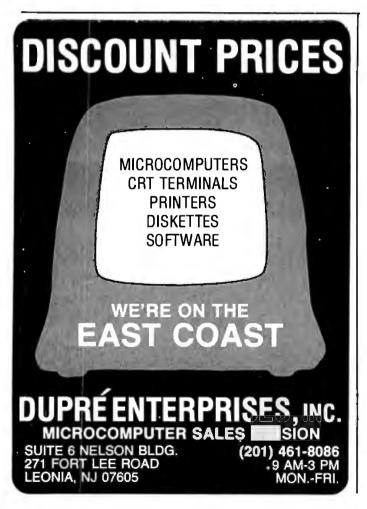
0494 EC 4E 0496 86 28 0498 8D 43 049A AED8 04 049A AFD8 A7 049A 1D	6	PROCESS R INC LDA BSR LDA TFR STA SEX	ELATIVE ADDRESSING MODES LENGTH, U #'(PUT '('INTO BUFFER PUTCH *WRKADR, U+ OUTPUT 1-BYTE OFFSET A, B BYTE1, U
	D	ADDD	#1 WRKADR,U PUT2H OUTPUT RELATIVE ADDRESS B,A PUT2H #') PUTCH
0482 6C 46 0484 6C 46 0486 86 28 0488 8D 23 0488 ECDS 04 0486 E7 46 04C1 C3 00 04C4 20 DF	5	INC INC LDA ESR LDTA STB ADDD ERA	LENGTH, U LENGTH, U LENGTH, U #'(PUT '(' INTO BUFFER PUTCH +WRKADR, U+ OUTPUT 2-BYTE OFFSET BYTE1, U BYTE2, U #2 REL8A
04C6 34 02 04C8 8D 05 04CA 35 02 04CC 8D 05 04CE 39)* PU 2		OUTINES CHARACTERS FROM A REG INTO BUFFER A PUT2HL A PUT2HR
Ø4CF 44 Ø4DØ 44 Ø4D1 44 Ø4D2 44) PUT2))	HL LSRA LSRA LSRA LSRA	SHIFT LEFT NIBBLE INTO RIGHT

Listing 1 continued on page 360

```
Listing 1 continued:
                                                                     CONVERT NIBBLE INTO ASCII
                                                      #$F
Ø4D3 84
                             > PUT2HR ANDA
                ØF
                                                      #'0
04D5 8B
04D7 81
04D9 23
                30
39
                                           ADDA
                                           CMPA
BLS
                                                       PUTCH
                                                                     DUTPUT NIBBLE
                02
                07
                                           ADDA
ØADB BB
                                         ASCII CHARACTER INTO BUFFER AND BUMP BUFFER POINTER
Ø4DD AE
Ø4DF A7
Ø4E1 AF
Ø4E3 39
                                           LDX
                               PUTCH
                                                      NX TBUF, U
                4E
                80
Ø4E1
Ø4E3
                                                       NXTBUF, U
                                           STX
                                           RTS
                              )*** END OF JOB ROUTINE
)* TERMINATE BUFFER WITH CR-LF
                ØD
F5
                              >FINISH LDA
                                                       #$ØD
                                                                     CR
Ø4E4 86
Ø4E6 8D
                                           BSR
                                                       PŮŤČH
                                                                     LF
Ø4E8 85
Ø4EA 8D
Ø4EC 85
                ØA
                                           LDA
                                                       #$ØA
                                                       PUTCH
#$15
                F1
15
                                           BSR
                                           LDA
                                                                     EOL
                                                       PUTCH
                 ĒĎ
                                           BSR
Ø4EE BD
                                                      ADDRESS AND OPCODE BYTES INTO BUFFER
                               * PUT CURRENT
04F0 30C8
04F3 AF
04F5 AB
04F7 BD
04F9 AB
04FB BD
                                           LEAX
STX
                                                       BUFFER, U
                10
                                                       NXTBUF, U
CURADR, U GET MSB OF ADDRESS
                4E
42
CD
                                           ĽĎÂ
                                           BSR
                                                       PUT2H
                402DC
                                                       CURADR+1, U LSB
                                           LDA
                                                       PUT2H
#$20
                                           BSR
Ø4FD
        86
                                           LDA
                                                                     BLANK
                                                       PUTCH
                                           RSR
Ø5Ø1
Ø5Ø3
                 46
02
47
                                                       LENGTH, U PRESERVE INSTRUCTION LENGTH
        AE
34
                                           LDA
                                           PSHS
LDA
0505 AE
0507 27
                                                       PAGE, U
                                                                     OUTPUT PAGE BYTE IF APPLICABLE
                                           BEQ
BSR
DEC
LEAX
                                                       EOJ1
PUT2H
                 04
 2509 SD
                 ΒB
0508 EA
050D 30C8
                 4E
17
                                                       LENGIA, D
BUFFER+7, U
NXTBUF, U POINT TO OPCODE
OPCO, U OUTPUT OPCODE
                                                       LENGTH, U
050B 6A
050D 3AF
0510 AB
0510 AB
0514 8D
0514 BA
0518 AB
0518 AB
051C 8D
051E 6A
                                EOJ1
                 4È
                                            STX
                 48
                                           LDA
                                            BSR
                 ΒØ
                                                       LENGTH, U
                 4E
                                            DEC
                 49
                                                       POSTB, U
                                                                     OUTPUT OPCODE POSTBYTE IF APPLICABLE
                                            LDA
                                                       EOJ2
PUT2H
                                           BEQ
                 ÃΘ
                                            BSR
                                                       LENGTH, U
                                           DEC
                                * OUTPUT OPERAND BYTES
EOJ2 LEAX HEXB, U
STX NXTBUF, U
TST LENGTH, U
                 10E 60
 Ø52Ø
         3008
                                                                      POINT TO OPERAND FIELD
0520 300
0523 AF
0525 6D
0527 AF
0529 8D
0528 8D
052D 6A
052F 27
0531 AF
0533 8D
                                                       LENGTH, U
EDJ4
BYTE1, U
PUT2H
                                            BED
                 4A
99
                                                                     OUTPUT MSB OF OPERAND
                                            LDA
                                            BSR
                 4Ē
                                                       LENGTH, U
                                            DEC
                 04
                                            BED
                                                       EDJ4
                                                       BYTE2,U
                                                                     OUTPUT LSB
                 4B
                                            LDA
                 91
                                            BSR
                                                       PUT2H
                              $* OUTPUT ENTIRE BUFFER TO CONSOLE
>EOJ4 LEAX BUFFER, U POINT TO START OF BUFFER
20535
20538
20538
20538
20538
        3008
A6
34
AD
35
A35
A35
                                           LEAX
                 10
                 8Ø
52
                              > EOJ5
                                            LDA
                                                       , X+
                                            PSHS
                                                       A, X, U
                                                                      SAVE REGISTERS
                 D4
                                            JSR
                                                       *·OUTCH, U←
                                                                        OUTPUT CHARACTER
                 52
15
                                            PULS
                                                       A, X, U
 Ø54Ø
                                            CMPA
                                                       #$15
                                                                      EOL?
                                            BNE
                                                       EÒJS
                                  SET UP FOR NEXT LINE OF DISASSEMBLY
PULS B GET INSTRUCTION LENGTH
SEX
 Ø544 35
Ø546 1D
Ø547 E3
                 04
                 42
42
                                            ADDD
                                                       CURADR, U CALCULATE START OF NEXT INSTRUCTION
                                                       CURADR, U
                                                       CURADR, U
ENDBUF-DUTCH, U RESTORE STACK
A, B, Y, U RESTORE REGISTERS
DETURN TO CAL
0548 AE
0540 3208
0550 35
0552 39
                                            LDX
LEAS
PULS
                                                                               RETURN TO CALLING ROUTINE
                                       TRANSFER INSTRUCTION REGISTER TABLE AB FCC /DXYUSP**ABCD****/
                               REGTAB FCC
 Ø553 44
                               >*** STACK REGISTER TABLE
```

```
Ø563 5Ø
                              >STKTAB FCC
                                                        /PSYXDBAC/
                               >*** MNEMONIC TABLE
Ø56B 4E
                                MNTAB
                                            FCC
FCC
FCC
FCC
       422442
422442
                                                        /NEG
Ø56F
Ø573
Ø577
Ø57B
                                MNILEG
                                                        /***
                                                                          ILLEGAL OPCODE
                                                        /***
                                                        /COM
/LSR
Ø57F
                                            FCC
                                                        / *: *: *:
Ø583
Ø587
        52
                                            /ROR
                                                        /ASR
/ASL
/ROL
        4.1
Ø588
Ø58F
        41
        52
0593
0597
059B
        44
20
49
                                                        /DEC
                                                        /***
/INC
/TST
/JMP
/CLR
 Ø59F
        54
        4A
43
 Ø5A3
Ø5A7
Ø5AB
Ø5AF
        20452AA22
                                                        /***
                                                        / *::*:*:
Ø5B3
Ø5B7
                                                        ZNOP
                                                        /SYNC/
Ø5BB
                                                        /:+:+::
                                            Ø5BF
                                                        /***
Ø5C3
Ø5C7
Ø5CB
                                                        /BRA
                                                        /BSR
        2Ā
                                                        /****
                                                        /DAA
Ø5CF
        44
        4F
2A
Ø5D3
                                                        /DRCC/
                                           Ø5D7
                                                        / sks4:s4:
Ø5DB 41
                                                        /ANDC/
/SEX
/EXG
/TFR
                                                        /BRA
/BRN
                                                        /BHI
                                                        /BLS
                                                        /BHS
                                                        /BLD
                                                        /BNE
                                                        /BED
                                                        VBVC
                                                        /BVS
                                                        /BPL
                                                        /BMI
                                                        /BGE
/BLT
/BGT
                                                        /BLE
                                                        /LEAX/
/LEAY/
/LEAU/
/PSHS/
                                                        /PULS/
/PSHU/
                                                        /PULU/
                                                        /*****
/RTS
064F
0653
0657
065B
41
                                                        /ABX
                                                        /RTI
                                                        /CWAI/
                                                        /MUL
                                                        / *: *: :
                                                        /SWI
                                                        /SUBA/
/CMPA/
/SBCA/
/SUBD/
                                                        /ANDA/
                                                        /BITA/
                                                        /LDA
/STA
                                                        /EURA/
068F
0693
0697
        4.1
4.F
                                                        /ADCA/
/ORA
                                                        /ADDA/
Ø69B
                                                        /CMPX/
```

Listing 1 con	itinued:										
Ø69F 4A Ø6A3 4C Ø6A7 53		}		FCC FCC FCC	/JSR /LDX /STX	//					
Ø6AB		5		END							
00000 E	RRORS										
ERFD FEUR13 A G FEDERAL A LMU FEDERAL A LMU	00000000000000000000000000000000000000	H H H I H I H I H I H I H I H I H I H I	AD4671EØ8F3B48FBDDB8FAS Ø9E5N9D148CD578F94EØC46 Ø545433Ø0ØØ011114NNQ0415	PEGRITHINGOOOOOOOOPRW	######################################	R G 1H A4R146 WEXNEWNEED 1 DIA RJTDDDDXXNEWNEED 1 DIA DOENNYNEED 11NOWNEED 1 DIA DOENNYNEED 11NOWNEED 1 DIA DOENNYNEED 1 DIA DOENNYN	Q5E8F936QE9QB5E8QA7QD3 Q54E3A7QQ9CF5E8718QQD5 Q6QQQQQQQQQQQQQQQQQQQQQQQQQQQQQQQQQ	TT 28 D E SENDENT SEND	948044878EFCDTX0018T800712 9200044862590515077501008 920004540500000001187800712	PA A1G1128 STED108 STED108 STED108 STED108 STED108 STED108 STED118 STED18 STED18	00000000000000000000000000000000000000



Listing 3 is a sample routine that demonstrates how to use the disassembler. First, the X register is loaded with the address where disassembly should begin by calling a monitor routine that asks for a 4-digit hexadecimal address. Then the Y register is loaded with the address of the monitor routine, which outputs the ASCII (American Standard Code for Information Exchange) character in the A register. This address can point to the console's or hard-copy device's output routine as desired. Next, the disassembler is called, and it outputs one line on the output device. A counter is used to output 19 lines (for my 20-line terminal), and then the keyboard input is checked. Disassembly continues for any input character other than an ESC (hexadecimal 1B); an ESC causes a return to the monitor.

The disassembler begins at DISAS by setting the U and S pointers, as described earlier. Next, the parameters passed in the X and 'registers are stored, and the temporary variables and output buffer are initialized. Then the first byte of code to be disassembled is examined. If it is not an op-code page byte (hexadecimal 10 or 11), it is looked up in the mnemonic table MNTAB to find its corresponding mnemonic. The mnemonic table is compressed from a maximum of 256 different entries to only 80 by converting op codes 40 through 7F to 00 through OF, and 80 through FF to 40 through 7F (hexadecimal). s nce the op-code mnemonic stem is similar in these cases.

Op codes are processed according to their first hexadecimal digit and again according to their addressing mode. Subroutines are provided for indexed (including indirect), direct, extended, and relative addressing. Immediate addressing is processed like direct or extended

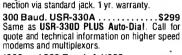
Text continued on page 364

Listing 2: A portion of the output of the disassembler working on itself.

######################################
--

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Listing 3: This short routine is an example of how to use the disassembler.

			>* EXAME	PLE OF I	aeu at war	THE DISASSEMBLER
	0000 FFB5 FFA3 FFA0 FFA6		DISAS DISAS DISAS DUTCH INCH MONITR	EQU EQU EQU EQU	\$0 \$FFB5 \$FFA3 \$FFA0 \$FFA6	DISASSEMBLER STARTING ADDRESS BUILD HEX ADDRESS IN X-REG CUTPUT CHARACTER IN A-REG INPUT CHARACTER INTO A-REG MONITOR RE-ENTRY POINT
0700 0703 0707 0709	BD 108E C6	FFB5 FFA3 13 FBF4	} } } } } } } } } } } } } } } } } } }	ORG JSR LDY LDB LBSR DECB	\$0700 BADDR #OUTCH #19 DISAS	CAN BE IN ROM WITH DISASSEMBLER GET STARTING ADDRESS POINT TO OUTPUT ROUTINE DISASSEMBLE 19 LINES
070C 070D 070F 0712 0714 0716 0719	5A 2BD 81 2E 7E	FA FFAØ 1B F1 FFAE	, , , , ,	BNE JSR CMPA BNE JMP END	LCOP1 INCH #\$1B LOOP MONITR	GET CHARACTER FROM KEYBOARD ESCAPE? YES, EXIT

Text continued from page 362:

addressing, depending on the number of bytes in the operand. If the program detects an illegal op code, page byte, or combination of the two, or an illegal indexed addressing postbyte, an illegal op-code routine is called to output "***" in place of the mnemonic.

By the time the program arrives at the end of job routine FINISH, the output buffer has been loaded with the op-code mnemonic and operand. The memory address location and the bytes of machine code are then placed into the buffer, and the entire buffer is output,

along with a CR-LF (carriage return-line feed) sequence. I use a Control U (hexadecimal 15) to erase a line on my video terminal, and this character acts as the terminator for the output sequence. Before exiting the program, the index registers are restored to facilitate further calls, and the S pointer is adjusted upward to release the user stack workspace.

In summary, this disassembler offers the advantages of speed and small size, while being both reentrant and relocatable. This flexibility makes it an ideal addition for a 6809 system.

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Conducted by Steve Clarcia

Thoughts on TRS-80 EPROMS

Dear Steve.

It may be good to add some details to your thoughts on using 2K-byte 2716 EPROMs (erasable programmable readonly memories) with the TRS-80 Model I. (See "In Need of a Way to the PROM," in the October 1981 BYTE, page 318.) In the case of a Model I with standard peripherals, Mr Fitzgerald's circuit must be changed, because there are not quite 2K addresses available. Expansion boxes for the Model I-which use the peripheral drivers in ROM (read-only memory) A-need eight addresses distributed within the 16-byte range 37E0 through 37EF hexadecimal. An EPROM, such as the one shown in your figure (page 318), extending up into these same addresses would create direct contention on the data bus. The peripherals would not work.

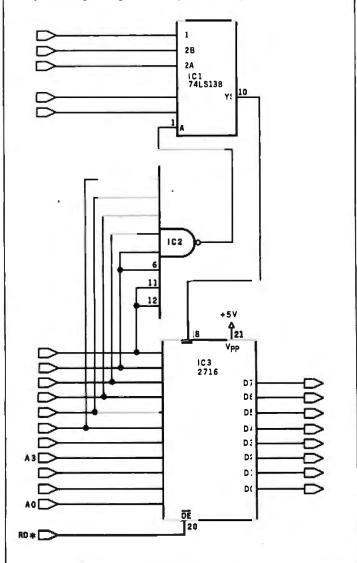
There are two possible solutions to the problem. One is to use a smaller EPROM. The second is to disable the 2716 when conflicting addresses occur. The two-device circuit in your figure enables all but the 2716's last 32 bytes (a compromise to save integrated circuits); there is no conflict when an expansion box is used, and 2016 bytes of EPROM are still available. The circuit also adds an RD signal from the control bus in a way recommended exclusively for the 2716 by its manufacturers.

Adding an EPROM to the Model III is a bit different, A corresponding system PROM, C, is already there (and is disabled in a way similar to the circuit shown here in figure 1,

but only at 37E8 and 37E9 hexadecimal (Radio Shack Service Manual, stock number 26-1061, page 14). In a 48K-byte system, no address space is free, and an EPROM would have to share space on the 16 available lines. Any of the three PROMs could be further qualified to accomplish this. The circuit would vary a lot, depending on when

and how one wished to select between the two ROMs. But it would not be difficult. What would be challenging in designing such a "phantom" EPROM circuit for the Model III would be avoiding any conflicts arising from memory references to the PROM whose space is shared.

Paul Fuller New York, NY



Number	Type	+ 5 V	GND
IC1	74LS138	16	8
IC2	74LS30	14	7
IC3	2716	24	12

Thank you for the information. . . . Steve

The Printer Connection

Dear Steve.

When I bought my TRS-80 microcomputer just about three years ago, I also bought Radio Shack's Ouick Printer II. Since then I've realized that I need a larger printer, so now the Q. P. II is sitting in a corner unused. The Q. P. II has three inputs, TRS-80 bus, TRS-80 Expansion Interface, and an RS-232C connection. Using the serial interface, the Q. P. II needs a 600 bps (bits per second) signal with 7 data bits, even or odd parity, and 1 or 2 stop bits; or 7 data bits. no parity, and 2 stop bits; or 8 data bits, no parity, and 1 or 2 stop bits. I would like to interface this printer to a Texas Instruments TI-58C calculator, but I do not have any information on the TI-58C's interface pins (in the battery compartment). Any help you could give me would be greatly appreciated. Michael W. E. Britt Favetteville, NC

For technical information on the TI-58C you should try calling Texas Instruments directly. The two numbers to call for technical information are (800) 858-1802 and (806) 741-2633.

One note, unless the outputs of the TI-58C calculator are either BCD (binary-coded decimal) or binary, it may be rather difficult to convert them to ASCII (American Standard Code for Information Interchange), The reason for this is that many printing calculators contain all the printer-control electronics on the same chip as the calculator itself. The output they produce is multiplexed for a thermal or a 5-wirematrix impact printhead. (This is what you have in your Q. P. II.)

In any event, it will be interesting to see how things turn out (imagine a remote numerical-entry terminal for your computer that also calculates?). . . . Steve

ROM-Based BASIC

Dear Steve,

I am looking for a ROMbased BASIC (equivalent to TRS-80's level II) that I could implement on an Intel 8085based microcomputer. Do you know of any vendor that could supply such an item with good documentation,

including a memory map and/or source listing? Richard P. Gabric Christchurch, New Zealand

A ROM-based 8K-byte Microsoft BASIC is available from:

Netronics Research and Development, Ltd. 333 Litchfield Rd. New Milford, CT 06776

It costs \$99.95 plus \$2 shipping and insurance. Netronics sells a complete line of 8085related products and is your best bet.

Microsoft does not publish its source code for BASIC (for obvious reasons). However, virtually every issue of Dr. Dobb's Journal published in 1976 had some article on Tiny BASIC, and these may be of some help. Contact the Hayden Book Co., 50 Essex St., Rochelle Park, NJ 07662, for a complete book of reprints of Volume I. . . . Steve

Power Backup

Dear Steve,

I am using a Commodore PET to control my solar-heating system, but I've run into a small problem. In our area, it is not uncommon to have momentary power failures that are long enough to result in the computer losing the data stored in memory. (Power-line "glitches" that simply disrupt operation are less usual.) The vast majority of these outages last for two or three seconds only. Is there some way I can use a large capacitor, or perhaps rechargeable batteries, to handle this power problem for as long as five seconds? Albert C. Pollard Irvington, VA

Generally speaking, it is not a good idea to increase the capacitance in a power supply to try to make up for more than a few milliseconds of power loss. Just for the heck of it, I decided to do some quick computations to see how much of a capacitor it would require if it were feasible. The general equation for this calculation is:

 $C = I \frac{dt}{dv}$

In this case, C is in farads, I is in amperes, v is in volts, and t is in seconds.

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The following assumptions are made: one is that the computer requires about 4 amps; the other is that the nominal voltage within a power supply is 9 volts into the regulator, which cannot maintain its full output voltage when the input voltage falls below 71/2 volts. Therefore, the allowable voltage drop is only 11/2 volts. So dv would then equal 1.5 volts; dt is equal to 5 seconds as per your request.

Solving the equation results in a huge capacitor value of 13.33 farads! As you can see, this is not feasible. It also could lead to burning out your power supply on turn-on because this gigantic capacitor would appear to the rectifier like a short circuit as it was charging up.

My recommendation is, rather than messing around with the power supply inside your PET, that you look toward providing an uninterruptible power source on the 115-volt power line. Many companies sell such items. One product that seems to be aimed primarily at the personal computer market is MayDay from Sun Technology.

I hope you solve your power loss problems without major expenses. . . . Steve

Control Sources

Dear Steve.

I am at present designing an automatic home-control system. I would appreciate any information and data that you may be able to offer. Faris Alamat

South Yorkshire, England

One of the main focuses of my articles over the years has been in the area of home control and security. In Ciarcia's Circuit Cellar, Volume II, there are four articles that may be of particular interest to you. Three concern the developing of a computer-controlled security system with emphasis on home control and data acquisition. The fourth article is on the design of a computer interface to the BSR X-10 AC remote-control system. This should be an integral part of any inexpensive home controller that you would be using. The book is available for \$12.95 from BYTE Books, 70 Main St.,

Peterborough, NH 03458. . . .

Search for Apple-to-**North Star Complier**

Dear Steve,

Do you know of a compiler that allows programs written for an Apple to run on a North Star? If so, please advise on where I can obtain this. If not, any suggestions? Thanks.

Harold Walton Pleasant Hill, CA

To my knowledge there is no compiler that allows you to go directly from Apple software to North Star.

If the Apple software is written in a higher-level lan-

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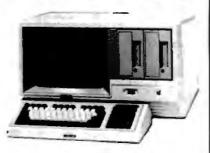
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guage such as BASIC, Pascal, PL/I or FORTRAN, however. you have a better chance of getting it to run on your North Star (if it also runs these languages). The inconvenience lies in finding language incompatibilities and correcting the statements to work on the North Star.

One possibility is an emulator. This is software, written for one processor, that emulates the program execution of another.

When it comes to direct use of machine-language programs, you are out of luck. The Apple uses the 6502 microprocessor, while the North Star uses the Z80Athey have incompatible instruction sets.

Finally, be aware that both types of programs, high-level and machine-language, will have instructions that manipulate the Apple I/O. The address and procedures for using cassette ports, keyboard, and video display are different between the Apple II and North Star, and also that some Apple software routines are in ROM. . . . Steve

Custom-Made System

Dear Steve,

I want to assemble my own custom computer system. I plan to use the S-100 bus since it appears to allow the most versatile system. I am most concerned with expandability, and I've noticed that a very large number of S-100 circuit cards are available.

I need a good high-level (preferably universal) language; but I need also the capability of programming in assembly language if the situation calls for it. I plan to use a Z80-based processor board.

One of my long-range goals is to have a multidisk system. I want to have two each of three or four types of drives (i.e., 35-track, 40-track, single-sided, etc.) This way I won't have to worry about disk-to-drive compatibility when I buy software. I also want to be able to copy from drive to drive in any combination. For example, I may want to copy a 40-track disk into a 77-track disk. I would appreciate any hints or information you can give me.

Ron Frazier Milledgeville, GA

Your concept of a custom computer system sounds fine to me. The S-100 bus has become a de facto standard and will give you all the versatility you desire, but . . . the multiple-drive approach may be quite expensive. Keep in mind a few facts about floppy-disk drives.

A double-density disk drive and controller can usually read single-density disks, and a 40-track, 51/4-inch disk drive only requires different software to work with 35-track disks. Unfortunately, there are many different formats for 51/4-inch disks, and most of them are mutually incompatible (an Apple II computer won't read disks from a TRS-80, which won't read Heath H-8 disks, and so on). Fortunately, most S-100 computers use 77-track 8-inch disks, and the IBM 3740 standard has been developed to ensure single-density compatibility. Most software is available in this format, which makes for a very versatile system. . . . Steve

Assembly Language

Dear Steve,

I am 14 years old and have my own 48K-byte Radio Shack TRS-80. I have mastered BASIC, and am trying to learn to program in assembly language. Unfortunately, after eight months, I am still trying. Even after studying books over and over, I can't seem to get the hang of it. Do you have any hints on how to learn assembly language, or do you know anybody near my home who could help me? David Natter Yonkers, NY

Sorry that you are having problems with assembly-language programming for the Z80 microprocessor. Here are some tips that may be of some help:

- 1. Assembly language requires some knowledge of how the Z80 operates. If you look at the architecture (a fancy word for the block diagram) of the Z80, you will see the various registers and how they are connected.
- 2. With this block diagram

- as a guide, review the instruction set. Try to understand what is happening physically when a particular instruction is executed
- 3. Understand that when certain instructions are executed, various flags (bits in a status register) are set or cleared. These flags can be tested, and their state can affect the action taken by the processor.
- 4. Try to understand routine programs that store data in memory and transfer memory contents to an output port.
- 5. Run short programs and understand what is happening. Certain locations are initialized at the start of a program and certain addresses have specific functions. Learn what they are and observe how they are called in other programs.

Also, check suppliers of TRS-80 software for a "single-step" or "breakpoint" program. This is a special routine that allows you to step through a machine-language program one instruction at a time. After each step, you should be able to examine all the registers and see what has changed. This facility aids in debugging as well as learning.

You don't mention what books you are using but here are three that will help: TRS-80 Assembly-Language Programming (Radio Shack), Z80 Microprocessor Programming and Interfacing, Book 1, by Joseph C. Nichols and Elizabeth A. Nichols. (Howard W. Sams and Co... 1979), and Practical Microcomputer Programming: The Z80, by W. J. Weller (Northern Technology Books, 1979; unfortunately, this book uses modified Intel mnemonics, not Zilog mnemonics).

Finally, check your local computer store for the meeting dates of computer clubs in your area. You are bound to find some help there.... Steve

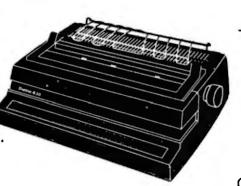
Apple 16-bit Hookup

Dear Steve.

I am a student at the University of Georgia, I own an Apple computer and I am looking for an inexpensive way to change the Apple to 16 bits. Can a Motorola 68000 microprocessor be plugged into the socket that the 6502 is in? If not, what is a simple way to change to 16 bits? Also, how can you change the display to 80 columns? I found a resistor I think controls the number of

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columns and it would seem to be easy to change the resistor to twice the value. Will this work?

Steve Albert Athens, GA

I am sorry to say that there is no simple way to change the Apple II to a 68000-based computer. The 68000 is not pin-compatible with any other microprocessor. Also, the Apple's memory is configured 8 bits wide, and Apple's software in ROM is intended for the 6502 instruction set. There are, however, complete 68000-based systems on the market. There is an accessory board that contains an Intel 8088, which allows 16-bit software for Intel's 8086 microprocessor to run on the Apple; it costs about \$1000. Contact: Metaphorphic Microsystems, POB 1541, Boulder, CO 80306, (303) 499-6502.

The display on the Apple II was set at 40 characters to enable an ordinary television receiver to be used as a monitor. I'm afraid that to obtain an 80-character line would require more than a resistor change. Again, there are plug-in boards available that convert the Apple to 80 characters (and to lowercase too). BYTE will be doing a comparison of these products soon. . . . Steve

Construction Tips

Dear Steve,

The only two computers I have used are a Commodore PET (in school) and a TRS-80 (at my local Radio Shack store). I have basic knowledge of electronics and microcomputers, and I have read many magazine articles and books (including yours) on building computers.

I have concentrated my study on Zilog's Z80 microprocessor and am interested in building a system around it. I want to use a video display and an ASCII keyboard to enter programs in BASIC, and a cassette tape recorder for storage. I also want some type of output for expansions (RS-232C, parallel, serial).

I would like to buy a TRS-80, but my budget is limited. Where can I get a book that has what I want? I was thinking of buying the 8K-byte floating-point super ROM (read-only memory) from Microace (see ad on page 359 of the August 1981 BYTE). Would that work instead of the monitor you described in your book? Would I need to change any circuits on the board?

Paul Perry Orinda, CA

It sounds like you've answered almost all your questions on your own. If you feel that my book (Build Your Own Z80 Computer, BYTE Books, 1981) does not have all the information you need, you might try looking at some of the other BYTE/McGraw-Hill books that are in print.

As to adding the Microace 8K Super BASIC, yes, it is possible, but (the ever-present catch) you will have to modify the circuitry. The Microace, like the Sinclair ZX80. uses so-called "cheap video." This means that the Z80 processor is doing all of the timing for the video display (sync and character generation) itself. Unless the Microace uses a jump vector in programmable memory for the inputs and outputs (like the TRS-80) you may have to patch the ROM somehow. You could do this by copying all of the Microace ROM into an EPROM (erasable programmable read-only memory) and changing the appropriate sections of the pro-

Very few of the ROM BASICs available are the same. Even when the machines use similar circuitry, they may use different addresses for I/O manipulations. This doesn't make it impossible to interface, just time consuming and aggravating.

Any of the kits on the market are excellent buys. The kit that is best for you depends on your budget and requirements.

In any event, have fun and good luck. . . . Steve

Selectric as Printer

Dear Steve,

I have an Atari 800 and would like to add a printer of some sort, but the cost of a quality unit is beyond my budget. My mom has an IBM Selectric typewriter, and I have seen ads for a device that enables a computer to use a Selectric as a printer. What do vou know about this? How much will it cost? Do I need an expansion interface? Which typewriter functions can the computer control? How much memory does the software require. At what speeds will it be capable of typing?

Mike Sutherland Appleton, WI

The IBM Selectric type-writer can be used as a printer for a computer only if the character selection solenoids are installed. Office Selectrics, which I assume is what your mother has, do not have these solenoids and thus cannot be driven by a computer. It is not practical to install these solenoids yourself.

The Selectric I/O (inputoutput) typewriter, currently available on the used-equipment market, has the necessary solenoids to be computer driven. In addition, these typewriters are of a heavier construction and quite durable. Consult the ads in BYTE for price and condition. Escon Products, Inc., 12919 Alcosta Blvd., San Ramon, CA 94583, sells a unit to adapt an office-type Selectric to a computer, but it costs around \$600, the price of a dot-matrix printer.

A line of universal electrictypewriter interfaces is made by Rochester Data Inc., 3000 South Winton Rd., Bldg. A, Rochester, NY 14623, (716) 224-7804. Different models cost \$600 to \$800.

You will need some kind of interface to take the TTL (transistor-transistor logic) signals from the computer and enable them to drive 30-or 48-volt solenoids.

The computer can enable all of the typewriter functions, if the solenoids are available for each function.

A computer program to drive the Selectric will take approximately 300 bytes including a look-up table for the type-ball codes.

Selectrics are rated for 13.4 cps (characters per second) maximum, but actual speed will depend on the driver program used.

For more information see "Interfacing the IBM Selectric Keyboard Printer" by Dan Fylstra in the June 1977 BYTE, page 46. It is an excellent article on interfacing the Selectric. . . . Steve

In "Ask BYTE," Steve Ciarcia answers questions on any area of microcomputing. The most representative questions received each month's will be answered and published. Do you have a nagging problem? Send your inquiry to:

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How to Become a Successful Computer Consultant

Leslie Nelson Essex Publishina Company, Caldwell, NJ 1980, 135 pages softcover \$28

Reviewed by Bruce Robert Evans. 16 Marwin Rd. Pickering, Ontario LIV 2N7, Canada

When I first received this book, I was convinced it was merely a rehash of the ob-. vious. In addition, I was put off by its poorly bound, onehundred plus pages: I felt that I'd wasted \$28 on a collection of single-sided, photocopied ramblings. But after rereading it and reflecting, I've concluded it is a must for anyone considering a career as a computer consultant.

Nelson approaches his subject, How to Become a Suc-

cessful Computer Consultant, in a straightforward, orderly fashion—he begins by defining what a computer consultant is, what he does, and where he does it. Next, he analyzes whether you should keep your present job (as a safety net) or whether you should jump into fulltime consulting.

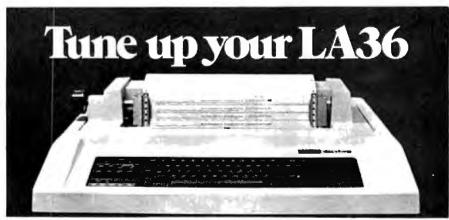
Next, Nelson proceeds to show how to package and market your services. Remember, you'll be trying to sell yourself to hard-nosed businessmen who might resent hiring an outside expert, so don't expect them to jump at the opportunity to consult a pink-cheeked, enthusiastic, former amateur. Nelson shows you, step by step, how to develop a resume and a marketing package, and explains where to get your leads and find business.

There's no point in running a business that pays you less than the minimum wage, even if the work is fun. How to... tells you how to negotiate fees and collect them. There are several charts showing what other consultants charge, examples that demonstrate calculations for obvious and hidden costs, and samples of several contracts. Copy and use them! In addition, there are checklists outlining what to do and which traps to avoid.

The only time Nelson is not specific is in the chapter on "big money." He glosses over software packages and turnkey systems. I realize that the topics are far too extensive to be covered in a single chapter, but this section should have been dropped or expanded.

The final chapter describes the computer consultant's legal liabilities, and it was a wise decision to leave this chapter for last. If you began here, you'd never go into business for yourself. However, Nelson lists the problems and then their solutions. a step at a time. You are advised when to seek a lawyer or an accountant, and how to choose them.

Nelson has successfully distilled the experiences of a number of years and presented them in a manageable package. In summary, this unassuming book should be on the shelf of everyone considering setting up a computer consulting practice.



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Color, I, and III Computer Club

The S & N Color, I, and III Club is interested in games, word processing, graphics,

and the inner workings of the TRS-80 I and III and the Color Computer. The club also produces a newsletter. Contact Neil Goldfarb, 3 Bohr Court, Spring Valley, NY 10977, or call Steve Kolokowsky at (914) 362-0713.

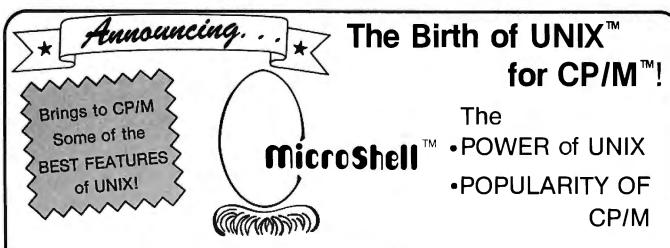
NCGA Opens New York Chapter

A chapter of the National Computer Graphics Association (NCGA) has been formed in New York City. The chapter's purpose is to disseminate and exchange information between vendors and users of computer-graphics technology. Two seminars and a quarterly newsletter are planned. Membership is open to individuals implementing computer graphics or distributing graphics products. For information, contact Dan Olasin (212) 832-3224 or Art

Kirsch (516) 826-4422.

Mid America Computer Hobbyists

MACH (Mid America Computer Hobbyists) is a nonprofit organization of computer hobbyists dedicated to the exchange of information on microcomputers. The club sponsors two

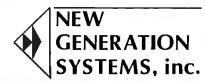


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Automatic Command File Search Path	 MicroShell finds your program. User concentrates on the big tasks, MicroShell does the details Permits development or data flies on one drive and all programs on another User-specified file types for Automatic Search. Example: ".com", ",int", etc. User-specified Search Path. Example: Current Drive 1st, then Drive A, etc.
Multiple Commands Per Line	User types a logical group of commands to be executed Example: compile file; link file; file MicroShell executes the commands one at a time
Direct Command File Execution	 Files of CP/M or MicroShell commands are executed by MicroShell simply by typing file name User-specified Command Filetypes. Example: ".sh", ".sub", etc. Argument substitution (\$1, \$2, etc.) as with CP/M SUBMIT/XSUB
Additional Features	User definable prompt with Disk Drive and/or User Number optional Install program to customize MicroShell to user's needs & system Others - ORDER MANUAL FOR FULL DETAILS

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major projects: a quarterly newlsetter and a summer computer fair. Membership is free. Contact MACH, POB 13303, Omaha, NE 68113.

Pascal/MT + Users Group

The Pascal/MT+ users group (MTPUG) is a newly formed organization promoting the use of Pascal as a programming language and serving as a vehicle for communications between users of the language. A quarterly newsletter with bug reports and fixes, programs, questions and answers, and items of interest is planned. Programs will be available on single-density 8-inch CP/M and 51/4-inch North Star or Heath/Zenith disks. Membership dues are \$7 in the U.S., \$8 in Canada or Mexico. All other countries, \$10 surface mail, \$16 air mail. Contact MTPUG, POB 192, Westmont, IL 60559. In Europe, contact MTPUG Europe, Schimmelmannstr, 37A, D-2070 Ahrensburg, West Germany.

TI-99/4 Users

A users group has been formed in the Cincinnati/ Dayton (Ohio) area for people interested in the TI-99/4 microcomputer. For information, contact 99/4 Users Group, c/o Jim Schwaller, 11987 Cedarcreek Dr., Cincinnati, OH 45240, (513) 825-6645.

Computer Club In **Central Jersey**

The Central Jersey Computer Club meets at 8 p.m. on the fourth Friday of each month at Armstrong Hall, Trenton State College, Trenton, New Jersey. Anyone interested in computing is in-

vited to attend. The club has an information exchange, a monthly newsletter, and frequent guest speakers. Visits to computer installations are organized. Contact Richard H. Williams, R.D.#1, Box 147, Hopewell, NJ 08525, (609) 466-2926.

Clubs and **Newsletters Notes**

Ham radio operators interested in starting a national Atari network should contact Sheldon Leemon, 14400 Elm St., Oak Park, MI 48237.

Larry Kamin would like to get in touch with any amateur computing club in New York City. Call (212) 389-3700, ext. 324.

Sinclair ZX81 users are in short supply in Switzerland. Mrs. Dane Kurth, Langgasse 51, CH-3292 Busswil, Switzerland would like to correspond with other ZX81 owners.

The Club Apple de Quebec has a new address. Contact Octavio Prieto-Cox, c/o Club Apple de Quebec, 1041 Jeanne Leber, Sainte-Foy, Quebec, Canada, G1W 4G7.

Graphics Group

Advanced Electronics Design (AED) has created a special-interest group for users of the AED512 color raster-graphics display system. Membership is free to anyone who purchases the system, and includes a free subscription to a newsletter, access to a library of usersubmitted AED512 programs and software, and applications information from group members. Members will also be informed of the latest AED new products and will have the opportunity to participate in the yearly group meeting at SIGGRAPH. Contact Robin Ratajczak, Advanced Electronics Design, Inc., 440 Potrero Ave., Sunnyvale, CA 94086, (408) 733-3555.

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Altos 8000-10	6.397.00	NEC 8001A	865.00
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Altos 8000-2	2,629.00	NEC 8031A	865.00
Altos 8600-10	9,385.00	North Star 64K DD	3,073.00
Archives Model I	4,794.00	North Star Advantage	2995.00
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Archives Model III	6,269.00	Televideo System II	5,311.00
CCS Series 300-1 A	4,414.00	Televideo TS-800 Term.	1,324.00
CCS Series 400-1 A	6,374.00	Televideo TS-802	2,578.00
Cromemco System 3	5,650.00	Vector 2600	4,221.00
Cromemco Z-2H	7,521.00	Vector 3005	6,458.00
Dynabyte 5200-A2	3,216.00	Vector 5005	7,308.00
Dynabyte:5200-B2	4,896,00		

Dbase II	500.00	Wordstar	305.00
Spellguard	200.00	Basic Compiler	277.00
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Spell Star	180.00	Visi Calc	160.00

Anadex 9000	1,100.00	NEC 5510	2,345.00
Anadex 9501	1,278.00	NEC 5520 KSR	2,645.00
C. Itoh 25 P	1,325.00	NEC 5530	2,345.00
C. Itoh 45 P	1,700.00	NEC 7710	2,345.00
Diablo 630	2,075.00	Epson MX80 in stock	485.00
Diablo 1640	2,444.00	Qume Sprint 9-35	1,738.00
Malibu 165	1,796.00	Qume Sprint 9-45	1,996.00
Malibu 200	2,320.00	Qume Sprint 9-55	2,085.00
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Hayes Micromodem S-100	319.00	Morrow 20 MEG	3,650.00
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Computers in Medical Offices

The Micro Medical Newsletter provides advice on the use and selection of applications for microcomputers in the medical office. Reviews of accounting and insuranceclaim management systems, plus reviews of applications software for the Apple II and III, TRS-80, and CP/M-based computer systems have been published. One issue includes an article on the use of minicomputers versus microcomputers in medical offices. The current issue is free to physicians and other health professionals when the request is made on office stationery. For more details, contact Charles Mann and Associates, 7594 San Remo Trail, Yucca Valley, CA 92284, (714) 365-9718.

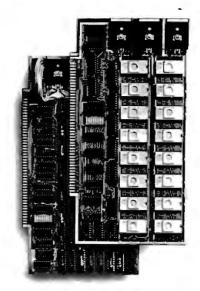
CSAA Hobbylsts

The CSAA Computer Club is an active group of computer hobbyists and professionals. The club meets at 7:30 p.m. on the third Thursday of the month in the Student Center of the Medical College of Georgia, Laney Walker and 15th St., Augusta, Georgia. Dues are \$6 per year. A newsletter is published. Contact the CSAA Computer Club, POB 284, Augusta, GA 30903. ■

Manager Corrected

Because of the way the TRS-80 Model III handles strings, two corrections need to be made to the program listing in Paul Swanson's article, "PDQ: A Data Manager for Beginners." (See the November 1981 BYTE, page 236.) Lines 640 and 950 of listing 1 should both be changed to read A\$ = I\$ + STRING\$(CA(5),32).■

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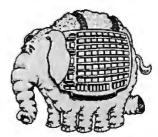
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February 1982

February

Public Courses, various sites throughout the U.S. Among the courses being offered by Ken Orr and Associates are "Structured Systems Design/Structured Program Design" and Structured Reguirements Definition." For schedule of meeting times and places, contact Ken Orr and Associates Inc., 715 East 8th, Topeka, KS 66607, (800) 255-2459; in Kansas (913) 233-2349.

February-March

Hands-On Local Network Workshops, various sites throughout the U.S. This series of four-day workshops provides hands-on experience with a local computer network. File, printer, and electronic-mail servers, and various software and hardware components of a localnetwork computer system will be provided. The local network used as the example will consist of at least a Nestar Cluster One/Model A. Write to Architecture Technology Corp., POB 24344, Minneapolis, MN 55424.

February-April

Computer Network Design and Protocols, various sites throughout the U.S. Participants in this workshop will learn to determine networksystem requirements and will perform design trade-offs, implement network-communication and control protocols, use packet- and message-switching techniques, evaluate network hardware and software components, interface local systems to networks, and design and build private networks. The course fee is \$845. Contact Ruth Dordick, c/o Integrated Computer Systems, 3304 Pico Blvd., POB 5339, Santa Monica, CA 90405, (800) 421-8166; in California (800) 352-8251,

February-April

Fundamentals of Data Processing for Administrative Assistants and Office Support Staff, various sites throughout the U.S. The American Management Associations (AMA) has designed this three-day course for secretaries, assistants, supervisors, and other personnel desiring to learn the fundamentals of data processing and its use in offices. Computer hardware, software, programming languages, and technology will all be covered. The team fee for AMA members is \$470 per individual and \$550 for nonmembers. Individual fees are \$550 for AMA members and \$630 for nonmembers. For a schedule of dates and locations, contact the AMA, 135 West 50th St., New York, NY 10020, (212) 586-8100. To register by phone, call (212) 246-0800.

February-June

Datamation Institute Seminars on Information Management, various sites throughout the U.S. Databases and communications, systems performance, data-processing management, word processing, office automation, computer graphics, and topics of general interest are among the areas to be covered by these two-day seminars. Fees range from \$495 to \$595. For schedules of times and places, contact Karen Smolens, c/c the Center for Management Research. Datamation Institute Seminar Coordination Office, 850 Boylston St., Chestnut Hill, MA 02167, (617) 738-5020.

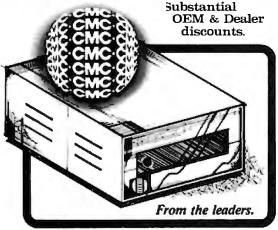




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February-June

Intensive Two-day Seminar for Professional Development, various sites throughout New England. Among the seminars to be offered by Worcester Polytechnic Institute are "Fundamentals of Data Processing," "Distributed Systems: The Architecture and Utilization of This Revolutionary Technology," and 'Microprocessors: Hardware, Software, and Applications." Registration fees range from \$445 for a twoday program to \$990 for a 7-day executive institute. For complete details, contact Ms. Ginny Bazarian, Office of Continuing Education, Worcester Polytechnic Institute, Worcester, MA 01609, (617) 793-5517.

February-June

One- and Two-day Professional Development Seminars, various sites in greater Boston. Among the courses being offered by Boston University are "Business Writing for Results," "Improving Customer Service," and "Assertive Management." Registration fees range from \$295 for a oneday program to \$445 for a two-day program. These seminars can be conducted within your company. For details, contact Ms. Joan Merrick, Center for Management Research, 850 Boylston St., Chestnut Hill, MA 02167, (617) 738-5020. For information on the in-company seminars, contact Ms. Elaine Dee at the same address.

February-June

Courses and Seminars from Sira Institute, various sites throughout England, Sira Institute is sponsoring seminars on a wide variety of subjects, ranging from microprocessor familiarization to design and development of microprocessor-based equipment. For details, contact Conferences &

Courses Unit, Sira Institute Ltd., South Hill, Chislehurst, Kent BR7 5EH, England.

February 14-18

The Kuwait Information Management Exhibition: INFO Kuwait, Kuwait International Exhibition Center, Kuwait. Industrial executives from the Middle East are among those expected to attend this conference. Exhibits and speakers will be featured. Contact Clapp & Poliak International, 7315 Wisconsin Ave., Washington, DC 20014, (301) 657-3090.

February 18-19

Computer/Micrographics Interface, Stouffer's Greenway Plaza, Houston, TX. The Computer/Micrographics Interface is designed for information managers, systems analysts, micrographics systems analysts, records managers, and others who need information on computer and micrographic technologies. The course is presented by Battelle Research Institute. Contact Battelle Seminars and Studies Program, 4000 Northeast 41st, Seattle, WA 98105, (800) 426-6762; in Washington (206) 527-0542.

February 18-19

The Second Annual Talmis Conference and Exhibit, Chicago, IL. The Talmis Conference will focus on educational and reference media for the institutional, training, home-computer, and video markets. Local computer networks in education, the market for electronic educational and reference media in the home, software piracy, and other topics will be discussed. Exhibits of products and services will be featured. The registration fee is \$450. For more information, contact Talmis, 115 North Oak Park Ave., Oak Park, IL 60301, (312) 848-4001.

February 18-20

The Ninth Annual Conference of the Mid-South Association for Educational Data Systems, Landmark Hotel, New Orleans, LA. The theme of the Ninth Annual Conference of the Mid-South Association for Educational Data Systems is "Computer Creativity." The conference will feature papers, workshops, and panel discussions on CAI (computer-aided instruction). CMI (computermanaged instruction), research developments, user/ producer communications, and administrative applications. For details, contact Mike Schouest, Director, MIS Data Center, Louisiana State Dept. of Education, 3455 Florida Blvd., Baton Rouge, LA 70806, (504) 342-3762.

February 22-24

The Eighth Federal DP Expo, Sheraton Washington Hotel, Washington, D.C. More than 150 computer industries will display and demonstrate hardware and software systems and services at the Federal DP Expo. Conferences on data processing and office automation will be Approximately 120 hold

computer-industry experts are scheduled to speak. Contact The Interface Group, 160 Speen St., Framingham, MA 01701, (800) 225-4620; in Massachusetts. (617) 879-4502.

February 22-24

Oasis Level Two Training Seminars, Phase One Systems, Oakland, CA. Using a step-by-step approach to developing applications software with the multiuser Oasis operating system, this seminar begins with program design and proceeds to a careful study of the Oasis system. Topics to be covered are the Oasis BASIC interpreter and compiler, program segments, file structures and I/O (input/output), matrices and matrix I/O, multi-line branching structures, and subroutine and error handling.

The registration fee for this three-day session is \$350. Some background in BASIC programming is recommended. Contact Phase One Systems, Suite 830, 7700 Edgewater Dr., Oakland, CA 94621, (415) 562-8085.

February 23-25

Computers and Automated Office Systems Exhibit for

Caribbean Markets, Holiday Inn, Paradise Island, Nassau, Bahamas. This show is intended to bring together buyers and distributors within the industry. Exhibits of equipment for businesses in the Caribbean will be featured. For more details, contact Ormand Vee Co., 8852 Leslie Ln., Desplaines, IL 60016, (312) 635-7347.

February 26-28

Computer Expo '82, Tupperware Convention Center, Orlando, FL. Focusing on computers in education, business, industry, professional trades, and the home, Computer Expo '82 will feature exhibits of computers and peripherals. It is sponsored by Adventure International, General admission is \$5. For details, contact Computer Expo '82, 377 East Highway 434, POB 1185, Longwood, FL 32750, (305) 339-1731.

March 1982

March

Courses and Seminars from George Washington University, Amsterdam, Netherlands; London, England; Long

Island, NY; San Diego, CA; and Washington, DC. Among the courses and seminars to be presented are "Microcomputers in Control Systems," "Comparative Database Management Systems," and "Structured Programming and Software Engineering." For further information, contact The Director, Continuing Engineering Education, George Washington University, Washington, DC 20052, (800) 424-9773; in Washington, DC, (202) 676-6106.

March-Iune

National Computer Graphics Association Seminar Provarious eram. sites throughout the U.S. The National Computer Graphics Association's (NCGA) Winter/Spring 1982 seminar program covers such topics as "Computer Graphics: Technology and Applications," "Successful Business Graphics," and "Applications of Computer Graphics to Transportation Problems." Seminar fees are \$395 for association members and \$425 for nonmembers. For complete details, contact Eloise Wenker, NCGA Seminar, 2033 M St., NW

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Early issues of Sextant will have articles on using the H89 to produce color slides and articles for publication, a new disk operating system for the H11, Tiny Pascal, H89 parallel ports, print spoolers, simulation of Rubik's Cube, and writing assembly language disk software that doesn't require HDOS.

Start your subscription with the premiere issue of **Sextant**, to be printed in February, and receive all four 1982 issues. Just send your payment of \$9.97 (\$11.50 in Canada, \$14 overseas) for a four-issue subscription. (Payment must be in U.S. dollars payable on a U.S. bank, by international postal money order or charge it on VISA or MasterCard.) A full refund is guaranteed any time you're not satisfied. Send your order today to: Sextant, Dept. B, 716 E St., S.E., Washington, DC 20003 or call 202/544-0900.



Event Queue _

#300, Washington, DC 20036, (202) 466-4102.

March 1-2

Sixth Annual Convention of the Michigan Association for Computers Users in Learning, Western Michigan University, Kalamazoo, MI. Featured will be presentations and sessions on various facets of computers in education. Also featured will be vendor demonstrations and displays. For further details, contact Carolyn Gilbreath, c/o Oakland Schools, 2100 Pontiac Lake Rd., Pontiac, MI 48054, (313) 858-1898.

March 1-4

Robots VI Conference and Exposition, Cobo Hall, Detroit, MI. An estimated 6000 manufacturing executives and engineers are expected to attend the Robots VI Conference, which features the latest in robotics technology and equipment. Among the topics to be addressed are assembly, foundry operations, aerospace applications, vision and handling, research and development, and sessions on human factors associated with robotics. Cincinnati Milacron, Unimation, and Hitachi America are a few of the companies that will be exhibiting. The show is being sponsored by Robotics International of the Society of Manufacturing Engineers (RI/SME). Contact RI/SME. One SME Dr.. POB 930, Dearborn, MI 48128, (313) 271-1500, ext. 416.

March 2-4

The 1982 Vancouver Island Business Show, Empress Hotel, Victoria, British Columbia, Canada. The Vancouver Island Business Show features word-processing, communications, and office systems. The show provides the Vancouver Island business community with the opportunity to meet with many

Canadian suppliers of computer equipment. For information, contact Southex Exhibitions, Suite 202, 2695 Granville St., Vancouver, British Columbia, V6H 3H4, Canada, (604) 736-3331. In eastern Canada, contact Judy Hurd, 1450 Don Mills Rd., Don Mills, Ontario, M3B 2X7, Canada, (416) 445-6641.

March 3-7

Microcomputer . Week '82, Jersey City State College, Jersey City, NJ. The third annual Microcomputer Week conference will focus on microcomputers in education at the elementary, secondary, and college levels. Sixty-six seminars or short courses will be offered, many of which will involve hands-on experience. Special-interest groups, addresses, and reports will be included in the conference, along with exhibits and displays of educational microcomputer hardware, software, courseware, books, and periodicals. Enrollment fees range from \$95 for one day to \$73 per day for the entire five-day conference. A three-day executive computing course for school and college administrators costs \$425. For details, contact Catalyst Conference, H 112, Jersey City State College, 2039 Kennedy Blvd., Jersey City, NJ 07305, (201) 434-2154 or (201) 547-3094.

March 7-10

The Eleventh Annual TI-MIX Symposium, Las Vegas Hilton, Las Vegas, NV. The TI-MIX, an organization for Texas Instruments computer users, will sponsor a symposium featuring exhibits, a business meeting, and a new products workshop. Individual presentations, panel discussions, and workshops are planned. Contact TI-MIX, M/S 2200, POB 2909, Austin, TX 78769, (512) 250-7151.

March 7-12

The Twenty-Eighth Audio-Visual Institute for Effective Communications, Indiana University, Bloomington, IN, The Institute provides audiovisual/video communicators with a comprehensive, practical overview of communication techniques and the opportunity to gain practical experience, exchange ideas, and receive individual instruction. Professionals will lead a series of lectures, discussions, and workshops. For details. contact Ed Richardson, c/o NAVA Institute. Audio-Visual Center, Indiana University, Bloomington, IN 47405

March 9-11

The 1982 International Zurich Seminar on Digital Communications, Zurich, Switzerland. The theme of this seminar is 'Man/Machine Interaction." Its aim is to present recent advances in theory and application of digital-communication systems. Services, facilities, ergonomics, and their impact on peripheral equipment, systems architecture and design, as well as I/O (input/output) concepts and principles will be covered. For details, contact Secretariat '82 IZS, Ms. M. Frey, EAE, Siemens-Albis AG, POB CH-8047, Zurich, Switzerland.

March 9-11

Understanding and Using Computer Graphics, Dallas Hilton Inn, Dallas, TX, The seminar is designed for those interested in the field of interactive computer graphics, including hardware, software, and applications. Headed by Carl Machover, the seminar provides a comprehensive overview of the state of the art in graphics systems. For details, contact Bob Sanzo, c/o Frost & Sullivan, Inc., 106 Fulton St., New York, NY 10038, (212) 233-1080.

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Event Queue_

March 9-12

Digital-Image Processing and Analysis, San Diego, CA. Integrated Computer Systems' course in digital-image processing is designed for engineers. scientists. technical managers, and other professionals responsible for specification, design, implementation, or application of digital-image processing systems. Among the topics to be covered are image acquisition, imageprocessing software and database structures, interactive two- and three-dimensional image processing and display, and real-time arrays. Some of the applications examples to be presented are quality assurance and robot vision. The course fee is \$795; on-site courses are available on request, Contact Ruth Dordick, c/o Integrated Computer Systems, 3304 Pico Blvd., POB 5339, Santa Monica, CA 90405, (800) 421-8166; in California (800) 352-8251.

March 9-12

VIO-Voice Input/Output for Computers, Los Angeles, CA. VIO-Voice Input/Output for Computers is a fourday course designed for product development and design engineers, systems analysts, programmers, and technical managers involved in planning, design, and implementation of voice input/output systems. The topics to be covered include voice-processing algorithms and software, evaluating VIO hardware components and systems, utilizing speech synthesis techniques, and designing voice-recognition techniques. Participants will have the opportunity to work with devices that permit online generation of computer-voice output, data entry by means of voice input, and voice input for system control. The course fee is \$795; on-site courses are available upon request. For information, contact Ruth Dordick, c/o Integrated Computer Systems, 3304 Pico Blvd., POB 5339, Santa Monica, CA 90405, (800) 421-8166; in California (800) 352-8251.

March 10-12

Cincinnati Business Show. Cincinnati Convention Center, Cincinnati, OH. The Cincinnati Business show features the latest in business technology, office systems, and products. Seminars will also be presented. For information, contact Ray G. Nemo, 5679 Creek Rd., Cincinnati, OH 45242, (513) 531-5959.

March 15-19

Short Course from UCLA. Boelter Hall, University of California-Los Angeles (UCLA), Los Angeles, CA. "Mechanical Reliability, Design by Reliability, Probabilistic Design-The Stress/Strength Interference Approach to Reliability Prediction" is a short course being presented by UCLA. The course fee is \$795, which includes comprehensive course notes. For details, contact Dr. Dimitri Kececioglu, Aerospace and Mechanical Engineering Dept., University of Arizona, Tucson, AZ 85721, (602) 626-2495 or (602) 626-3901. In California, call Robert Rector at UCLA. (213) 825-1295 or (213) 825-3344.

March 16-18

Software/Expo-West, Anaheim Convention Center, Anaheim, CA. The Software/Expo-West is a conference and show devoted to packaged software. Exhibitors will display a wide range of software products. For additional information, contact Software/Expo-West, Suite 400, 222 West Adams St., Chicago, IL 60606, (312) 263-3131.

Mail to: Journal of Pascal and Ada

March 16-19

Digital Filters and Spectral Analysis, Boston, MA. Integrated Computer Systems (ICS) is presenting a four-day course on digital filters and spectral analysis for project and design engineers, programmers and technical managers responsible for implementing advanced digital signal-processing systems. and those who must understand them and their potential. Fundamentals of digital signal processing, fast Fourier transform (FFT) algorithms, and special- and generalpurpose LSI/VLSI (largescale and very large-scale integration) devices are among the topics to be addressed. The course fee is \$795; on-site courses are available by request. Contact Ruth Dordick, c/o ICS, 3304 Pico Blvd., POB 5339, Santa Monica. CA90405. (800)421-8166; in California (800) 352-8251.

March 19

The Eleventh Annual International Computer Programs Awards Ceremony and Executive Conference, Savoy Hotel, London, England, The annual International Computer Programs Inc. (ICP) awards ceremony and executive conference honors super software salespeople, advertising agencies, public relations firms, and achievements in the industry. The executive conference is one and a half days of discussion of the major issues and concerns of the industry. The fee for the executive conference is \$250. For information, contact Carol Stumpf, 9000 Keystone Crossing, POB 40946, Indianapolis, IN 46240, (800) 428-6179; in Indiana (317) 844-7461. In England, contact International Computer Programs, Inc., 2 Deanery St., Park Lane, London WIY 5LH, England, Tel. 01 499 6621.

The Seventh West Coast Computer Faire, Civic Auditorium and Brooks Hall, San Francisco, CA. Attendance this year is expected to reach 35,000, More than 300 exhibitors and a wide assortment of seminars make this one of this largest annual computer shows. For more information, contact The Computer Faire, 333 Swett Rd., Woodside, CA 94062, (415) 851-7075.

March 22-23

Oasis Level Two Training Seminars, Phase One Systems, Oakland, CA. For details, see February 22-24.

March 22-25

Interface '82 Conference and Expo, Dallas Convention Center, Dallas, TX. Cosponsored by McGraw-Hill's Business Week and Data Communications magazines, Interface '82 is aimed at users of data-communication equipment, distributed-data processing, and various networks. For details, contact The Interface Group, POB 927, 160 Speen St., Framingham, MA 01701, (800) 225-4620; in Massachusetts (617) 879-4502.

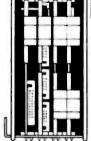
March 22-26

Computers/Graphics in the Building Process, Washington, DC. Computers/ Graphics in the Building Process is an international conference sponsored by the Advisory Board on the Built Environment (ABBE) of the National Academy of Sciences and by the World Computer Graphics Association (WCGA). The conference features tutorials, technical paper sessions, and exhibits that reflect the state of the art of computers and computergraphics technology in the building industry. Sessions on case studies, current achievements, and research and development of com-

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A Message to our Subscribers

From time to time we make the BYTE subscriber list available to other companies who wish to send our subscribers promotional material about their products. We take great care to screen these companies, choosing only those who are reputable, and whose products, services, or information we feel would be of interest to you. Direct mail is an efficient medium for presenting the latest personal computer goods and services to our subscribers.

Many BYTE subscribers appreciate this controlled use of our mailing list, and look forward to finding

information of interest to them in the mail. Used are our subscribers' names and addresses only (no other information we may have is ever given).

While we believe the distribution of this information is of benefit to our subscribers, we firmly respect the wishes of any subscriber who does not want to receive such promotional literature. Should you wish to restrict the use of your name, simply send your request to BYTE Publications Inc. Attn: Circulation Department, 70 Main St, Peterborough NH 03458. Thank you.

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Event Queue_

puter hardware, software, and database programs will be presented. Conference topics include computer aids to management, computer technology, and computeraided synthesis in design development and construction documents. For further details, contact the WCGA, Suite 250, 2033 M St., NW, Washington, DC 20036, (202) 775-9556.

March 22-26

Tutorial Week East '82. Orlando Marriott Inn. Orlando, FL. Tutorial Week East is sponsored by the Institute of Electrical and Electronics Engineers (IEEE) and will consist of 15 tutorials arranged in 3 tracks: VLSI (very large-scale integration) microprocessor-interfacing techniques and graphics; aspects of software design, analysis, and techniques; and data communications, computer networking, and databases. Fees are \$90 per tutorial, \$400 all week, for IEEE members and \$110 per tutorial, \$500 all week, for nonmembers. For information, contact Tutorial Week East '82, POB 639, Silver Spring, MD 20901, (301) 589-3386.

March 23-25

Southcon '82, Sheraton Twin Towers Hotel, Orlando Hyatt Hotel, and Holiday Inn, International Drive, Orlando, FL. Among the topics to be presented at Southcon '82 will be artificial intelligence and robotics, office automation, computers and microprocessors, and software. For complete details, contact Robert Myers, Electronic Conventions Inc., Suite 410, 999 North Sepulveda Blvd., El Segundo, CA 90245, (213) 772-2965.

March 29-30

Information Utilities '82, Rye Town Hilton Hotel and Conference Center, Rve, NY, The Information Utilities conference will focus on videotex, transactional services, electronic publishing, online database services, cable advertising, and regulations concerning copyright, censorship, and communications. More than 60 speakers are scheduled. For details, contact Online, Inc., 11 Tannery Ln., Weston, CT 06883, (203) 227-8466.

March 29-April 1

INFOCOM '82, Las Vegas, NV. INFOCOM '82 is sponsored by the Institute of Electrical and Electronics Engineers (IEEE) Computer and Communications Societies. The conference theme is "Data Processing-Data Communications: The Illusory Boundary." Focusing on the convergence of computer and communication technology, this conference will explore the fine boundaries between the two disciplines. Discussions on programming-language and operating system design, performance evaluation and analysis of computercommunication networks and protocols, standards, and the design of distributed computing and database management systems will be held. Exhibits and tutorials are planned. Write to IN-FOCOM'82, POB 639, Silver Spring, MD 20901, (301) 589-3386,

March30-April 2

Digital-Image Processing and Analysis, Washington, D.C. For details, see March 9-12.

April 1982

April 1-2

The Eleventh Annual International Computer Programs

Awards Ceremony and Executive Conference. Marriott Mountain Shadows Resort. Scottsdale, AZ. The annual International Computer Pro-(ICP) grams awards ceremony honors super software salesman, advertising agencies, public relations firms, and microcomputer software achievements. The executive conference discusses the main issues and concerns of the industry, such as productivity through proper use of people and machines, new softwarepiracy solutions, and how to get the most out of advertising dollars. The fee for the executive conference is \$250. For detailed information. contact Carol Stumpf, 9000 Keystone Crossing, POB 40946, Indianapolis, IN 46240, (800) 428-6179; in Indiana (317) 844-7461.

April 2-3

Educational Computing-The Future Is Now, Anchorage, AK. The Educational Computing conference is sponsored by the Alaska Association for Computers in Education, Invited speakers, exhibits, and demonstrations of microcomputer products

for educational purposes will be featured. Admission to the exhibition area is free of charge. For further details. contact Pat Stowers, '82 Educational Computing, Drawer 129, Healy, AK 99743, (907) 683-2278.

Anril 2-4

The Second Annual Eighty/ Apple Computer Show, New York Statler Hotel, New York, NY. The Eighty/Apple Computer Show features products and services for the TRS-80 and Apple computer systems. More than 100 exhibitors of hardware, software, books, magazines, supplies, services, and accessories will attend. For more information, contact Ken Gordon, Kengore Corp., 3001 Rte. 27, Franklin Park, NI 08823. (201) 297-2526.

April 13-16

Digital-Image Processing and Analysis, Boston, MA. For details, see March 9-12.

April 15-18

The Second Southwest Computer Show and Office Equipment Exposition, Market Hall, Dallas Market Center, Dallas, TX. The Southwest Computer Show and Office Equipment Exposition features mini- and microcomputers for business. education, government, industry, home, and personal use. Data- and word-processing equipment, office machines, computer peripherals, and office supplies will be displayed. General admission is \$5. Contact National Computer Shows, 824 Boylston St., Chestnut Hill, MA 02167, (617) 739-2000.

April 20-22

D-COM, Hynes Auditorium. Boston, MA, A trade show for products and services compatible with Digital Equipment Corporation's products, D-COM will involve vendors and users. For information, contact Ron Davies, D-COM Inc., 7312 Burdette Court, Bethesda, MD 20817, (301) 469-7650.

April 20-23

VIO-Voice Input/Output for Computers, Boston, MA. For details, see March 9-12.

April 21-28

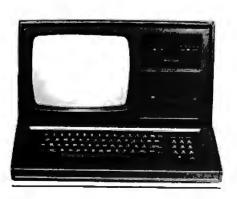
Hanover Fair '82, Hanover, West Germany. The annual Hanover Fair is one of the world's largest industrial and trade exhibitions. More than 330 American firms are expected to exhibit products, services, and technology at the Fair, Contact M.A. Delia, Hanover Fairs Information Center, POB 338, Whitehouse, NJ 08888, (800) 526-5978; in New Jersey, (201) 534-9044.

April 22-25

New York Computer Show and Office Equipment Exposition, Nassau Coliseum, Uniondale, NY. For details, see April 15-18. ■

In order to gain optimal coverage of your organization's computer conferences, seminars, workshops, courses, etc, notice should reach our office at least three months in advance of the date of the event. Entries should be sent to: Event Queue, BYTE Publications, POB 372, Hancock NH 03449. Each month we publish the current contents of the queue for the month of the cover date and the two following calendar months. Thus a given event may appear as many as three times in this section if it is sent to us far enough in advance.

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Apple II

Escape from Arcturus, a graphics arcade game for the Apple II. Floppy disk, \$35. Synergistic Software, 5221 120th Ave. SE, Bellevue, WA 98006.

Portware, a stock-portfolio-management system for the Apple II. Floppy disk, \$195. Portware Inc., 5724 Tucker Ln., Edina, MN 55463.

Whizkit, a program package for converting units of measure for the Apple II Plus. Floppy disk, \$39.95. P. V. Systems, POB 21577, San Jose, CA 95151.

Heath

Airport, a flight-controller simulation game for the Heath H-8/H-89. Floppy disk, \$19.95. The Software Toolworks, 14478 Glorietta Dr., Sherman Oaks, CA 91423.

Ed-a-Sketch, a full-screen graphics editor for the Heath H-8/H-89 (will also run under CP/M). Floppy disk, \$29.95. The Software Toolworks (see address above).

Introduction to BASIC Programming, a course in BASIC programming for the

Heath H-8/H-89. Floppy disk, \$29.95. The Software Toolworks (see address above).

Invaders, a graphics arcade game for the Heath H-8/H-89 (will also run under CP/M). Floppy disk, \$19.95. The Software Toolworks (see address above).

Mychess, a computerized chess program for the Heath H-8/H-89 (will also run under CP/M). Floppy disk, \$34.95. The Software Toolworks (see address above).

PIE 1.5, a full-screen text editor for the Heath H-8/H-89 (will also run under CP/M). Floppy disk, \$29.95. The Software Toolworks (see address above).

Reach, a telecommunications terminal program for the Heath H-89 (will also run under CP/M). Floppy disk, \$19.95. The Software Toolworks (see address above).

TRS-80

Color Maze, a graphics arcade game for the TRS-80 Extended BASIC Color Computer. Cassette, \$10. Baranwear, POB 1448, Hayfork, CA 96041.

AC and DC Circuit Analysis Programs, analyzes AC and DC circuits for the TRS-80 Model I Level II. Cassette, \$17.97. Computer Heroes, 1961 Dunn Rd., East Liverpool, OH 43920.

Multidos, a versatile disk operating system for the TRS-80 Models I and III. Floppy disk, \$79.95. Cosmopolitan Electronics Corp., POB 234, Plymouth, MI 48170.

Whizkit, a program package for converting units of measure for the TRS-80 Models I and III. Floppy disk, \$39.95. P. V. Systems, POB 21577, San Jose, CA 95151.

Other Computers

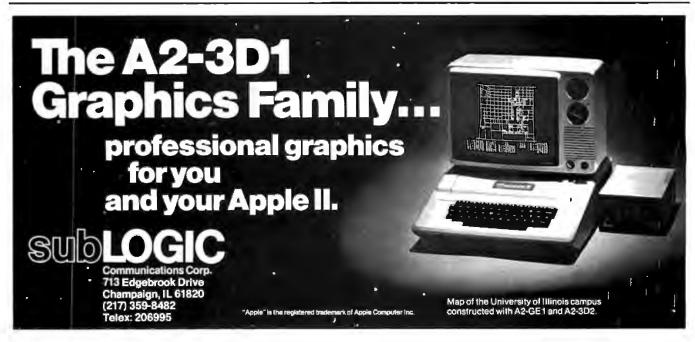
C/80, a compiler for the C programming language running under CP/M. 8-inch floppy disk, \$39.95. The Software Toolworks, 14478 Glorietta Dr., Sherman Oaks, CA 91423.

Edit-11 Ver. 2.02, a screenoriented text editor running under CP/M version 1.4 and the Oasis disk operating system. 8-inch floppy disk, \$50. C. C. Software, 2564 Walnut Blvd., #106, Walnut Creek, CA 94598.

This is a list of software packages that have been received by BYTE Publications during the past month. The list is correct to the best of our knowledge, but it is not meant to be a full description of the product or the forms in which the product is available. In particular, some packages may be sold for several machines or in both cassette and floppy-disk format; the product listed here is the version received by BYTE Publications.

This is an all-inclusive list that makes no comment on the quality

This is an all-inclusive list that makes no comment on the quality or usefulness of the software listed. We regret that we cannot review every software package we receive. Instead, this list is meant to be a monthly acknowledgment of these packages and the companies that sent them. All software received is considered to be on loan to BYTE and is returned to the manufacturer after a set period of time. Companies sending software packages should be sure to include the list price of the packages and [where appropriate] the alternate forms in which they are available.



Books Received

Advanced Programming and Problem Solving with Pascal, G.M. Schneider and S.C. Bruell. New York: John Wiley & Sons, 1981; 506 pages, 23 by 16 cm, hard-cover, ISBN 0-471-07876-X, \$23.95.

The Coattails of God, The Ultimate Spaceflight—The Trip to the Stars, Robert M. Powers. New York: Warner Books, 1981; 288 pages, 23 by 15.5 cm, hardcover, ISBN 0-446-51231-1, \$15.95.

The Computer Establishment, Katherine Davis Fishman. New York: Harper & Row, 1981; 468 pages, 23.5 by 15.5 cm, hardcover, ISBN 0-06-011283-2, \$20.95.

The Computerization of Society, A Report to the President of France, Simon Nora and Alain Minc. Cambridge, MA: The MIT Press, 1980; 186 pages, 19.5 by 13.5 cm, softcover, ISBN 0-262-64020-1, \$4.95.

Developing a Data Dictionary System, J. Van Duyn. Englewood Cliffs, NJ: Prentice-Hall, 1982; 204 pages, 23 by 15 cm, hard-cover, ISBN 0-13-204289-4, \$25.

Digital Logic Design and Applications, An Experimental Approach, Lyle B. McCurdy and Albert L. McHenry. Englewood Cliffs, NJ: Prentice-Hall, 1981; 122 pages, 27.5 by 21.5 cm, softcover, ISBN 0-13-212381-9, \$12.95.

Electronics and Instrumentation for Scientists, Howard V. Malmstadt, Christie G. Enke, and Stanley R. Crouch. Reading, MA: The Benjamin/Cummings Publishing Co., 1981; 543 pages, 23.5 by 21.5 cm, hardcover, ISBN 0-8053-6917-1, \$24.95.

Elements of Structured COBOL Programming, 2nd edition, Jack L. Olson and Wilson T. Price. New York: Holt, Rinehart and Winston,

1982; 380 pages, 27 by 21 cm, softcover, ISBN 0-03-058052-8, \$16.95.

50 More Programs in BASIC for the Home, School & Office, 2nd edition, Jim Cole. Woodsboro, MD: Arcsoft Publishers, 1981; 96 pages, 21 by 13.5 cm, softcover, ISBN 0-86668-502-2, \$9.95.

Locate, Law Office Computer Applications, Techniques and Equipment, 1981 edition, Bruce D. Heintz and Lavina S. Dill, eds. Chicago, IL: American Bar Association, 1981; 27 by 21 cm, 113 pages, softcover, ISBN 0-89707-045-3, \$28.

The Logic Design of Computers, M. Paul Chinitz. Indianapolis, IN: Howard W. Sams & Co., 1981; 413 pages, 13 by 21 cm, softcover, ISBN 0-672-21800-3, \$15.95.

Microprocessor Operating Systems, John Zarrella, ed. Suisun City, CA: Microcomputer Applications, 1981; 166 pages, 22.5 cm by 15 cm, softcover, ISBN 0-935230-03-3, \$11.95.

Natural Language Information Processing, A Computer Grammar of English and Its Applications, Naomi Sager. Reading, MA: Addison-Wesley Publishing, 1981; 399 pages, 21.5 by 23.5 cm, hard-cover, ISBN 0-201-06769-2, \$37.50..

Office Automation: The Productivity Challenge, Dimitris N. Chorafas. Engle-

wood Cliffs, NJ: Prentice-Hall, 1982; 272 pages, 23.5 by 13 cm, hardcover, ISBN 0-13-631028-1, \$24.95.

101 Pocket Computer Programming Tips & Tricks, Jim Cole. Woodsboro, MD: Arcsoft Publishers, 1981; 128 pages, 21 by 13.5 cm, softcover, ISBN 0-86668-004-7, \$7.95.

Understanding Your VIC Volume 1: BASIC Programming, David E. Schultz. Los Alamos, NM: Total Information Services (POB 921), 1981; 140 pages, 27 by 21 cm, softcover, ISBN none, \$11.95.■

This is a list of books received at BYTE Publications during this past month. Although the list is not meant to be exhaustive, its purpose is to acquaint BYTE readers with recently published titles in computer science and related fields. We regret that we cannot review or comment on all the books we receive, instead, this list is meant to be a monthly acknowledgment of these books and the publishers who sent them.



```
IF CH IN C'Q', 'a'] THEN EXIT(EDIT);
  WRITELNS
  EDIT_WHAT := CH;
END; {edit_what}
PROCEDURE ED_SEQUENT (FIRST, LAST: TLINE_NUM);
{edit TLINES[first] to TLINES[last] unless the line is a calculated line}
VAR
              LN : TLINE_NUM;
 BEGIN
  FOR LN := FIRST TO LAST DO IF NOT (LN IN CALCSET)
         THEN BEGIN
                EDIT ... TI. INE (LN) ;
                GOTOXY(10,23);
                WRITELN('ENTER KESC> TO CONTINUE KQ> TO QUIT');
                REPEAT
                 READ(CH)
                UNTIL OH IN C'Q','a', CHR(ESC)];
                IF CH IN ['Q','a'] THEN EXIT(ED_SEQUENT);
              END;
END; {ed_sequent}
PROCEDURE ED_INDIVIDUAL;
{select a single line to edit}
                  OK : BOOLEAN;
  VAR
  BEGIN
    REPEAT
      CLEARS
      WRITE('ENTER LINE NUMBER TO BE CHANGED O) for help ');
      REPEAT
        OK := FALSE;
        INT := READINT(2);
        IF INT = 0 {a request for help}
          THEN BEGIN
                 CLEAR
                 CASE EDIT_CHAR OF
                   'A','a' : FOR LN := MINALINE TO MAXALINE DO
                               IF NOT (LN IN CALCSET)
                                  THEN WRITE((LN-MINALINE+1):8,TITLES[LN]:32);
                            : FOR LN := MINBLINE TO MAXBLINE DO
                    'B', 'b'
                               IF NOT (LN IN CALCSET)
                                  THEN WRITE((L'N-MINBLINE+1):8,TITLESCLN3:32);
                    'Z','z' : FOR LN := 8 TO MAXTLINE DO
                               IF NOT (LN IN CALCSET)
                                  THEN WRITE(LN:8, TITLESCLN3:32);
                   END; {case}
                 WRITELNS
               END(if int=0);
        CASE EDIT_CHAR OF
                                {convert from form line number to array index}
                   # BEGIN
                       IF (INT > 0) AND. (INT <= 41) THEN OK := TRUE;
                       LN := INT + MINALINE-1;
                     END;
          'B', 'b'
                    # BEGIN
                       IF (INT > 0) AND (INT <= 8) THEN DK := TRUE;
                       LN := INT + MINBLINE-1;
                     ENI;
          12','z'
                   # BEGIN
                       IF (INT > 7) AND (INT <= MAXTLINE) THEN REGIN.
                                                                   OK := TRUE;
                                                                   LN := INT;
                                                                 END; {if}
```

```
END; {case of Z}
          END; {case}
     UNTIL OK;
                       {a valid line number has been requested}
      IF (LN IN CALCSET)
      THEN BEGIN
          CLEAR:
          WRITELN('LINE ',INT,' IS A CALCULATED VALUE AND MAY NOT BE EDITED ');
          WAIT
      ELSE EDIT_TLINE(LN);
      GOTOXY(O,O); EEOL;
     WRITE('
                   DO YOU WANT TO --> C)ontinue Q)uit');
      REPEAT
        READ(CH)
     UNTIL (CH IN ['C', 'c', 'Q', '@'])
   UNTIL CH IN ['Q', 'a'];
END; {individual}
BEGIN(edit)
 REPEAT
    CLEARS
   EDIT_CHAR := EDIT_WHAT;
                                       {what form should be edited?}
    IF EDIT_CHAR IN ['F','f']
     THEN EDIT_SPEC
      ELSE BEGIN
             CLEAR;
             WRITE(' EDIT COMMAND-->');
             WRITE(' S)equentially
                                        I)ndividual lines (2)uit ();
             REPEAT
               READ(CH)
             UNTIL (CH IN ['S','s','I','i','Q','@']);
             CASE CH OF
               'S','s' : BEGIN
                           CASE EDIT_CHAR OF
                              'A','a'
                                           : ED_SEQUENT(MINALINE, MAXALINE);
                              'B', 'b'
                                           : ED_SEQUENT(MINBLINE, MAXBLINE);
                              1Z', z'
                                           : BEGIN
                                               ED_SEQUENT(8, MAXTLINE);
                                             END#
                             END; {case}
                         END;
                'I','i' : ED_INDIVIDUAL;
              END; {case}
      END; {else}
   UNTIL CH IN ['Q','a']
END; {edit}
```

SEGMENT PROCEDURE CALCULATE;

```
UAR
              LN : TLINE_NUM;
 PROCEDURE AD(FIRST, SECOND, SUM: TLINE_NUM);
  {add two lines}
                  LN : TLINE_NUM;
    VAR
    BEGIN
      TLINESCSUMJ.HUS := TLINESCFIRSTJ.HUS + TLINESCSECONDJ.HUS;
      TLINES[SUM].WIF := TLINES[FIRST].WIF + TLINES[SECOND].WIF;
      TLINESCSUMD. TOT := TLINESCFIRSTD. TOT + TLINESCSECONDD. TOT;
    END;
  PROCEDURE ADD(START, FINISH, SUM: TLINE_NUM);
  {add several sequential lines}
    VAR
                  LN : TLINE_NUM;
    BEGIN
      FOR LN := START TO FINISH DO
        BEGIN
         TLINESCSUMJ.HUS := TLINESCSUMJ.HUS + TLINESCLNJ.HUS;
         TLINES(SUM).WIF := TLINES(SUM).WIF + TLINES(LN).WIF;
         TLINESCSUMD. TOT := TLINESCSUMD. TOT + TLINESCLND. TOT;
        END;
    END;
  PROCEDURE SUB(FIRST, SECOND, DIF: TLINE_NUM);
  (subtract two lines)
                   LN : TLINE_NUM;
     BEGIN
       TLINESCRIFT.HUS := TLINESCFIRSTI.HUS - TLINESCSECONDI.HUS;
       TLINESCDIF].WIF := TLINESCFIRST].WIF - TLINESCSECOND].WIF;
       TLINESCDIFI.TOT := TLINESCFIRSTI.TOT - TLINESCSECONDI.TOT;
     END;
PROCEDURE TAXCALC;
  {the tax calculation is done here}
  VAR
        CH : CHAR;
        HTAXABLE, WTAXABLE, TTAXABLE : LONGINT;
        XFS : FILING_STATUS;
        I : 1..16;
        WHICH : LONGINT;
  PROCEDURE GETTAX(TT : TAX...TABLE;
                     TAX_ABLE : LONGINT ; VAR TAX : LONGINT; W : OWNER);
  {set the factors from the taxtable and do calculate the tax}
   BEGIN
                               {search the array for the correct tax bracket}
     FOR I := 1 TO 16 DO
       IF(TAX_ABLE > TAXRAY(TT,I,LOWER]) AND (TAX_ABLE <= TAXRAY(TT,I,UPPER])
           THEN BEGIN (bracket found now calculate tax)
                   TAX : TAXRAYCTT, [, BASE] + (TAXRAYCTT, [, PERCENT])*
                                         ((TAX_ABLE-TAXRAYFTT; J; LOWER]) DIV 100);
                   MAX_TAXEW3 := TAXRAYETT, I, PERCENT3;
                   EXIT(GETTAX)
                ENDI
    END: {settax}
  BEGIN
```

```
FSTAT := TLINES[7].FS; {set filing status}
  IF FSTAT IN [2,3]
   THEN BEGIN
                    {detexemptions for married}
           HTAXABLE := TLINES[34].HUS - 100000;
           WTAXABLE := TLINES[34].WIF - 100000;
           TTAXABLE := TLINES[34].TOT - 100000 * (TLINES[7].EXEM);
           {calculate total as _joint return use tax table Y}
           GETTAX(Y,TTAXABLE,TLINESC357,TOT,T_OWN);
           REFEAT
             CLEAR?
             WRITELN('SHOULD THE INDIVIDUAL TAXES BE CALCULATED ');
             WRITE(
                            AS M)MARRIED FILING SEPARATELY U)UNMARRIED ');
             READ(CH)
           UNTIL CH IN ['M', 'm', 'U', 'u'];
           ME OH IN EYUYAYMYD
             THEN BEGIN
                    {calculate taxes for husband and wife as if they
                                                  could file as individuals}
                    GETTAX(X, HTAXABLE, TLINES[35], HUS, H_OWN);
                    GETTAX(X, WTAXABLE, TLINES[35].WIF, W_OWN);
                  END
             ELSE BEGIN
                   {calculate taxes for husband and wife as filing severate}
                    GETTAX(YS, HTAXABLE, TLINES[35], HUS, H_OWN);
                    GETTAX(YS, WTAXABLE, TLINES[35], WIF, W_OWN);
                  END?
          END(if married)
    ELSE BEGIN
                   {set exemptions for unmarried}
           TTAXABLE := TLINES[34].TOT - 100000 * (TLINES[7].EXEM);
           CASE FSTAT OF
                  1 CETTAX(X,TTAXABLE,TLINES[35],TOT,T_OWN);
                  4 : GETTAX(Z,TTAXABLE,TLINES[35],TOT,T_OWN);
                  5 : GETTAX(Y,TTAXABLE,TLINES[35].TOT,T_OWN);
            END; {case}
          END?
 END; {calctax}
PROCEDURE LINEA40;
{compensate for zero base }
  REGIN
    IF TLINES[7].FS IN [2,3]
      THEN BEGIN
             TLINES[106].HUS :: 170000;
             TLINES[106].WIF := 170000;
             TL.INES[106].TOT := 340000;
           END
     ELSE CASE TLINES[7].FS OF
                    : TLINES[106].TOT := 230000;
                5
                     : TLINES[106].TOT := 340000;
             END; {case}
  END;{lines40}
PROCEDURE CALSCH_A;
{do the calculations required by schedule A}
REGIN
  TLINES[69].HUS := TLINES[31].HUS DIV 100; {line A 3}
  TLINES[69].WIF :: TLINES[31].WIF DIV 100; {line A 3}
  TLINES[69].TOT := TLINES[31].TOT DIV 100; {line A 3}
  SUB(68,69,70);
                                             {line A 4}
```

```
WITH TLINES[70] DO
        BEGIN
          IF HUS < 0 THEN HUS := 0;
                                                 {line A 4}
          IF WIF < 0 THEN WIF := 0;
                                                 {line A 4}
          IF TOT < 0 THEN TOT := 0; .
                                                  {line A 4}
        END;
      ADD(70,72,73);
                                                  {line A 7}
      TLINESC74].HUS := 3*TLINESC69].HUS;
TLINESC74].WIF := 3*TLINESC69].WIF;
TLINESC74].TOT := 3*TLINESC69].TOT;
                                                  {line A 8}
                                                 {line A 8}
                                                  {line A 8}
      SUB(73,74,75);
                                                  {line A 9}
      WITH TLINES[75] DO
        BEGIN
          IF HUS < 0 THEN HUS : 0;
                                                 {line A 9}
          IF WIF < 0 THEN WIF :- 0;
                                                  {line A 9}
          IF TOT < 0 THEN TOT :: 0;
                                                  {line A 9}
        END;
      AD(67,75,76);
                                                  {line A 10}
      TLINES[99] := TLINES[76];
                                                  {line A 33}
      ADD(77,81,82);
                                                  {line A 16}
      TLINESC1003 : TLINESC823;
                                                  {line A 34}
      ADD(83,85,86);
                                                  {line A 20}
      TLINES[101] := TLINES[86];
                                                  {line A 35}
      ADD(87,89,90);
                                                  {line A 24}
      TLINES[102] := TLINES[90];
                                                  {line A 36}
                                                  {line A 27}
      SUB(91,92,93);
      IF TLINES[93].HUS < 10000 THEN TLINES[94].HUS := TLINES[93].HUS
                                 ELSE TLINES[94].HUS :- 10000;
      IF TLINES[93].WIF < 10000 THEN TLINES[94].WIF : TLINES[93].WIF
                                 ELSE TLINES[94].WIF := 10000;
      IF TLINES[93].TOT < 10000 THEN TLINES[94].TOT :- TLINES[93].TOT
                                 ELSE TLINES[94].TOT := 10000;
      SUB(93,94,95);
                                                  Kline A 29>
      TLINES[103] := TLINES[95];
                                                  {line A 37}
      ADD(96,97,98);
                                                  {line A 32}
      TLINES[104] := TLINES[98];
                                                  {line A 38}
      ADD(99,104,105);
                                                  {line A 39}
      LINEA40;
      SUB(105,106,107);
                                                  {line A 41}
      TLINES[33] := TLINES[107];
     END; {calsch_a}
  PROCEDURE CALSCHIB?
    REGIN
      TLINESCMINBLINE + 13 := TLINESCMINBLINE3;
                                                         {line R 1}
      TLINESC93 : TLINESCMINBLINE + 13;
      TLINESUMINBLINE + 33 := TLINESUMINBLINE + 23;
                                                         {line B 3}
      ADD(MINBLINE+4, MINBLINE+5, MINBLINE+6);
                                                          {line R 6}
      SUB(MINBLINE+3, MINBLINE+6, MINBLINE+7);
                                                          {line B 7}
      TLINES[10] := TLINES[MINBLINE+7];
    ENDO
BEGIN (calculate)
  FOR LM := 8 TO MAXLINE DO IF LN IN CALCSET THEN BEGIN
                                                       TLI NES[LN].HUS : 0;
                                                       TLINES[LN].WIF := 0;
                                                       TLINESCLNJ.TOT := 0;
                                                     ENI)
 CALSCHIBE
 WITH TLINESCION DO
 BEGIN
                              {dividend exclusion}
    HUS := HUS - 10000;
```

```
IF HUS < O THEN HUS :- O;
    WIF :- WIF - 10000;
    IF WIF < 0 THEN WIF :- 0;
    TOT := HUS + WIF;
  END;
ADD(8,21,22);
                               {total income}
                               {total adjustments}
ADD(23,29,30);
                               {adjusted sross}
SUB(22,30,31);
TLINESC323 := TLINESC313;
                               {transfer 31 to 32}
CALSCH_A;
                               {income for start of tax calculation}
SUB(32,33,34);
TAXCALC;
ADD(35,36,37);
                               {total taxes}
                               {total credits}
ADD(38,45,46);
SUB(37,46,47);
                               {balance}
ADD(47,53,54);
                               {balance}
ADD(55,61,62);
                               {total tax payments}
SUB(54,62,63);
                               {taxes-tax payments}
IF TLINES[63].HUS < 0
   THEN TLINES[63].HUS := -1 * TLINES[63].HUS
                                                      - {overpayment}
   ELSE BEGIN
          TLINES[66].HUS := TLINES[63].HUS;
                                                        {balance due}
          TLINES[63].HUS : 0;
        END;
IF TLINES[63].WIF < 0
   THEN TLINES[63].WIF := -1 * TLINES[63].WIF
          TLINES[66].WIF := TLINES[63].WJF;
          TLINES[63].W(F := O;
        END;
IF TLINES[63].TOT < 0
   THEN TLINES[63].TOT := -1 * TLINES[63].TOT
   ELSE REGIN
          TLINES[66].TOT := TLINES[63].TOT;
          TLINES[63].TOT := 0;
        ENI:
```

FOR LN := 8 TO MAXLINE DO IF LN IN CALCSET THEN TLINESCLNJ.IPTR :: NJL END; {calculate}

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Text continued from page 162:

four tax tables (X, Y, YS, and Z), I made the complete set of tables the array TAXRAY, which has four tables \times the previously defined two-dimensional array FACTORARRAY.

Program Structure

I organized FIT in a main body, 11 support procedures and one support function, five segment procedures (defined later), and two separate programs. I'll begin by describing the general relationships among all these elements of FIT, then give more detail about each. Listing 6 contains the main body and the support procedures. The main body, at the end of listing 6, calls the five segment procedures START (listing 7), EDIT (listing 8), CALCULATE (listing 9), PRINTER (listing 10), and RW (listing 11). The segment procedures and the main program use the support procedures to perform basic tasks. To reduce FIT's memory requirements, I used the separate programs TAXNAMES (listing 12) and TAX-TABLE (listing 13) to create the arrays TITLES and TAX-RAY respectively, and to write these arrays to disk files (LINENAMS.FTAX for TITLES and FACTORS.FTAX for TAXRAY).

The Main Body and the Support Procedures

At the beginning of listing 6 are all the declarations, most of which have already been described. I declared all the support procedures with the FORWARD statement so that each support procedure can be called by other procedures before it is formally defined. Otherwise, the compiler would reject each such call as use of an undeclared identifier. The support procedures and one support function and their tasks are as follows:

•PROCEDURE MEM displays on the console the current amount of memory available.

- PROCEDURES CLEAR, ELINE, EEOL, and EEOS perform screen manipulations.
- •PROCEDURE WAIT halts the program to allow inspection of output.
- PROCEDURE PDOL converts a long integer into a printable string with two decimal places.
- PROCEDURE CENTER centers output on the screen.
- •PROCEDURE READDOL prompts for input of dollars and cents, checks for errors, and converts input to a long integer.
- •PROCEDURE NAMER prompts for entry of a string from the keyboard, reads the input, and checks the input for errors.
- PROCEDURE LINE prints on the screen a line of one repeated character.
- FUNCTION READINT prompts for entry of an integer, reads the input, and checks it for errors.

When you execute FIT, the main program (found at the end of listing 6) calls the segment procedure START (listing 7), which sets up the program's variables, and reads LINENAMS.FTAX and FACTORS.FTAX. Then, the main program sets up FIT's now familiar main prompt line:

FIT COMMAND--> P)rint E)dit C)alculate R)ead W)rite O)uit

If you input P, the program goes to segment procedure PRINTER; E takes you to segment procedure EDIT; C, to segment procedure CALCULATE; R, to segment procedure RW (to read in a data file); W, to segment procedure RW (to write a file).

The Segment Procedures

A segment procedure is an overlay; that is, each segment procedure occupies memory space previously used



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by a different part of the program. As soon as the segment procedure finishes running, the space it occupied is released; most of the time, the segment procedure resides on the disk. At any time during the execution of a program that uses segment procedures, the memory required is only enough space for the code of the main body, the global variables, and the segment (if any) currently in use. The time required to fetch a segment from disk into memory is insignificant; you only know it's happening because you hear the disk access.

The structure of FIT lends itself to the use of segment procedures because there is little movement between segments. Segmenting saves about 10K bytes of RAM during execution. As a result of my efforts to conserve memory, FIT should work with a 48K-byte system. I have a 56K-byte system and have always had at least 8.5K bytes free while running FIT.

If you know chaining in BASIC, you will see that these segment procedures give a similar result. However, segment procedures are much faster than chaining.

I also took advantage of segmenting to make my editing of FIT easier by dividing its source code into several files. At the end of the declarations in listing 6, I set up a text file for the source code for each segmented procedure. At compile time, I used the include directive to the compiler; this directive caused the compiler to read all the indicated source files and produce a single file of compiled code, FIT.CODE.

I have already described the segment procedure START. Now I'll give some details about the other segment procedures.

Segment Procedure EDIT

The most complex segment procedure is EDIT (listing 8). The main body of EDIT begins by calling EDIT-

CHAR, which is a function that returns a character designating which tax form you want to edit. EDIT then asks you to choose either individual or sequential line editing. A CASE statement uses the selected character to call either ED-INDIVIDUAL or ED-SEQUENT. If ED-SEQUENT is called, the main body of EDIT passes the range of line numbers to be edited to the procedure ED-SEQUENT. Both of the ED- procedures call the procedure EDIT-TLINE to do the real editing. ED-SEQUENT steps from the lowest line number to the highest, checks to see if the line number is in CALCSET (the set of calculated lines, which can't be edited), and, if not, calls EDIT-TLINE.

ED-INDIVIDUAL gets the desired line number from operator input or, if you ask, provides help by displaying a list of line numbers and line names. ED-INDIVIDUAL converts the input line number to the correct array index, then calls EDIT-TLINE.

EDIT-TLINE, the workhorse of the Edit function, operates on the tax line whose number is passed to it. EDIT-TLINE's first step is to see if the pointer in TLINES[LN], the record for the given line number, points to anything. If not, there are no previous entries for this line number. If the pointer does point to something, the function VIEWITEM displays the ITEM on the screen and allows editing or deletion of the ITEM. VIEWITEM also returns to EDIT-TLINE the pointer to the next ITEM.

Providing the ability to delete an ITEM complicates the code. In order to delete a record from a linked list, you assign the pointer in the record to the pointer in the parent of the record. As a result, the deleted record is bypassed. Since, in this case, the first pointer is in a TLINES record and all other pointers are in ITEM records, we have to keep track of which record is the parent and which record type the parent belongs to. I used two variables for this purpose. The Boolean variable

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TL is true if the parent is a TLINES record; the pointer LASTPTR points to the parent if the parent is an ITEM record. The procedure VIEWITEM performs the deletion following an IF statement conditioned on the variable TL.

When all the existing ITEMs have been presented to you, EDIT-TLINE offers the option to add new ITEMs. A Repeat loop provides for continuing entry of new ITEMs. When they all have been entered, EDIT-TLINE calls the procedure SUMS to add the amounts of all the ITEMs and put the sums in TLINE[LN]. Then EDIT-TLINE calls VIEW to display the data contained in TLINE[LN]. Finally, EDIT-TLINE exits to either ED-INDIVIDUAL or ED-SEQUENT.

Segment Procedure CALCULATE

This segment procedure, shown in listing 9, is straightforward. For any calculation for a given line, if the filing status is married, three calculations are needed—one each for HUS, WIF, and TOT. To simplify additions and subtractions, I wrote three procedures: AD, ADD, and SUM. These procedures are passed the line number to act upon and then do the three calculations (on HUS, WIF, and TOT).

The calculations are done in the following order. First, Schedule B is calculated and its results placed in lines 10 and 11 of form 1040. The dividend exclusion is then applied to line 10. Form 1040 is then calculated to line 32 and CALSCH-A is called to calculate Schedule A and place the results in line 33 of form 1040. Line 34 is calculated and PROCEDURE TAXCALC is called.

PROCEDURE TAXCALC adjusts the taxable income for the number of dependents, selects the correct tax table based on the filing status, and calls PROCEDURE GETTAX.

PROCEDURE GETTAX searches the tax table for the correct bracket, calculates the tax, and inserts it in line 35.

Lines 37-63 of form 1040 are next calculated. Based on the value of line 63, either an overpayment or an underpayment exists. The balance of the lines is adjusted accordingly.

Segment Procedure PRINTER

The main body of PRINTER, shown in listing 10, begins by initializing three sets of TLINE-NUMs. These three sets contain the TLINE-NUMs that:

- •have a separator line printed after them (SLINESET)
- •have a summation line printed after them (DLINESET)
- are the last line written to a screen (SPAGESET)

The main body of PRINTER also contains the Boolean variable SCREEN, which determines whether the output goes to the screen or the printer. The Boolean variable DETAIL determines if all the ITEMs are to be printed for each line, or just the totals.

Segment Procedure RW

The segment procedure RW, shown in listing 11, contains the code that reads and writes disk files. The data are stored on disk in two files. One file contains the TLINE records; the other contains the ITEM records. The two files have the same file identified with ".LINE" or ".ITEM" appended to the end of the name.

The procedure to write the data to file is WRITER, which prompts for the name of the file name to be written, adds ".LINE", and calls WRITE-TLINES. WRITE-TLINES calls LOOKUP, which checks to see if a file with the same name is already on the disk. If the file name already exists, you are asked if the file should be rewritten.

After WRITE-TLINES returns control to WRITER,

Text continued on page 400

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Listing 10: The FIT segment procedure PRINTER. This procedure prints FIT's output. The procedure DETAIL_PRINT prints all the entries for each line, as well as the totals. The procedure PRINT prints just the total for each line.

```
SEGMENT PROCEDURE PRINTER;
  VAR
                DETAIL : BOOLEAN;
                LINES : INTEGER;
                PRINT_WHAT, CH1 : CHAR;
 PROCEDURE PRINT .. DATE;
  VAR
                CMONTH : STRINGE33;
  BEGIN
    CASE MONTH OF
         1: CMONTH := 'Jan';
         2: CMONTH := 'Feb';
         3: CMONTH := 'Mar';
         4: CMONTH := 'Apr';
         5: CMONTH := 'May';
         6: CMONTH : " /June';
         7: CMONTH := 'Juls';
         8: CMONTH := 'Aus';
         9: CMONTH := 'Sept';
         10: CMONTH := 'Oct';
         11: CMONTH := 'Nov';
         12: CMONTH := 'Dec'
       END;
     WRITELN(P, DAY: 2, ' ', CHONTH,' ', '19', YEAR: 2);
   END;
  PROCEDURE HEADING(TITLE : FILENAME);
  {prints heading}
    BEGIN
      LINE('*',79);
                         {Print a line of 79 '*'s}
                         {doto next line}
      WRITELN(F);
      WRITE(P,TLINES[6].NAME);
      WRITE(P, 'TAX YEAR ': (44-LENGTH(TLINES[6], NAME)));
      WRITELN(P,TLINES[7].TAXYEAR:4,TITLE :29);
      WRITE(F, 'FILING STATUS ');
      CASE TLINES[7].FS OF
        1 # WRITE(P,'1');
        2 : WRITE(P,'2');
        3 : WRITE(P,'3');
        4 : WRITE(P,'4');
        5 # WRITE(P, '5');
       END;
      WRITE(P,'
                             EXEMPTIONS ();
      WRITE(P,TLINESC73,EXEM, ' ':27);
      PRINT_DATE;
      LINE('*',79);WRITELN(F);
      IF FSTAT IN [2,3]
        THEN WRITELN(P) / ':40,' HUSBAND ':12,'
                                                   WIFE
                                                           1:12,1
                                                                   TOTAL
                                                                           (112)
        ELSE WRITELN(P);
      LINES := 4;
    END; {heading}
PROCEDURE DETAIL_PRINT(FIRST, LAST : TLINE ... NUM; TITLE : FILENAME);
  {prints items by tax line}
  VAR
        LN : TLINE_NUM;
```

```
OBJ, HDOL, WDOL, TDOL: STRING[10];
      NEXTETR : POINTER;
BEGIN
 IF SCREEN THEN CLEAR;
 HEADING(TITLE);
 FOR LN := FIRST TO LAST DO
   IF TLINESCLND.IPTR <> NIL {do not bother unless line has an ITEM}
      THEN BEGIN
             CASE FRINT WHAT OF
                                                {print form line number}
                'A','a'
                          # WRITE(P,(LN-MINALINE+1);2);
                'B', 'b'
                             # WRITE(P,(LN-MINBLINE+1):2);
                'Z', 'z'
                           * # WRITE(P,(LN):2);
             END; {case}
             WRITELN(P, ' ', TITLESCLNI);
                                               {print name of line}
                                                {increment the line counter}
             LINES := LINES + 1;
             NEXTETR := TLINESCLNJ.IPTR;
                                               {first rointer}
                                                {until the last ITEM}
             WHILE NEXTPIR <> NIL DO
               BEGIN
                 WITH NEXTETR" DO
                   BEGIN
                     WRITE (PINAME) #
                     PDOL(AMT, OBJ);
                                               {convert longint to string}
                     CASE WHOSE OF
                       H_OWN : BEGIN
                                   WRITE(F, 'HUS':(25-LENGTH(NAME)));
                                   WRITELN(P,08J:25)
                                 END)
                        M_OWN
                               : BEGIN
                                   WRITE(F;'NIF':(25-LENGTH(NAME)));
                                   WRITELN(F,OBJ:38)
                                 ENDI
                        T_OWN
                              * BEGIN
                                   WRITE(P, 'YOY': (25-LENGTH(NAME)));
                                   WRITELN(P,O8J:51)
                                 END;
                      END; {case}
                   LINES := LINES + 1;
                   NEXTPTR : " NPTR:
               END; {with}
           END; {while}
    WITH TLINESCLMJ DO
                                                {now summarize the line}
      BEGIN
        PDOL(HUS, HDOL);
                                                {convert longint to string}
                                                {convert longint to string}
        PDOL(WIF, WDOL);
        PDOL(TOT, TDOL);
                                                {convert lungint to string}
        IF FSTAT IN [2,3]
          THEN WRITELN(F,'TOTAL', HDOL: 45, WDOL: 13, TDOL: 13)
          ELSE WRITELN(P, 'TOTAL', ' ':58, TDOL:13);
        WRITELN(P);
        LINES := LINES + 1;
                                                {increment the line counter}
      END; {with tlines}
 IF SCREEN
   THEN IF (16 - LINES) < 0
                                                {test line counter}
      THEN BEGIN
             WAIT
             CLEAR
             LINES := 0;
           END
    ELSE IF (54 - LINES) < 0
                                                {test line counter}
      THEN BEGIN
             WRITE(F,CHR(12));
             HEADING(TITLE)
           END;
```

```
END; {for}
IF SCREEN THEN WAIT;
 WRITE(P,CHR(12));
END; {detail_print}
PROCEDURE PRINT(FIRST, LAST : TLINE_NUM; TITLE : FILENAME);
  CONST
          S1='
  VAR
        LN : TLINE_NUM;
        HDOL, WDOL, TDOL: STRING[10];
  BEGIN
    IF SCREEN THEN CLEAR;
    HEADING(TITLE);
    FOR LN := FIRST TO LAST DO
      WITH TLINESCLNI DO
        BEGIN
          FDOL(HUS, HDOL);
          PDOL(WIF, WDOL);
          PDOL(TOT, TDOL);
          CASE PRINT_WHAT OF
            'A', 'a'
                        # WRITE(F,(LN-MINALINE+1);2);
            'B', 'b'
                         # WRITE(P,(LN-MINBLINE+1):2);
            'Z','z'
                         : WRITE(P,(LN):2);
            END;
          WRITELN(P, ' ',TITLES[LN],' ':5,HDOL:12,WDOL:12,TDOL:12);
          IF (LN IN DLINESET) THEN WRITELN(P,S1:79);
                                                           {print dashed line}
          IF (LN IN SLINESET)
                                                           {print separator}
                 THEN BEGIN
                        LINE('=',79);
                        WRITELN(P);
                      END;
          IF ((SCREEN) AND (LN IN SPAGESET)) {do not overfill the screen}
            THEN BEGIN
                   WAIT;
                    CLEAR;
                 END;
          IF (NOT SCREEN) AND (LN=37)
                                                  {do not overfill the rase}
            THEN BEGIN
                   WRITE(P,CHR(12));
                    HEADING(TITLE);
                  END;
       END; {with}
  IF PRINT_WHAT IN ['Z','z']
        THEN BEGIN
               WRITE(P, ' MAXINUM TAX BRACKET', ' '120);
                WRITELN(P,MAX_TAXCH..OWN]:12,MAX..TAXEW_OWN]:12,MAX_TAXET_OWN]:12)
             END:
  IF SCREEN THEN WAIT;
  WRITE(P,CHR(12))
END; {print}
BEGIN(printer)
    {a separator line is printed after a line in SLINESET}
    SLINESET := [22,30,37,47,54,62,66,76,82,86,90,95,98,107,109,111];
    {a dashed line is printed after a line in SLINESET}
    DLINESET := [21,29,33,36,45,46,53,61,69,72,75,81,85,89,92,94,97,106,113];
    {last lines on a SCREEN page are in SPAGESET}
                                                               Listing 10 continued on page 400
```

```
SPAGESET : □ [22,37,54,76,90,98];
 CLEAR;
 memi
 REPEAT
  DETAIL := FALSE;
                                       {control to print detail}
  CLEAR
  WRITE ('PRINTER COMMAND --> A)sched A
                                           B)sched B
                                                       Z) form 1040 ');
  WRITE(' #)for detail
                           Q)uit');
  REPEAT
    READ(PRINT_WHAT);
    IF PRINT_WHAT = '#' THEN DETAIL := TRUE
  UNTIL ( PRINT_WHAT IN ['A','a','B','b','Z','2','Q','a']);
  IF NOT ( PRINT_WHAT IN ['Q','a'])
    THEN BEGIN
     WRITELN;
     WRITE('DO YOU WANT TO OUTPUT TO --> P)rinter S)creen
      REPEAT
        READ(CH1)
      UNTIL CH1 IN ['P','p','S','s'];
      IF CH1 IN ['S', 's']
        THEN BEGIN
               SCREEN := TRUE;
               REWRITE(P, 'CONSOLE:')
             END
        ELSE BEGIN
               SCREEN := FALSE;
               REWRITE(P, 'PRINTER:')
             END;
      IF DETAIL
        THEN CASE PRINT_WHAT OF
                  'A','a'
                           : DETAIL_PRINT(67,107,'SCHEDULE A');
                  'B', 'b'
                            : DETAIL_PRINT(108,115,'SCHEDULE B');
                 12','z'
                            : DETAIL_PRINT(8,66,'FORM 1040');
                END
        ELSE CASE PRINT_WHAT OF
                  'A', 'a'
                           PRINT(67,107,'SCHEDULE A');
                  'B', 'b'
                            # PRINT(108,115,'SCHEDULE B');
                 121,1z1
                            : PRINT(B,66,'FORM 1040');
                END;
      END(if);
CLOSE(P);
UNTIL PRINT_WHAT IN ['Q','a'];
END; {printer}
```

Text continued from page 396:

WRITE-ITEMS is called. This procedure scans the TLINEs for the existence of ITEMs and writes them to "FILENAME.ITEM" when found.

READER reads the ".LINE" and ".ITEM" files into the array and linked lists, respectively. The array read is straightforward. When the ITEMs are read in, they must be linked to the proper list, which begins with the TLINE[LN]. Since each ITEM contains the number of the TLINE[LN] to which it belongs, the correct starting point can be found. The list is then traversed to the end and the ITEM inserted. Since these lists are short, the whole operation goes quickly. If a long list were involved, an array could be created to hold the pointer to the last ITEM in each list; that would allow direct insertion without traversing the list.

Closing Comments

I think you will find FIT a useful program and the basis for other useful programs. Its framework will permit you to add other tax forms with relative ease. If another federal form interests you, try adding it to FIT. It won't take long.

You may also be able to adapt FIT to do your state taxes. I live in Delaware, which has a tax form similar to the federal form. I had no difficulty using FIT as the basis for developing a similar program for the state form.

Without modification, FIT should help you adjust your federal withholding tax, compile thorough and convenient tax records, and examine the tax consequences of different investment strategies. I hope you find FIT helpful in all these ways.

```
SEGMENT PROCEDURE RW(CH : CHAR); {reads or whites Files of THINES and ITEMS >
VAR
            * FILE OF TLS;
            # FILE OF ITEM?
 FUNCTION LOOKUP(FN:STRING):BOOLEAN;
  {checks to see if file is on disk}
   VAR
            IOR:0..15;
    BEGIN
      {$I-}
      RESET(P,FN);
      IOR:=IORESULT;
      CLOSE (F) ;
      {$I+}
      IF (IOR=0)
       THEN LOOKUP: #TRUE
       ELSE BEGIN
              LOOKUP:=FALSE;
              IF (IOR<>10) THEN WRITELN('IORESULT FOR FILE ',FN,' IS ',IOR);
            END; {else}
    END; {lookup}
  PROCEDURE READER;
                                  {reads files of TLINES and ITEMs}
    CONST
                   FN1='.LINE';
                                   FN2='.ITEM';
    VAR
                   ST : STRING;
                   FN : FILENAME;
    PROCEDURE READ_TLINES(FN : FILENAME) #
      VAR
            I : TL.INE_NUM;
      BEGIN
        IF NOT LOOKUP(FN)
          THEN BEGIN
                 CLEAR;
                  GOTOXY(12,20);
                  WRITELN('FILE ',FN,' NOT FOUND');
                  WATT;
                 EXIT(READ_TLINES)
               END;
        RESET(FL,FN);
        TLINES := FL^;
        CLOSE(FL);
        FOR I := 8 TO MAXLINE DO TLINESCIJ.1PTR := N1L;
        WRITELN('FILE ',FN,' REAU ');
      END;
    PROCEDURE READ_ITEMS(FN : FILENAME);
      VAR
            CH : CHAR;
            PT, NEWPT : POINTER;
```

```
BEGIN
      IF NOT LOOKUP(FN)
        THEN BEGIN
                CLEAR; GOTOXY(10,10);
                WRITE('FILE ', FN, ' NOT FOUND ');
                WAIT;
                EXIT(READ_ITEMS)
          END;
  RESET(FI, FN);
  WRITE('READING FILE ', FN);
  WHILE NOT EOF(FI) DO
    BEGIN
       NEW(NEWPT);
       NEWPTO := FIO;
       NEWPT".NPTR := NIL;
       IF (TLINES[NEWPTO.TLNUM].IPTR = NIL)
         THEN TLINESINEWPTO.THNUMD.IPTR := NEWPT
         ELSE BEGIN
                PT : TLINESCHEWPTO.TLNUMD.JPTR;
                WHILE (PTO.NPTR <> NIL) DO PT := PTO.NPTR;
                PTC.NPTR := NEWPT;
              END;
        GET(FI);
        WRITE(('.');
      END; {WHILE}
   CLOSE(FI);
 END; {read_items}
 BEGIN{reader}
    NAMER ('FILE TO BE READ ', ST, 8);
    FN := CONCAT(ST, FN1);
    READ ... TLINE (FN);
    FN := CONCAT(ST:FN2);
    READ_ITEMS(FN);
    WAIT
 END; {reader}
                      {writes file of TLINES and ITEMs}
PROCEDURE WRITER;
 CONST
                 FN1:='.LINE';
                                 FN2='.ITEM';
  VAR
                 ST : STRING;
                 FN : FILENAME?
 PROCEDURE WRITE_TLINES(FN : FILENAME);
                   CH : CHAR;
    VAR
                  LN : TLX NF _NUM #
    BEGIN
      IF LOOKUP(FN)
        THEN BEGIN
               CLEAR;
                GOTOXY(0,20);
               WRITELN('FILE ',FN,' ALREADY EXISTS ');
                WRITE('DO YOU WANT TO REMOVE THE OLD FILE Y/N') #
                REPEAT
                  READ(CH)
               UNTIL (CH IN E'Y', 'g', 'N', 'n');
                IF ( CH IN ['N','n']) THEN EXIT(WRITER);
             END;
      REWRITE(FL,FN);
      FL" := TLINES;
      PUT(FL);
      CLOSE (FL, LOCK)
    END; {write_tlines}
```

PROCEDURE WRITE_ITEMS (FN : FILENAME); CH : CHAR; PT : POINTER; LN : TLINE_NUM; BEGIN REWRITE (FI, FN); FOR LN := 8 TO MAXLINE DO IF NOT (LN IN CALCSET) THEN BEGIN TLINESCLNJ.IPTR <> NIL THEN BEGIN PT := TLINESCLND.IPTR; WHILE (PT <> NIL) DO BEGIN FI" := PT"; PUT(FI); PT := PTO.NPTR END: (while) END; (if) END; (if) CLOSE(FI,LOCK); END; {write_items} BEGIN(writer)

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NAMER('FILE TO BE WRITTEN ',ST,8);

FN := CONCAT(ST, FN1);

FN := CONCAT(ST, FN2);

WRITE_TLINE(FN);



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Listing 11 continued on page 404

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```
WRITE_ITEMS(FN);
  END: (writer)
BEGIN
  CASE CH OF
    'R' : READER:
    'W' : WRITER;
  END!
END; {rw}
```

Listing 12: The program TAXNAMES. Separate from FIT, this program creates the one-dimensional array TITLES and writes the array to the disk file LINENAMS.FTAX. FIT uses the array TITLES to store the names of the lines on form 1040, Schedule A, and Schedule B.

```
{$L TNAME.PRN.TEXT}
PROGRAM TAXNAMES;
                         (program to create file of names of tax lines)
CONST
        MAXTLINE # 115;
TYPE
                T=ARRAY [1..MAXTLINE] OF STRING[30];
VAR
        TITLES : T;
        TFILE : FILE OF T;
PROCEDURE WAIT;
  VAR
      CH : CHAR;
  BEGIN
    GOTOXY(10,23);
    WRITE('ENTER <ESC> TO CONTINUE');
    REPEAT
      READ (CH)
    UNTIL CH=CHR(27);
  END;
PROCEDURE WRITEFILE;
  BEGIN
    REWRITE(TFILE, 'LINENAMS, FTAX');
    TFILE? := TITLES;
    PUT(TFILE);
    CLOSE(TFILE, LOCK);
  END;
PROCEDURE READFILE;
  VAR
        I:1 . . MAXTLINE;
  REGIN
    RESET(TFILE, 'LINENAMS, FTAX');
    TITLES := TFILE";
    FOR I := 1 TO MAXTLINE DO
      BEGIN
        WRITE N(TITLESCID);
         IF (I MOD 16) = 0
          THEN BEGIN
                 WAITS
                 WRITE(CHR(12));
               END;
      END;
```

END;

Listing 12 continued:

PROCEDURE INIT1;	
BEGIN	
TITLES[1] := 'FILING STATUS	' ;
TITLES[2] ;= 'FILING STATUS	' ÷
TITLES[3] := 'FILING STATUS	′ \$
TITLES[4] ;= 'FILING STATUS	/ ŷ
TITLES[5] := 'FILING STATUS	' ;
TITLES[6] := 'EXEMPTIONS	' ;
TITLES[7] := 'EXEMPTIONS	/ 9
TITLES[8] := 'WAGES, SALARIES, ETC	' ŷ
TITLES[9] := 'INTEREST INCOME	′;
TITLESC10] := 'DIVIDENDS	' ĵ
TITLES[11] := 'INCOME TAX REFUNDS	′;
TITLES[12] : 'ALIMONY RECEIVED	′;
TITLES[13] := 'BUSINESS INCOME	' ;
TITLESC14] := 'CAPITAL GAIN	' }
TITLES[15] := 'CAPITAL GAIN DIST	' ;
TITLESCI6] := 'SUPPLEMENTAL GAINS	' ‡
TITLES[17] := 'TAXABLE PENSIONS & ANNUITIES	' ;
TITLES[18] := 'PENSIONS, RENTS, ROYS, PARTNER	1;
TITLES[19] := 'FARM INCOME	′;
TITLES[20] : 'UNEMPLOYMENT	*)
TITLESC21] := 'OTHER INCOME	1.
TITLES[22] := 'TOTAL INCOME	, ģ
TITLES[23] := 'MOVING EXPENSE	' ;
TITLESC24] := 'EMP BUSINESS EXPENSE	1 9
TITLES[25] := 'PAYMENTS TO IRA	' ;
TITLES[26] := 'PAYMENTS TO KEOGH	' ;
END; {init1}	
PROCEDURE INIT2;	
BEGIN	
TITLESC273 := 'INTEREST PENALTY	2 \$
TITLES[28] := 'ALIMONY PAID	- × ;
TITLES[29] := 'DISABILITY INCOME	/ ;
TITLES[30] := 'TOTAL ADJUSTMENTS	/ ģ
TITLES[31] := 'ADJUSTED GROSS INCOME	/ ;
TITLESC32] := 'ADJUSTED GROSS INCOME	1;
TITLES[33] := 'DEDUCTIONS	/ 5
TITLES[34] := '32-33	19
TITLES[35] := 'TAX	1 ;
TITLES[36] := 'ADDITIONAL TAXES	/ ý
TITLES[37] := 'TOTAL TAXES	' ;
TITLES[38] := 'POLITICAL CONTRIBUTIONS	/ \$
TITLES[39] := 'CREDIT FOR ELDERLY	13
TITLESC40] :- 'CHILD AND DEPENDENT	/ ;
TITLES[41] :- 'INVESTMENT CREDIT	/ ;
TITLES[42] := 'FOREIGN TAX CREDIT	/ ;
TITLES[43] := 'WORK INCENTIVE	′;
TITLES[44] :: 'JOBS CREDIT	′;
TITLES[45] := 'ENERGY CREDITS	′ ;
TITLES[46] := 'TOTAL CREDITS (lines 38 to 45	
TITLES[47] := 'BALANCE (line 37 - line 46)	′;
TITLES[48] := 'SELF EMPLOYMENT TAX	/ ;
TITLES[49] := 'MINIMUM TAX	′ ;
END; {init 2}	
SECONDUCE THEFT	
PROCEDURE INIT3; 'BEGIN	
TITLES[50] := 'TAX FROM PRIOR YEAR INV-CREDIT	T / +
TITLESCOOL = 'FICA AND RRTA TAXES	. 1 ' ?
TITLESC52] := 'TAX ON IRA	, ,

more ...

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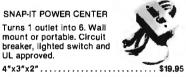
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Listing 12 continued on page 406



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END;

Listing 12 continued: TITLES[53] := 'ADVANCEEIC PAYMTS RECEIVED TITLES[54] := 'BALANCE (lines 47 to 53) TITLESC55] := 'TOTAL FICA WITHHELD TITLES[56] := '1980 ESTIMATED TAX PAYMENTS TITLES[57] := 'EARNED INCOME CREDIT TITLES[58] := 'AMOUNT PAID WITH FORM 4868 ' **;** TITLES[59] := 'EXCESS FICA AND RRTA TAX PAID ' ÷ TITLES[60] := 'CREDIT FOR FED TAX ON SP FUEL **′**; TITLES[61] := 'REGULATED INVESTMENT CO CREDIT'; TITLES[62] :- 'TOTAL (line 55 to 61) ŝ TITLES[63] := 'OVERPAID TITLES[64] := 'TO BE REFUNDED TO YOU TITLES[65] := 'APPLIED TO EST 1981 TAX TITLES[66] := 'BALANCE DUE END; {init3} PROCEDURE INIT4; BEGIN TITLES[67] := '50 % OF MEDICAL INS PREMS TITLES[68] := 'MEDICINE AND DRUGS '1% OF LINE 31 FORM 1040 TITLES[69] := TITLES[70] := 'SUB TOTAL line 3-line 2 TITLES[71] := 'BALANCE OF INS PREMS TITLES[72] := 'OTHER MEDICAL AND DENTAL TITLES[73] := 'TOTAL (lines 4 to 6) '3% OF LINE 31 FORM 1040 TITLES[74] := TITLES[75] := 'LINE 7 - LINE 8 TITLES[76] := 'TOTAL MED & DENTAL TITLES[77] := 'STATE & LOCAL INCOME TAX TITLES[78] := 'REAL ESTATE TAXES 'GENERAL SALES TAXES TITLES[79] := 'PERSONAL PROPERTY TAXES TITLES[80] := TITLES[81] := 'OTHER TAXES TITLES[82] := 'TOTAL TAXES lines 11 to 15 TITLES[83] := 'HOME MORTGAGE INTEREST TITLES[84] := 'CREDIT & CHARGE CARDS TITLES[85] := 'OTHER INTEREST TITLES[86] := 'TOTAL INT (lines 17 to 19) END; PROCEDURE INITS; BEGIN TITLES[87] := 'CASH CONTRIBUTIONS TITLES[88] := 'OTHER CASH CONTRIBUTIONS TITLES[89] := 'CARRYOVER TITLES[90] := 'TOTAL CONTRIBUTIONS 'LOSS BEFORE INSURANCE TITLES[92] := 'INSURANCE REIMBURSEMENT TITLES[93] := 'LINE 25 - LINE 26 TITLES[94] := '\$100 OR LINE 27 TITLES[95] ;= 'TOTAL CASUALTY OR THEFT ş TITLES[96] === 'UNION DUES TITLES[97] := 'OTHER MISC DEDUCTIONS TITLES[98] := 'TOTAL MISCELLANEOUS TITLES[99] :- 'TOTAL MEDICAL & DENTAL TITLESC1001 :- 'TOTAL TAXES TITLES[101] := 'TOTAL INTEREST TITLES[102] := 'TOTAL CONTRIBUTIONS

TITLES[103] :- 'TOTAL CASUALTY OR THEFT

TITLES[104] := 'TOTAL MISCELLANEOUS TITLES[105] := 'SUM (lines 33 to 38)

TITLES[106] := 'ADJUSTMENT

PROCEDURE INIT6; BEGIN

/ ; TITLES[107] :- 'LINE 39 - LINE 40 1 ; TITLES[108] := 'INTEREST INCOME **′** ; TITLES[109] := 'TOTAL INTEREST INCOME TITLESC1103 : " 'DIVIDEND INCOME / **\$** TITLES[111] := 'TOTAL DIVIDEND INCOME / **;** TITLES[112] :-· ; 'CAPITAL GAIN DISTRIBUTION / **;** TITLES[113] :- 'NONTAXABLE DISTRIBUTIONS TITLES[114] :4 'TOTAL (lines 5 % 6) / ÷ TITLES[115] := 'DIVIDENDS BEFORE EXCLUSIONS · ;

END;

BEGIN
INIT1;
INIT2;
INIT3;
INIT4;
INIT5;
INIT6;
WRITEFILE;
WAIT;
READFILE;

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741-0 RH SS/DD 8"	. 37.00

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PRIAS. Shapping and 3% for product dynamic visitins continued USA via UPS surface promises 100 II this eries in placed proposed with 8.5, bents in the form of charts or immerger sets, a finish surpline of 5.00 for integrating is all you pay within the condisional UEA via UPS surface. Also, in 100 of 100 of

Listing 13: The program TAXTABLE. Like TAXNAMES, this program is separate from FIT. TAXTABLE creates the array TAXRAY and writes the array to the disk file FACTORS.FTAX. TAXRAY is a three-dimensional array that holds the four factors needed to calculate a tax: the lower limit of a bracket, the upper limit, the minimum tax for the bracket, and the tax rate.

2

```
{$L TTABLE,PRN,TEXT}
                           terestes a file of taxt factors for ws
  PROGRAM TAXTABLE;
                                                                    by FIT>
  TYPE
          TFACTORS=(LOWER, UPPER, MASE, PER);
          FACTORRAY=ARRAY [1..14,TFACTORS] OF INTEGER[9]$
          T=ARRAY [1..4] OF FACTORRAY;
  VAR
          TY t Tf
          TFILE : FILE OF T;
  PROCEDURE WRITEFILE®
    BEGIN
      REWRITE(TFILE, 'FACTORS, FTAX');
      TFILE? : TY;
      PUT(TFILE);
      CLOSE(TFILE, LOCK);
    END;
    PROCEDURE INIT1A;
    (schedule X single tax payers lower bracket limit)
      BEGIN
                         := 230000;
        TYC1,1,LOWERT
                         t= 340000;
        TY[1,2,LOWER]
                         :- 440000;
        TY[1,3,LOWER]
        TYC1,4,LOWER]
                         i= 650000;
        TY[1,5,LOWER]
                         ;= 850000;
        TY[1,6,LOWER]
                         := 1080000#
        TY[1,7,LOWER]
                         := 1290000;
        TYC1,8,LOWER3
                         ;= 1500000;
        TY[1,9,LOWER]
                         := 1820000;
        TY[1,10,LOWER]
                          := 2350000;
        TY[1,11,LOWER]
                          := 2880000;
                          := 3410000;
        TYC1,12,LOWER3
        TY[1,13,LOWER]
                          := 4150000;
                          := 5530000¢
        TYC1,14,LOWER]
        TYC1,15,LOWERD
                          := 8180000;
        TY[1,16,LOWER]
                          := 1083000;
        END;
    PROCEDURE INITIB;
    {schedule X single tax payers upper bracket limit}
      BEGIN
        TYC1,1,UPPER3
                         := 340000;
        TYC1,2,UPPERD
                         := 440000¢
        TYC1,3,UPPERD
                         1= 650000;
        TYC1,4,UPPER]
                         := 850000;
        TY[1,5,UPPER]
                         := 1080000;
        TY[1,6,UPPER]
                         := 1290000;
        TYC1,7,UFFER]
                         := 1500000;
        TYC1,8,UPPER3
                         t= 1820000;
        TYC1,9,UPPER]
                         := 2350000;
        TY[1,10,UPPER]
                          := 2880000;
        TYC1,11,UFFERD
                          := 3410000;
        TYC1,12,UFFERD
                          ‡≈ 4150000;
        TY[1,13,UFFER]
                          :- 5530000;
```

TY[1,14,UPPER]

:= 8180000;

```
Listing 13 continued:
                                                                                       Buy with Confidence
                                    i= 10830000 i 製
       TYC1,15,UPPERJ
                                                                                               from the best
       TYC1,16,UPPERI
                                          999999999
                                                                                       GREAT PRICES, GREAT SERVICE, GUARANTEED
                                                                                                CALL TOLL FREE: 1 800 421-1520
       END;
                                                                                             COMPUTERS, PRINTERS, TERMINALS
                                                NA.
PROCEDURE INITICO
                                                                                            COMPUTERS & TERMINALS
                                                                                       CALL TOLL FREE FOR PRICES
(schedule X single tax payer
                                                    base
                                                             tax}
   BEGIN
       TYC1,1,BASE ]
                                   : =
                                        00;
       TY[1,2,BASE
                                        154009
                                   :-
       TYC1,3,BASE
                                        31400 $
                                                                                                      VIDEO TERMINALS
& MONITORS:
ADDS
                                                                                      COMPUTERS:
                                        62900;
       TYC1,4,BASE
                                                                                      Apple
       TYC1,5,BASE
                                        107200;
                                   :=
                                                                                      Atari
                                                                                                      Amdek
                                                                                      Commodore
Hewlett-Packard
       TY[1,6,BASE
                            ]
                                   :=
                                        155500;
                                                                                                      Apple
B.M.C
       TYE1,7,BASE
                                        205900;
                                                                                      B.M.C.
                            J
                                   :=
                                                                                                      Hazeltine
I.B.M.
       TYC1,8,BASE
                                   ! ==
                                        2605001
                                                                                                      Lear-Siegler
                                                                                      Northstar
       TYC1,9,BASE
                            ]
                                   : ==
                                        356500;
                                                                                      Onyx
Point Four
                                                                                                      N.E.C
                                                                                                      Sanyo
       TYC1,10,BASE ]
                                     :=
                                          536700;
                                                                                      Sharp
                                                                                      Televideo
       TYC1,11,BASE
                                     : =
                                          743400;
                             ]
                                                                                                     PRINTERS
       TY[1,12,BASE
                             ]
                                     := 976600;
       TYC1,13,BASE
                                     :=
                                          1339200#
       TYC1,14,BASE
                                          2098200;
                                                                                                    t:pson
I.D.S./Paper Tiger
       TYC1,15,BASE ]
                                          3767700;
                                                                                      THERMAL
                                                                                                    Microline/Okida
                                                                                      Apple
Trendcom
                                                                                                    M.P.I.
                                          5569700;
       TY[1,16,BASE ]
                                                                                                    Novell
       END;
                                                                                      DOT MATRIX
                                                                                                    Texas Instruments
                                                                                       Anscom
                                                                                                    LETTER QUALITY
                                                                                                    Diable/Xerox
N E.C.
PROCEDURE INIT1D;
                                                                                      Anadex
                                                                                      Centronix
                                                                                      Commodore
                                                                                                  ACCESSORIES
(schedule X single tax rayers
tax rate}
   BEGIN
                                                                                                        CP/M
Fortran
       TY[1,1,PER] := 14;
                                                                                                        Pascal
                                                                                      80 COLUMN
                                                                                                        Vanguard AP/L
                                                                                      VIDEO CARDS
Double Vision
       TY[1,2,PER] := 16;
                                                                                                        OTHER PRODUCTS
Bar Code Readers
       TY[1,3,FER] :=
                                                                                      Smart-Term
                                                                                                        Card Readers
Game Controls
Graphics Tablet
                                                                                      Videx
       TY[1,4,FER] :=
                                19;
                                                                                      LOWER CASE
ADAPTERS
       TYC1,5,PER3 :=
                                                                                                        Music System
Numeric Keypads
Programming Aids
Type-and-Talk
                                                                                      Dan Paymai
       TY[1,6,PER] := 24;
                                                                                      INTERFACE CARDS
                                                                                      Apple
California Computer
       TYC1,7,PERG :=
                               261
                                                                                                        Video Digitizers
Voice Entry
       TY[1,8,PER] := 30;
                                                                                                     The best non-technical right to Computer (How to Choose a Computer St2 95. Book for Laymen for Complete Directory for Apple Software only $14.95.
                                                                                      Mountain Computer
                                                                                      Microsoft
       TY[1,9,8ER3 :::
                                34;
                                                                                      Thunderclock
       TY[1,10,PER] :=
                                  39 î
                                                                                      LANGUAGES
       TY[1,11,PER] :=
                                  44:
                                                                                      Basic Compiler
C.I.S. Cobol
       TY[1,12,PER]
                             ; :::
                                  49:
                                                                                            apple
      TY[1,13,PER]
                             4= 55;
                                                                                                                   MX 100 &
                            ‡ az
       TY[1,14,PER]
                                                                                                                  MX 80 F/T
       TYC1,15,PER] := 68;
                                                                                      MONITORS
                                                                                                                    The new 136
       TY[1,16,PER] :-
                                  701
                                                                                      BMC & NEC Green Screen
NOW IN STOCK
                                                                                                                 column Epson
                                                                                                        printer with graphics and the Friction/tractor MX 80 are in stock, WEHAVETHE GRAPHIC PACKAGE FOR MX 80 cgil.
   END;
                                                                                       FDUCATORS
                                                                                      systems to hook up several
PROCEDURE INIT2A;
                                                                                                           SILENTYPE PRINTER
                                                                                      Apple computers at once
(schedule Y married tax payers lower
                                                                                      NEC & DIABLO PRINTERS
                                                                                      Anudex, Paper
bracket limit>
    BEGIN
        TY[2,1,LOWER]
                                    := 340000f
        TYC2,2,LOWERJ
                                    := 550000$
                                                                                                                    ALL IN ONE
                                                                                           SOFTWARE
        TY[2,3,LOWER]
                                    := 760000;
                                                                                      PLOTTERS
BauschäLomb plotters
for your computer by
Houston instruments
CALL!
        TYC2,4,LOWER]
                                    := 1190000
        TYC2,5,LOWER]
                                         160000;
        TYC2,6,LOWER]
                                         20200003
        TY[2,7,LOWER]
                                    :=
                                         2460000;
                                                                                                         Dysan Uisks for Apple on
        TYC2,8,LOWER]
                                         2990000;
                                    : =
        TYC2,9,LOWER]
                                         3520000;
                                                                                          1(800)421-1520
in Cal (213)3204772
                                                                                                         53 95 AV
5000 los
        TY[2,10,LOWER]
                                     t= 4580000¢
        TY[2,11,LOWER]
                                     :=
                                          6000000;
                                                                                                       20% OFF ALL SOFTWARE
        TYC2,12,LOWERJ
                                     := 8560000;
                                                                                                        Mail orders ONLY:
NET PROFIT COMPUTERS
                                                                                       Visit our retail store
Net Profit Computers
521 W Chapman Ave
Anaheim, Cal 92802
714 750-7318
        TY[2,13,LOWER]
                                     ;= 10940000;
                                                                                                        2908 Oregon Court, Bid G1
Torrance, Ca 90503
1(800)421-1520
        TY[2,14,LOWER]
                                      := 16240000;
```

Ŋ

Listing 13 continued on page 410

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```
Listing 13 continued:
      TY[2,15,LOWER]
                     := 21540000;
      TY[2,16,LOWER]
                       := 99999999;
   END;
 PROCEDURE INIT2B;
   BEGIN
      TYC2,1,UPPERD
                       :- 550000;
                       := 760000;
      TYC2,2,UPPERD
      TYC2,3,UPPERJ
                       := 119000;
                       := 160000;
      TY[2,4,UPPER]
                       :- 2020000;
      TYC2,5,UPPERD
                       ;= 2460000;
      TY[2,6,UPPER]
      TYC2,7,UPPERI
                       := 2990000;
      TYC2,8,UPPERI
                       t= 3520000¢
      TYC2,9,UPPERI
                       := 4580000;
      TYC2,10,UPPERJ
                        := 6000000;
      TYC2,11,UPPERD
                        : 8560000;
      TYC2,12,UFFERD
                        1º 10940000;
      TYC2,13,UPPERD
                        := 16240000;
      TYC2,14,UPPERD
                        := 21540000;
                        1= 9999999999
      TYC2,15,UPPERD
      TYC2,16,UPPERI
                        := 9999999999
   END;
 PROCEDURE INIT2C;
   BEGIN
      TYC2,1,BASE J
                       := 00;
      TYC2,2,BASE
                   J
                       := 29400;
      TYC2,3,BASE ]
                       := 63000;
      TYE2,4,BASE ]
                       :- 14040;
      TYC2,5,BASE ]
                       := 226500;
                      := 327300;
      TYC2,6,BASE ]
      TYC2,7,BASE ]
                       :L 450500;
      TY[2,8,BASE ]
                       := 620100;
                       := 816200;
      TY[2,9,BASE ]
      TY[2,10,BASE ]
                        :- 1272000#
                        :- 1967800;
      TYC2,11,BASE
                   ]
      TYC2,12,BASE ]
                        :L 3350200;
      TYC2,13,BASE
                        := 4754400;
      TYC2,14,BASE ]
                        := 8146400;
      TYC2,15,BASE ]
                        ‡: 11750400;
      TYC2,16,BASE ]
                        := 11750400;
      END:
 PROCEDURE INIT2D#
   BEGIN
      TYE2,1,FER] :- 14;
      TYE2,2,PER3 := 16;
      TY[2,3,PER] :- 18;
      TYE2,4, PER3 := 21;
      TY02,5,PER3 ::
                     24:
      TYC2,6,PER1 := 28;
      TY[2,7,PER] := 32;
      TY[2,8,PER] := 37;
      TYC2,9,PER3 := 43;
      TYC2,10,PERI := 49;
      TYC2,11,PERT := 54;
      TYC2,12,PER3 := 59;
      TYE2,13,PERJ := 64;
      TYC2,14,PER3 := 689
      TYC2,15,PERD := 70;
      TY[2,16,PER] :: 70)
   ENDA
```

Listing 13 continued:

END;

```
PROCEDURE INIT3A;
(schedule YS married tax payers filing separately
lower bracket limit}
  REGIN
    TY[3,1,LOWER]
                     :=: 170000;
    TY[3,2,LOWER]
                     := 275000;
    TY[3,3,LOWER]
                     := 380000;
    TY[3,4,LOWER]
                     := 595000;
    TYC3,5,LOWERD
                     := 800000;
    TY[3,6,LOWER]
                     := 1010000;
    TY[3,7,LOWER]
                     := 1230000;
    TY[3,8,LOWER]
                     := 1495000;
    TYC3,9,LOWER]
                     := 1760000;
    TYE3,10,LOWER]
                      := 2290000;
                         3000000;
    TY[3,11,LOWER]
                      :=
    TYC3,12,LOWERJ
                       := 4280000;
    TYC3,13,LOWER]
                      := 5470000j
    TY[3,14,LOWER]
                      := 8120000;
    TY[3,15,LOWER]
                      :- 10770000;
    TY[3,16,LOWER]
                       :- 999999999;
  END;
PROCEDURE INITSB;
  BEGIN
    TYES, 1, UPPERD
                     := 275000;
    TYC3,2,UPPER3
                     :=
                        380000;
    TYC3,3,UPPERJ
                     t =
                        595000;
    TYC3,4,UPPER]
                        800000;
    TYC3,5,UPPERD
                        1010000;
    TYC3,6,UPPER]
                     :=
                        1230000;
    TYC3,7,UPPERI
                     := 1495000;
    TYC3,8,UPPERI

↓- 1760000;

    TYC3,9,UPPERD
                     := 2290000;
    TYC3,10,UPPER3
                       := 3000000$
                      := 4280000;
    TYC3,11,UFFER]
    TYC3,12,UPPERJ
                       := 5470000;
    TYC3,13,UPPERD
                      :=
                          $120000;
    TYC3,14,UPPER]
                          10770000
    TYC3,15,UPPERD
                          999999999
    TYC3,16,UPPERD
                       := 999999999
  END;
PROCEDURE INIT3C;
  BEGIN
    TYC3,1,BASE ]
                     := 00;
    TYE3,2,BASE
                      :- 147009
    TYC3,3,BASE
                     ; =
                        31500;
    TY[3,4,BASE
                 ]
                      := 70200;
    TYE3,5,BASE
                     : ==
                        113250;
    TYE3,6,BASE
                      := 163650;
    TYE3,7,BASE
                 1
                     := 225250;
    TY[3,8,BASE ]
                      := 310050;
    TY[3,9,BASE ]
                     := 408100;
    TYC3,10,BASE ]
                       : = 636000;
    TY[3,11,BASE ]
                       := 983900;
    TYE3,12,BASE
                       :=
                          1675100;
    TYE3,13,BASE
                       ! =
                          2377200;
    TYC3,14,BASE
                       :- 4073200;
    TYE3,15,BASE
                  ٦
                       : =
                          5875200:
    TY[3,16,BASE ]
                       := 5875200;
```

Listing 13 continued on page 412



```
PROCEDURE INIT3D;
                                                   TY[4,10,UPPER]
                                                                     := 4470000;
  BEGIN
                                                   TY[4,11,UPPER]
                                                                     :- 6060000;
    TYE3,1,PERD :- 14;
                                                   TY[4,12,UPPER]
                                                                     := 8180000;
    TYE3,2,PER3 := 16;
                                                   TY[4,13,UFFER]
                                                                     := 10830000;
    TY[3,3,FER] :
                    18;
                                                   TY[4,14,UPPER]
                                                                     := 16130000;
    TY[3,4,PER] :- 21;
                                                                     :- 99999999;
                                                   TY[4,15,UPPER]
    TYE3,5,PER3 := 24;
                                                   TY[4,16,UPPER]
                                                                     := 99999999;
    TY[3,6,PER] := 28;
                                                 END;
    TYE3,7,PER1 := 32;
    TYE3,8,PER] := 37;
                                               PROCEDURE INIT4C;
    TY[3,9,PER] := 43;
                                                 BEGIN
    TYE3,10, PERI := 490
                                                   TY[4,1,BASE ]
                                                                    := 00;
                                                   TY[4,2,BASE ]
    TY[3,11,PER] := 54;
                                                                    := 294009
    TY[3,12,PER] := 59;
                                                   TY[4,3,BASE ]
                                                                    :- 63000;
    TY[3,13,PER] := 64;
                                                   TY[4,4,BASE ]
                                                                    ;= 102600;
    TYE3,14,PER3 := 68;
                                                   TY[4,5,BASE ]
                                                                    := 170800;
    TY[3,15,PER] := 70;
                                                   TYE4,6,BASE ]
                                                                    :- 247600;
                                                   TY[4,7,BASE
                                                                    :- 330800;
    TYE3,16,PERI := 70;
                                                                J
                                                   TY[4,8,BASE
                                                                7
                                                                    :_ 495100;
  END;
                                                   TY[4,9,BASE ]
                                                                    := 685900;
                                                   TY[4,10,BASE ]
                                                                     := 908500;
                                                   TY[4,11,BASE ]
                                                                     := 1396100;
PROCEDURE INIT4A;
                                                   TY[4,12,BASE ]
                                                                     := 2254700;
{schedule Z head of househeld
                                                   TY[4,13,BASE ]
                                                                     := 3505500;
lower bracket limit}
                                                   TY[4,14,BASE ]
                                                                      := 5175000;
                                                   TY[4,15,BASE ]
                                                                     := 8779000;
  BEGIN
                                                                      := 9999999;
                                                   TY[4,16,BASE ]
    TY[4,1,LOWER]
                     := 230000;
                                                 END;
    TY[4,2,LOWER]
                     := 440000;
    TY[4,3,LOWER]
                     := 650000;
                                               PROCEDURE INITAD;
    TY[4,4,LOWER]
                     :- 870000;
                                                 BEGIN
    TY[4,5,LOWER]
                     :- 1180000;
                                                   TY[4,1,PER] :- 14;
    TY[4,6,LOWER]
                     := 1500000;
                                                   TY[4,2,PER] := 16;
    TY[4,7,LOWER]
                     ;- 1820000;
                                                   TY[4,3,FER] := 18;
    TY[4,8,LOWER]
                     :_ 2350000;
                                                   TY[4,4,PER] := 229
    TY[4,9,LOWER]
                     := 2880000;
                                                   TY[4,5,PER] :
                                                                   24;
    TY[4,10,LOWER]
                      := 3410000;
                                                   TY[4,6,FER] :- 26;
    TY[4,11,LOWER]
                      := 4470000;
                                                   TYE4,7,PER3 := 31;
                      := 6060000;
    TY[4,12,LOWER]
                                                   TY[4,8,PER] := 36;
    TY[4,13,LOWER]
                      :- 8180000;
                                                   TY[4,9,PER] := 42;
    TY[4,14,LOWER]
                      := 10800000;
                                                   TY[4,10,PER] := 469
    TY[4,15,LOWER]
                      := 16130000;
                                                   TY[4,11,PER] := 54;
    TY[4,16,LOWER]
                      :- 99999999;
                                                   TY[4,12,PER] :=
  END
                                                   TY[4,13,PER] := 63;
                                                   TY[4,14,PER] := 68;
PROCEDURE INITAR;
                                                   TYE4,15,PERD := 70;
  BEGIN
                                                   TY[4,16,PER] := 70;
    TY[4,1,UPFER]
                        440000;
                                                 END
                     :- 650000;
    TYE4,2,UPPERJ
    TY[4,3,UPPER]
                     :
                        870000;
                                             BEGIN
    TY[4,4,UPPER]
                     :- 1180000;
                                               INIT1A; INIT1B; INIT1C; INIT1D;
    TYC4,5,UPPERI
                     := 15000000;
                                               INIT2A; INIT2B; INIT2C; INIT2D;
    TYE4,6,UFFER]
                     := 1820000¢
                                               INIT3A # INIT3B # INIT3C # INIT3D #
    TY[4,7,UPPER]
                     := 2350000;
                                               INITAA; INITAB; INITAC; INITAD;
    TYC4,8,UPPERI
                     := 2880000;
                                               WRITEFILE;
    TY[4,9,UPPER]
                     := 3410000;
                                             END.
```

System Notes

Double-Width Silentype Graphics for Your Apple

Charles H. Putney 18 Quinns Rd. Shankill County Dublin Ireland

Now your Apple II computer can print double-sized graphics on your Silentype thermal printer. Using the method presented here, each pixel on the Apple's high-resolution (hi-res) screen is represented by a two-by-two array of dots on the printer.

To generate double-sized graphics, first load a picture into either of the Apple's hi-res screens. Then load the program given in listing 1 or 2 starting at hexadecimal location 800 (2048 decimal). Set the parameters according to table 1 and begin execution at 800 hexadecimal (using either 800G in the monitor or CALL 2048 from BASIC). The printer will dump the chosen hi-res page in either normal or inverse video mode.

How It Works

The Silentype printer is connected to the Apple with a small serial interface card that plugs into one of the peripheral slots inside the computer. This card provides two-way serial communications between the computer and the printer. If the card is plugged into peripheral slot 0, the output to the printer is addressed at hexadecimal memory location C081, and the input is at C084 (-16255 and -16252 in decimal). To determine the new port addresses if the card is plugged into a different slot, multiply the slot number by hexadecimal 10 (or 16 if working in decimal) and add the result to the above memory locations.

The high-order bit (7) of bytes read from the printer (location C084 hexadecimal) is set (1xxxxxx) when the printhead is fully returned to the left

margin and is reset (0xxxxxxx) if the printhead is anywhere else.

The Silentype expects data to be transmitted to it in 16-bit words, one for each movement of the printhead

or paper roller. Since writing a byte of data to the output port at location C081 results in the low-order bit (0) being transmitted (only bit 0 of the

Text continued on page 423

Parameter Location Table

Parameter	Location	Setting
NORMAL / INVERT	\$803 (2051)	NORMAL = \$FF(255), INVERT = \$00 (0)
SLOT NUMBER	\$804 (2052)	SLOT 1 = \$10 (16), SLOT 2 = \$20 (32) ETC
HI-RES PAGE	\$805 (2053)	PAGE 1 = \$20 (32), PAGE 2 = \$40 (64)
PAGE LENGTH	\$806 (2054)	159 LINES = \$9F. 192 LINES = \$C0

Table 1: Parameters which must be set before running the Silentype thermal-printer double-width graphics program. The desired parameter values are stored in the memory locations shown.



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EMPIRICAL RESEARCH

United Kingdom MicroAPL LTD. London 834-2687 GROUP, INC. POB 1176 MILTON, WA 98354 206-631-4855 Australia/New Zealand S.I. MicroComputer Prod. LTD. Sidney 231-4091 Listing 1: A 6502 assembly-language program that will provide hard copy of Apple graphics displays by dumping the contents of the Apple high-resolution graphics screen to the Silentype thermal printer. This screen print uses a two-by-two array of dots on the paper for each pixel on the screen. The program is loaded and executed at memory location 800 hexadecimal (2048 decimal).

ASM

```
0800- 4C 7F 09 1000 GRAPH JMP PICTUR
                                         GET RIGHT TO IT
               1010 *
               1020 *
               1030 *----
               1040 *
               1050 *
               1060 * INPUT AND OUTPUT ADDRESSES
               1070 *
               1080 *
               1090 *
C081-
               1100 STROBE .EQ $C081
                                         PRINTER STROBE
C084-
               1110 RETURN .EQ $C084
                                         PRINTER CARRIAGE RETURNED
               1120 *
               1130 *
               1140 *----
               1150 *
               1160 *
                      CONSTANTS AND VARIABLES
               1170 *
               1180 *
               1190 *
               1200 *
0803- FF
               1210 NEG
                            .DA #$FF
                                         POS/NEG PICTURE (POS = $FF , NEG = $00)
0804- 10
               1220 SLOT
                            .DA #$10
                                         SLOT NUMBER ( SLOT ONE )
0805- 20
                                         HI RES PAGE (PAGE 1 = 20 , PAGE 2 = 40)
               1230 PAGE
                            .DA #$20
0806- C0
               1240 LEN
                            .DA #$CO
                                         HI RES PAGE LENGTH ($9F=157, $C0=192)
                            .DA #*-*
0807- 00
               1250 DOTS
                                         DOTS DATA
0808- 00
                            .DA #*-*
               1260 WINDS
                                         WINDING DATA
0809- 00
               1270 STEPX
                            .DA #*-*
                                         OLD X STEP
                            .DA #*-*
080A-00
               1280 STEPY
                                         OLD Y STEP
                                         X DIRECTION
080B- 00
                            .DA #*-*
               1290 DIRX
               1300 DIRY
                            .DA #*-*
080C- 00
                                         Y DIRECTION
080D- 00
               1310 SUML
                            .DA #*-*
                                         SUM - LOW BYTE
080E- 00
               1320 SUMH
                            .DA #*-*
                                         SUM - HIGH BYTE
               1330 WIND
                                         STEPPER WINDING TABLE
080F- 03
                            .DA #$03
0810- 02
               1340
                            .DA #$02
0811- 06
                            .DA #$06
               1350
                            .DA #$04
0812- 04
               1360
0813- OC
               1370
                            .DA #$0C
0814- 08
               1380
                            .DA #$08
0815- 09
               1390
                            .DA #$09
                            .DA #$01
0816- 01
               1400
               1410 XL
0817- 00
                            .DA #*-*
                                         PIXEL X COORDINATE - LOW BYTE
                                         PIXEL X COORDINATE - HIGH BYTE
0818- 00
               1420 XH
                            ,DA #*-*
               1430 Y
                            .DA #*-*
                                         PIXEL Y COORDINATE
0819- 00
                                         Y ADDRESS - LOW BYTE
0060~
               1440 ADRESL .EQ $60
                                         Y ADDRESS - HIGH BYTE
0061 -
               1450 ADRESH .EQ $61
081W- 00
               1460 XMOD7
                            .DA #*-*
                                         TEMP FOR REMAINDER
081B- 00
               1470 ADRESX .DA #*-*
                                         X ADDRESS - USED AS INDEX
081C- 00
                           .DA #*--*
                                         MASK FOR PIXEL
               1480 XMASK
                            .DA #*-*
                                         PRINT LINE FOR TRANSLATION
081D- 00
               1490 PRINT
               1500 *
```

```
Listing 1 continued:
```

```
1510 *
              1520 *----
              1530 *
              1540 *
              1550 * ROUTINE TO CLOCK DATA TO PRINTER INTERFACE
              1560 *
              1570 * X REGISTER CONTAINS SLOT NUMBER TIMES SIXTEEN
              1580 * DOTS AND WINDS ARE CHANGED UPON EXIT
              1590 *
              1600 *
081E- AE 04 08 1610 CLOCK LDX SLOT
                                      GET SLOT NUMBER
0821- AO 10
              1620
                          LDY #$10
                                      SET INDEX
0823- AD 07 08 1630 CLK1
                          LDA DOTS
                                       GET BOTTOM WORD
0826- 29 01
              1640
                          AND #$01
                                       MASK IT
0828- 09 OE
                                      MAKE E OR F
              1650
                          ORA #$OE
082A- 9D 81 CO 1660
                         STA STROBE, X CLOCK IT IN
082D- 6E 08 08 1670
                          ROR WINDS
                                     SHIFT TOP WORD
0830- 6E 07 08 1680
                         ROR DOTS
                                       CARRY INTO BOTTOM
                                       DEC LOOP
0833- 88
              1690
                         DEY
0834- DO ED
                                       DONE 16 TIMES ?
                          BNE CLK1
              1700
0836- A9 1C
                          LDA #$1C
              1710
0838- 9D 81 CO 1720
                          STA STROBE, X *
083B- A9 18
                         LDA #$18
              1730
083D- 9D 81 CO 1740
                          STA STROBE, X CLOCK IN
                                    THE FOUR STOP CODES
0840- A9 1C
              1750
                         LDA #$1C
0842- 9D 81 CO 1760
                         STA STROBE, X *
0845- A9 OC
              1770
                          LDA #$OC
0847- 9D 81 CO 1780
                          STA STROBE, X *
084A- 60
              1790
                          RTS
              1800 *
              1810 *
              1820 *----
              1830 *
              1840 *
              1850 *
                      ROUTINE TO PRINT DOTS
              1860 *
              1870 *
              1880 PRINTS LDA #$00
084B- A9 00
                                       NO MOVEMENT
084D- 8D 08 08 1890
                          STA WINDS
0850- 20 1E 08 1900
                          JSR CLOCK
                                       SEND IT
                                       DELAY LOOP
                          LDY #$02
0853- AO 02
              1910
                                       FOR DARKER PRINT - LENGTHEN THIS DELAY
0855- A2 FF
                          LDX #$FF
              1920
              1930 PRIN1 DEX
0857- CA
                          BNE PRIN1
                                      ENOUGH X ?
0858- DO FD
              1940
                         DEY
085A- 88
              1950
                                       ENOUGH Y ?
085B- DO FA
               1960
                          BNE PRIN1
                          RTS
085D- 60
              1970
               1980 *
               1990 *
               2000 *-----*
               2010 *
               2020 *
               2030 * ROUTINE TO INCREMENT OR DECREMENT
               2040 * POINTER TO WINDING TABLE AND KEEP
               2050 * IT IN THE RANGE O TO 7
                                                                 Listing 1 continued on page 416
```

```
Listing 1 continued:
               2060 *
               2070 *
085E- 10 07
               2080 STEPER BPL STEP1
                                        POSITIVE STEP
0860- CA
                           DEX
                                         DEC STEP
               2090
0861- 10 OC
                            BPL STEP2
                                         WRAPAROUND?
               2100
                                       START AT TOP
                            LDX #$07
0863- A2 07
               2110
0865- 10 08
                           BPL STEP2
                                         ALWAYS JUMP
               2120
0867- E8
               2130 STEP1 INX
                                         INC STEP
                                             500
                            TXA
· 0868- 8A
               2140
0869- C9 08
               2150
                            CMP #$08
                                        WRAPAROUND?
086B- 90 02
               2160
                            BCC STEP2
086D- A2 00
               2170
                            LDX #$00
                                         START : AT BOTTOM
086F- 60
                2180 STEP2 RTS
                2190 *
                2200 *
                2210 *-
                2220 *
                2230 *
                2240 * ROUTINE TO MOVE ALONG Y AXIS (CARRIAGE)
                2250 *
                2260 *
 0870- AE OA O8 2270 MOVEY LDX STEPY
                                         GET OLD Y S
 0873- AD OC 08 2280
                          LDA DIRY
                                         GET Y DIRECTION
 0876- FO 1E
               2290
                          BEQ MOVEY2
                                         NO MOVEMENT
 0878- 20 5E 08 2300
                            JSR STEPER
                                         INC OR DEC
087B- 8E 0A 08 2310
                            STX STEPY
                                         SAVE NEW POSITIO
087E- BD OF 08 2320
                           LDA WIND, X
                                         GET Y WINDING
0881- 8D 08 08 2330
                           STA WINDS
                                         PASS IT
0884- A9 00
                            LDA #$00
                2340
                           STA DOTS
0886- 8D 07 08 2350
                                         NO DOTS
0889- 20 1E 08 2360
                           JSR CLOCK
                                         CLOCK THE
088C- AO 11
               2370
                            LDY #$11
                                         DELAY IC
088E- A2 FF
               2380
                            LDX #$FF
0890- CA
               2390 MOVEY1 DEX
0891- DO FD
                            BNE MOVEY1
               2400
                                         ENOUGH
0893- 88
               2410
                            DEY
0894- DO FA
               2420
                            BNE MOVEY1
0896- 60
               2430 MOVEY2 RTS
               2440 *
                2450 *
                2460 *-
                2470 *
                2480 *
                2490 * ROUTINE TO MOVE ALONG X AXIS (PRINTHEAD)
                2500 *
                2510 *
 0897- AE 09 08 2520 MOVEX LDX STEPX
                                         GET OLD X STEP
 089A- AD OB 08 2530
                            LDA DIRX
                                         GET X DIRECTION
 089D- FO 22
                2540
                            BEQ MOVEX2
                                       NO MOVEMENT ?
089F- 20 5E 08 2550
                            JSR STEPER
                                         INC OR DEC '
08A2- 8E 09 08 2560
                            STX STEPX
                                         SAVE NEW POSITION
08A5- BD OF 08 2570
                            LDA WIND, X GET Y WINDINGS
08A8- 0A
                2580
                           ASL
08A9- 0A
                2590
                            ASL
08AA- 0A
                2600
                            ASL
                                        NOW X WINDINGS
08AB- OA
                2610
                            ASL
```

```
Listing 1 continued:
08AC- 8D 08 08 2620
                           STA WINDS
08AF- A9 00
               2630
                           LDA #$00
08B1- 8D 07 08 2640
                           STA DOTS
                                        NO DOTS
08B4- 20 1E 08 2650
                                        CLOCK THE DATA
                           JSR CLOCK
                                      DELAY LOOP
08B7- AO 02
                           LDY #$02
               2660
08B9- A2 40
                           LDX #$40
               2670
08BB- CA
               2680 MOVEX1 DEX
O8BC- DO FD
                           BNE MOVEX1
                                       ENOUGH X ?
               2690
08BE- 88
               2700
                           DEY
                           BNE MOVEX1 ENOUGH Y ?
O8BF- DO FA
               2710
08C1- 60
               2720 MOVEX2 RTS
               2730 *
               2740 *
               2750 *----
               2760 *
               2770
               2780 * ROUTINE TO CALCULATE ADDRESS OF
               2790 * PIXEL AT XH, XL AND Y AND RETURN
               2800 * ACC POSITIVE IF ITS ON
               2810 *
               2820 *
08C2- AD 19 08 2830 PIXEL
                           LDA Y
                                        GET Y ·
08C5- 29 07
                           AND #$07
                                        GET Y2 - YO
               2840
08C7- 18
               2850
                           CLC
08C8- 2A
                           ROL
               2860
08C9- 2A
                                        MOVE INTO POSITION
               2870
                           ROL
08CA- 85 61
               2880
                           STA ADRESH
08CC- AD 19 08 2890
                           LDA Y
                                        GET Y AGAIN
08CF- 29 30
                           AND #$30
               2900
                                        MASK INTO Y5 - Y4
08D1- 4A
               2910
                           LSR
08D2- 4A
               2920
                           LSR
08D3- 4A
               2930
                           LSR
08D4- 4A
                                       MOVE INTO BOTTOM TWO BITS
               2940
                           LSR
08D5- 05 61
               2950
                           ORA ADRESH ADD TO EXISTING
08D7- OD 05 08 2960
                           ORA PAGE
                                        HI RES PAGE
08DA- 85 61
                           STA ADRESH
               2970
                                        FINISHED WITH ADRESH
08DC- AD 19 08 2980
                           LDA Y
08DF- 29 08
                           AND #$08
                                        GET Y3 ONLY
               2990
08E1- 18
                           CLC
               3000
08E2- 2A
               3010
                           ROL
08E3- 2A
               3020
                           ROL
08E4- 2A
                           ROL
               3030
08E5- 2A
                                        MOVE INTO ADRESL BIT 7
               3040
                           ROL
08E6- 85 60
               3050
                           STA ADRESL
08E8- AD 19 08 3060
                           LDA Y
08EB- 29 40
                           AND #$40
                                        CHECK Y6
               3070
08ED- F0 06
                           BEQ ADD1
                                        ZERO ?
               3080
08EF- A5 60
               3090
                           LDA ADRESL
08F1- 69 28
                           ADC #$28
               3100
                                        ONE LINE OF PIXELS ( 40 DEC )
08F3- 85 60
               3110
                           STA ADRESL
08F5- AD 19 08 3120 ADD1
                           LDA Y
                                        CHECK Y7
08F8- 29 80
               3130
                           AND #$80
08FA- FO 06
               3140
                           BEQ ADD2
                                        ZERO ?
08FC- A5 60
               3150
                           LDA ADRESL
                                        TWO LINES OF PIXELS ( 80 DEC )
08FE- 69 50
               3160
                           ADC #$50
```

Listing 1 continued on page 418

System N	lete.					
						
Listing 1 continue		2170		CMA	ADDECT	
0900- 85 6	50	3170	* DD0		ADRESL	
0902- 38	20		ADD2	SEC	#400	TVTTT11707 00170
0903- A2 C		3190			#\$00	INITIALIZE COUNT
0905- AD 1				LDA		uan a a muo
0908- 8D 0					SUML	USE AS TEMP
090B- AD 1				LDA		
090E- 8D 0			3.000		SUMH	USE AS TEMP
0911- AD C			ADD3		SUML	BEGIN DIVIDE
0914- F9 C		3250			#\$07	BY SEVEN
0916- 8D C					SUML	
0919- AD 0					SUMH	
091C- E9 C		3280			#\$00	
091E- 8D C					SUMH	
0921- 30 0)4	3300			ADD4	BELOW ZERO ?
0923- E8		3310		INX		ADD TO COUNT OF SUBTRACTIONS
0924- 4C 1					ADD3	REPEAT
0927- AD 0			ADD4		SUML	GET SUML AGAIN
092A- 69 C		3340			#\$07	RESTORE TO > ZERO
092C- 8D 1		_			XMOD7	REMAINDER
092F- 8E 1	LB 08				ADRESX	LATER INDEX
0932- 18 0933- A9 0	. .	3370		CLC	#\$01	BUILD MASK
0935- AE 1		3380			XMOD7	BOILD MASK
0938- CA	IA OU		ADD5	DEX	AHOD7	
0939- 30 C	14	3410			ADD6	SHIFT IF POSITIVE
093B- 2A		3420		ROL	ADDO	SHIFT MASK
093C- 4C 3	98 09				ADD5	REPEAT
093F- 8D 1			ADD6		XMASK	NOW WILL MASK CORRECT BIT
0942- AC 1					ADRESX	USE FOR INDEX
0945- B1 6		3460			(ADRESL)	
0947- 4D 0				EOR	` .	SHOULD WE INVERT
094A- 2D]						EXTRACT PIXEL
094D- 60		3490		RTS		PIXEL ON IF ACC = 1 (POSITIVE CASE)
		3500				,
		3510				
		3520	*			*
		3530	*			
		3540	*			
		3550	* ROUTI	NE T	O RETURN	PRINTHEAD AND
		3560	* SPACE	CAF	RRIAGE DOW	NN SIX DOTS
		3570	*			
		3580	*			
094E- A9 F	FF	3590	CARRET	LDA	#\$FF	SOMETHING NEGATIVE
0950- 8D 0	B 08	3600		STA	DIRX	RETURN PRINTHEAD .
0953- 20 9	97 08	3610				

0956- AE 04 08 3620 LDX SLOT GET SLOT NUMBER 0959- BD 84 CO 3630 LDA RETURN, X CHECK MICROSWITCH 095C- 10 F5 3640 BPL CAR1 KEEP NUDGING 095E- A9 01 3650 LDA #\$01 SOMETHING POSITIVE 0960- 8D OB 08 3660 STA DIRX NOW BACK A LITTLE 0963- AE 04 08 3670 CAR2 LDX SLOT GET SLOT NUMBER 0966- BD 84 CO 3680 LDA RETURN, X GET STATUS 0969- 10 06 BPL CAR3 ENOUGH ? 3690 096B- 20 97 08 3700 JSR MOVEX NO, NOT QUITE JMP CAR2 KEEP GOING 096E- 4C 63 09 3710 0971- A9 06 3720 CAR3 LDA #\$06 SIX DOTS TOTAL

```
Listing 1 continued:
0973- 8D OC 08 3730
                             STA DIRY
0976- 20 70 08 3740 CAR4
                             JSR MOVEY
                                          MOVE DOWN ONE STEP
0979- CE OC
            08 3750
                             DEC DIRY
                                          DIRY = DIRY - 1
097C- DO F8
                3760
                            BNE CAR4
                                          AGAIN ?
097E- 60
                3770
                             RTS
                3780 *
                3790 *
                3800 *-
                3810 *
                3820 *
                        ROUTINE TO TRANSFER HI RES SCREEN TO SILENTYPE
                3830 *
                3840 *
                3850 *
097F- 20 4E 09 3860 PICTUR JSR CARRET
                                          START AT RIGHT PLACE
0982- A9 00
                3870
                             LDA #$00
                                          INITIALIZE
0984- 8D 19 08 3880
                             STA Y
                                          Y = 0
0987- A9 OC
                3890 PICT1
                            LDA #$OC
                                          XL = LEFT EDGE (CLIPPED)
0989- 8D 17 08 3900
                             STA XL
098C- A9 00
                3910
                             LDA #$00
                                          XH = 0
098E- 8D 18 08 3920
                             STA XH
0991- A9 00
                            LDA #$00
                3930 PICT2
0993- 8D 1D 08 3940
                             STA PRINT
                                          PRINTLINE = 0
0996- 20 C2 08 3950
                             JSR PIXEL
                                          CHECK FIRST DOT
.0999- FO 08
                             BEO PICT3
                                          PIXEL ON ?
                3960
099B- A9 03
                             LDA #$03
                                          TOP TWO DOTS
                3970
099D- 6D 1D 08 3980
                             ADC PRINT
09A0- 8D 1D 08 3990
                             STA PRINT
                                          ADD TO PRINTLINE
09A3- EE 19 08 4000 PICT3
                             INC Y
                                          NEXT PIXEL
                                           CHECK SECOND PIXEL
09A6- 20 C2 08 4010
                             JSR PIXEL
09A9- FO 08
                4020
                             BEQ PICT4
                                           PIXEL ON ?
                                           MIDDLE TWO DOTS
09AB- A9 OC
                4030
                             LDA #$OC
                             ADC PRINT
09AD- 6D 1D 08 4040
09BO- 8D 1D 08 4050
                             STA PRINT
                                           ADD TO PRINTLINE
09B3- EE 19 08 4060 PICT4
                             INC Y
                                           NEXT PIXEL
09B6- 20 C2 08 4070
                             JSR PIXEL
                                           CHECK THIRD PIXEL
09B9- F0 08
                4080
                             BEQ PICT5
                                           PIXEL ON ?
                             LDA #$30
                                           BOTTOM TWO DOTS
09BB- A9 30
                4090
09BD- 6D 1D 08 4100
                             ADC PRINT
                                           ADD TO PRINTLINE
09CO- 8D 1D 08 4110
                             STA PRINT
                                           PUT IT DOTS
09C3- AD 1D 08 4120 PICT5
                             LDA PRINT
09C6- 8D 07 08 4130
                             STA DOTS
09C9- 20 4B 08 4140
                             JSR PRINTS
                                           PLOT THREE PIXELS
09CC- A9 01
                4150
                             LDA #$01
                                           MOVE RIGHT ONE DOT
                             STA DIRX
09CE- 8D 0B 08 4160
09D1- 20 97 08 4170
                             JSR MOVEX
                             JSR MOVEX
09D4- 20 97 08 4180
09D7- AD 1D 08 4190
                             LDA PRINT
09DA- 8D 07 08 4200
                             STA DOTS
09DD- 20 4B 08 4210
                             JSR PRINTS
                                           DO IT AGAIN
09E0- A9 01
                             LDA #$01
                4220
                             STA DIRX
09E2- 8D 0B 08 4230
                             JSR MOVEX
                                           MOVE RIGHT ONE DOT
09E5- 20 97 08 4240
09E8- 20 97 08 4250
                             JSR MOVEX
09EB- EE 17 08 4260
                             INC XL
                                           X = X + 1
                                           CARRY TO XH ?
09EE- DO 03
                4270
                             BNE PICT6
```

Listing 1 continued on page 420

System Notes,

Listing 1 c	ontin	ued:						
09F0-	EE	18	08	4280		INC	XH	
09F3-	CE	19	80	4290	PICT6	DEC	Y	
09F6-	CE	19	08	4300		DEC	Y	Y = Y - 2
09F9-	Α9	OC.		4310		LDA	#\$0C	XL = OC ? (XL , $XH = 268$, $CLIPPED$)
09FB-	CD	17	08	4320		CMP	\mathbf{XL}	
09FE-	DO	91		4330		BNE	PICT2	NOT AT END YET
0A00-	Α9	01		4340		LDA	#\$01	XH = 1 ?
0A02-	CD	18	08	4350		CMP	XH	
0A05-	DO	8A		4360		BNE	PICT2	NOT AT END YET
0A07-	EE	19	08	4370	PICT7	INC	Y	
OAOA-	EE	19	80	4380		INC	Y	
OAOD-	EE	19	08	4390		INC	Y	Y = Y + 3
0A10-	AD	19	80	4400		LDA	Y	
0A13-	CD	06	08	4410		CMP	LEN	HI RES PAGE END
0A16-	BO	06		4420		BCS	PICT8	WE'RE DONE
OA18-	20	4E	09	4430		JSR	CARRET	START NEW PRINT LINE
OA1B-	4C	87	09	4440		JMP	PICT1	
OA1E-	ΑE	04	08	4450	PICT8	LDX	SLOT	GET SLOT NUMBER
0A21-	Α9	00		4460		LDA	#\$00	GET ZERO .
OA23-	9D	81	CO	4470		STA	STROBE, X	MAKE SURE PRINTER WINDINGS ARE OFF
0A26-	60			4480		RTS		

SYMBOL TABLE			
	0823- CLK1	0987- PICT1	0867- STEP1
08F5- ADD1	081E- CLOCK	0991- PICT2	086F- STEP2
0902- ADD2	080B- DIRX	09A3- PICT3	085E- STEPER
0911- ADD3	080C- DIRY	09B3- PICT4	0809- STEPX
0927- ADD4	0807- DOTS	09C3- PICT5	080A- STEPY
0938- ADD5	0800- GRAPH	09F3- PICT6	CO81- STROBE
093F- ADD6	0806- LEN	OAO7- PICT7	080E- SUMH
0061- ADRESH	0897- MOVEX	OA1E- PICT8	080D- SUML
0060- ADRESL	08BB- MOVEX1	097F- PICTUR	080F- WIND
081B- ADRESX	08C1- MOVEX2	08C2- PIXEL	0808- WINDS
0953- CAR1	0870- MOVEY	0857- PRIN1	0818- XH
0963- CAR2	0890- MOVEY1	081D- PRINT	0817- XL
0971- CAR3	0896- MOVEY2	084B- PRINTS	081C- XMASK
0976- CAR4	0803- NEG	CO84- RETURN	081A- XMOD7
094E- CARRET	0805- PAGE	0804- SLOT	0819- Y
	I .	1	



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* When used for business.

Listing 2: If you do not have a 6502 assembler for your Apple, you can enter this previously assembled version of the graphics-print program directly into the Apple's memory using the machinelanguage monitor.

:\$800.A26

```
0800- 4C 7F 09 FF 10 20 C0 00
0808- 00 00 00 00 00 00 03
0810- 02 06 04 OC 08 09 01
                            იი
         00 00 00 00 00
0818- 00
                         AE
                            04
0820- 08 AO 10 AD 07
                         29
0828- 09
         OE 9D 81 CO 6E
                         ΩR
                            ΩR
0830- 6E 07 08 88 D0 ED A9
0838- 9D 81 C0 A9 18
                     9D 81
                            CO
0840- A9 1C 9D 81 CO A9 0C
                            90
0848- 81 CO 60 A9 00 8D 08
                            ΩR
0850- 20 1E 08 A0 02 A2 FF
                            CA
0858- DO FD 88 DO FA 60 10
                            07
0860- CA 10 OC A2 07 10 08
0868- 8A C9 08 90 02 A2 00
0870- AE OA O8 AD OC 08 FO
0878- 20 5E 08 8E 0A 08 BD
                            OF
0880- 08
        8D 08 08 A9
                     00
                         8D
                            07
0888- 08 20 1E 08 A0 11 A2 FF
0890- CA DO FD 88
                  DO
                     FA
                         60
                            AE
0898- 09 08 AD 0B
                  08
                     FO
                         22
                            20
08AO- 5E 08 8E 09
                  08
                     BD
                         OF
                            08
            OA OA 8D
                         08
08A8- 0A
         OA
                      08
08B0- 00 8D 07
               08
                  20
                     1E
                         08
                            ΑO
                         88
08B8- 02 A2 40
               CA DO
                     FD
                            DO
OSCO- FA 60 AD
               19
                  OB
                      29
                         07
                            18
08C8- 2A 2A 85 61 AD 19
                         OB
                            29
08D0- 30
         4A 4A
               4A 4A 05
                         61
                            OD
08D8- 05
         08 85 61 AD 19
                            29
08E0- 08
         18 2A 2A 2A 2A 85
                            60
08E8- AD
         19 08 29 40 F0 06
                            A5
08F0- 60
         69
            28 85
                  60 AD 19
                            OB
08F8- 29
         80 FO 06
                  A5
                     60 69
                            50
0900- 85
         60
            38 A2
                  00
                      AD
0908- 8D
         OD 08 AD 18
                      08
                         8D
0910- 08
        AD OD 08 E9
                      07
                         8D
                            OD
0918- 08 AD OF 08 E9 00
                         8D
                            OE
0920- 08
         30 04 E8 4C 11 09
                            AD
0928- OD 08 69 07
                         08
                             8E
0930- 1B 08
            18 A9
                  01
                            0.8
0938- CA 30 04 2A 4C
                      38 09
                            8 D
0940- 1C 08 AC 1B 08
                     B1 60
                            40
0948- 03
         08
            2D
               10
                   08
                      60 A9
                            FF
0950- 8D
         OB
            08
               20
                   97
                      08
                         ΑE
                            04
0958- 08
        BD
            84 CO
                   10 F5
                         A 9
                            01
0960- 8D OB O8
               AE
                   04
                      08
                         BD
                            84
0968- CO 10 06
                  97
                      08
                            63
               20
                         4C
0970- 09 A9 06 8D 0C 08
                            70
                         20
0978- 08 CE OC 08 DO F8
                         60
                            2.0
0980- 4E 09 A9 00 8D 19
                         08
                            A 9
0988- OC
         8D 17 08 A9
                     00
                         8D 18
0990- 08
         A9 00 8D 1D 08
0998- 08 FO 08 A9 03 6D
                            0.8
09A0- 8D 1D 08 EE 19
                     08
                         20
                            C2
09A8- 08 FO 08 A9 OC 6D 1D 08
```

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System Notes

Listing 2 continued:

09BO- 8D 1D 08 EE 19 08 20 C2 09B8- 08 FO 08 A9 30 6D 1D 08 09CO- 8D 1D 08 AD 1D 08 09C8- 08 20 4B 08 A9 01 8D 0B 09D0- 08 20 97 08 20 97 08 AD 09D8- 1D 08 8D 07 08 20 4B 08 09EO- A9 01 8D 0B 08 20 97 08 09E8- 20 97 08 EE 17 08 DO 03 09F0- EE 18 08 CE 19 08 09F8- 08 A9 0C CD 17 08 OAOO- A9 O1 CD 18 O8 DO OAO8- 19 08 EE 19 08 EE 19 08 OA10- AD 19 08 CD 06 08 B0 06 OA18- 20 4E 09 4C 87 09 AE 04 OA20- 08 A9 00 9D 81 CO 60

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Listing 3: Several examples of Apple high-resolution pictures printed on a Silentype using the author's double-width graphics-print routine.

High-Resolution Character Set

. ,	αĖ	*	ተ		-	. 196"		.l <u>*</u> .	***	a di				., ¹ 1 1 2	'n	7
4	ф	-ii	in the second	100	أر	î;	, l'i	jil.	1/1	di.	laki.	`IL'		alt	:#:	1
	•	11	list.	:	350	2,	1	<	3	ቀ	4/11	a [‡]	-	4	page.	
3	g T strin	2	1 m	.111	(EE)	6	mgr.		-	:	j	4"	-		41-74. 44.	1
£	-114		C		((, =	Ç.	-	Ţ	·f	K		[14]	14		1
÷.	i M	(T)	€.	T	U	∇	j _e m.		³ _t r ¹		f	4]	, r ^a i,	*******	
	γ	0	o	ø	æ	4	<u> </u>	j _{en} g	**		, de	1	777	re ^{let} e	f ^{arr} i	
9,37	<u>-</u> 2*	-	:27	77	i ii esse	J	M	"#";	land.	:::::	4	1	Ĵ	ve		*

INVERSE





Text continued from page 413:

output port is connected to the serial data line), 16 bytes of data must be written to the port for each command sent to the printer. Bits 1, 2, and 3 of each byte have been set as guard bits to prevent confusion over the value of bit 0. Once the 16 data bytes have been stored to the output location, 4 stop bits must be transmitted to inform the printer that we have reached the end of a command word. An example of a typical transmission is given in table 2.

The first 7 bits of the 2 transmission bytes control the thermal printhead. The thermal printhead consists of seven resistors (transistors are also used) deposited on a ceramic base. When these elements are heated, a dot will appear on the paper if the printhead is allowed to dwell at that position. The darkness of the dot will depend on the dwell time. (Darkness may also be controlled by multiple firings of the thermal elements.)

The stepper-motor windings are controlled by the last 8 data bits. (Bit 8 is not used as far as I can determine.) In the Silentype, there are separate stepper motors to move the drive roller and the thermal printhead. Both motors are identical four-winding stepper motors with 48 steps

per revolution. To step either motor, you must know the last step made and energize the windings for the next step. In the full-step sequence (used by the Silentype routines) there are four steps. I use an 8-step sequence (called electronic half-stepping) for slightly smoother operation. Table 3 shows the two stepping sequences for the printhead motor. The carriage motor is similar, but the upper 4 bits are used. Either motor can be stepped clockwise or counterclockwise by exercising the stepping sequence in reverse order.

Fine Tuning

The dot density can be adjusted by changing the delays in the PRINT DOTS routine. The 2-byte value is at locations 854 and 856 hexadecimal (2132 and 2134 decimal). The current delay value is 02FF (767). The movement of the printhead can be speeded up or slowed down by the delay values in locations 8B8 and 8BA hexadecimal (2232 and 2234 decimal). The delay I found to give the fastest movement without any skipping was 0240 (576). Likewise, the movements of the carriage can be speeded up or slowed down by the delay values at locations 88D and 88F hexadecimal

(2189 and 2191 decimal). The carriage has considerably more inertia so this delay value is currently 11FF hexadecimal (4607 decimal). The PICTUR routine can print the lines of pixels only in multiples of three (printhead dot 7 is not used) so the page length parameter in location 806 hexadecimal (2054 decimal) prints 159 lines (9F in hexadecimal) instead of 160.

One likely reason that Apple did not develop the double-sized graphics is that some pixels have to be clipped from the left and right edges because of paper size. I clip twelve vertical rows from each side of the screen. In most cases, this still gives a good picture, but these limits can be changed if necessary. The left edge is checked at location 987, and the right edge is checked at 9F9.

With the basics of the Silentype printer in mind, the operation of the assembly-language routines should be fairly clear. Now—double your fun with Silentype.

Full Step Sequence Step Winding Hex W4 W3 W2 \$03 0 0 1 \$06 0 1 0 Õ \$0C 0 1 n 0 \$09 Half Step Sequence Step Winding Hex W₁ W2 **W**3 0 \$03 0 2 0 0 1 0 \$02 \$06 3 0 1 1 0 0 1 0 0 \$04 1 1 0 0 \$OC 6 0 0 \$08 1 0 7 1 0 0 \$09 0 0 \$01

Fable 3: To control the two stepper motors in the Silentype printer, these 4-bit codes are inserted into the command word described in table 2. Each motor-control sequence must be transmitted sequentially, as shown; skipping a code will result in improper operation. Transmitting the sequence in reverse order will step the motors in the opposite direction. The author uses the half-step sequence for smoother operation.

Transmission Details

```
$1E or $1F Data bit 1 = Printhead dot 1 (top dot)
$1E or $1F Data bit 2 = Printhead dot 2
$1E or $1F Data bit 3 = Printhead dot 3
$1E or $1F Data bit 4 = Printhead dot 4
$1E or $1F Data bit 5 = Printhead dot 5
$1E or $1F Data bit 6 = Printhead dot 6
$1E or $1F Data bit 7 = Printhead dot 7 (bottom dot)
$1E or $1F Data bit 8 = Not Used (?)
$1E or $1F Data bit 9 = Drive roller stepper winding 1
$1E or $1F Data bit 10 = Drive roller stepper winding 2
$1E or $1F Data bit 11 = Drive roller stepper winding 3
$1E or $1F Data bit 12 = Drive roller stepper winding 4
$1E or $1F Data bit 13 = Printhead stepper winding 1
$1E or $1F Data bit 14 = Printhead stepper winding 2
$1E or $1F Data bit 15 = Printhead stepper winding 3
$1E or $1F Data bit 16 = Printhead stepper winding 4
$1C
           Stop bit
$18
           Stop bit
$1C
           Stop bit
$0C
           Stop bit
```

Table 2: Details of the 20-bit command word which controls the Silentype printer. Each of the first 7 bits corresponds to a thermal element in the printhead or one dot on the paper. Bits 9 through 12 control the stepping of the paper roller motor, while bits 13 through 16 control the motor, which positions the printhead. The 4 stop bits inform the printer that the current command word has ended.

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SYSTEMS

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North Star Takes Advantage

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The Advantage costs \$3999. Contact North Star Computers Inc., 14440 Catalina St., San Leandro, CA 94577, [415] 357-8500.

Circle 427 on inquiry card.

operating systems. The 128K-byte bank-switched memory option allows the program to select from 48 to 63K bytes of user-programmable memory, controlled through an I/O (input/outputl port. Each serial port can be customized for a variety of applications, such as an interface with a serial printer. The interrupt controller provides standard interrupt configurations by means of jumper plugs, but wire-wrap connections can be made to achieve special interrupt configurations. The real-time clock provides a 60-Hz interrupt source, which is derived from the data-rate clock. In a networking configuration, the Net/82 performs as a slave processor. Each slave operates independently, except for resource queuing in the master, which makes the entire system appear to be dedicated to each user. The master processor has complete control over each slave and can reset or interrupt a slave at any time.

The Net/82 costs \$1395 or, with 128K bytes and the floating-point processor. \$1995. Contact MuSYS Corp., Suite 11, 1451 Irvine Blvd., Tustin, CA 92680, (714)750-5693.

Circle 426 on inquiry card.

Multiuser **Development System**

Ithaca Intersystems' DPS-8000 is a 16-bit, Z8000-based, multiuser system. It features a 20-slot S-100 mainframe, advanced memory management with up to 128K bvtes of protected memory per user, 2.5 megabytes of parity memory in 256K-byte increments, serial and parallel I/O (input/output), and DMA (direct memory access) hard-disk controller with 32-bit error checking and control.

The DPS-8000 has an advanced multiuser and multitasking Unix-compatible operating system called Coherent. Coherent has a full range of utilities and compilers, file and device handling capabilities, and real-time responsiveness. Also included is Interpak 8000-a special set of utilities designed to aid programmers in the rapid editing, correcting, and documentation of software. For details, contact Ithaca Intersystems, Inc., 1650 Hanshaw Rd., POB 91, Ithaca, NY 14850, (800) 847-2088; in New York (607) 257-0190. Circle 428 on inquiry card.



Flexible Business Computer

Data Technology Industries' System 10 is a Z80-based single-user business computer that runs CP/M software. The System 10 has 65K bytes of read and write user-programmable memory and 2K bytes of PROM (programmable read-only

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Circle 429 on inquiry card.

Fortune Shines on the 68000

The Fortune 32:16 desktop microcomputer is based on the Motorola 68000 microprocessor. It features the Unix operating system and a full range of business applications software packages. The basic Fortune 32:16 includes a 32-bit microprocessor with a 16-bit data path, expandable memory from 128K bytes to 1 megabyte, a 1-megabyte 51/4-inch floppy-disk drive, a keyboard, and a 12-inch video-display screen. For applications requiring greater storage capacities, a 5¼-inch

Winchester disk drive with 5, 10, or 20 megabytes of storage is available.

The single-user Fortune 32:16 is readily expandable to a multiuser, multiapplication system. It can be upgraded in the field to a multiuser, timeshared system that can be employed in a Xerox Ethernet network.

The Fortune 32:16 supports most widely used languages, including BASIC, COBOL, FOR-TRAN, Pascal, and C. Its 99-key keyboard is removable. The keyboard has a 15-key numeric keypad with nine cursor-control kevs and 16 programmable-function keys.

The basic Fortune 32:16 system costs \$4995. Contact Fortune Systems Corp., 1501 Industrial Rd., San Carlos, CA 94070, (415) 595-8444.

Circle 430 on inquiry card.



Gateway for Designers

Forward Technology has unveiled the third member of its Gateway Series of Multibus-compatible single-board computers: the FT-68M. Based on the 16-bit Motorola 68000, the FT-68M has 256K bytes of user-programmable memory, including error detection, two-level, multiprocess memory management and protection, serial and

parallel communication facilities. and five The counter/timers. FT-68M is designed to assist system designers who need the power and flexibility of the 68000 combined with 256K bytes on a single Multibuscompatible board.

The FT-68M has two user-programmable RS-232C interfaces, and its serial interfaces will operate in either synchronous or asynchronous modes. Among its other features are Xenix operating system compatibility, no wait states with local RAM (randomaccess memory), up to 32K bytes of PROM (programmable read-only memory), dual serial-communication channels, single 16-bit input port, 8-megabyte addressability, 8 MHz clock rate, and IEEE Ilnstitute of Electrical and Electronics Enginneers P-796 Bus (Multibus) with Multimaster capabilities. The FT-68M costs \$3495. Contact Forward Technology Inc., 2595 Martin Ave., Santa Clara, CA 95050, (408) 988-2378.

Circle 431 on inquiry card.

Single-Board Computer

RCP Systems' IEEE (Institute of Electrical and Electronics Engineers) S-100 interface board is a single-board computer for the hobbyist or small-systems manufacturer. The board has a 4-MHz Z80 microprocessor, a 2716 EPROM (erasable programmable read-only memory), a four-channel timer, two parallel ports. two serial ports with onboard drivers and receivers with data rates ranging from 75 to 38,400 bits per second, and 16K bytes of dynamic user-programmable memory expandable to 128K bytes with software bank-select of the upper and lower banks. Other features include an S-100 slave address of 1 to 64, an interrupt-driven system, and five onboard regulators.

The board costs \$1395, assembled and tested. Contact RCP Systems Inc., 1020 East 18th Ave., North Kansas City, MO 64116, (816) 221-0816.

Circle 432 on inquiry card.



Let the Professor Show You

Looking for an inexpensive way to learn how to design a program? Let the Micro-Professor show vou. The Micro-Professor İS а book-shaped Z80-based microcomputer learning tool. It has a 2K-byte ROM (read-only memory) monitor program with system initialization, keyboard and display scan, and tape write and read. Micro-Professor features 2K bytes of userprogrammable memory, 24 parallel I/O (input/out-

put) lines, audiotape interface, system clock, and a single power supply. As your knowledge of microcomputing grows, you can expand the Micro-Professor to Z80-CTC and Z80-PIO and add an EPROM (erasable programmable read-only memory) and a prototypina board.

Documentation includes a user's manual and a book of 18 sample programs and experiments that range from simple software programming to complex electronic-control systems. The manual includes the source listings for the 2K-byte monitor program, schematic diagrams, and operating instructions. It also describes the hardware and software specifications. The Micro-Professor costs \$99; dealer inquiries are welcomed. Contact Multitech Industrial Corp., 977-1 Min Shen E. Rd., Taipei 105, Taiwan, Republic of China, Telex: 23756 Multiic.

Circle 433 on inquiry card.

6-MHz Card for S-100 Systems

The CP 600 Central Processor Card can increase your S-100 system's throughput by as much as 50%. The CP 600 is a 6-MHz. 8-bit Z80 card that conforms to the IEEE (Institute of Electrical and Electronics Engineers) 696 (i.e., S-100) standard. Two onboard ports extend memory addressing to 24 bits and I/O (input/output) addressing to 16 bits, which allows up to 16

megabytes of system memory and 64K bytes of system I/O. The system memory refresh is performed as a standard S-100 memory-read cycle, minimizing the need for special logic on memory cards. To accommodate 64K-byte dynamic-memory devices, the 8 lower address bits are used for refreshing.

The CP 600 has a crystal-controlled master clock, jumper-selectable onboard-generated memory and I/O wait states, and onboard EPROM (erasable programmable readonly memory). The CP 600 is available from Echo Communications Corp., 1708 Stierlin Rd., Mountain View, CA 94043, (415) 969-6086.

Circle 434 on inquiry card.

Single-Chip Microcomputer

General Instrument has introduced a new 8-bit single-chip microcomputer called the PIC16C55. The PIC16C55 is a low-power consumption, 28-pin device with wide powertolerances. viggus Although nominally a 5-V device, the chip will accept voltages ranging between 2.5 and 6 V. The device is a CMOS (complementary metal-oxide semiconductor) circuit array that contains user-programmable memory, eight user-defined I/O (input/output) lines, a central processing unit, and ROM (read-only memory). The device can perform logical processing, basic code conversions and formatting, and can generate timing and control signals for I/O devices.

Internally, the device consists of three functional elements connected by a single bidirectional bus: the register file, consisting of 32 addressable 8-bit registers, an arithmetic logic unit, and a program ROM of 512 program words, each 12 bits wide. The device features an intelligent controller for stand-alone operations, 32 by 8-bit programmable memory, a real-time clock counter, onboard or crystal-controlled oscillator, single-word instructions, single-supply operation, and software compatibility with other members of General Instrument's PIC family. The eight I/O registers provide latched lines for interfacing to a wide variety of applications, such as scan keyboards, drive displays, electronic-game control, and vending machines.

Software support is available, and sample programs can be used to develop programs that can be assembled into machine language using PICAL, which was specially designed for the PIC series. PICAL is available in a FORTRAN IV version. Contact General Instrument, 600 West John St., Hicksville, NY 11802, (516) 733-3107.

Circle 436 on inquiry card.

Link Sorcerers to S-100 Bus

Exidy Systems' Display/S-100 unit links the Sorcerer computer to any S-100-bus product. The Display/S-100 combines the expansion capability of S-100 products within an enclosure that houses a 12-inch green-phosphor video display for the Sorcerer. The unit is mounted on a swivel-base stand, and the video screen sports a 20-MHz bandwidth for high res-

olution. The unit's S-100 bus is a self-contained motherboard with power supply and translation logic for the Sorcerer computer.

The Display/S-100 includes cables and documentation. The suggested retail price is \$699. Contact Exidy Systems, Inc., 1234 Elko Dr., Sunnyvale, CA14081 94086, 734-9831.

Circle 435 on inquiry card.

Programming and Design System

The IDC-8 is a programming and design subsystem based on the Intel 8088 microprocessor. Soft-

ware developed on the IDC-8 is compatible with other 8088-based computers, including the IBM Personal Computer. The device features 18-square-inch wire-wrap area for special design applications, card expansions, and additional peripheral-support circuitry and processors. The IDC-8 includes a 5-MHz 8088 microprocessor, monitor software in an 8755 I/O (input/output) ROM (readonly memory), 1K bytes of static RAM (randomaccess memory), 256 bytes of I/O memory, and an 8251-based video-display interface. The I/O ROM and the I/O RAM have a total of 38 parallel I/O lines. The device requires 5 volts at 1 amp, and it communicates by means of an RS-232C terminal

The IDC-8 is fully assembled and tested and is shipped with complete documentation for hardware and software applications. It costs \$399; kit versions are available. For details, contact Intelligent Devices Corp., One Cameron Pl., Wellesley, MA 02181, (617) 237-7327.

Circle 467 on inquiry card.

Symbol-Processing System

The Symbolics 3600 is a dedicated computer system that's designed for high-productivity software development and support of large symbolic systems. Typical applications include CAD (computeraided design), artificial intelligence, and expert systems. The primary language of the 3600 is Symbolics' ZetaLisp, an expressive, efficient, and extensible langauge. Fully integrated into the ZetaLisp language is a unique approach to object-oriented programming called the Flavor System. In addition to ZetaLisp, FORTRAN-77 and Pascal can be run on the 3600.

The basic Symbolics 3600 hardware consists of a high-performance microcoded central processing unit with 36-bit tagged architecture and 32-bit data paths, special features for symbolic computing, 1.125 megabytes of main memory, a fast-access 67-megabyte Winchester hard-disk drive, 10-megabit-per-second Ethernet II network interface, two serial lines, and a graphics console with 100-key keyboard with N-key rollover, landscape-format 1000-line black-and-white bit-mapped display, a mouse, and audio output. The 3600's virtual memory consists of more than one million pages of 256 words of 36 bits each.

The 3600 has a Motorola MC68000-based front-end processor that serves two functions: during normal operation it lowcontrols and medium-speed I/O (input/output) devices and performs error logging and recovery; when the 3600 is not running, it is used for debugging. Contact Symbolics Inc., 21150 Califa St., Woodland Hills, CA 91367, (213) 347-9224.

Circle 437 on inquiry card.



Little Big Computer

The Findex computer is a complete microcomputer system that weighs only 31 pounds and is no larger than the average electric typewriter. The Findex has a keyboard, memory capacity of up to 2 million characters on floppy-disk drives, a display, and a printer. Serial, parallel, and S-100 bus interfaces are standard, and Bell 103 and CCITT acoustic couplers are available as options. Many high-level languages are supported, including Business BASIC, COBOL, Pascal, FOR-

TRAN, APL, and PL/I. Applications software is also available.

The Findex computer will operate on 110 V (volts), 220 V, or 12 V, and its battery backup will let the machine operate for 30 minutes. Depending on the peripherals and software selected, the Findex computer costs between \$6980 and \$20,000. Contact Findex, 20775 South Western Ave., Torrance, CA 90501, [213] 533-6842.

Circle 438 on inquiry card.

Versatile Business Computers

The System 12B is the heart of a new line of business computers from Midwest Scientific Instruments. The 12B supports four users simultaneously, contains 328K bytes of memory, and employs a 10-megabyte partially fixed and partially removable hard-disk drive that is capable of supporting several hundred megabytes of online disk storage.

The 12B uses the SDOS operating system and runs a complete library of business-software modules, including inventory control, bills of material, sales order entry, accounts receivable and payable, and payroll. The system starts at \$2495 for a 64K-byte model. For details, contact Midwest Scientific Instruments, 220 West Cedar, Olathe, KS 66061, (913) 764-3273.

Circle 439 on inquiry card.



Have Angels In Your Office

The Angel-I is an S-100-based word- and data-processing system featuring a Z80 centralprocessing unit, 64K bytes of programmable memory, two large-capacity 8-inch floppy-disk drives, an 80-character by 24-line video-display screen, and a daisy-wheel printer. The new multiterminal Angel-I small-business system can support up to sixteen terminals and from four to six users concurrently writing and testing programs. Programs can be developed for 16-bit target computers, such as the 8086 microprocessor. Three versions are offered: a lowcost model for order desks and doctors' offices, a medium-priced model for word and data processing, and a multiterminal system that features off-line processing.

Angel-I system terminals feature Z80 processors, from 48,000 to 68,000 characters of memory, and serial I/O (input/output). In the top-of-the-line multiterminal Angel-I system, each terminal has a separate mainframe. 64,000 characters of memory, a single largecapacity 8-inch floppy-disk drive, and a serial I/O channel for communication with the central processor. The Angel-I costs \$7995; add-on terminals range from \$1500 to \$3500, depending upon model selected. Contact E & U Engel Consulting, 1719 South Carmelina Ave., Los Angeles, CA 90025, [213] 820-4231.

Circle 440 on inquiry card.

System Has **Robotics Potential**

The VIµP (Versatile Industrial Microprocessor) 7000 is a small, 18- by 27-cm (6½-by 10¾-inch), microcomputer system designed for OEM (original equipment manufacturer) and small-user applications in industrial control. machine automation, and robotics. Among the VIμP's features are stepper-motor drivers, A/D (analog-to-digital) and D/A

(digital-to-analog) converters, a real-time calendar clock, and optically isolated I/O (input/output).

The VIµP uses a 6502 microprocessor, and its bus is KIM-compatible. The bus uses two 44-pin edge card connectors per slot. one for the central bus and the other for additional applications.

The VIµP 7000 costs between \$500 and \$2000, depending on configuration, Contact Systems Innovations Inc., POB 2066, Lowell, MA 01851, 16171 459-4449.

Circle 441 on inquiry card.

Electronic Mail Data Sheet

The CDI/Comet Portable Electronic Mail System is a business-communications software package that uses Computer Devices' Miniterm computer as an electronic mailbox. The CDI/Comet features quaranteed message distribution, 24-hour-a-day accessibility, English-language commands, and word-processing and editing functions. A data sheet describing the CDI/Comet is available from the company. It explains how the CDI/Comet, when used with Miniterm computer terminals, provides efficient, cost-effective, and instantaneous access to field personnel and how it ensures accurate, complete, and quaranteed message delivery. The CDI/Comet data sheet can be obtained from Computer Devices Inc., 25

North Ave., Burlington, 01803. (800) MΑ 225-1230; in Massachusetts (617) 273-1550.

Circle 442 on inquiry card.

PERIPHERALS



High-Resolution Alphanumerics Display

The GT-1 Z80-based Multibus-compatible video-display board features a high-resolution 1640 by 500 pixell monochrome graphics display with onboard vector, arc, circle, and text generation. Two user-programmable and several built-in patterns are available for different line and area fill styles, as well as eight text sizes. The GT-1 includes a separately addressable scrolling alphanumerics display that features 80 by 25 characters, four individually programmable attributes, and a fully addressable cursor. The 96-character ASCII (American Standard Code for Information Interchange) set is standard. The ASCII code is enhanced with 32 special characters, with the option of a second userspecified set.

The GT-1 uses 5 volts at 1.5 amperes from the Multibus. Communication with the host computer is

accomplished by a separate 25-pin EIA (Electronics Industry Association) connector. The GT-1's RS-232C interface supports full-duplex serial communication with 16 switch-selectable data rates to 38.4 kbps (thousand bits per second). Up to 256 characters can be buffered in both directions. A connector is provided for attaching an 8-bit parallel keyboard, and composite and XYZ video connections are standard. The GT-1 uses XOFF/XON protocols.

In single quantities, the GT-1 costs \$1995. Contact Micrographics Research, 28 Pioneer Dr., Nashua, NH 03062, (603) 888-6790.

Circle 443 on inquiry card.

Macrosystem-88

The Macrosystem-88 adds 16-bit processing power and up to 128K bytes of additional RAM (random-access memory) to the Apple II. The Macrosystem-88 is a full microcomputer system based on the 5-MHz Intel 8088 8/16-bit microprocessor. It has 64K bytes of programmable memory, expandable to 128K bytes, and 4K bytes of PROM (programmable read-only memory) on a single self-contained board with power supply. The Macrosystem-88 features front-panel power and reset switches and indicators for run, pause, and select.

The Macrosystem-88's DMA (direct memory access) control card, which

can be installed in any Apple slot except 0, handles communications between the Macrosystem-88 and the Apple. On this basis, the Macrosystem-88 has complete access to the Apple's memory and peripherals. The Apple's 6502 microprocessor handles I/O (input/output) processing.

Macrosystem-88 can run Digital Research's CP/M-86 and Softech Microsystems' UCSD Pascal p-System 4.0 with UCSD Pascal along with FORTRAN-77 and a BASIC compiler. Switching between Apple DOS (disk operating system) and CP/M-86 is as simple as booting with the appropriate disk.

The Macrosystem-88 has a suggested retail price of \$995. Contact Cal-Tech Computer Services Inc., 4112 Napier St., San Diego, CA 92110, (714) 275-4350.

Circle 445 on inquiry card.



Paper Tape for Apples

Your Apple II can have complete paper-tape capability for less than \$1800 with Addmaster's parallel interface board and datahandling program. The cable, which connects the Model 600-1 punch and the Model 605 reader to your Apple, costs \$75. The Data Handling Program

costs \$100, the Model 600-1 is \$1099, and the Model 605 is \$495. Applications include numerical control and secure communications systems. Contact Addmaster Corp., 416 Junipero Serra Dr., San Gabriel, CA 91776, (213) 285-1121.

Circle 444 on ineuiry card.

IBM-Compatible Equipment

Tecmar's new line of hardware products are compatible with the IBM Personal Computer. In the vanguard is the Tecmate Expansion Chassis, a seven-slot expansion cabinet for IBM-compatible boards. It features heavyduty power supplies and provision for a 51/4-inch Winchester hard-disk drive

Some of Tecmar's other products include a time-ofday clock, a BSR X-10 device-control module, a

Winchester disk and controller, a 256K-byte programmable memory board, a serial and parallel port I/O (input/output) board, D/A (digital-toanalog) and A/D (analogto-digital) converters, a video digitizer, and a stepper motor controller. Contact Tecmar, 23600 Mercantile Rd., Cleveland, OH 44122, (216) 464-7410.

Circle 446 on inquiry card.



Super Isolator

Electronic Specialists' Super Isolator is designed to control electrical pollution that can damage your hardware. The Super Isolator features three individually dual-pi-filtered AC sockets and heavyduty spike and surge suppression. Equipment interactions are eliminated and disruptive or damaging power-line pollution, such as spikes from lightning or heavy machinery, is controlled. The Super Isolater can control pollution for a 1875-watt load; each socket can handle a 1000-watt load. The Model ISO-3 Super Isolator costs \$94.95 and is available from Electronic Specialists Inc., 171 South Main St., Natick, MA 01760, (617) 655-1532.

Circle 447 on inquiry card.



Modular Color Printer

The Prism printer is a modular 80- or 132column dot-matrix printer that allows add-on modules for expanded graphics, resolution, speed, type style, singlesheet feeding, and color abilities. The basic Prism printer is a correspondence-quality device capable of printing at up to 150 cps (characters per second) in a 24 by 9 dot matrix, expandable to a high-speed data mode of 200 cps and a character resolution of 24 by 18.

The Prism printer is based on the Motorola 6803 microprocessor and features bidirectional printing, logic-seeking abilities, and high-speed slew for increased throughput.

Optional equipment for the Prism printer includes a graphics module and a color module with a choice of three four-zone color ribbons and software for text or data modes. Up to eight colors can be produced using a four-color ribbon. Paper feed is semiautomatic cutsheet, where the operator inserts an 8½-by 11-inch sheet and the printer automatically positions it. The basic 80-column Prism printer costs \$899. Contact Integral Data Systems Inc., Milford, NH 03055, (800) 258-1386; in New Hampshire (603) 673-9100.

Circle 448 on inquiry card.



DMM Connects to Microprocessors

Sabtronics' Model 2020 Digital Multimeter (DMM) nas microprocessor interaces so that it can adapt :o any personal computer. The DMM has a 3½-digit _ED (light-emitting diode) display and 0.1% basic DC accuracy. It is capable of directly measuring AC and DC voltages of up to 1000 volts, resistances up to 20 megohms, and AC and DC currents up to 10 amperes. Optical coupling between the DMM and :he computer protects the computer from damage and serves to isolate ground noises that can affect sensitive measurements.

The Model 2020 DMM is supplied with cables and I/O (input/output) support needed for connection with TRS-80, Apple, PET, or Atari microcomputers. The DMM costs \$299, including interface and some software support. Contact Sabtronics International Inc., 5709 North 50th St., Tampa, FL 33610, (813) 623-2631. Circle 449 on inquiry card.

Timer/Counter Board

The STD-VI08 I/O timer/ counter board is handy for process control, production testing, or data logging. It features eight programmable I/O (input/ output) ports and 64 individually programmable I/O lines. The STD-VI08 has 16 programmable handshake lines that permit high-speed data transfers to peripherals and four 16-bit timers that allow a wide range of timing (2 microseconds to many hours), automatic pulse output to an I/O line, and interrupt-on-timeout capabilities. Incoming I/O signals can be monitored without the intervention of the central processor by means of four 16-bit event counters. Four programmable shift registers permit serial data to be sent and received. Fully programmable interrupts on all functions avoid the overhead of software polling. Connection to I/O devices is accomplished by standard 50-pin headers and switch-selectable addressing facilitates system configuration.

The STD-VI08 costs \$199, including a onevear warranty and documentation. It's available from Forethought Products. 87070 Dukhobar Rd., Eugene, OR 97402, (503) 485-8575.

Circle 450 on inquiry card.

Winchester and Floppy Disk System

The Model SCS-10/F Winchester hard-disk and 8-inch floppy-disk drive subsystem can interface with most popular microcomputers, including the Apple II, the TRS-80 I, II, and III, and S-100 microcomputers. The SCS-10 permits the use of most disk operating systems, which allows standard 8-inch CP/M floppy disks to operate with Apple II machines and 3.3 Apple DOS with 1.1 Pascal. Its storage capacities start at 10-megabyte configurations and range as high as 120 megabytes. For higher storage levels, daisy-chaining is permitted. The SCS-10 supports Supercalc, DB Master, and medical, legal, accounting, stock, and educational applications software packages.

The SCS-10 is shipped complète with controller, host adapter, operating software, power supply, cables, cabinet, and user manuals. For details, contact Santa Clara Systems, Inc., 560 Division St., Campbell, CA 95008. (408) 997-2010.

Circle 451 on inquiry card.

PUBLICATIONS

Short Form Catalog

Micro Power Systems has an updated edition of its short form catalog that lists all of its current products. Micro Power Systems markets digital-to-analog (D/A) and analog-to-digital (A/D) converters, precision voltage references, analog multiplexers, analog switches, op amps, and dual transistors. Included in the updated catalog is a comparison of standard MOS (metal-oxide semiconductor) devices to Micro Power Systems' custom high-density CMOS (complementary metal-oxide semiconductor) devices. Micro Power Systems custom designs LSI (large-scale integration) circuits for such applications as pacemakers and digital meters.

The short form catalog

is available from Micro Powers Systems Inc.. 3100 Alfred St., Santa Clara, CA 95050, (408) 247-5350.

Circle 452 on inquiry card.

Telecommunications Policy

Each issue of Telecommunications Policy includes articles on assessment, control, and management of developments in telecommunications and information systems. A one-year subscription to this quarterly journal costs \$124.80. Contact IPC Science and Technology Press, Ltd., 205 East 42nd St., New York, NY 10017. (212) 867-2080. In England, contact IPC Science and Technology Press, Ltd., POB 63, Westbury House, Bury St., Guildford, Surrey, GU2 5BH, England. Circle 453 on inquiry card.



Stepper Motor Catalog

Stepper motors and controls are described in Catalog ST-1 from the Bodine Electric Company. The catalog includes test data, application guides, check lists, and thermal-characteristics

information showing motor temperatures. For your free catalog, write to Bodine Electric Co., 2500 West Bradley Place, Chicago, IL 60618. Circle 454 on inquiry card.

New Books from Arcsoft

Books on the TRS-80 Color Computer and Pocket Computer are described in a free 16-page catalog from Arcsoft Publishers. The books include tips, tricks, secrets, and programming shortcuts as well as many new programs. Among Arcsoft's titles are BASIC Made Easy, 50 Color Computer Programs in BASIC for the Home, School, & Office, and 101 Pocket Computer Programming Tips & Tricks. The books range in price from \$6.95 to \$9.95. For your free catalog, contact Arcsoft Publishers, POB 132BY, Woodsboro, MD 21798, (301) 845-8856.

Circle 455 on inquiry card.

Experiments in Artificial Intelligence

John Krutch's Experiments in Artificial Intelligence for Small Computers begins with an explanation of artificial intelligence illustrated by a short Microsoft Level II BASIC program. Problemsolving, natural-language processing, and other aspects of artificial intelligence are covered in the same easily understood manner.

Experiments in Artificial Intelligence for Small Computers is available in softcover for \$8.95. Contact Howard W. Sams & Co., 4300 West 62nd St., Indianapolis, IN 46268, (800) 428-3696; in Indiana, (317) 298-5400. Circle 456 on inquiry card.

SOFTWARE

Engineering Software

Micro-Tech Associates has structural and foundation engineering software programs for the Apple II Plus microcomputer that provide an alternative to high-cost service bureaus. The disk-based Pascal and FORTRAN programs are designed for interactive use and include SBEAM, GRID, and TRUSS2D. The programs are easy to use and do not require programming knowledge. Contact Micro-Tech Associates, 2305 Appleby Court, Wheaton, IL 60187.

Circle 457 on inquiry card.

Multiplan — Electronic Spreadsheet

Multiplan, a new electronic spreadsheet, is now available from Microsoft. The spreadsheet is 63 columns wide, 255 rows deep, and several pages thick. You enter the numbers, titles, or formulas, and all computations are performed automatically. You can assign a name to any given cell or area and then access that name in future plannina activities.

Multiplan offers extensive screen messages, a menu of commands, and a Help file that's always available. Multiplan gives you a number of features: easy editing, relative references, cell formatting, and a copy command. Column widths can be

reduced from the standard 10-character column with the Format command and you can watch up to eight different areas through Multiplan's windows as you work.

Multiplan is available to run on CP/M systems and the Apple II. For details, contact Microsoft, 10700 Northup Way, Bellevue, 98004, WA 12061 828-8080.

Circle 458 on inquiry card.

Pascal Sourcebooks

The Pascal Sourcebooks are a complete library of well-structured Pascal software written in a self-documenting style. Among the Pascal Sourcebooks being offered are File System, Incremental Backup System, Report Generator, Graphic Applications-I, and Typewriter Simulators. File System lets you interrogate directories from applications program. Incremental Backup System will save recently used files so that loss of disk data is prevented. Using the UCSD Pascal system's screen editor, Report Generator lets you create word-processing-quality documentation. Examples of Pascal programs driving applications-oriented graphics are provided in Graphics Applications-I, and Typewriter Simulators turns a printer and a terminal into an electric typewriter with automatic address accumulation, envelope addressing, and line-by-line correction.

With an Apple Pascal disk, the Pascal Sourcebooks range in price from

\$49.95 to \$109.95. Contact North American Technology, Suite 23, Strand Building, 174 Concord St., Peterborough, NH 03458, (800) 854-0561, operator 860; in California (800) 432-7257, operator 860; in New Hampshire (603) 924-6048.

Circle 459 on inquiry card.

You've Earned an MBA

Context Management Systems' MBA software package blends database, electronic spreadsheet, word-processing, graphics, and communications capabilities into a single system. Once information has been added to MBA's database, it can be used without further typing or keystrokes. Specific figures can be called up and inserted into a report automatically. You can communicate numbers in rows or columns, let MBA format figures into charts or graphs, or you can return to your figures and run experimental simulations. As an electronic spreadsheet, you can change a number, and MBA will recalculate affected items.

MBA's word processor lets you prepare concise, accurate reports. The reports can use data stored in other MBA modules, so you can have MBA fill in appropriate figures as you write the report.

MBA requires an IBM Personal Computer with 192K bytes of randomaccess memory, dual disk drives, and a video monitor or an Apple III

with 256K bytes of memory, dual disk drives, and a video monitor. A modem and a printer are recommended. Contact Context Management Systems Inc., Suite 101, 23864 Hawthorne Blvd... Torrance, CA 90505, (213) 378-8277.

Circle 460 on inquiry card.

Report Manager

The Report Manager creates and instantly updates a variety of reports for financial, accounting, engineering, and scientific applications. The CP/Mbased Report Manager can generate income statements, balance sheets, sales forecasts, and other business reports. The reports can be created from any plane in the X, Y, and Z axis "data cube" generated by the program. This "third dimension" calculating ability allows for the existence of thousands of individual cells, each of which can contain a number, a label, or a formula. Report Manager has editing commands for changing or adding to a cell's contents. Reports can be up to 255 cells wide, long, and deep, and multiple report pages with controls to scan data on any page or all the pages on one column are provided.

The Report Manager has the ability to copy portions of rows or columns, entire portions of pages, or full sections from sets of pages. It lets you view four independent sections onscreen and define headings that are longer than

nominal cell widths. Calculations on calendar and time entries for determining the duration of flowcharts and work in progress can be performed.

The Report Manager is a standard feature with NEC's PC-8000 series microcomputer. Contact NEC Home Electronics USA, 1401 Estes Ave., Elk Grove Village, IL 60007, (312) 228-5900.

Circle 461 on inquiry card.

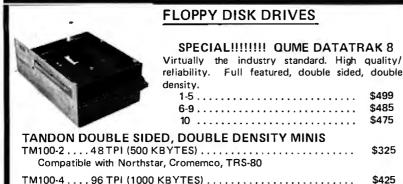
MISCELLANEOUS



Head-Cleaning Kits

The Verbatim Datalife head-cleaning kit consists of a reusable Lexan jacket, which is impervious to head-cleaning solvents, and presaturated, disposable cleaning disks. The kits are available in 51/4and 8-inch sizes and can be used on both singleand dual-head drives. Operation is easy: the disk is removed from its protective foil and polyethylene pouch, inserted in the Lexan jacket, and the whole assembly is placed in the drive for 60 seconds.

The Verbatim Datalife head-cleaning kit is not recommended for use on Vydec 8-inch-drive word processors. The kit has a



Compatible with Zenith, Heath, etc.		
TANDON 5¼" HARD DISKS TM 602 (5MB)	 	¢1105
CONTROLLERS		
Tarbell single density kit		No.

Tarbell double density A & T \$425 CCS 2422 w/CPM 2.2 \$350 \$450 MDA MXV-21 LSI-11 controller (RX-01, RX-02 compatible) \$1050

MISCELLANEOUS 2 Disk drive enclosure \$ 95 (fits Siemens, Shugart, Qume) CP-206 power supply \$110 (powers two floppies)

Mini-Enclosure with power supply 1 drive \$ 85 2 drives \$120

\$499 \$485

\$475

\$325

\$425

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2104	\$1.00	\$.75	\$.65	280	\$8.95	\$8.75	\$8.50
4116	2.25	2.15	2.00	Z80A	9.95	9.75	9.50
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2114 (450)	2.25	2.25	2.00	8085A	10.00	9.00	8.75
2114 (300)	2.50	2.25	2.00	9900	25.00	23.00	20.00
4044-25NL	3.25	3.00	2.75				
6104-3	2.00	1.75	1.50				
5101L	3.00	2.85	2.75				
2147	3.50	3.25	3.15				
EPROM	1-49	50- 99	100+up	MISC	1-49	50-9 9	100+up
5203Q	\$7.50	\$6.50	\$5.50	3242	\$9.00	\$8.00	\$7.00
5204Q	7.50	6.50	5.50	8202A	45.00	43.00	40.00
2708	3.25	2.75	2.50	8255A	5.75	5.65	5.50
2716	5.00	4.50	4.00	MM530	3/		
2732	12.00	11.00	9.00	TR1602	2B 4.00	3.85	3.75
68764	30.00	25.00	20.00	9901	4.00	3.75	3.65

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suggested price of \$12.50; a 10-pack of replacement disks costs \$20. Contact Verbatim Corp., 323 Soquel Way, Sunnyvale, CA 94086, [408] 245-4400.

Circle 462 on inquiry card.

Programmable CMOS interrupt Controller

The CDP1877 CMOS (complementary metaloxide semiconductor) IC (integrated-circuit) programmable interrupt controller is designed to minimize software and real-time overhead for multilevel priority interrupts in CDP1800-based microprocessor systems. The device features eight levels of prioritized interrupts and software-programmable vectoring to interrupt routines. The CDP1877 is a memorymapped device with latched interrupt requests and hard-wired interrupt priorities. Interrupts can be expanded in increments of eight. The CDP1877 can be cascaded into a large number of interrupts, limited only by the amount of memory space available and the extent of address coding in the microprocessor. Its multiple chip-select inputs minimize the amount of address space required for operation. Selectable 2-, 4-, 8-, and 16-byte intervals provide flexibility for interrupt-routine memory allocations.

The CDP1877 operates from a single supply voltage of 4 to 10.5 V (volts). The CDP1877C is identical to the the CDP1877 except for the operating voltage range, which is 4 to 6.5 V. Both are supplied in 28-lead plastic or hermeticallysealed ceramic DIPs (dual inline packages). The CDP1877 and the CDP1877C are priced at \$11.96 and \$8.16, respectively. Contact RCA Solid State Div., POB 3200, Somerville, NJ 08876 Circle 463 on inquiry card.

Low-Cost Oscilloscopes

The low-cost Models 2213 and 2215 are members of Tektronix's 2200 series of dual-trace, delayed-sweep oscilloscopes. Both models achieve a 60-MHz bandwidth at 20 mV to 10 V and 50 MHz at 2, 5, and 10 mV settings. The maximum sweep speed is 5 nanoseconds per division. The lightweight oscilloscopes incorporate advanced systems for easy triggering and provide Z-axis input, front-panel trace rotation, and beamfinder controls. Fewer operator adjustments are required because both units have automatic intensity and focus.

The Model 2213, with a single time base, has a screen-calibrated delayed sweep with 3% accuracy and an intensified sweep. The Model 2215 has a dual time base with 1.5% delay time accuracy and features alternate sweep switching, A/B sweep separation control, and B triggering after delay for jitterfree delayed time measurements.

The Tektronix Models

2213 and 2215 cost \$1100 and \$1400, respectively. For further details, contact Tektronix, Inc., Marketing Communications Dept., POB 1700, Beaverton, OR 97077, (800) 547-1845: in Oregon (800) 452-6773. Circle 464 on inquiry card.

Timeshared Typesetting Service

Type Share Inc. is a timeshared typesetting service that can accept sequential ASCII IAmerican Standard Code for Information Interchange) files from any computer and return typeset copy according to user coding and specifications. A computer user can input and format material for typesetting on his or her computer, send it to a Type Share center over a telephone, and receive typeset copy that's ready for paste-up and printing.

To use the Type Share system a user must have a computer/modem combination that can transmit ASCII sequential files over telephone lines. Contact Type Share Inc., 8315 Firestone Blvd., Downey, CA 90241, [213] 923-9361.

Circle 465 on inquiry card.



Add-On Memory Cards for the IBM Personal Computer

A.S.T. Research has introduced a series of ultra high-density add-on memory cards for the IBM Personal Computer that feature storage capacities ranging from 64K to 256K bytes of random-access memory. The Personal Computer-compatible cards include parity checking to ensure data integrity. Each card is thoroughly tested.

In addition to the memory cards, A.S.T. has introduced a communications option card that has two RS-232C ports and a wirewrap extender card set. The add-on memory cards range in price from \$495 to \$1595, which includes a one-year warranty. The RS-232C port communications card costs \$240, and the wire-wrap extender is available for \$95. Contact A.S.T. Research Inc., 17925 B Skypark Circle, Irvine, CA 92714, (714) 540-1333.

Circle 466 on inquiry card.

Where Do New Products Items Come From?

The information printed in the new products pages of BYTE is obtained from "new product" or "press release" copy sent by the promoters of new products. If in our judgment the information might be of interest to the personal computing experimenters and homebrewers who read BYTE, we print it in some form. We openly solicit releases and photos from manufacturers and suppliers to this marketplace. The information is printed more or less as a first-in first-out queue, subject to occasional priority modifica-tions. While we would not knowingly print untrue or inaccurate data, or data from unreliable companies, our capacity to evaluate the products and companies appearing in the "What's New?" feature is necessarily limited. We therefore cannot be responsible for product quality or company performance.

POWER SUPPLIES

FOR S-100, FLOPPY DISKS.



KIT 1, 2 & 3 For S-100



R3 For Three 8" or 51/4" Disk Drives



 $$\bf S3$$ 2 in 1 Unit for S-100 and two 8" or 5\%" Disk Drives. It fits most Disk System Mainframes.

S-100 POWER SUPPLY KITS	(OPEN FRAME WITH BASE PLATE, 3 HRS. ASSY. TIME)
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ITEM	USED FOR	@ + 8 Vdc	_ @ ~ 9 Vdc	@ + 16 Vdc	@ - 16 Vdc	@ + 28 Vdc	SIZE W × D × H	PRICE
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	SYSTEM SOURCE	25A		3A	3A		12" × 5" × 4%"	61.95
KIT 3	DISK SYSTEM	15A	1A	2A	2A	4A	14" × 6" × 4%"	69.95

DISK SYSTEM PWR SUPPLY "S3" OPEN FRAME, ASSY. & TESTED. COMPACT SIZE: 10 (W) × 6" (D) × 5" (H) 97.95 REGULATED OUTPUTS FOR DISK DRIVES +5V @ 4A. -5V @ 1A. +24V @ 4A (OR + 12V @ 4A) SHORTS PROTECT UNREGULATED OUTPUTS FOR S-100 +8V @ 14A. ± 16V @ 3A (OPTION ADD OVP FOR +5V. ADD \$5.00] A COMPLETE UNIT FOR DISK SYSTEM WITH THE MAINFRAME CONTAINING 12 SLOTS & TWO 8" or 5/4" DISK DRIVES

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ITEM	PRIMARY	SECONDARY #1	SECONDARY #2	SECONDARY #3	SIZE $W \times D \times H$	PRICE
<u>T</u> 1	110/120	2 x 8 Vac. 15A	28 Vac, CT, 2.5A		3¾" × 3¾" × 3½"	22.95
T ₂	110/120	2 × 8 Vac, 25A	28 Vac, CT, 3.5A		3¾" × 4¾" × 3½"	28.95
Tз	110/120	2 × 8 Vac, 15A	28 Vac, CT, 2.5A	48 Vac, CT, 2A	3¾" × 4¾8" × 31/8"	30.95
<u>T</u> 4	110/120	2 × 8 Vac. 6A	28 Vac, CT, 1.5A	48 Vac, CT, 3A	3¾" × 35/8" × 31/8"	23.95
T ₅	110/120	2 × 8 Vac, 6A	28 Vac, CT, 2A		3" × 3" × 2½"	15.95

SHIPPING For each power supply \$5.50 in Calif., \$8.00 in other states, \$18.00 in Canada. For each Transformer \$5.00 in all States, \$12.00 in Canada. Calif. Residents add 6% Sales Tax.



TVI 920C

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TI 783 KSR				1325
TI 765 20K				2295
TEL	EVIDEO	N C	EW	
TV/ 950				2 000

TVI 912B	855	
FLOPPY DISCS NEW		
Techtran 951		
TAPE DRIVE EIA NEW		
Techtran 8421 Dual Tape Techtran 8420 Dual Tape Techtran 8400 Single Tape Techtran 818 Single Tape Techtran 817 Single Tape	1200 995 1255	

TAPE DRIVE EIA USED	
Techtran 815 Single Tape	\$ 495 975
MODEMS AND COUPLERS	NEW
Penrit 300/1200	\$ 595

	Anderson Jacobson 1258	
	MODEMS AND COUPLERS USE	D
Ī	AJ ADAC 1200 (1200 Baud)	350
	Ventel 103 Coupler	100
	A.I. 242-A 300 Baud	110

VIDEO TERMINALS NEW-USED			
Lear ADM-3A USED 395		VIDEO TERMINALS	NEW-USED
Lear ADM-3A USED 395		Lear ADM 3A NEW	\$ 575
Beehive 8-100 USED 296 Televideo 912-B USED 396		Lear ADM-3A USED	395
Televideo 912-8 USED 396 TERMINALS & PRINTERS MISC. Execupori 3000 USED 51095 Execupori 300 USED 595 Ti 733 ASR USED 695 Teletippe 43AAA USED 775 DEC LS-120 (120 CPS) USED 795 DEC LA-36 DK Refurbished 595 Diablo 1841-1 USED 1450 1620-1 USED 1395 (Like DEC LA-120) Computer Devices 1132 Returb 750 DEC TERMINALS VT-100 USED 595 VT-100 NEW 1395 VT-52 NEW 1295 LA-180 USED 475 LS-120 USED 475 LS-120 USED 7785 LA-34DA NEW 995 LA-180 USED 475 LA-34DA NEW 995 Interface LA-180 200 DEC USI NEW TU 58-88 Dual Tape Drive 8 Ctrl (10 Meg) 4985 RLV11-AK Disk Drive & Ctrl (10 Meg) 4985 RLO1-AK Disk Drive (10 Meg) 4045 RLO1-AK DISK DRIVE (10 Meg)		Beehive 8-100 USED	296
TERMINALS & PRINTERS Stock		Televideo 912-B USED	
Execupori 3000 USED	1		
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1620 USED		Dieble 1041 1 LICED	1/50
1660-1 USED			
Clike DEC LA 120 Computer Devices 1132 Returb 750			**********
DEC TERMINALS			
DEC TERMINALS S 995			750
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Interface LA-180 DEC LS NEW		LA-34DA NEW	795
Interface LA-180 DEC LS NEW		LA-34DA USED	495
TU 58-BB Dual Tape Drive		Interface LA-180	200
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MXV11-AA Multi-Mod-8K			
MXV11-A2		MXV11-AA Multi-Mod-8K	450
MSV11-DD		MXV11-A2	22
MRV11-BA Prom Memory Unit 208			760
		MRV11-BA Prom Memory Unit.	208

KDF11-RG	2880	
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	1245	
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H317-E	799	
DUV 11-DA	470	
DRV11-J	259	
DRV11,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	135	
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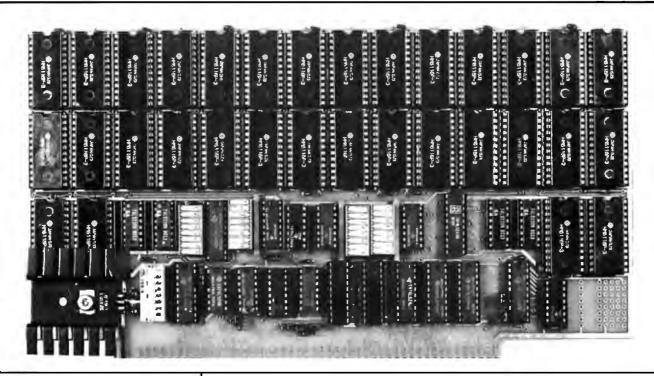
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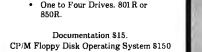
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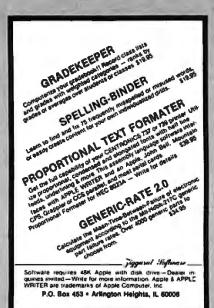
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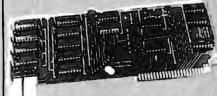
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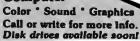
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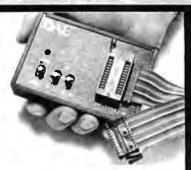


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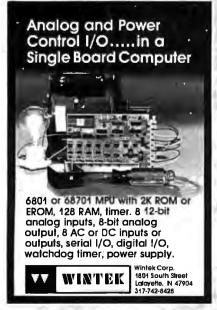
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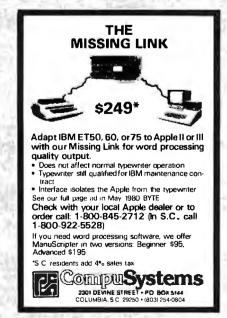
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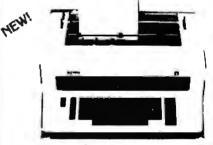
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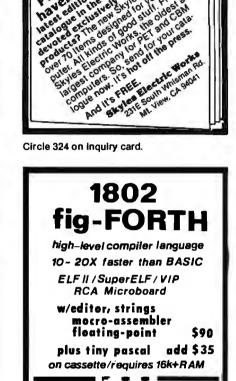
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Questdata, a software publication for 1802 computer users is available by subscription for \$12.00 per 12 issues. Single issues \$1.50. Issues 1-12 bound \$16.50.

Moews Video Graphics \$3.53, Games and Music \$3.00, Chip 8 Interpreter \$5.50, Starship 4K cassette \$14.95.

Free 14 page brochure of complete Super Elf system.

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This is truly an astounding value! This board has been designed to allow you to decide how you want it optioned. The Super Expansion Board comes with 4K of low power RAM fully addressable anywhere in 64K with built-in memory pro-tect and a cassette interface. Provisions have been made for all other options on the same board and it fits neatly into the hardwood cabinet alongside the **Super Elf**. The board includes slots for up to 6K of **EPROM** (2708, 2758, 2716 or TI 2716) and is fully socketed. EPROM can be used for the monitor and Tiny Basic or other purposes.

A 1K Super ROM Monitor \$19.95 is available as an preprogrammed with a program loader/editor and error checking multi-file cassette read/write software (relocatable cassette file) another exclusive from Quest. It includes register save and readout, block move capability and video graphics driver with blinking cursor. Break points can be used with the register save feature to isolate pro-

gram bugs quickly, then follow with single step. If you have the Super Expansion Board and Super Monitor the monitor is up and running at the push

Other on board options include Parallel Input and Output Ports with full handshake. They allow easy connection of an ASCII keyboard to the input port. RS 232 and 20 ma Current Loop for teletype or other device are on board and if you need more memory there are two S-100 slots for static RAM or video boards. Also a 1K Super Monitor version Of video boards. Also a fix super womlor version 2 with video driver for full capability display with Tiny Basic and a video interface board. Parallel I/Q Ports \$9.85, RS 232 \$4.50, TTY 20 ma I/F \$1.95, S-100 \$4.50. A 50 pin connector set with ribbon cable is available at \$18,95 for easy connection between the Super Elf and the Super

Power Supply Kit for the complete system (see Multi-volt Power Supply below).

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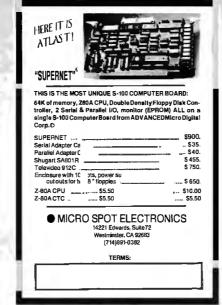
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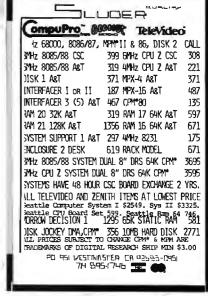
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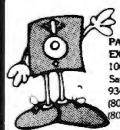
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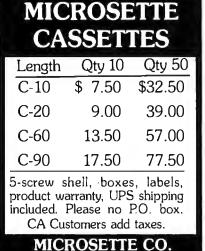
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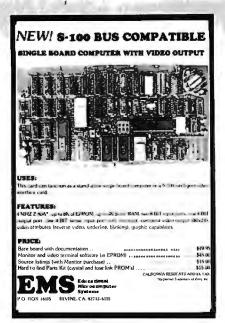
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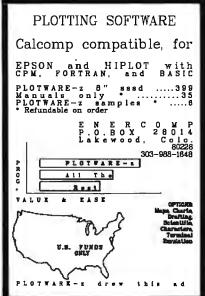
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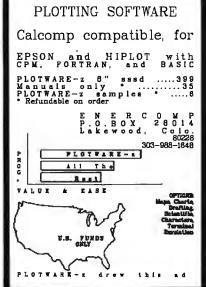




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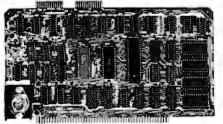
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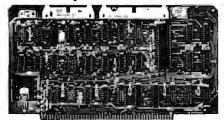
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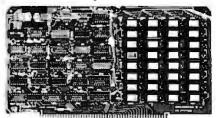


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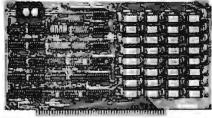
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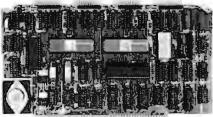
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CAT 300 band, acoustic, answer/orginate IOM-5200A List \$189.95 \$149.95 D-CAT 300 band direct connect, answer/orginate IOM-5201A List \$199.95 \$169.95

AUTO-CAT Auto answer orginate, direct connect IOM-5230A List \$299.95 \$239.95

Apple-CAT - Novation

Software selectable 1200 or 300 band, direct connect, auto-answer-auto-dial, auxiliary 3-wire RS232C serial port for printer.

IOM-5232A Save \$50.00!!! \$325.00

Accessories for Apple

16K MEMORY UPGRADE

Add 16K of RAM to your TRS-80, Apple, or Exidy in just minutes. We've sold thousands of these 16K RAM upgrades which include the appropriate memory chips (as specified by the manufacturers, all necessary jumper blocks, fool-proof instructions, and our I year guarantee. MEX-16100K TRS-80 kit \$25.00 MEX-16101K Apple kit \$25.00

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16K RAM CARD - for Apple II Expand your Apple to 64K. I year warranty MEX-16500A Save \$70.00 !!! \$129.95

Z-80* CARD for APPLE

Two computers in one, Z-80 & 6502, more than doubles the power & potential of your Apple, includes Z-80° CPU card, CP M 2.2. & BASIC-80

CPX-30800A A & T \$299.95

8" DISK CONTROLLER

New from Vista Computer, single or double sided, single or double density, compatible with DOS 3.2/3.3, Pascal, & CPM 22. Shugart & Qume compatible IOD-2700A A & T \$499.95

2 MEGABYTES for Apple II

Complete package includes: Two 8" double-density disk drives, Vista double-density 8" disk controller, cabinet, power supply. & cables. DOS 3.2/3.3. CP/M 2.2. & Pascal compatible.

1 MegaByte Package (Kit) MegaByte Package (A & T) \$1695.00 2 MegaByte Package (Kit) \$1795.00

2 MegaByte Package (A & T) \$1995.00

DISK DRIVES - Micro Sci

Inexpensive disk drives for your Apple

A2 Direct replacement for Apple Disk II, works with Apple II controller as first or second drive. MSM-123101 Micro Sci A2 \$429.95

A40 40 track drive for Apple II. Improved storage capacity and speed over Apple Brand drives - requires Micro Sci controller.

IOD-2340A Micro Sci A40 \$399.95

A70 70 track drive for Apple II. Twice the storage capacity and three times faster than Apple Brand drives requires Micro Sci controller

IOD-2370A Micro Sci A70 \$499.95

Micro Sci Controller Disk controller for up to two Micro Sci A40 or A70 disk drives, DOS 3.2, 3.3. Pascal, and Z-80 SoftCard compatible, includes utility disk and 40/70

IOD-2300A Micro Sci controller \$95.00

VISION 80 - Vista Computer

80 column x 24 line video card for Apple II. 128 ASCII characters, upper and lower case, 9 x 10 dot matrix with 3 dot descenders, standard data media terminal control codes, CP/M Pascal & Fortran compatible, 50/60 Hz

IOV-2400A Vista Vision 80 \$375.00

AIO, ASIO, APIO - S.S.M.

Parallel & serial interface for your Apple (see Byte pg 11)

CPS MULTICARD - Mtn. Computer

Three cards in one! Real time clock-calendar, serial interface, & parallel interface - all on one card. IOX-2300A A & T \$199.95

Single Board Computer

Z-80 STARTER KIT - SD Systems

Complete Z-80 microcomputer with RAM, ROM, I O, keyboard, display, kludge area, manual, & workbook CPS-30100K KIT\$299.95 CPS-30100A A & T\$469.95

SYM-1 - Synertek Systems

Single board computer with 1K of RAM, 4K of ROM, key-pad, LED display, 20ma & cassette interface on board. CPK-50020A A & T \$249.95

VIC 20 - Commodore

Complete personal computer with 5K RAM, full color, 61 key keyboard, 4 dual special-function keys, serial ports, cassette port, composite video output (connects to standard color TV set). BASIC language. & expansion port.
COM-VIC20 VIC-20 Under \$300.00

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Also available from Jade - Callfor Price and Info

AIM-65, Altos, Apple II, Atari, Commodore, California Computer Sys Hewlett-Packard, Intersystems Jade, NEC, Novell, SD Systems SYM-1, Xerox, and more...

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1.5 MHz bandwidth, 700 lines/inch, P31 green phosphor, switchahle 40 or 80 columns, small, light-weight & portable. VDM-201201 List price \$150.00 \$118.95

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20 MHz, P31 phosphor video monitor with audio, exceptionally high resolution - A fantastic monitor at a pery reasonable price

VDM-651200 Special Sale Price \$199.95

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Hi-res monitor with audio & sculptured case VDC-651212 Color Monitor \$479.95

NEC-1202D RGB color monitor ... \$1045.00

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VDM-801210 Video 100 12" B&W ... \$139.95 VDM-801230 Video 100-80 12" B&W \$179.95 VDM-801250 12" Green Phospor \$169.95

VDC-801310 13" Color I \$379.95 VDC-801320 Color II \$895.00

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TELEVIDEO 910

Full featured - inexpensive terminal VDT-901210 List 795.00 \$695.00

TELEVIDEO 950

VDT-901250 List \$1195.00 ,..... \$995.00

AMBER SCREEN - Volker Craig

Detachable keyboard, amber on black display, 7 x 9 dot matrix, 10 program function keys, 14 key numeric pad, 12" non-glare screen, 50 to 19,200 band, direct cursor centrol, auxiliary bi-directional serial port

VDT-351200 List \$795.00 \$645.00

VIEWPIONT - ADDS

Detachable keyboard, serial RS232C interface, band rates from 110 to 19,200, auxiliary serial output port, 24x 80display, VDT-501210 Sale Priced \$639.95

DIALOGUE 80 - Ampex VDT-230080 List \$1195.00 \$895.00

Circle 163 on inquiry card.

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FREE 1982 CATALOG Just circle our reader service number on the information request card located near the index.

S-100 CPU Boards

THE BIG Z* - Jade

2 or 4 MHz switchable Z-80* CPU with serial 1/O, accomodates 2708, 2716, or 2732 EPROM, baud rates from

 CPU-30201K
 Kit
 \$139.95

 CPU-30201A
 A & T
 \$189.95

 CPU-30200B
 Bare board
 \$35.00

2810 Z-80* CPU - Cal Comp Sys

2/4 MHz Z-80A* CPU with RS-232C serial I/O port and onboard MOSS 2.2 monitor PROM, front panel compatible. CPU-30400A A & T \$269.95

CB-2 Z-80 CPU - S.S.M.

2 or 4 MHz Z-80 CPU board with provision for up to 8K of ROM or 4K of RAM on board, extended addressing, IEEE S-100, front panel compatible.

S-100 PROM Boards

PROM-100 - SD Systems

2708, 2716, 2732 EPROM programmer w/software MEM-99520A A & T \$249.95

PB-1 - S.S.M.

2708, 2716 EPROM board with built-in programmer

EPROM BOARD - Jade

16K or 32K uses 2708's or 2716's, 1K houndary MEM-16230K Kit \$79.95 MEM-16230A A & T \$119.95

S-100 Video Boards

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80 characters x 24 lines expandable to 80 x 48 for a full page of text, upper & lower case, 256 user defined symbols, 160 x 192 graphics matrix, memory mapped, has key board input.

 IOV-1095K
 4 MHz kit
 \$349.95

 IOV-1095A
 4 MHz A & T
 \$439.95

 IOV-1096K
 80 x 48 upgrade
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VDB-8024 - SD Systems

80 x 24 1/O mapped video board with keyboard 1/O, and on-board Z-80A*

IOV-1020A A & T \$459.95

VIDEO BOARD - S.S.M.

64 characters x 16 lines, 128 x 48 matrix for graphics, full upper/lower case ASCII character set, numbers, symbols, and greek letters, normal/reverse/blinking video, S-100. IOV-1051B Bare board \$34.95

S-100 Motherboards

ISO-BUS - Jade

Silent, sim	ple, and on sale - a better mot	herboard
	6 Slot (54" x 8%")	
MBS-061B	Bare board	\$19.95
MBS-061K	Kit	\$39.95
MBS-061A	A & T	\$49.95
	12 Slot (9%" x 8%")	
MBS-121B	Bare board	\$29.95
MBS-121K	Kit	\$69.95
MBS-121A	A & T	\$89.95
	18 Slot (14½" x 8¾")	
MBS-181B	Bare board	\$49.95
MBS-181K	Kit	\$99.95
MBS-181A	A & T	\$139.95

S-100 RAM Boards

MEMORY BANK - Jade

4 MHz, S-100, bankselectable, expandable from 16K to 64K MEM-99730B Bare Board \$49.95 MEM-99730K Kit no RAM \$199.95 MEM-32731K 32K Kit \$239.95 MEM-64733K 64K Kit \$279.95 Assembled & Tested add \$50.00

64K RAM - Calif Computer Sys

4 MHz bank port / bank byte selectable, extended addressing, 16K bank selectable, PHANTOM line allows memory overlay, 8080 / Z-80 / front panel compatible. MEM-64565A A & T \$575.00

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64K static S-100 RAM card, 4-16K banks, up to 8MHz MEM-64400A A & T \$789.95

32K STATIC RAM - Jade

2 or 4 MHz expandable static RAM board uses 21141/s MEM-16151K 16K 4 MHz kit \$169.95 MEM-32151K 32K 4 MHz kit \$299.95 Assembled & tested add \$50.00

16K STATIC RAM - Mem Merchant

4 MHz IGK static RAM board, IEEE S-100, bank selectable. Phantom capability, addressable in 4K blocks, "disable-able" in 1K segments, extended addressing, low power MEM-16171A A & T \$164.95

S-100 Disk Controllers

DOUBLE-D - Jade

Double density controller with the inside track, on-board Z-80A*, printer port, IEEE S-100, can function on an interrupt driven buss

IOD-1200K	Kit	\$299.95
IOD-1200A	A & T	\$375.00
IOD-1200B	Bare board	. \$59.95

DOUBLE DENSITY - Cal Comp Sys

5W" and 8" disk controller, single or double density, with on-board boot loader ROM, and free CP/M 2.2* and

IOD-1300A A & T \$374.95

S-100 I/O Boards

S.P.I.C. - Jade

Our new 1/O card with 2 SIO's, 4 CTC's, and 1 PIO IOI-1045K 2 CTC's, 1 SIO, 1 PIO .. \$179.95 IOI-1046A A & T \$299.95 IOI-1045B Bare board w/ manual ... \$49.95

I/O-4 - S.S.M.

2 serial	I/O ports plus 2 parallel I/O po	orts
IOI-1010K	Kit	\$179.95
IOI-1010A	A & T	\$249.95
IOI-1010B	Bare board	\$35.00

S-100 Mainframes

MAINFRAME - Cal Comp Sys

12 slot S 100 mainframe with 20 amp power supply

EPROM ERASER - Spectronics

Ultra-violet EPROM crasers XME-3100A With out timer

Disk Drives



Handsome metal cabinet with proportionally balanced air flow system . Rugged dual drive power supply . Power cable kit . Power switch, line cord, fuse holder, cooling fan . Never-Mar rubber feet . All necessary hardware to mount 2-8" disk drives, power supply, and fan' . Does not include signal cable

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END-000431	$A \& T \dots \dots$	\$359.95

8" Disk Drive Subsystems

Single Sided, Double Density

END-000423 Kit w/2 FD100-8Ds . \$924.35 END-000424 A & Tw/2 FD100-8Ds \$1124.35. END-000433 Kit w/2 SA-801Rs ... \$999.95 END-000434 A & Tw/2 SA-801Rs \$1195.00

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Shugart SA400L sngl-sided dbl-density 40 track MSM-104000 .. \$234.95 ea 2 for \$224.95 ea

Shugart SA450 dbl-sided dbl-density 70 track MSM-104500 .. \$349.95 ea 2 for \$329.95 ea

Qume DT-5 dbl-sided dbl-density 80 track MSM-750050 . . \$359.95 ea 2 for \$349.95 ea

MPI B-51 sngl-sided dbl-density 40 track MSM-155100 .. \$234.95 ea 2 for \$224.95 ea

MPI B-52 dbl-sided dbl-density 40 track

MSM-155200 .. \$344.95 ea 2 for \$334.95 ea MPI B-91 sngl-sided dbl-density 77 track

MSM-155300 ... \$369.95 ea 2 for \$359.95 ea

MPI B-92 dbl-sided dbl-density 77 track MSM-155400 . . \$469.95 ea 2 for \$459.95 ea

8" Disk Drives

Shugart SA801R single-sided double-density MSF-10801R .. \$394.95 ea 2 for \$389.95 ea

Shugart SA851R double-sided double-density MSF-10851R .. \$554.95 ea 2 for \$529.95 ea

Qume DT-8 double-sided double-density

MSF-750080 .. \$524.95 ea 2 for \$499.95 ea

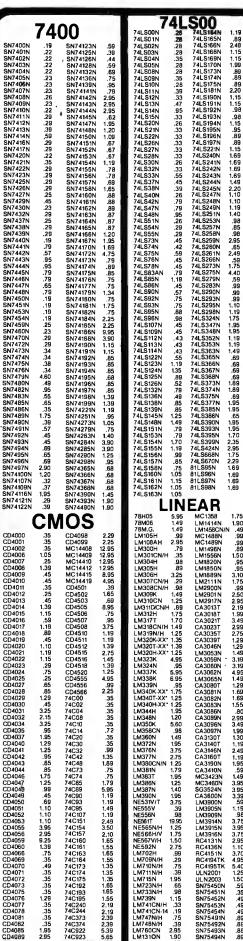
Siemens FDD 100-8 sngl-sided dbl-density MSF-201120 .. \$384.95 ea 2 for \$349.95 ea

BUS PROBE - Jade

S-100 diagnostic analyzer board, dynamic visual display of all 96 IEEE S-100 signals, aids in real time analysis of faulty hardware and software

TSX-200B Bare Board\$59.95
TSX-200K Kit\$119.95
TSX-200A A & T\$149.95

Circle 163 on inquiry card.



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ΙP	=	LOW	POWER

(200ns) (150ns)

(200ns) (150ns) (120ns) CALL

CALL

2048 x 8

2048 x 8

TMM2016

HM6116

55 55 55 55 55 55	32.766 1.0 Ml 1.8432 2.0 2.097 2.4576 3.2768 3.5799 4.0 5.0688
cs	5.185 5.7143
00 .L 95 80 75 .L	6.5536 8.0 10.0 14.318 18.0 18.432 20.0
CS	22,118
5 5 5	32.0
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74LS00	.25	74L\$85	1.15	74LS168	1.75	74LS295	1.05
74LS01	.25	74LS86	.40	74L5169	1.75	74LS298	1.20
74LS02	.25	74LS90	.65	74LS170	1.75	74LS324	1.75
74LS03	.25	74LS91	.89	74LS173	.80	74LS352	1.55
74LS04	.25	74LS92	.70	74LS174	.95	74LS353	1.55
74LS05	.25	74LS93	.65	74LS175	.95	74LS363	1.35
74LS08	.35	74LS95	.85	74LS181	2.15	74LS364	1.95
74LS10	.25	74LS96	.95	74LS189	9.95	74LS365	.95
74LS11	.35	74L\$107	.40	74LS190	1.00	74LS366	.95
74LS12	.35	74LS109	.40	74LS191	1.00		.70
74LS13	.45	74LS112	.45	74LS192	.85	74LS368	.70
74LS14	1.00	74LS113	.45	74LS193	.95	74LS373	.99
74LS15	.35	74LS114	.50	74LS194	1.00	74LS374	1.75
74LS20	.25	74LS122	45	74LS195	.95	74LS377	1.45
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74LS30	.25	74LS136	.55	74LS242	1.85	74LS393	1.90
74LS32	.35	74LS137	.99	74LS243	1.85	74LS395	1.65
74LS33	.55	74LS138	.75	74LS244	.99	74LS399	1.70
74LS37	.55	74LS139	.75	74LS245	1.90	74LS424	2.95
74LS38	.35	74LS145	1.20	74LS247	.76	74LS447	.37
74LS40	.35	74LS147	2.49	74LS248	1.25	74LS490	1.95
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74LS48	.75	74LS153	2.35	74LS253 74.LS257	.85	74LS670 74LS674	2.20
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74LS55	.35	74LS157	.75	74LS260	.65	74LS684	2.40
74LS63	1.25	74LS 158	.75	74LS266	.55	74LS685	2.40
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74LS78	.50	74LS164	.95	74LS283	1.00	81LS97	1.69
74LS83	.75	74LS165	.95	74LS290	1.25	81LS98	1.69
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7403	.19	7460	.23	74143	2.95	74192	.79
7404	.19	7470	.35	74144	2.95	74193	.79
7405	.22	7472	.29	74145	.60	74194	.85
7406	.22	7473	.34	74 147	1.75	74195	.85
7407	.22	7474	.35	74148	1.20	74196	.79
7408	.24	7475	.49	74150	1.35	74197	.75
7409	.19	7476	.35	74 15 1	.65	74198	1.35
7410	.19	7480	.59	74152	.65	74199	1.35
7411	.25	7481	1.10	74153	.55	74221	1.35
7412	.30	7482	.95	74154	1.40	74246	1.35
7413	.35	7483	.50	74155	.75	74247	1.25
74 14	.55	7485	.65	74156	,65	74248	1.85
7416	.25	7486	.35	74157	.55	74249	1.95
7417	.25	7489	4.95	74159	1.65	74251	.75
7420	.19	7490	.35	74160	.85	74259	2.25
7421	.35	7491	.40	74161	.70	74265	1.35
7422	.29	7492	.50	74162	.85	74273	1.95
7423	.29	7493	.49	74163	.85	74276	1.25
7425	.29	7494	.65	74164	.85	74279	.75
7426	.29	7495	.55	74165	.85	74283	2.00
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7428	.45	7497	2.75	74167	2.95	74285	3.75
7430	.19	74100	1.00	74170	1.65	74290	.95
7432	.29	74107	.30	74172	5.95	74293	.75
7433	.45	74109	.45	74173	.75	74298	.85
7437	.29	74110	.45	74 17 4	.89	74351	2.25
7438	.29	74111	.55	74 175	.89	74365	.65
7440	.19	74116	1.55	74176	.89	74366	.65
7442	.49	74120	1.20	74177	.75	74367	.65
7443	.65	74121	.29	74178	1.15	74368	.65
7444	.69	74122	.45	74179	1.75	74376	2.20
7445	.69	74123	.55	74180	.75	74390	1.75
7446	.59	74125	.45	74181	2.25	74393	1.35
7447	.69	74126	.45	74182	.75	74425	3.15
7448	.69	74128	.55	74184	2.00	74426	.85
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than 60 inversions, exploring a wide
range of ideas and lettering styles.

In the accompanying text, Scott explains how inversions are created, so that you may try your hand at them.

"Scott Kim's Inversions...
is one of the most astonishing and
delightful books ever printed...
Over the years Kim has developed
the magical ability to take just
about any word or short phrase
and letter it in such a way that it
exhibits some kind of striking
geometrical symmetry."

Martin Gardner,
 Scientific American

Infinity



name	
address	
cîty	
state	zip
xssmlxl xssmlxl	Black on white @ \$8.00 White on black @ \$8.75
Infinity: 100% cotton, silkscreened. Check or money order only. Sorry – no C.O.D. Dealer inquiries invited.	Postage (add \$1 per shirt) Calif. residents: add 6½% sales tax Total Enclosed

Inversions Dept. B1, P.O. Box 50697, Palo Alto, CA 94303-0662

Infinity

In this design, Scott Kim mixes idea and image, art and technology, in a swirling evocation of infinity. This intricate design was created with the aid of a computer program, which took a basic hand-drawn design,



repeated it symmetrically,



then bent it into a continuously expanding spiral.

As you look at the design, you'll discover that it can be read in two different ways. Notice that the letters "fi" when turned upside down become the "y" at the end of "infinity." And so the spiral can be read as either "infinity" going in or "infinity" coming out! Which do you see?

Infinity is the first in a series of wearable wordplays from the book *Inversions: a Catalog of Callig-raphic Cartwheels* by Scott Kim. The book is available through your local bookstore, or by calling Byte Books toll-free at 800-258-5420.

Give the Infinity shirt as a gift, wear it while doing double back somersaults, take one on your next space flight. The possibilities are infinite.

WEHAVEIT



TOMORROW'S **COMPUTERS NOW!**

from Cromemco

System Two - 64K-Z2 with dualsided mini floppies (780K), List \$4,695 . . \$3549

Call for Super Prices on Hard Disk and Multi-User systems.

CROMIX, or MP/M or OASIS Systems now available from Mini Micro Mart running CROMIX (or MP/M or OASIS) on a CDC PHOENIX (Ninety-six MB-Sixteen Removeable-Eighty Fixed) hard disk.

COMPUTER SYSTEMS

CS-0 Computer System w/SCC & MCB-216, List \$1295 \$1,099 CS-0/D Computer System 780 SCC CPU, 64 KZ, 16 FDC, List \$2,995 \$2,595 DDF Dual Double-Sided 5" Drives for CS-0, List \$1,295..... \$1,099 Z-2H Hard Disk Computer System, List \$9,995 \$8,495 A combination of the 64K System 2 with dual double-sided mini floppies and an 11-r egabyte hard disk. A complete system! HDD-11 11Megabyte Hard Disk System. List \$6,995. \$5,945 Single drive system HDD-22 22Megabyte Hard Disk System, List \$11,995 \$10,195 System Three — features 4MHz CPU, with 64K of RAM, List \$7,995\$6,795 Dual-sided PerSci 8" floppy disk drives, RS232C Interface **PRINTERS** Line Printer 3779, List \$1695 \$1,269 60 characters/sec., up to 132 ch./line; 12" platen Line Printer 3715, List \$1,295.....\$1,099 150 characters/sec., 80 ch./line or 132 ch./line; 8" line length Letter Quality Printer 3355A, List \$3,495 \$2,969 55 characters/sec., 15" platen, tractor-feed **TERMINALS** CRT Terminal 3102, List \$2,295\$1,949 80 char./line: 24 line display

CROMEMCO BOARDS

Dual drive system

SCC Single Card Computer,
List \$495 \$382
ZPU Z-80 CPU 2/4MHz, List \$395 \$335
48KTP 2 Port 48K Memory,
List \$1495 \$1269
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List \$495 \$419
64KZ Dynamic RAM Memory,
List \$1195 \$995
16FDC Disk Controller, DD,
List \$595 \$499
8K Bytesaver II Prom Programmer,
List \$295 \$249
32K Bytesaver Prom Card for 2716s,
List \$345 \$295
TU-ART I/O Interface, List \$345 \$249
D + 7A Digital/Analog Interface,
List \$295 \$210

or to or orthuraner interface,	
List \$295	\$249
4PIO 4 Port Parallel Interface,	
List \$395	\$335
QDRT 4 Channel Syn/Asyn Interface,	
List \$595	\$499
IOP Intelligent I/O Processor,	
List \$695	\$589
PRI Printer Interface Card, List \$245	\$209
16KPR 16K Prom Memory Card,	
List \$245	\$209
CGI TV Dazzler, List \$395	\$335
SDI Hi-Res Color Graphics, List \$795	\$675
EXC-2 Extender Board, List \$65	\$38
WWB-2 Wire Wrap Board, List \$65	\$38
CROMEMCO SOFTWARE	
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(Specify 8" or 51/4") CROMIX Multi-User, List \$595 \$249

FDA Macro Assembler, List \$295 \$249
FDB 16K Extended Basic, List \$195 \$165
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List \$395\$335
STB 32K Structured BASIC,
List \$295\$249
SGS Super Dazzler Graphics,
List \$595\$299
DBM Data Base Management w/Report,
List \$395 \$249
WPS Word Processing System,
List \$295 \$249
TSS Trace System Simulator,
List \$195 \$95
WRMR Writemaster Word Processing,
List \$595\$499
SLMR Slidemaster, List \$595 \$499

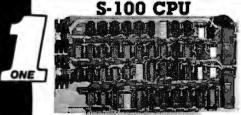
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943 W.Genesee St. Syracuse, N.Y. 13204 (315) 422-4467









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	CPU-Z - GODBOUT	
2/4	MHZ Z80 CPU 24 Bit Addre	ssing
8868T 160A	A&T	\$199.00
BBGBT 160C	CSC 3-6 MHZ	\$375.00
DUAL	PROCESSOR 8085-8088 - GC	DBOUT
6 or 8 MZ P	Provides true 16 Bit Power with	a standard 8

	bit S	-100 bus.	
BBGBT 1612A	A & T	6 MHZ	\$399.00
8868T 1612C	CSC	8 MHZ	\$498.00
SOLID	STATE DISK	DRIVE, 3500%	FASTER!
Not Really,	But the Ne	xt Best Thing	For Godbout

8085/88 Users, Call for Details on M-Drive, See Page
340 of November BYTE
BBGBT MD 128K\$1,550.00
BBGDT MD 256K\$3,000.00
2810 Z80 CPU-CA. COMP. SYST.

2/4 MHZ Z80A CPU with RS232C Serial I/O Port complete with Monitor PROM for 2422 Disk Controller BBCCS 2810A A & T.....\$280.00

CB2 Z80 CPU · S.S.M. 2/4 MHZ will accept 2716, or 2732, or RAM

RU	JN/STOP and single step switches
BBSSMCB2K	Kit
BBSSMCB2A	A & T\$310.00
BBSSMZBOM	SSMZ80 Monitor

DECEMBER OF THE PROPERTY OF TH	900.
CBIA 8080 CPU - S.S.M.	
8080 CPU, 1K RAM, Holds 1 2708,	
1 Bit parallel input port.	

BBSSMCBIA A&T.....\$225.00 BBSSM8080M SM 8080 Monitor.....\$59.00 **S-100 I/O BOARDS**

SYSTEM SUPPORT I - GODBOUT

Serial port (software prog baud), 4K EPROM OR RAM provision, 15 levels of interrupt, real time clock.

	optional math pri	ocessor	
PART NO.	DESCRIPTION	LIST PRICE	OUR PRICE
BBGBT162A	Assembled & Tested	\$399.00	\$360.00
BBGBT162C	CSC	\$495.00	\$460.00
BBGBT8231	Math Chip		\$195.00
BBG BT8232	Math Chip		\$195.00
BBGBT162AM1	A&T with 8231 Math	Chip	\$555.00
BBGBT162CM1	CSC with 8231 Math	Chip	\$655.00
BBGBT162AM2	A&T with 8232 math	Chip	\$555.00
BBGBT162CM2	CSC with 8232 Math	Chip	\$655.00

M	PX CHANNEL BO	DARD - GODBOUT	
1/0 Mu	Itiplexer, using 8	8085A-2 CPU on	board
8868T166A	A & T.	\$495.00	\$450.00
8868T166C	CSC	\$595.00	\$550.00
	TATERDE & CEN	CODROUT	

8868T166C	CSC	\$595.00	\$550.00
	INTERFACE	R I - GODBOUT	
	Two S	Serial I/O	
68687133A	A & T	\$249.00	\$219.00
-	000	6224.00	6200.00

BEG BT133C	CSC	\$324.00	\$298.00
	INTERFAC	ER II - GODBOUT	
TI	ree parallel,	one serial I/O board	
BBGBT150A	A & T	\$249.00	\$219.00

BAPR I IDAN	AGI	\$249.00	\$219.00
B868T150C	CSC	\$324.00	\$289.00
	INTERFACER III -	GODBOUT	
Eight	channel multi-use	serial I/O	board
GREBT1748A	ART	\$600.00	6630 00

8868T1748A	A & T	\$699.00	\$629.00
BBGBT1748C	CSC 200 hr. Burn In	\$849.00	\$629.00
INT	ERFACER 3 WITH 5 S	ERIAL PORTS	
BBGBT1745A	A&T	\$599.00	\$559.00
BBGBT1745C	CSC 200 hr. Burn In	\$699.00	\$629.00

MULTI I/O - MO	RROW DESIGNS	
Three Serial,	Two parallel	
88M0SM83200 A & T	\$359.00	\$329.00

BBM0SMB3200 A & T \$359.00	\$32
SWITCHBOARD - MORROW DESIGNS	
Two serial I/O, four parallel I/O,	

		one strobe port	
BBM05582411		\$299.00	\$269.00
	1/04	CCM	

	Two seria	I I/O, two parallel I/O	
8855M104X	Kit		\$210.00
8635M104A	A & T	\$290.00	\$260.00

8855M104X	Kit	•	\$210.00
8635M104A	A & T	\$290.00	\$260.00
		I/O 5 - SSM	

			I/O	5 · SSM		
2 .	Serial,	3	Parallel	including	1 Centroni	CS
BBSSMIOS	1 A	å	T	3	329.00	\$309.00

30 I/O 8 - SSM 8 Port Serial I/O with Timer BBSSMIO8A A & T \$550.00 \$495.00

2710 4 PORT SERIAL - CCS 4 Full handshaking RS232 ports and optional 2K ROM 88CCS271001 A & T \$360.00 \$310.00

2718 2 SERIAL & 2 PARALLEL - CCS

2720 4 PORT PARALLEL - CCS 4 8 bit parallel ports and optional 2K ROM BBCC\$272001 A& T \$250.00 \$225.00

S-100 10 MHZ STATIC RAM **NEW LOW PRICES!**



32K STATIC RAM - GODBOUT

RAM 20 10 MHZ, 4K byte block disable, bank select or 24 bit addressing available 8, 16, 24 or 32K

PART NO.	DESCRIPTION	LIST PRICE	OUR PRICE
BBGBT164AA8	8K A&T	\$210.00	\$190.00
BBGBT164AC8	8K CSC	\$280.00	\$260.00
BBGBT164AA16	16K A&T	\$285.00	\$260.00
8868T164AC16	16K CSC	\$355.00	\$325.00
BBGBT164AA24	24K A&T	\$355.00	\$325.00
BBGBT164AC24	24K CSC	\$425.00	\$385.00
BBGBT164AA32	32K A&T	\$425.00	\$385.00
BBGBT164AC32	32K CSC	\$495.00	\$450.00
	64K STATIC	PAM - GODBOUT	

RAM 17, 10 MHZ, 2 Watt, DMA Compatable

24 Dit Addi essing					
BBGBT175A48	48K A&T	\$650.00	\$619.00		
8868T175C48	48K CSC 200hr.	\$ 750.00	\$71 0.00		
8868T175A64	64K A&T	\$795.00	\$755.00		
8868T175C64	64K CSC 200hr.	\$895.00	\$850.00		

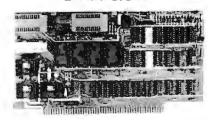
NEW! 32K x 16 BIT STATIC RAM - GODBOUT

RAM 16 10 MHZ, 32K x 16 or 64K x 8 IEEE/696 16 BIT 2 Watt, 24 Bit Addressing 64K A&T 64K CSC \$895.00 \$995.00 ARCATIANA \$850.00 BBGBT180C \$945.00

NEW! 128K STATIC RAM - GODBOUT RAM 21 10MHZ 128K X 8 OR 64K x 16

IEEE/696 8 or 16 Bit 1.2 Amps 24 Bit Addressing \$1695.00 \$1610.00 128K CSC \$1895.00 \$1795.00

S-100 ROM



PBI PROM PROGRAMMER - SSM

Programs 2708 or 2716's, operates as a

	AIN OU E	FROM DOMAD AS MELL.	
BBSSMPB1K	Kit		\$179.00
BBSSMPBIA	A & T	\$265.00	\$220.00
	ECON	DROM 2708 - GODBOUT	
16K x	8 EPRO	M Board using 2708, Power	on
	in	mn to any 256 hyte	

	jump to	o any 256 byte	
BBGBT125A	A&T	\$135.00	\$120.00
BBGBT125C	CSC	\$ 195.00	\$175.00
	M	B8A - SSM	
1K/16K 27	08 EPROM E	ooard, disable in 1K inc	rements

BBSSMMBBAK Kit BBSSMMBBAA A&T \$114.00 \$179.00 \$159.00

S-100 VIDEO BOARDS SPECTRUM - GODBOUT

Col	or Graphics	s board with Parallel I/C)		
8868T144A	A & T	\$399.00	\$349.00		
BBGBT144C	CSC	\$449.00	\$399.00		
8868T20	Sublogic L	Iniversal	\$35.00		
Graphics Interpreter Software					
	V	B - 3 S.S.M.			
00 05	CO				

80 x 25 or 50 character video display Memory Magned, Parallel Keyboard port

BBSSMVB3K24	80 x 24 Kit	•	\$425.0
BBSSMVB3A24	80 x 24 A&T	\$499.00	\$440.0
BBSSMVB3UP	80 x 50 Line Up	grade	\$ 39.0
	VB2-S	.S.M.	

I/O Mapped Video Board, with Parallel Keyboard port 64 x 16

BBSSMVB2K	Kit		\$199.00
BBSSMVB2A	A&T	\$269.00	\$229.00
		VBBB - S.S.M	
Memory	Mapped	Video Board 64 x 16 cha	racter

display or 64 x 16 graphics display BBSSMVB1 I \$179.00 RRSSMVBIA ART \$242.00 \$220.00 S-100 MOTHERBOARDS - GODBOUT

Active termination, 6-12-20 slot 8868T153A A&T 6 slot. 2 lbs \$140.00 8868T153C CSC 6 slot, 2 lbs. A&T 12 slot, 3 lbs. \$190.00 \$175.00 \$175,00 \$155.00 B068T154A RECETIOAC CSC 12 slot, 3 lbs. \$240.00 \$220.00 A&T 20 slot, 4 lbs. BRGTRISSC CSC 20 slot, 4 lbs. \$340.00 \$310.00

S-100 DYNAMIC RAM



THE EXPANDABLE 1 PRIORITY 1 ELECTRONICS THE EXPANDABLE 1" 64K Dynamic Ram board

provides your S-100 system with 64K of reliable, highspeed dynamic RAM. Compatable with most of the major S-100 systems on the market, including those with front panels, it supports DMA operations and requires no Wait states with current microprocessors. User expandable from 16 to 64K

Supports DMA Designed to IEEE proposed S-100bus standards ● 2 or

4 MHz operation • Operates with either an 8080 or Z-80 based S-100 system, providing processor-transparent refreshes with both • Supports IMSAI-type front panels made bank-independent • Fully buffered address and data lines • Fail-sale refresh circuitry for extended Wait states

Board configuration with reliable, easy-to-con-

ligure Berg jumpers BBPRIEXP116 16 16K Assembled & Tested BBPRIEXP132 32K Assembled & Tested \$339.00 48K Assembled & Tested BBPRIEXP164 64K Assembled & Tested \$409.00

S-100 DISK CONTROLLERS



DISK I - CODBOUT

FAST DMA, Soft Sector, Controls 8" or 51/4",

3,,,,5	ne u dodone d	charly con bec	1.7
		LIST PRICE	DUR PRICE
BBGBT171A	A & T	\$495.00	\$450.00
8868T171C	CSC	\$595.00	\$555.00
BBGBTCPM80*	CP/M 2.2 for ZI	30/8085 with	\$175.00
	manuals & BIO	S 8" S/D disk	
BBGBTOAS8S	Oasis 8 bit sing	gle user 8" S/D	\$500.00

disk **BBGBTOASBM** Oasis 8 bit multiuser, 8" S/D \$850.00 disk 2422A - CA. COMP. SYST.

I/O Mapped, controls 8", single or double density A&T with CPM 2.2 8" S.D. \$375.00

DISK JOCKEY 2D - MORROW Memory Mapped, controls 8", single or

double density, serial I/O RAMOSOJ2208 A&T with CP/M 2.2 \$399.00 \$375.00

S-100 DISK SUBSYSTEMS DISCUSSINGLE SIDED MORROW

8" DBL Density drives with cabinet, power supply controller, with CP/M 2.2 and Microsoft Basic BBM0\$F1218 Single Drive System \$1095.00 \$956 BBM0\$F1228 Dual Drive System \$1875.00 \$1590 DISCUS DOUBLE SIDED - MORROW \$1598.00

8" DBL Density/sided drives with cabinet Power supply controller, with CP/M 2.2 and Microsoft Basic BBMBSF2218 Single Drive System \$1395.00 Dual Drive System \$2495.00 \$1250.00



S-100 HARD DISK - MORROW

5MB, 8" 10 & 20MB, 14" 26MB formatted hard disk complete with cabinet, P.S., Controller, CP/M 2.2 and Microsoft MRASIC 80.

		LIST PRICE	SALE PRICE	
BBMOSDMAMS	5 MB	\$2495.00	\$2250.00	
BOMOSMIOS	10 MB	\$3695.00	\$2950.00	
BBMOSM20S	20 MB	\$4795.00	\$3825.00	
BBM OSM26S	26 MB	\$4495.00	\$3495.00	

S-100 SYSTEMS



"LITTLE 8" Z80 SYSTEM STARTER SET GODBOUT

CPIL 7:A 4MHz 780 A-hased 8-hit workhorse CPII hoard that includes all the standard leatures plus many of the convenience options. Meets all IEEE 696/S-100 specifications, in-

DISK 1 DMA Righ Performance Disk Controller: disk controllers don't have to be your system's bottleneck! The DISK 1 is lightning last thanks to properly implemented DMA (with arbitration)

and transfer that is independent of CPU speed.
RAM 20 32K High Speed Static RAM. This board has it all! Operates at speeds up to 10MHz, ultra-low power consumption, IEEE 696/S-100 extended addressing protocol, bank select and

CP/M 2.2: The de facto standard of 8-bit operating systems ready to load and go!

ANOTHER PRIORITY 1 EXCLUSIVE! We went to GODBOUT and made a special buy on the nucleous of the best S-100 Z80A systems ever.

LOOK AT WHAT YOU GET:

1 BBGBT160A 2/4 MHz Z80 CPU\$295	i.00
1 8868T164A32 32K 10MHz Static Ram\$42	j.00
1 BB6BT171A DMA Disk Controllers \$495	i.00
I BBG0TCPM80 CP/M 2.2	i.00
IT ALL ADDS UP TO\$1390	.00

TOTAL PACKAGE PRICE ONLY \$1095.00 ORDER NO. BBPDBGBTSG

SUPERSIXTEEN - GODBOUT LOOK WHAT \$3495.00 WILL BUY! WHY WAIT ANY LONGER?

HERE IS WHAT EACH PACKAGE INCLUDES:

BBGBT1612A 6 MHz 8085/8088 Dual Processor Board BBGBT171A High Speed DMA Disk Controller BBGBT162A System Support 1 Multi-Function Board

BBBBT1334 Interfacer 1 Dual Serial I/O BB128K IDMHz Low Power Static Ram

BBGBTCP/M 86 16 Bit Operating System Ready to Load & Go Cables and Documentation Three interfacer cables one disk I/O cable, complete documentator for all hardware, and manuals

for both CP/M operating systems.

Compu Pro's famous 1 Year limited warranty.

Now to the best part of all. If purchased separately, these quality components would list for \$4,344.00. BUT SuperSixteen's low package price is an amazing \$3.495.00. You save \$849.00!/For boards qualified under the Certified System Component high-reliability program - with extended 2 year warranty, 200 hour burn-in and 6 MHz processors - add \$600.00 to the package price. Sh. Wt. 15 lbs. BBPDBBBTS SuperSixteen A&T \$3405.00 BBPOBGBTSK SuperSixteen CSC \$4095.00

S-100 SOFTWARE

PRIORITY 1 is pleased to offer the linest in industry standard software. All software is supplied on 8" Single Density IBM 3740 CP/M compatable disketts. All software is sold "AS IS" and is non-returnable. If you have questions about the software for your application, order the manual first.

CP/M Version 2.2 Microcomputer

0000000	Control Program	inputer	3100.00
222222	MACO OD MANAGER A		***
BBCCS2301	MAC-CP/M Macro Assemb		DO.002
BBCC\$2401	SID-CP/M Symbolic Instru	\$75.00	
21214	Debugger		100.00
BBCCS2501	TEX-CP/M Text Formatter		\$75.00
BBCCS2601	DESPOOL-CP/M Backgro.	und	\$50.00
	Print Utility		
BBMOSBASBO	Microsoft Basic 80	\$200.00	
BBMOSPAS/C	Whitesmith's Pascal and C	00.0002	
BBMDSCCOMP	Whitesmith C Compiler	\$700.00	
BBMDSMFT	Microsoft Fortran	\$400.00	
	AC. SID. TEX, and DESPOO		stored
Or /IVI, IVI	trademarks of Digital Res		SICICU
PART NO.	DESCRIPTION	LIST PRICE	QUR PRICE
BBCCS401	C-BAS/C-2 Interp	\$150.00	\$139.00
BBCCS 401M		\$130.00	\$ 32.00
	Manual	1005.00	
BBCCS1101	FMS-80 by Systems Plus	\$995.00	\$895.00
BBCCS IIO1M	Manual		\$ 70.00
	Sraham-Dorian accou		
BBCCS1301	General Ledyer	\$820.00	\$750.00
BBCCS1301M	Manual		\$ 50.00
BBCCS1501	Accounts Receivable	\$820.00	\$750.00
BBCCS1501M	Manual		\$ 50.00
BBCCS1401	Accounts Payable	\$820.00	\$750.00
BBCCS1401 M	Manual		\$ 50.00
BBCCS1701	Inventory II	\$820.00	\$750.00
BBCCS1701M	Manual		\$ 50,00
BBCC\$1601	Payroll II	\$555.00	\$495.00
BBCCS1601M	Manual		\$ 50.00
BBCCS2001	Job Costing	\$820.00	\$750.00
BBCCS2001M	Manual		\$ 50.00
BBCC\$2701	Order Entry/Invoice	\$820.00	\$750.00
BBCCS2701M	Manual	0020.00	\$ 50.00
	DICAL PRACTICE PATIEN	T BILLING	
BBCCS1801	15 PROGRAMS	\$820.00	\$750.00
BBCCS1801M	Manual	WUZU.UU	\$ 50.00
	NTAL PRACTICE PATIEN	- DTT 1 1240	
BBCCS1901	19 PROGRAMS	\$820.00	\$750.00
BBCCS1901M	Manual		\$ 50.00

S-100 MAINFRAMES



S-100 MICROFRAME - TEI

110V 60HZ CVT Mainframes, the best money can buy! 12 Slot +RV 17A+16V @ 2A 22 Slot ±8V @ 30A± 6V @ 4A

PRIORITY 1 has delayed the 8% TEI Price Increase until March 1st. ORDER TODAY!

				OUR F	RICE
PART NO.			LIST PRICE	1-9	10-24
BOTEIMCS 112	12 Slot	Desk	\$685.00	\$615.00	\$570.00
BBTEIMCS 122	22 Slot	Desk	\$825.00	\$760.00	\$705.00
BOTEIRM 12	12 Slot	Rackmount	\$725.00	\$720.00	\$619.00
BBTEIRM 22	22 Slot	Rackmount	\$875.00	\$850.00	\$750.00
Shipping	Weigh	t: On 12 SI	ot Mainfr	ame 45	Ibs.

Neight: On 12 Slot Mainframe 45 lbs. On 22 Slot Mainframes 55 lbs.

TEI S-100 FRAMES 2 - 5" DISK CUTOUTS

±8V @ 17±16V @ 1.2A, Internal Cables

		1-9	10-24
BBTEITF12	12 Slot desk	\$675.00 \$625.00	\$580.00
BBTEIR012	12 Slot Rackmou	nt \$795.00\$715.00	\$665.00
Ship	ping Weight; On 1	2 Slot Desk 40 lb	s.
	On 12 Slot Rack	mount 45 lbs.	

DUAL 8" DISK DRIVE CHASSIS - TEI

For Shugart 800/801R or 850/851R with internal power cables provided

+24V @ 1.5A+5V @ 1.0A - 5V @ .25A

		1-9	10-24
BOTEIOFOO	Desk Top	\$535.00 \$485.00	\$455.00
BOTEIRFOO	Rack Mount	\$ 720.00\$670,00	\$630.00
BBPOBOFDOS1	DFDO with 1	Shugart 801R	\$\$970.00
BBP0B0F00S2	DFDO with 2	Shugart 801Rs	\$1375.00
BBPOBRFOOS1	RFDO with 1	Shugart 801R	\$1095.00
BBPOBRFOOS2	RFDO with 2	Shugart 801Rs	\$1495.00
BBPRISOPGCE2		Cable .50 pin or to 2 Card Edge	\$34.95

Due to UPS shipping regulations, disk drives will be shipped separately from the cabinet. Don't forget to include shipping for each drive. (Shipping Weight, 16 lbs each.)

CALL FOR NEW TEI PRICES MARCH 1st

S-100 MAINFRAME - GODBOUT

110V 60HZ CVT Mainframe uses famous 20 slot GODBOUT Motherboard. 55 lbs. BBGBTENC20RM 20 Slot Rack Mount BBGBTENC200K 20 Slot Desk Top \$895.00 \$825.00 \$825.00 \$760.00

GODBOUT Mainframe, Less Motherboard

& Power Supply-Kit. 23 lbs. BAGATADX DESI Desk Top Main Frame Rack Mount Main Frame **BBGBTBDX RACK** \$329.00

S-100 MAINFRAME - CCS

12-slot motherboard with removable termination card BBCC\$220001 Office Cream BBCC\$220002 Blue 35 lbs \$575.00 \$535.00 35 lbs \$575.00 \$535.00

SOFTWARE - MICROPRO

All software is supplied on 8" Single Density IBM 3740 CP/M Compatable Diskettes WORDSTAR

Screen-Oriented, integrated word processing system specifically designed for non-technical personnel 8MPRWR0STAI \$495.00 \$300.00

MAIL MERGE WORD STAR OPTION

Powerful file merging tool
BBMPRMLMRSA1(Requires Word Star 2.1 orlater)\$250.00 \$100.00

SPELLSTAR WORD STAR OPTION
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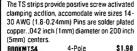
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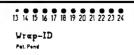


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50 PIN SOCKET 36"DBL "D" CONNECTORS

Mates with any standard female DB25 "D"

Subminiature Connector BBPGC 250P36 25 PIN IDB25P 36"SGL \$12.00 88FGC 250F060F 25 PIN IDB25P 06"DBL 88FGC 250F120F 25 PIN IDB25P 12"DBL \$17.95 \$18.25 88PGC 250P180P 25 PIN IDB25P 18"DBL \$18.55 BBPGC 250P240P 25 PIN IDB25P 24"DBL. \$18.85 BBPGC 250P360P 25 PIN IDB25P 36"DBL. 88PGC 250P600P 25 PIN IDB25P 60"DBL.

\$20.65 SPECIAL COMBINATIONS

Designed to meet the needs of computer I/O and Floppy Disi

intertacing BBPGC 265050S 26 PIN SOCKET/25 PIN IDB25S 06" BBPGC 265120S 26 PIN SOCKET/25 PIN IDB25S 12" \$14.05 BBPGC 26\$180\$ 26 PIN SOCKET/25 PIN IDB25\$ 18" \$14.35 BBPGC 26\$240\$ 26 PIN SOCKET/25 PIN IDB25S 24" \$14.65 BBPGC 253360S 26 PIN SOCKET/25 PIN IDB25S 36" BBPGC 253600S 26 PIN SOCKET/25 PIN IDB25S 60" \$16.55 BBPGC 250P060\$25 PIN IDB25P/IDB25S 06" \$18.80 BBPGC 250P120\$25 PIN IDB25P/IDB25\$ 12 \$19.10 88PGC 250P180\$25 PIN IDB25P/IDB25S 18" 88PGC 250P240\$25 PIN IDB25P/IDB25S 24" \$19.40 BBPGC 250P360\$ 25 PIN IDB25P/IDB25\$ 36" \$20.35

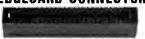
88PGC 250P600\$ 25 PIN IDB25P/IDB25S 60' 88PGC 50E06\$ 50 PIN CARD EDGE/SOCKET 06" \$16.35 50 PIN CARD EDGE/SOCKET 12'
50 PIN CARD EDGE/SOCKET 18' BAPEC SOFIAS \$17.55 50 PIN CARD EDGE/SOCKET 24" \$19.35 BRPGC 50E36S 50 PIN CARD EDGE/SOCKET 36

BBPGC 50E60S 50 PIN CARD EDGE/SOCKET 60" BBPGC 34S48E30E34 PIN SOCKET/CARD EDGE 48"/30" \$22.95 BBPGC 34S60E30E34 PIN SOCKET/CARD EDGE 60"/30" BBPGC 50\$48E30E50 PIN SOCKET/CARD EDGE 48"/30" \$31.95 \$32.95

BBPGC 50S60E30E50 PIN SOCKET/CARD EDGE 60"/30" BBPGC 34\$4BEX4 34 PIN SOCKET/EDGE CARD X 4 \$34.95 BBPGC 34S6DEX4 34 PIN SOCKET/EDGE CARD X 4 BBPGC 50S4BEX4 50 PIN SOCKET/EDGE CARD X 4 \$35.95 88PGC 50S60EX4 50 PIN SOCKET/EDGE CARD X 4

RODINSON NUGENT, INC. INCHESTER

EDGECARD CONNECTOR



1" Spacing, Crimps onto cable with ordinary vise & mates with standard .062" Card Edge.

	NO. OF		PRICE		
PART NO.	PINS	1-9	10-24	25-99	100-249
BBRNIOE20	10/20	4.35	4.00	3.30	3.00
BBANIDE26	13/26	5.00	4.50	5.75	3.25
BBRNIDE34	17/34	6.00	5.40	4.50	4.00
BBANICE 40	20/40	6.90	6.20	5.30	4.80
BBANIDE50	25/50	7.25	6.80	5.90	5.30

SOCKET CONNECTOR



.1" Spacing. Crimps onto cable with ordinary vise & mounts to header sold

	NU. UF		PHICE		
PART NO.	PINS	1-9	10-24	25-99	100-249
BBRN10S20	10/20	2.75	2.50	1.85	1.70
BBANIS26	13/26	3.50	3.20	2.40	2.20
BBANIS34	17/34	4.50	4.20	3.10	2.90
BBRNI\$40	20/40	5.40	5.00	3.65	3.30
BBRN1S50	25/50	6.50	6.00	4.60	4.20
		_	and the later of t		

HEADER CONNECTOR



RIGHT	ANGLE SOLD	ERTAIL	GOLD	HEADER	
PART NO.	1-9	10-24	25-99	100-249	
BBANSIDH2DSA	1.90	1.60	1.20	1.00	
BORNSIOH26SR	2.25	2.00	1.55	1.30	
BBANSIOH34SA	2.95	2.60	2.05	1.70	
BBANSIDH40SA	3.60	3.00	2.40	2.10	
REPRINCIPATION	4.30	3.60	3.00	2.55	

RIGHT ANGLE WIRE WRAP GOLD HEADER PART NO. 10-24 25-99 100-249 ABBRIOUSOWS 4.15 2.40 BBANIDH26WA 5.30 4.30 3.60 BERNICH34WB 5 05 5 00 4.15 3.70 BBRNIOH40WR 6.00 4.90 4.30 REGNIONSOWA 7 95 6 RO

Straight headers are also available at the ab WE Drices Drop the B from the end of the part number to specify Straight BBRNIEJ24 Header Elector Bars (Package of 4) \$1.00



COLOR CODED LAMINATED CABLE FOR INSULATION DISPLACEMENT 28 GUAGE, 7 STRAND

	NO. OF	PRICE PER	SPOOL/C
PART NO.	CONDUCTORS	10 Ft.	100 Ft.
BBIDCO9CC*	9	3.80	30.00
BBIDC14CC*	14	4.75	40.00
BBIDC16CC*	16	5.50	45.00
BBIOC2OCC*	20	7.00	60.00
BBIOC25CC*	25	8.50	72.00
BBIDC26CC*	26	8.50	72.00
BBIDC34CC*	34	11.00	100.00
BBIOC4DCC*	40	13.00	115.00
BBIDC5DCC*	50	16.00	145.00
GRAY LAMINAT	TEN CARLE FOR INSII	ATION DISPLA	CEMENT

28 Raune 7 Str

	zo bauge / Strano		
	NO. OF	PRICE PE	A SPOOL /C
PART NO.	CONDUCTORS	10 Ft.	100 Ft,
BBIOCD96Y*	9	2.50	18.05
80IDC146Y*	14	3.50	28.00
BBIOCI66Y*	16	4.00	32.00
BBIDC206Y*	20	4.80	40.00
BBI0C256Y*	25	6.00	50.00
8810C266Y*	26	6.00	50.00
BBIOC34GY*	34	8.30	66.00
BBIDC4DGY*	40	10.00	77.00
BBIOC50GY*	50	12.00	95.00
	*Add "/C" to Part No. for 100 F	. Spool	

Connectors, Plu

D-SUBMINIATURE CONNECTORS





Solder Style solders onto cale, IDC. Style comps onto cable with vise.

INSULATION DISPLACEMENT TYPE

1000
00-249
3.20
3.40
.95
3.40
3.90
.95
4.70
5.00
1.20
6.40
B.20
1.60

SOLDER TYPE

PART NO.	DESCRIPTION		PRICE	
		1-9	10-24	25-99
BBCNOOE9P	9 Pin Male	\$2.10	\$1.90	\$1.70
BBCNODE9S	9 Pin Female	\$2.70	\$2.40	\$2.10
BBCNOOE9C	9 Pin Cover	\$1.50	\$1.25	\$1.10
BBCMDDA15P	15 Pin Male	\$2.75	S2.45	\$2,15
BBCNOUA15S	15 Pin Female	\$3.95	\$3.60	\$3.20
BBCNOOA15C	15 Pin Cover	\$1.50	\$1.30	\$1.10
88CN00825P	25 Pin Male	\$3.00	\$2.75	\$2.25
* BBCNDD	B25P 100 pcs	at \$	1.95	ea *
BBCNOD825S	25 Pin Female	\$4.00	\$3.75	\$3.00
* BBCNDD	B25S 100 pcs	at S	2.95	ea *
BBCN00B51226	2 Pc. Black Hood	\$1.90	\$1.65	\$1.45
00CNOD051212	1 Pc. Grey Hood	\$1.60	\$1.45	\$1.30
BBCNOP25H	2 Pc. Grey Hood	\$1.50	\$1.25	\$1.10
BBCNDOC37P	37 Pin Male	\$5.80	\$5.10	\$4.45
BBCNOOC37S	37 Pin Female	\$8.70	\$7.70	\$6.70
BBCNODC37C	37 Pin Cover	\$1.80	\$1.55	\$1.30
BBCN00050P	50 Pin Male	\$8.75	\$7.75	\$6.70
BBCNODO50S	50 Pin Female	\$11.65	\$10.25	\$8.90
BOCNOO050C	50 Prn Cover	\$2.00	\$1.80	\$1.60
BBCN0020418	Hardware Set 2 Pr. RS232, DB25P, EIA	\$1.00	\$.80	\$.70
BBCNDAS232BF	Class 1 Cable 8 Con. 8 F	t\$19.95	\$17.95	\$15.95
BBCN05730360	Cent. 700 Series/Epson Printer Conn.		\$7.50	\$6.00
	· · · · · · · · · · · · · · · · · · ·			

DIP PLUGS

881005730360



IDC Version of Above

\$9.00

\$8.00

SUCKEL.	NO. OF				
PART NO.	PINS	1-9	10-24	25-99	100-249
BBANIOP14	14	1.50	1.40	1.25	1.10
BORNIDPI6	16	1.70	1.60	1.45	1.30
BBRN1DP24	24	2.50	2.20	2.00	1.80
BBRNIDP40	40	4.15	3.65	3.30	3,00

RN ICU Series Solder Tail Sockets

End side stackable, Low profile Closed Entry, Lead Entry has RN "EZ" Entry teature to guide IC leads into socket. Standoft to facilitate board cleaning. Self lock leads hold socket firmly in place while soldering. Contact's long movement arm provides low insertion force. Normal force of contact combined with uncoiling force provide high retention (making socket vibration resistant). Gas tight. Tin Plated.

			11	J-49	- 1	UU·49	9	1.000+
	PART NO.	PINS	1-9		50-9	9	500	999
	BBANSOBLP	80	N/A	.15	.10	.08	.07	.06
	BBRNS14LP	14	N/A	.18	.15	.14	.12	.11
	BBANS16LP	16	N/A	.20	.18	.16	.13	.12
	BBRNSIBLE	18	.30	.25	.22	.18	.15	.13
	BORNS2OLP	20	.30	.25	.23	.20	.17	.145
1	BBRNS2ZLP	22	.35	.30	.25	.22	.19	.17
ı	BBRNS24LP	24	.40	.35	.30	.24	.20	.18
ı	BBANS2BLP	28	.45	.40	.35	.28	.24	.21
	BBRNS40LP	40	.50	.45	.42	.40	.35	.31

*MINIMUM ORDER \$1.00 Per Line Item Call tor RN High Reliability Solder Sockets

ELECTRONICS PRIORITY 9161-B DEERING AVE. • CHATSWORTH, CA 91311

ORDER TOLL FREE (800) 423-5922 CA, AK, HI CALL (213) 709-5464
Terms UE YEA, MC, IMC, Check Maney Order U.S. Puris Only CAnadomis add of Sales Tax, MINIMUM PRPAID ORDER \$1500
Include MINIMUM SHIPPINGS HAND LING of \$250 for the first 3 lbs. plus 25s for each additional pound. Orders over 50 lbs. sent reight collect. Just in case, please include your phone no. Prices subject to change without notice. We will do our best to maintain prices through 1 Petruriny, 1982, SCCKLT and CONNECTOR prices brased on GOLD Indexceeding \$700.00 per oz. Credit Card orders will be charged appropriate freight



Sockets

ICN SERIES GOLD 3 LEVEL WIRE WRAF SOCKETS

ONE'

10 # in GOLD Plated Pins Deep Chamfered Clused Ent Contacts

RN Side Wipe Contact Design Phosphor Bronze Contact Material Terminal Barbs Allow Self-lock into

PC Board Rugged Socket Body Design

Deep Chamfered Closed Entry Contacts PRICE

PART NO.	PINS	1.9	10-24	25-99	100-249	250-999
BBANSO8WWG	8	.60	.55	.49	.45	.41
BBRNS14WWG	14	.75	.70	.65	.55	.48
BBRNSIGWWG	16	.85	.75	.70	.60	.52
BBRNSIBWWG	18	1.00	.90	.80	.75	.71
BBRNS2DWWG	20	1.20	1.05	.96	.91	.87
BBRNS22WWG	22	1.35	1.25	1.15	1.05	.29
BBRNS24WWG	24	1.35	1.25	1.15	1.05	.99
BBANS28WWG	28	1.70	1.55	1.40	1.34	1.25
BBRNS40WWG	40	2.20	2.05	1.85	1.60	1.50



NEW! SELECTIVE PLATED PINS THAT WILL SAVE YOU MONEY BY HAVING GOLD ONLY WHERE IT COUNTS! Same as above except pins are selectively plated.

	•		PRICE			
		1-9	10-24	25.99	100-24	250-99
JERNSOETWW	В	.55	.50	.45	.41	.37
BBRNS14TWW	14	.65	.55	.50	.47	.44
BBRNS16TWW	16	.75	.65	.52	.51	.46
BBRNSI8TWW	18	.90	.79	.75	.70	.65
BORNS20TWW	20	1.10	.95	.91	.87	.82
BBRNS22TWW	22	1.25	1.15	1.05	.94	.89
BBRNS24TWW	24	1.25	1.15	1.05	.96	.89
BBRNS2BTWW	28	1.50	1.45	1.35	1.25	1.15
BBRNS40TWW	40	2.00	1.80	1.60	1.40	1.30

Call for RN High Reliability Wire Wrap Sockets

PRECUT WIRE WRAP WIRE Precut Wire Save Time and Costs Less Than Wire on Spools



Kynar precut wire. All lengths are overall, including 1" strip on each end. Color and lengths cannot be mixed for quantity pricing. Choose from colors Red (R) Blue (U) Black (B) and Yellow (Y)

		/C	/0	/M
		100/Tube	SOR/Tube	1000/Teha
BBLFLASS1.	۷۵	31.38	\$3.94	\$0.1A
BBP6P030†*	3.0	1.43	4.25	6.78
88P G P035†*	35	1.51	4.57	7.37
BBPGPD40†*	40	1.56	4.88	7.94
BBPGP045 † *	4.5	1.63	5.21	B.54
88PGP050†*	5 0	1.69	5.54	9.13
BBPGP055 † *	55	1.74	5.92	9.72
88P6P060 † *	60	1.82	6.23	10.31
88P6P070†*	70	2.19	7.44	12.44
88P6P080†*	60	2.35	B 12	13.79
88P6P090†*	90	2.46	B.92	15.01
BBP6P100 + *	100	2.63	9.58	16.28
	kage si	ze when ordering	j: 100 (C), 50	00 (0), 1000 (M

* Specify color wien ordering. REO (R). BLUE (U). BLACK (8). &

<u> </u>							
BBPGP	WKI*		£9.95 I	BBPGPV	rk3*		
	CON	TAINS			CON	TAINS	
200	3"	100	41,"	250	212"	500	45"
200	31	100	5	500	3	500	5
100	4.	100	ŧ	500	31.√	500	51,2
BOPSP	WK2*		\$2 95	500	4	500	6
	CON	TAINS		BOPGPW	K4°		\$54.93
250	21	250	5		CON	TAINS	
500	3°	100	51.	500	212	1000	412"
500	31."	100	6	1000	3	1000	5 -
500	4"	100	61	1000	312	1000	5"
250	41.	100	7	1000	4"	1000	6"

Wire kit assortments are available in the 4 colors mentioned above along with a rainbow assortment. Use color code (A) for the rainbow

REPERMEND

Tomas 7	400	As Seen on "Good Morning America" Replaces the Telephone Ringer Bell with a Selection of 30 Familiar Tune	INVERSIL
SN7400N 20 SN7427 SN7400N 20 SN7427 SN7402N 25 SN7428 SN7402N 25 SN7428 SN7402N 25 SN7428 SN7402N 25 SN7428 SN7403N 25 SN7428 SN7403N 25 SN7428 SN7403N 25 SN7428 SN7403N 29 SN7428 SN7403N 29 SN7428 SN7410N 29 SN7428 SN7428N 25 SN7428 SN7428N 29 SN7428 SN7428N 29 SN7428 SN7428N 29 SN7428 SN7428N 29 SN7418 SN7428N 29 SN7418 SN7438N 29 SN7418 SN7439N 29 SN7418 SN7448N 30 SN	N .29 SN/14156N N .35 SN/14157N N .35 SN/14161N N .36 SN/14161N N .36 SN/14161N N .36 SN/14161N N .36 SN/14165N N .36 SN/14165N N .36 SN/14165N N .36 SN/14157N N .37 SN/14157N N .37 SN/14157N N .37 SN/14157N N .38 SN/14157N N .38 SN/14157N N .38 SN/14157N N .38 SN/14157N N .39 SN/1	Rude Britania Rude B	Part No. Function Price 7058 P CMOS Precision Timer 14,95 7058 P 16, Circuit Board, Oisplay 34,95 7057 P 16, Circuit Board, Oisplay 34,95 7057 P 16, Circuit Board, Oisplay 24,95 7057 P 16, Circuit Board, Oisplay 24,95 7057 P 16, Circuit Board, Oisplay 24,95 7057 P 7057 P 7057 P 7057 P 7057 P 7057 P 7058
SN7470N .29 SN74155	N .79	MAN 74	74C00 39 74C5 1.59 74C240 2.25 74C04 39 74C101 1.89 74C240 2.25 74C10 1.99 74C101 1.89 74C240 2.25 74C10 1.99 74C101 1.89 74C241 2.25 74C10 1.99 74C101 1.99 74C241 2.99 74C10 1.99 74C10 1.99 74C903 1.99 74C10 1.99 74C10 1.99 74C903 1.99 74C10 1.99 74C10 1.99 74C911 1.095 74C10 1.99 74C10 1.99 74C911 1.095 74C10 1.99 74C10 1.99 74C911 1.99 74C10 1.99 74C10 1.99 74C91 1.99 74C90 1.99 74C10 1.99 74C91 1.99 74C90 1.99 74C10 1.99 74C91 1.99 74C90 1.99 74C10 1.99 74C10 1.
74500 45 74502 45 74503 45 745124 74504 55 745124 74506 55 745134 74508 50 745126 74510 45 74512 74510 45 74512 45 74512	745243 3.95 745241 745241 745241 745241 745241 745241 745241 745241 745241 745241 745241 745241 745241 745241 745241 745242 745242 745243	1	LM317MP
CA3023H 3.25 CA3060 CA3035H 2.49 CA3080 CA3039H 1.35 CA3082 CA3046N 1.30 CA3083 CA3059N 3.25 CA3083	N 3.25 CA3130H H 1.25 CA3140H N 2.00 CA3160H N 1.60 CA3401N	25 ASST. 1 5ea. 27 Ohm 33 Ohm 39 Ohm 18 Ohm 22 Ohm 50 pcs. \$1.95	50 VOLT CERAMIC DISC CAPACITORS Value 1-9 10-99 100+ Value 1-9 10-99 100+
CD4000 .39 CD4000 .39 CD4000 .39 CD4000 .39 CD4000 .39 CD4000 .49 CD4001 .49 CD4011 .39 CD4012 .25 CD4012 .25 CD4013 .49 CD4014 .39 CD4015 .119 CD4015 .19 CD4016 .139 CD4017 .139 CD4018	1.49 C DM562 C DM565 G DM565 G DM565 G DM563 G DM593 G C DM723 C DM724 M G MC14410 9 M C14410 1.39 MC14411 1.39 MC14412 1.55 M C14419	ASST. 2 Sea. 180 Dhm 220 Dhm 330 Dhm 390 Dhm 50 ppcs. \$1,95 4 ASST. 3 Sea. 1,2K 1,5K 1,8K 2,2K 2,7K 50pcs. \$1,95 ASST. 4 Sea. 3,3K 3,9K 4,7K 5,6K 6,8K 50pcs. \$1,95 ASST. 5 Sea. 22K 27K 33K 39K 47K 50pcs. \$1,95 ASST. 5 Sea. 22K 27K 33K 39K 47K 50pcs. \$1,95 ASST. 6 Sea. 190K 120K 220K 270K 330K 50pcs. \$1,95 ASST. 6 Sea. 2,3M 3,3M 1,5M 220K 50pcs. \$1,95 ASST. 7 Sea. 2,7M 3,3M 1,5M 220K 50pcs. \$1,95 ASST. 8 Includes Resistor Assts. 1-7 (350 pcs.) \$1,95 ASST. 8 Includes Resistor Assts. 1-7 (350 pcs.) \$10,95 ea. S10,00 Min. Order – U.S. Funds Only Calif. Residents Add 6% Sales Tax Postage—Add 5% plus \$1,50 Insurance ASST. 8 Sea. 2 Sea. 10 Se	100 pt



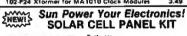
AUTOMOTIVE/
INSTRUMENT
CLOCK
APPLICATIONS:
In-dash autoclocks
After-market auto/
RV clocks
- aircraft-marine ciks.
- 12VDC oper, instruParable-battatay
powered instrumnts.

Features:Bright 0.3" green display, Internal crystal time-base. 20.5 sec./day accur. Auto. display brightness control logic. Display color filterable to blue, blue-green, green & yellow. Complete—just add switches and lens.

MA1003 Module (3.05"L.x1.75"Hx.98"D) . \$16.95

CLOCK MODULES	
MA1023 ,7" Red Digital LED Clock Medule	8.0
MA1026 .7" Dig LED Alarm Clock/Thermometer	18.9
MA5036 .3" Red Digital LED Clock/Timer	6.9
MA1002 ,5" Red Digital LED Clock & Xformer	9.9
MA1010 .8" Red Digital LED Clock	7.9
MA1032 CBA 5" Digital LCD Clock	17.9
MA1043 .7" Green Digital LED Clock	8.9

TRANSFORMERS	0.92
102-P20 Xformer for MA 1023, 1043 & 5036 102-P22 Xformer for MA 1026 Clock Modul 102-P24 Xformer for MA 1010 Clock Modul	Pa 3.49





- Features:

 Output: 10VDC, to 100mA in Series
 SVDC, to 200mA in Parallel Panel may be easily connected for Series or Parallel out
 Over 11 square inches of active cell surface
- * Voltage line tap @ 0.5V increments
 Provision for charging batteries
 Overall panel size:
 4%"L x 47;"H x ½"D

The JE305 Solar Cell Panel Kit contains 20 each solar cells. On the panel board ære power line tags which allow the user to select voltages lonevoltage at a time! from GSVDC to NVDC. The applications of each for more voltage or in parellel for more current. The premium grade solar cells provide the current necessary for the operation of most por-table transistor radios, small battery powered cassette tape players and winimide deperimental solar projects.

EPROM Erasing Lamp



- inates static build-up.

UVS-11E \$79.95

JOYSTICKS





	4 34640
JS-5K	5K Linear Taper Pots \$5.29
JS-100K	100K Linear Taper Pots , , \$4.95
JVC-40	40K (2) Video Controller in case \$4.95

ALLIGATOR CLIP TEST LEADS



Heavy.duty leads, colorcoded, insutated alligator clip on each end. 15' long, Two each black, red, blue, white and vallow.

#ALCP (10 per pack) \$2.95/pkg.

JE215 Adjustable Dual Power Supply

General Description: The JE215 is a Dual Power Supply with independent adjustable positive and negative output voltages. A separate adjustment of the supplies provides the user unlimited applications for 1C current voltage requirements. The supply can also be used as a general all-purpose variable power supply.

- s a general all-purpose variable power FEATURES:
 Adjustable regulated power supplies, pos. and neg. 1.2VDC to 15VDC. Power Output (sech supply): 52VDC 9000 pos. and neg. 1.2VDC 9000 pos. and 15VDC 9000 pos.

JE215 Adj. Dual Power Supply Kit (as shown) . . \$24.95

(Picture not shown but similar in construction to above)
JE 200 Reg. Power Supply Kit (SVOC, 1 amp) . \$14.95
JE205 Adapter Brd. (to JE200) : 5; 98 & : 12V. \$12.95
JE210Var. Pwr. Sply. Kit, 5-15VOC, to 1.5amp. \$19.95 2/82

MICROPROCESSOR COMPONENTS

A/SUSUA SUPPORT DEVICE

#Bil input/Outout

Priority Interrupt Control

Bi-Directional Bus Driver

Clock Generator/Driver

Bus Oriver

System Controller/Bus Driver Bey Direct

Stylen Controller (Bus Oriver)

System Controller

I/O Eapander for 48 Series

Asynchronous Comm. A seweral

Prog. Comm. I/O (L/S ART)

Prog. Dermaner I I/O (PPI)

Prog. Dermaner I I/O (PPI)

Prog. Interval Controller

Prog. CRT Controller

Prog. CRT Controller

System Timing Element

Sell 8 Gilorectional Receiver

Sell 8 Gilorectional Receiver

Sell 8 Gilorectional Receiver

Sell 8 Gilorectional Receiver

CRT Controller

COLS Lational Peripheral Dirver

Octal Lational Peripheral Dirver

Octal Lational Peripheral Dirver RAM'S - Zéni Shatta 1024xi Dynamic 26x4 Static 1024xi Static 1024xi Static 1101 1103 2101 (8101) 109As I Static
109As I Static
109As Static
1 8.% 6.95 3.95 3.95 3.95 5.25 5.25 2111(1111) O/BROD SUPPORT DEVICES O/BROD SUPPORT DEVICES - MPU
MPU with Clock and RAB
Black State, RAM
Periphrasi state, Adapt 14/C6006
Periphrasi state, Adapt 14/C6006
Periphrasi state, Adapt 14/C6006
Periphrasi state, Adapt 14/C6006
Periphrasi state, Deven Appler
Sprich Periphrasi Santa Data Adapter
Petition State State State
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ICHOPROCESSOR CHIPS 284 (700C) 2844 (700-1) CD2002 Deuj Men, -- Bit State (Com. Temp, Geger)
Men, -- Bit State (Com. Temp, Geger)
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MPU-II-IIII

SHIFT REGISTERS

Dual 7-Bit Dynamic

Dual 19-Bit Dynamic

Dual 19-Bit Static

Cual 64-Bit Accumulator

26-Bit Dynamic

204-Bit Dynamic/Accumulator

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Calculation of the calculation of the Pos. Configuration Socket 123456 14 pln 1234567 14 pin 12345678 16 pin 123456789 18 pin 0123456789 20 pin

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Developmental systam for microcomputer circuits

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To compare EPROMIN 9 received filterances.

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x 8 ½". Weight: 5 lbs. The JEGGE PROM Programmer is a completely self-conteined unit which is independent of computer control and requires no additional systems for its operations. The EPROM can be programmed Profit on the Hexadecimal Keyboard or from a programmed EPROM by the use of its internal RAM cliculas. This well-slow shause in classification of the state of t

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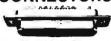
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**Documentation for Conversion

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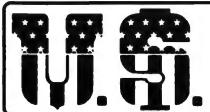
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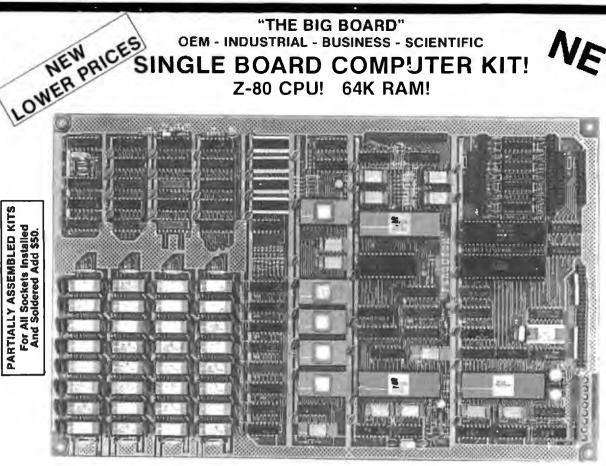
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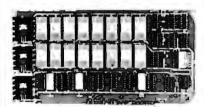
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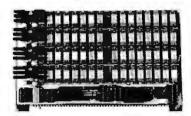
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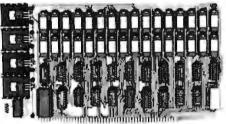
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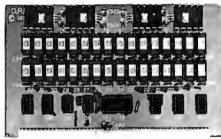
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FOR SALE: Anderson Jacobson AJ 841 Selectric terminal, RS-232C-type serial interface needs some work. Asking \$500. Nancy McCarty. 422 Washington St., Auburn, ME 04220, [207] 784-5354.

FOR SALE: Computers in Medicine: An Introduction by Derek Enlander. This is a good book on the subject. I have extra copies from a course. \$15 including postage. Tobin, 444 East 75th St., New York, NY 10021.

SORCERER OWNERS: Do you have any programs or information you would like to trade? I have 100 programs to offer, Rick Carlsen, 247 Bath Rd., Apt. #710, Kingston, Ontario K7M 2X9 Canada.

WANTED: Alpha Micro hardware, compatible hardware, and peripherals. Also want a Cromemco Z2 mainframe. Must be reasonable. Steve Waechter, 3691 Linnet Dr., Lake Elsinore. CA 92330, [714] 674-3071.

FOR SALE: HP-85 computer with 16 K, five data cartridges, carrying case, many programs, and all accessories. In excellent condition: \$2500. Also, HP-2621P video-display terminal with 80 by 24 display, internal thermal printer, and 12 rolls of paper; \$1500. Barry McDonald, 103 Godwin Ave., Midland Park, NJ 07432.

FOR SALE: LEX-11 modem with wall mount transformer equal to Bell 103A; \$100 or best offer. California Computer Systems #2718 parallel/serial interface board for \$-100; \$200 or best offer. M.R. Essig, 1005 Market \$1. #208, San Francisco, CA 94103, [415] 861-5482.

FOR SALE: Polymorphic 8813 engineering computer (can run under CP/M) with 56 K programmable memory, floating-point hardware, two disk drives, serial interface, BASIC, FORTRAN, Word Master, Finite Element Analysis, and miscellaneous engineering software. \$3000. R. Krofick, 520 Blankschool Rd., Greensburg, PA 15601. (412) 832-9759.

FOR SALE: SSM AIO serial/parallel interface card (assembled): \$130. Mountain Computer Supertaiker speech synthesizer: \$180. For Apple II. David Chau, 87 Valley Rd., Larchmont, NY 10538, [212] 834-4851,

FOR SALE: RS-232 cables. New and unused. 6½ feet long with hoods. Pins 1 through 7 and 20 are connected, male to female (can be used as extensions). \$10 each. Will rewire—specify gender and whether null modem or normal wiring—for \$1 each. Please add \$2 shipping. I have 30 of these. Mark Whitis, 7415 Colton Lane. Manassas, VA 22110.

FOR SALE: Assembled and working Heathkit H-B with 16 K memory and H-B-5 serial cassette interface board. Also, H-9 video-display terminal. Included are Extended BASIC, regular BASIC, TED-B, HASL-B, and all operations manuals. Best offer received by 30 days after this issue is published takes it all. Jerry Gunn, 5317 North Diane Court, Peoria, IL 61615.

FOR SALE: Micro-Sci A70 disk drive with controller and system master disk. Used less than six months. \$550, shipping included. Warren Spivack, 6625 Avenue M, Brooklyn, NY 11234, [212] 494-5250 days.

WANTED: A few copies of magazines: Popular Electronics for January to May 1981 and Microsystems, vol. 1, no. 1 and 3; vol. 2, no. 2. Will sell or trade BYTEs for 1978 and 1979. O.K. Hudson. 334 Olney Dr., San Antonio, TX 78209, [512] 878-1738.

FOR SALE: Heath H-10A paper-tape punch/reader with paper-tape software kit for H-11A, in excellent condition; \$100. Heath H-11-5 serial interface card and cable, no manual, in excellent condition; \$100. John Emberley, 5614 Nicollet Ave. S, Minneapolis, MN 55419, [612] 866-8364 between 9 a.m. and 2 p.m.

WANTED: Front panel for Cromemco, intersystems, IMSAI, or Altair S-100 computer, in that order of preference. Will consider buying entire mainframe less boards. Gary Sanford. POB 1689, Lowell, MA 01853, [617] 263-2389 evenings.

WANTED: Used TRS-80 Model II business computer and daisy-wheel printer II, plus table and accessories. Good condition, prefer warranty. Joe Boyd, POB 6, West Union, WV 26456.

FORSALE: Working ASR33 terminal with RS-232C interface. Includes paper-tape reader and punch. Also includes stand, schematic diagrams, and technical manual. \$400 or best offer. Joseph Mueck. 943 Hyacinth Dr., Deiray Beach, FL 33444, [305] 272-2779.

WANTED: Any and all information regarding the VideoBrain computer (e.g., source of cartridges, operating manuals, etc.). Currently working to enable the VideoBrain to run TRS-80 programs. Bryan McPhee, 418 Virginia Dr., Browns Milfs, NJ 08015.

FOR SALE: Two REMEX RFD-4000 double-sided 8-inch disk drives. Each with formatted capacity of 1.2 megabytes. Fast step time of 3 ms. Doorlocks and write protect. Power supply. Used a total of 11 hours. \$1000 or best offer. David Tulbert, 6700 Grauer Rd., Niagara Falls. NY 14305, (716) 297-6347.

FOR SALE: Two Micropolis Mod I drives (one never used) with WordStar and manuals. \$400. Jack Koch, POB 765. Cherry Hill, NJ 08003.

FOR SALE: Compucolor II microcomputer with 16 K memory. built-in floppy plus add-on drive, sound generator, two keyboards (one expanded, one standard), all manuals, cables, and lots of software. Best offer or would consider satellite receiving equipment or other interesting trades. M.A. Franco, 232 Holiday Village, Enterprise, AL 36330.

FOR SALE: Vector Graphics 8080 processor, Bitstreamer VO board, Tarbell single-density 8-inch controller, two Shugart 801R drives, 64 K IMS static programmable memory (bank selectable). All in new Integrand Main/Frame. \$3000. With SOROC IO 120; \$3660. With SOROC and new Epson MX-80; \$4100. Can upgrade to Z80, double density, and TI-810. Ralph Patlow, 6551 Southwest 8th St., Pembroke Pines, FL 33023, [305] 962-8307.

WANTED: The Cheap Video Cookbook by Don Lancaster. Will pay S6 if you will wait one month for payment. Also want four 280 assembler programs. Will pay S0.50 each. Unused programs will go back to Sender, so include return address. Eric Schissel, 30 Entrance Rd., Roslyn, NY 11577.

NEEDED: Repair manual and other manuals for Flexwriter (recorder-reproducer) Model FL made by Commercial Controls Corp. Also, need North Star BASIC floppy disk Release 5 or later. Will pay reasonable reproduction charges. State cost. Harry Mazur, 1450 Chestnut Pl., Boulder, CO 80302, (303) 447-0306.

FOR SALE: PDP-11/15 with 16 K bytes of core memory. Teletype interface, cable, and Teletype ASR33 with stand. Complete documentation. Only \$1200. C.F. Shank, POB 248627, University Branch, Miami, FL 33124, [305] 625-3269.

NEEDED: Replacement print head for Epson TX-80 (not MX-80) printer. Have been unable to obtain from local Epson representative. Will buy from dealer or individual. Samuel Gamoran, 228 Graham St., Highland Park, NJ 08904. (201) 949-3625 days, 246-7572 evenings.

FOR SALE: Pertec Attache 8080 S-100 system. \$ 1500 or best offer. 32 K static memory. 9-inch monitor, keyboard, PROM board, 16 by 64 video, Pertec \$10 B-inch floppy, Wameco disk controller, and cabinets. Also, Z80/S-100 processor card [\$125] and Digital Group Phi-Deck [4] system in dress cabinets with controller board [\$200]. Dean I. Lawry. PO8 1157, Corrales. NM 87048, [505] 898-5145.

FOR SALE: Atari 400 with 8 K and a set of paddles. Just like new. Or will trade Atari 400 and \$200 for Atari 800 in good condition. Dave Zalokar, 1845 Gerda SE, Kentwood, MI 49508.

FOR SALE: North Star Horizon 2. Includes two 5-inch double-density disks, 48 K programmable memory, sound-generation board, software, documentation, and Hazeltine 1500 24 by 80 super terminal. Complete system: \$2900. Duane Brummer, Rte. 2. Brooklyn, WI 53521, [608] 835-7554.

FOR SALE: ADDS Regent 25 video-display terminal; s800. Little used and in excellent condition. Display is 24 lines by 80 characters per line. Separate 18-key numeric data entry and cursor control pad. Cursor addressing. David Bainum. POB 139, Hartford. KS 66854, [316] 343-6255 after 6 p.m. weekdays.

FOR SALE: BYTE from June 1977 to July 1981. Excellent condition. Dennis R. Yelle, 655 South Fair Oaks Apt. P306. Sunnyvale, CA 94086, [408] 245-6335.

WANTED: DEC PDP-8, PDP-11, and LSI-11 computers, parts, boards, manuals, peripherals, documentation, courses, etc., working or not. Also interested in DEC-compatible items and software that works. H. Kolesnik, 5277 South Kenton Way, Englewood, CO B0111, [303] 779-5256.

FOR SALE: Heathkit H-89 with 48 K programmable memory, cassette interface, and two floppy-disk drives (open slot for third drive). Includes HDOS, Microsoft BASIC, cassette operating system, and many miscellaneous software products (business, financial, games, etc.). Complete with all manuals. s2500 for all. I will pay postage for delivery. Bill Jimerson, 15115 Parthenia #178, Sepulveda, CA 91343.

FOR SALE: 16 K Commodore PET with built-in cassette drive: \$649. Also available: Toolkit read-only memory. Channel Data System's Omnifile and CB2 sound system Port Noise. CURSOR magazine tapes #1. 7. 21. 23-28. Commodore's Spacetrek, Blackjack, and A Treasure Trove of Games. United Software of America's Checkbook. Radio Shack Line Printer Two; 5599. Steven Dean, POB 1083, Springfield, VA 22151, [703] 978-3322.

FOR SALE: Versatile 3B computer, all units in one enclosure. Ten-slot \$-100 bus with Spacebyte 8085 processor, dual Mod I Micropolis disk drives, 32 K Dynabyte static memory, two 85-232 serial and three parallel ports, Ball 9-inch monitor, 80 by 24 Dynabyte video board, and numeric keypad. Software included: MDOS and BASIC, Versatile business package, games, and more. In excellent condition. \$2495, original price \$4000. Ralph Pullmann, 2765 Sierra Dr., Colorado Springs, CO 80917, [303] 599-0712.

FOR SALE: Commodore CBM 80328; \$995. 2040 disk drives; \$995. 2022 tractor printer; \$595. Unused, except to check system out, and works fine. Will ship in original cattons with all cables and manuals. Compumax accounting software included free with purchase of system. 16/32 service kit; \$195. Louis Robert, POB 144, Hessmer, LA 71341, [318] 563-4428.

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Clarcia Wins BOMB

It looks like Steve Ciarcia has out-"poled" his competition. Steve won the November BOMB with his article, "Switching Power Supplies, An Introduction," a fine tutorial on the design and construction of a nonisolated, singleended, switching voltage regulator. He will receive the \$100 prize. Kathryn S. Barley and James R. Driscoll's "A Survey of Data-Base Management Systems for Microcomputers" took second place. They will share the \$50 prize. Third place goes to Michael Gagle, Gary J. Koehler, and Andrew Whinston for their article, "Data-Base Management Systems: Powerful Newcomers to Microcomputers."

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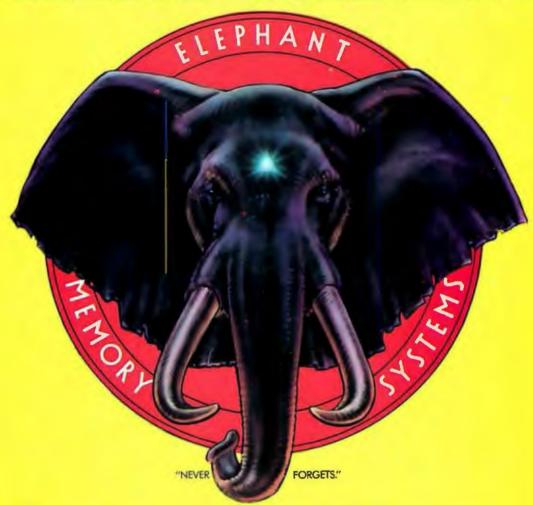
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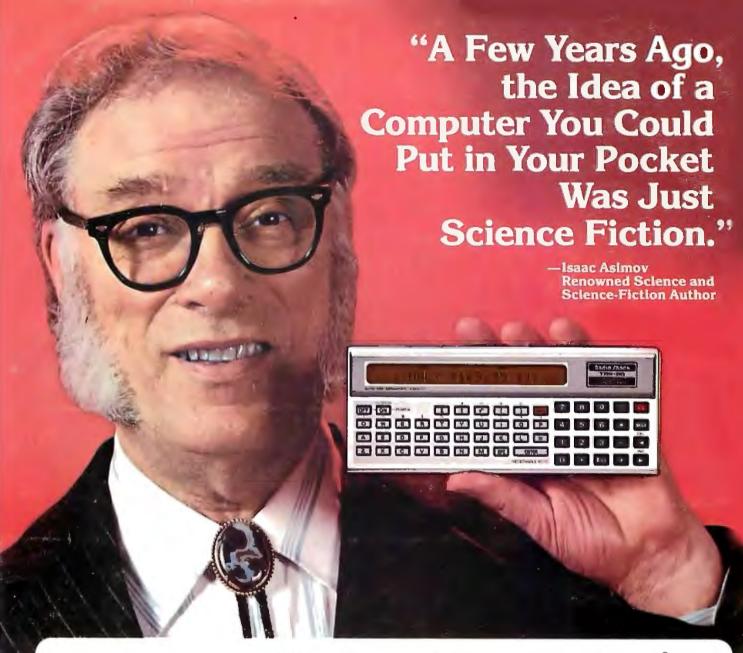
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